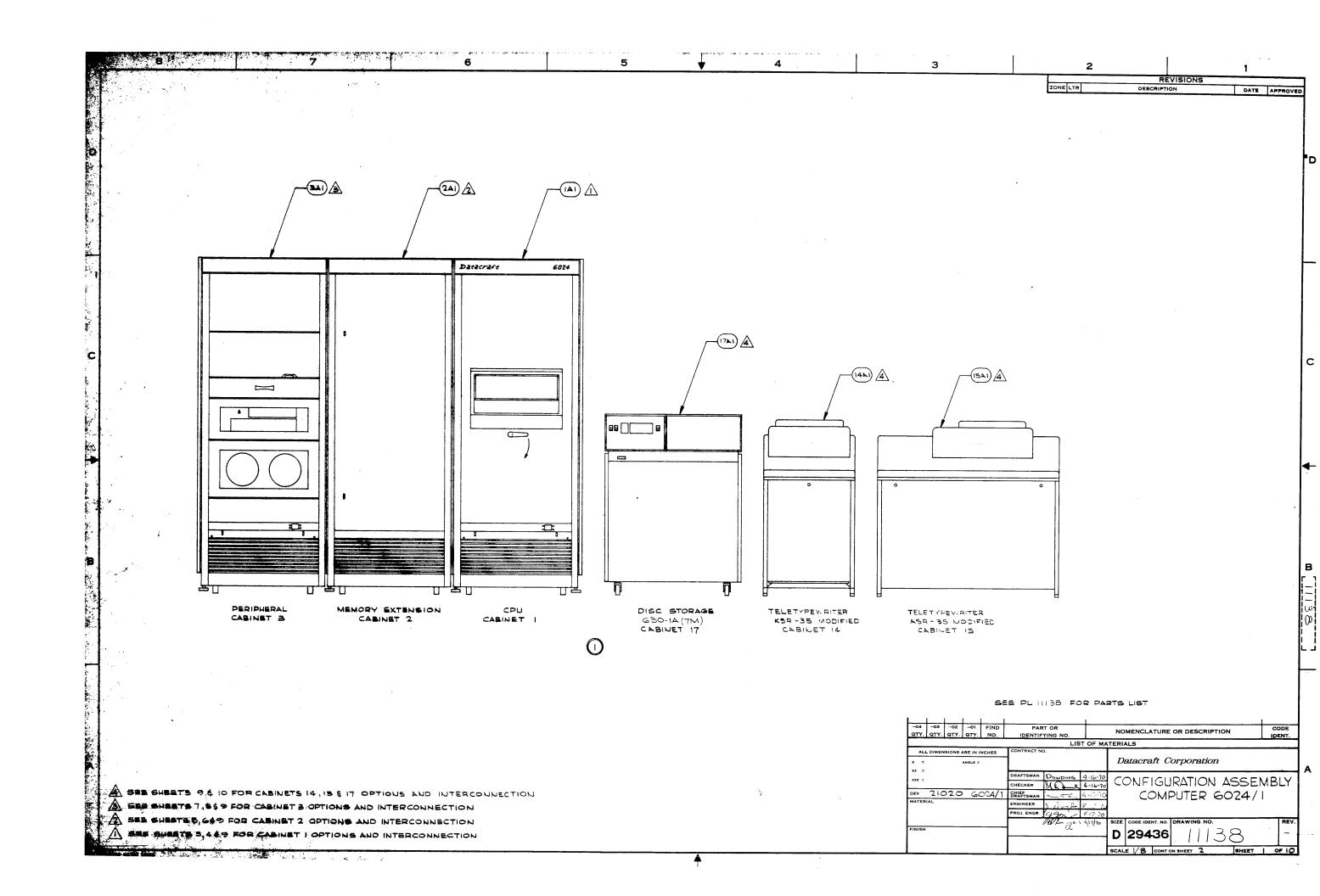
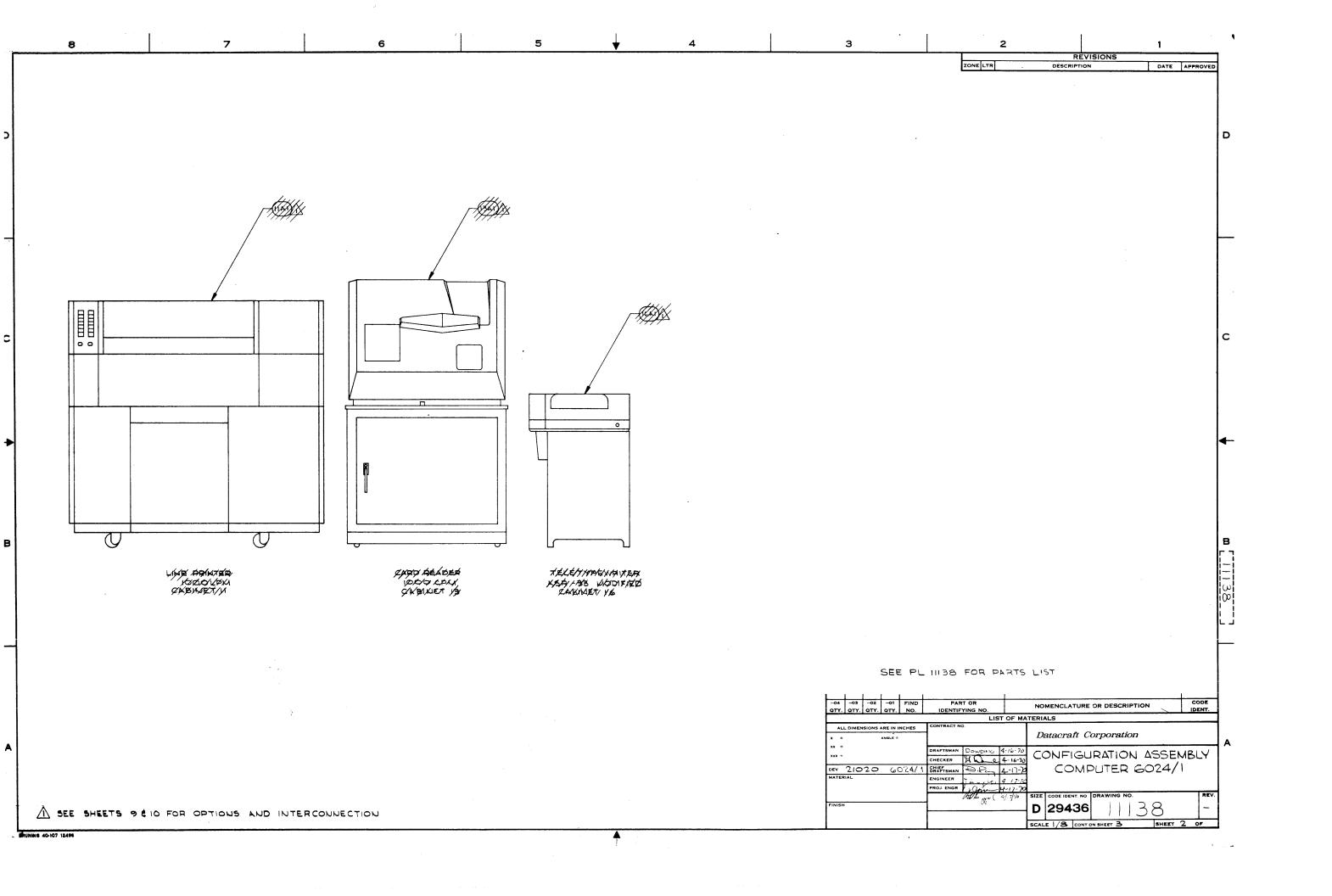
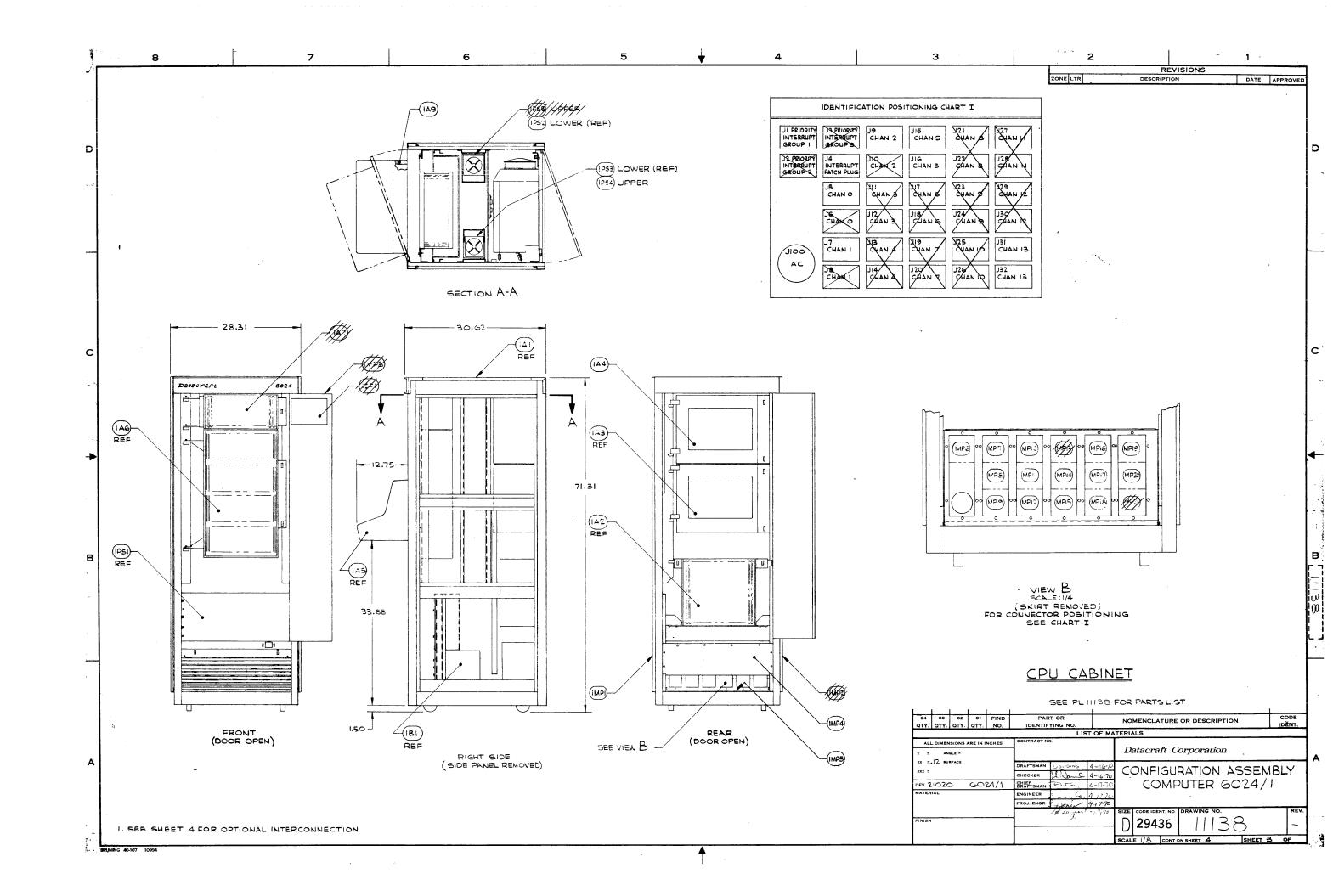
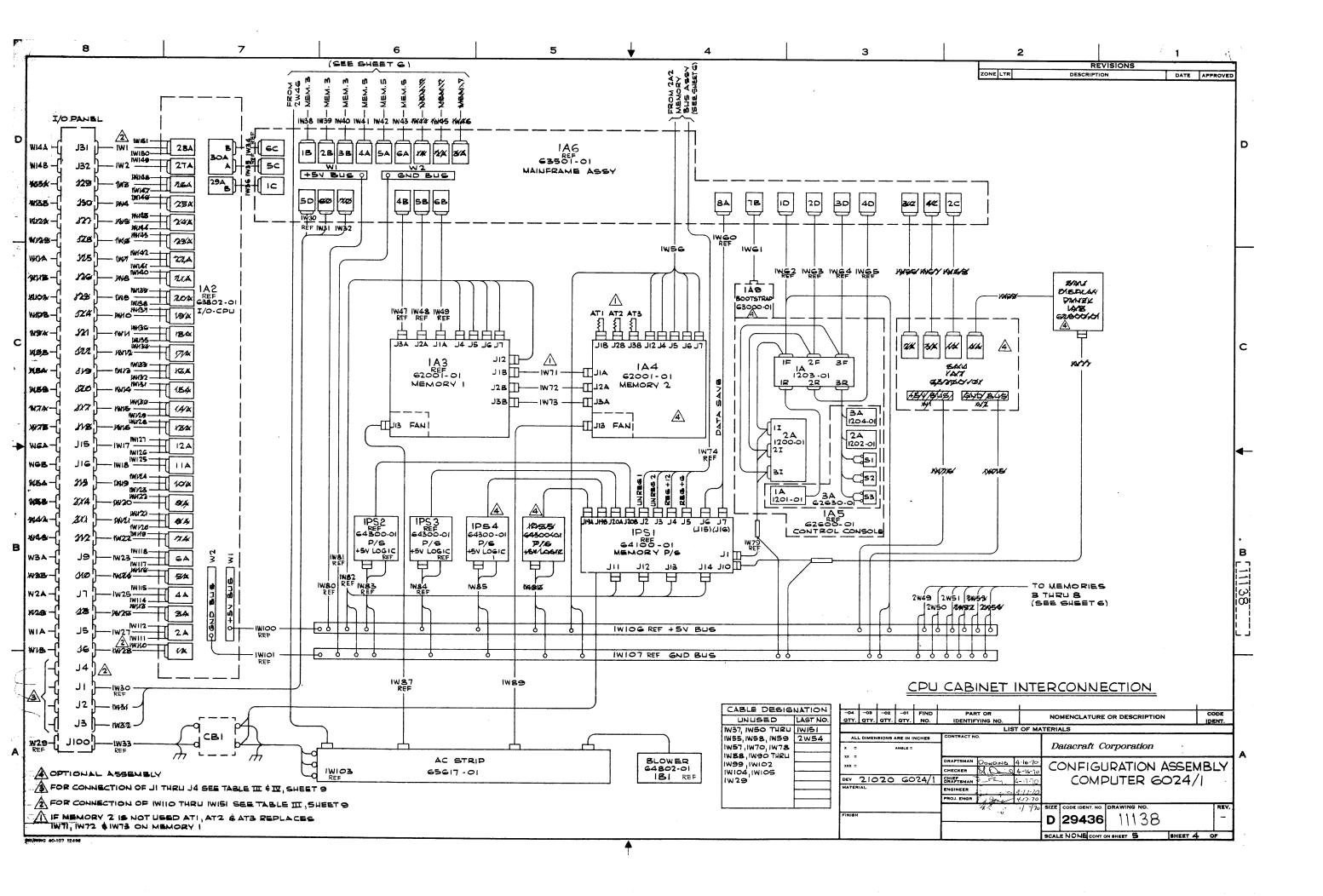
Series 6000 Computer System

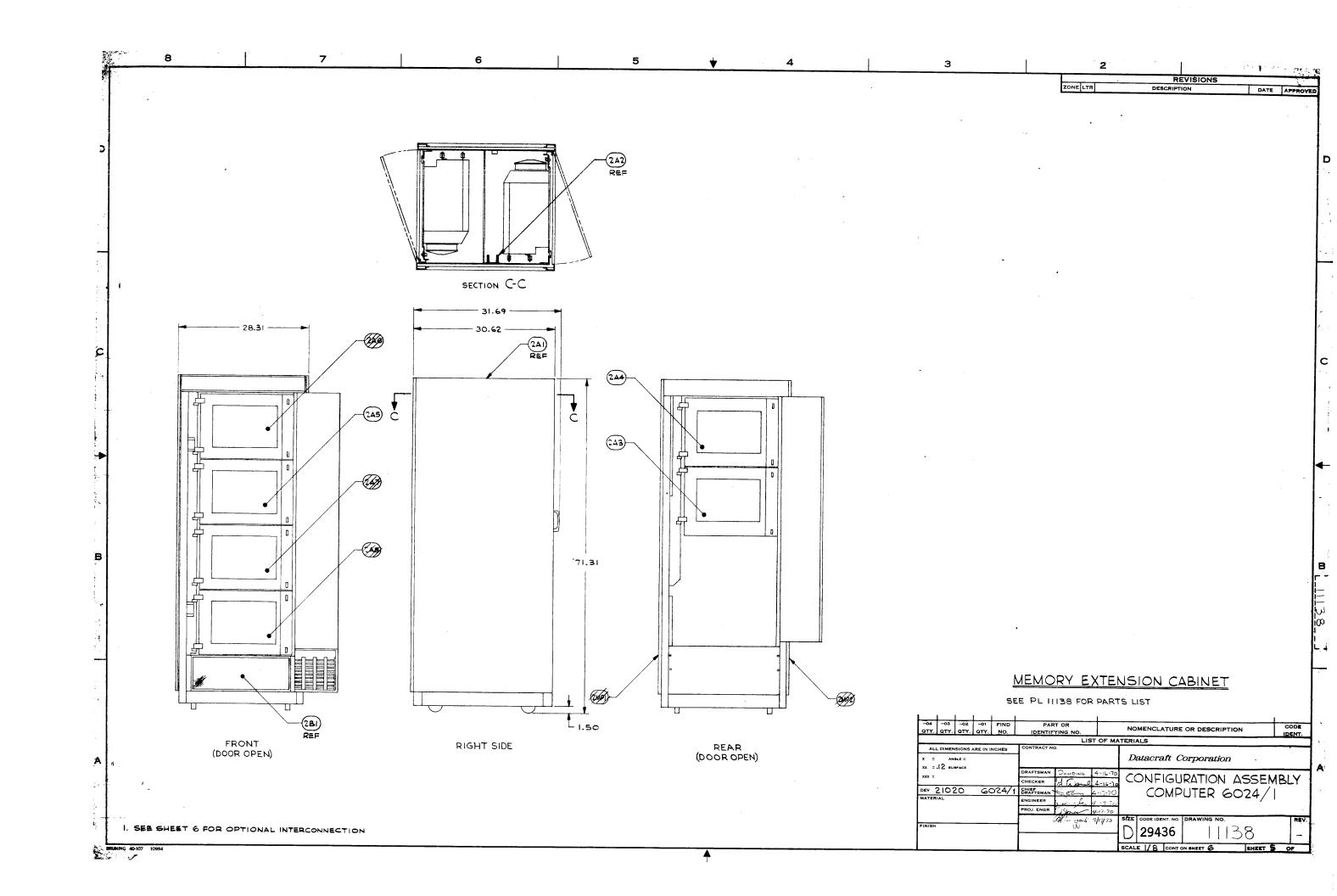
> Datacraft Corporation

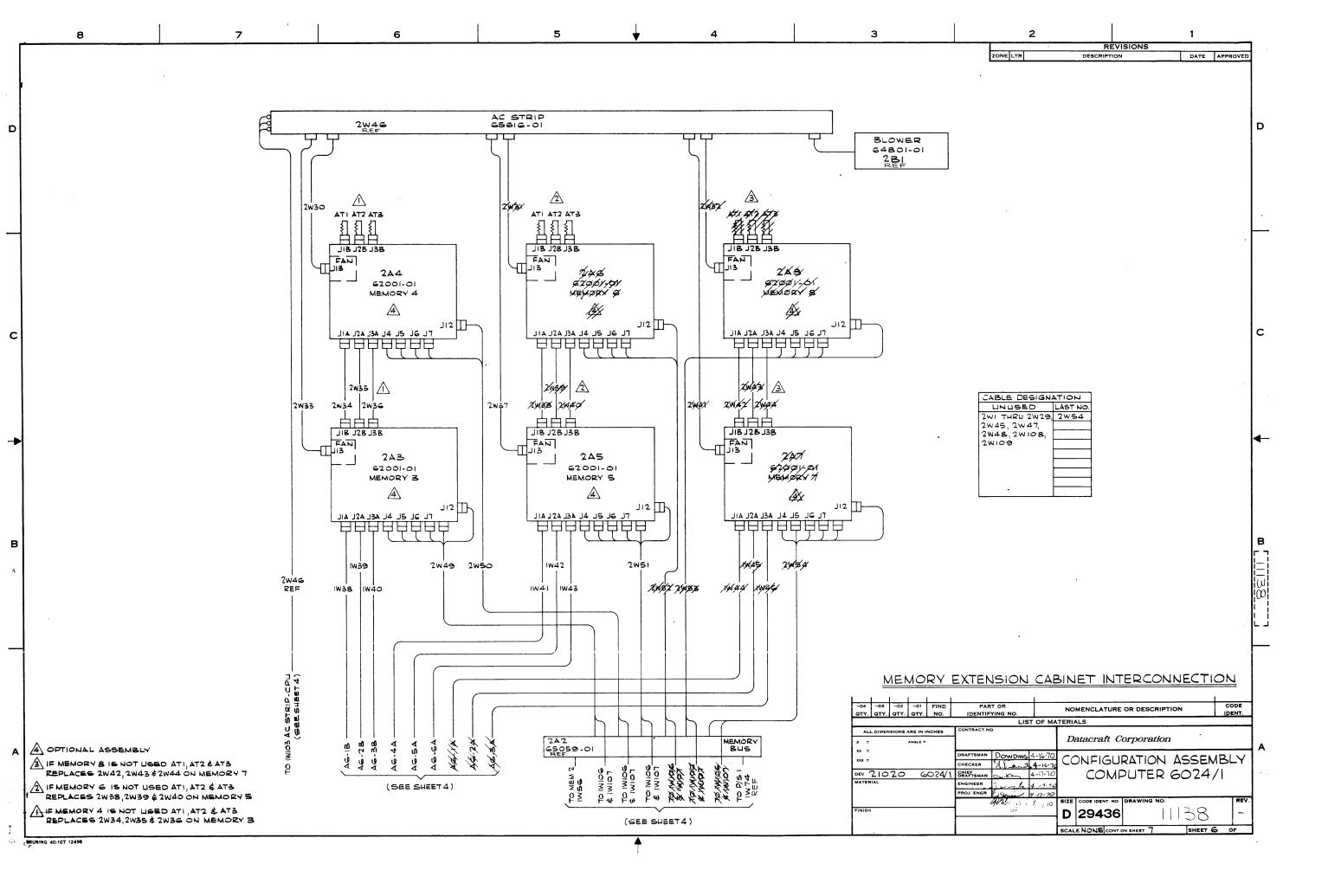


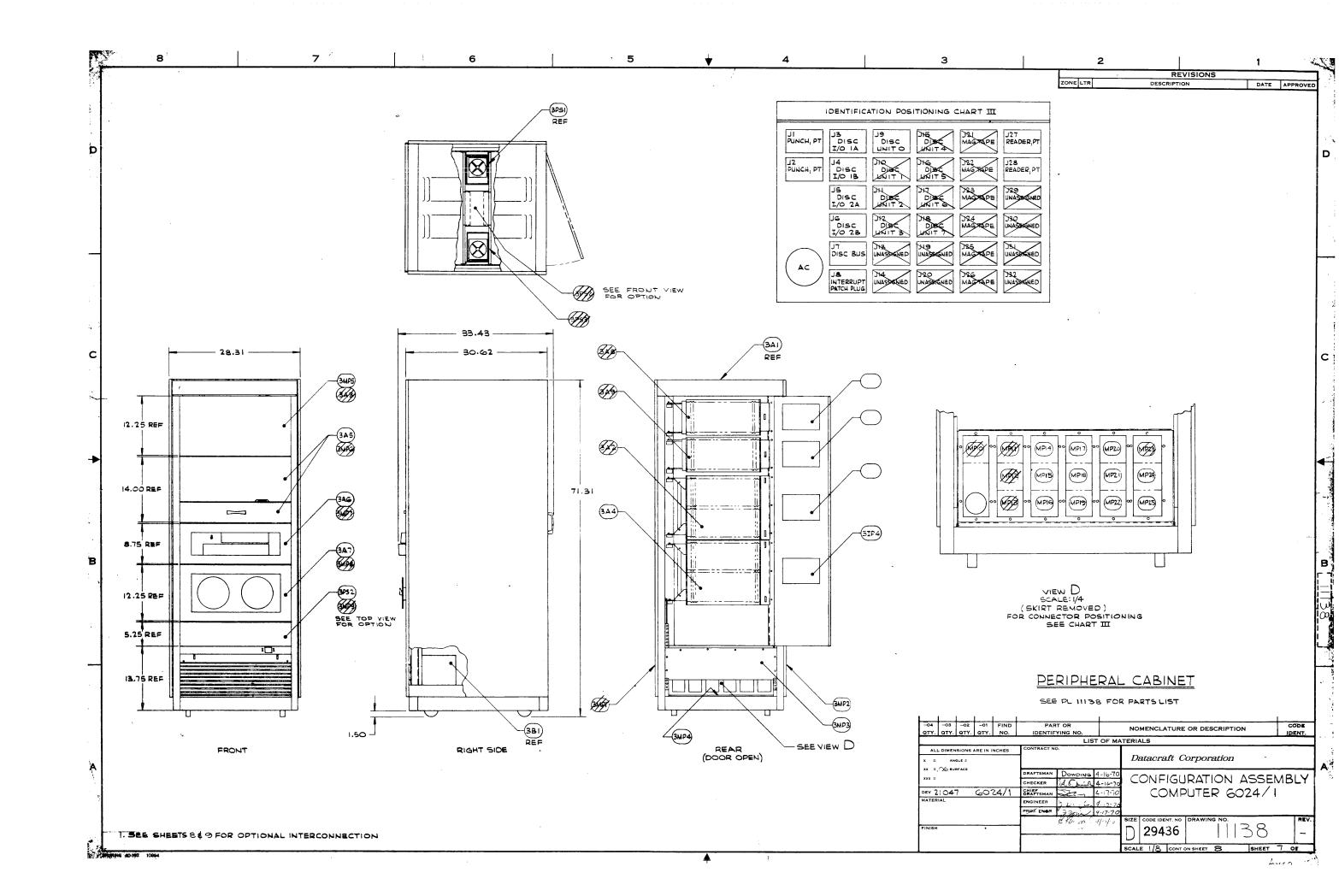


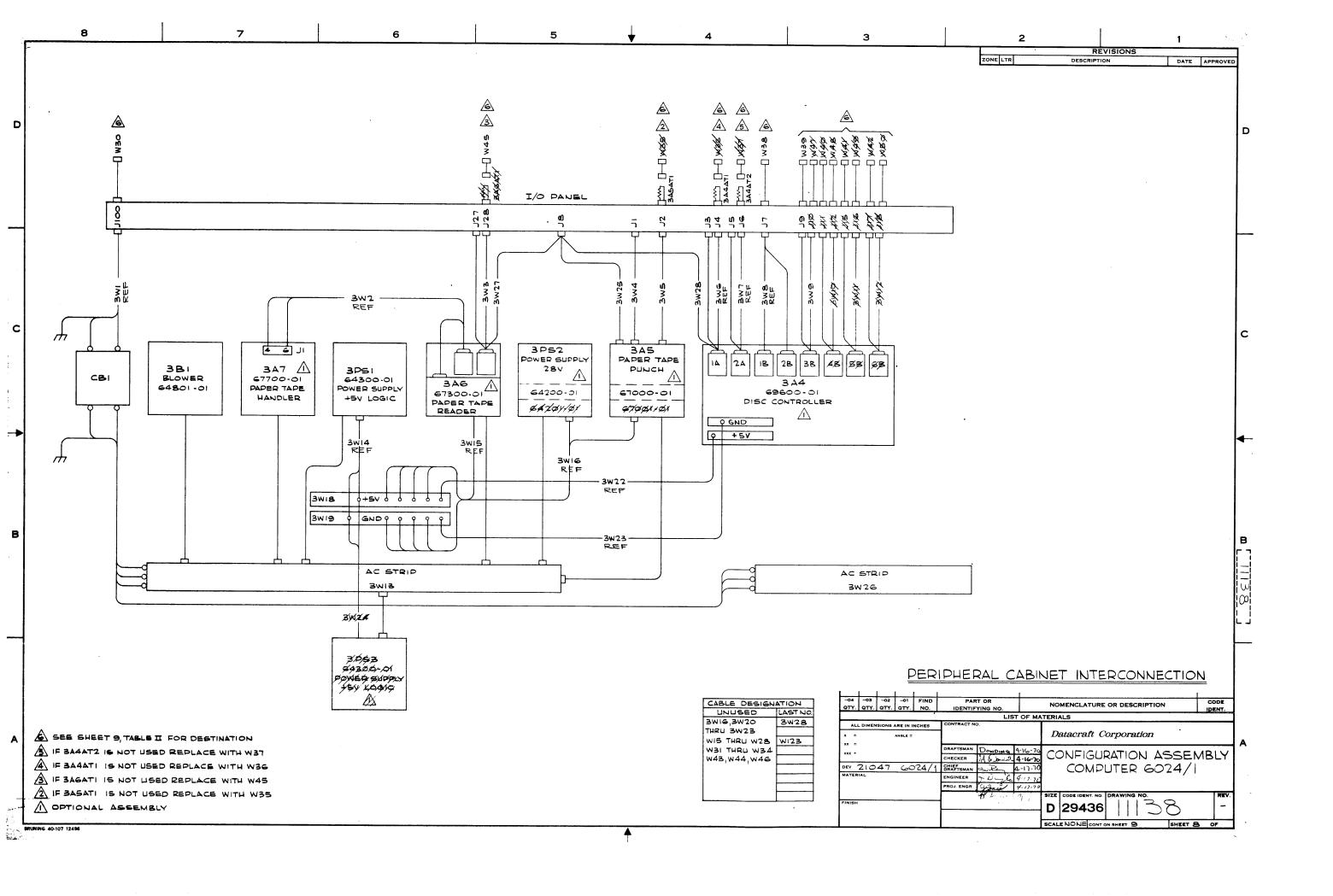












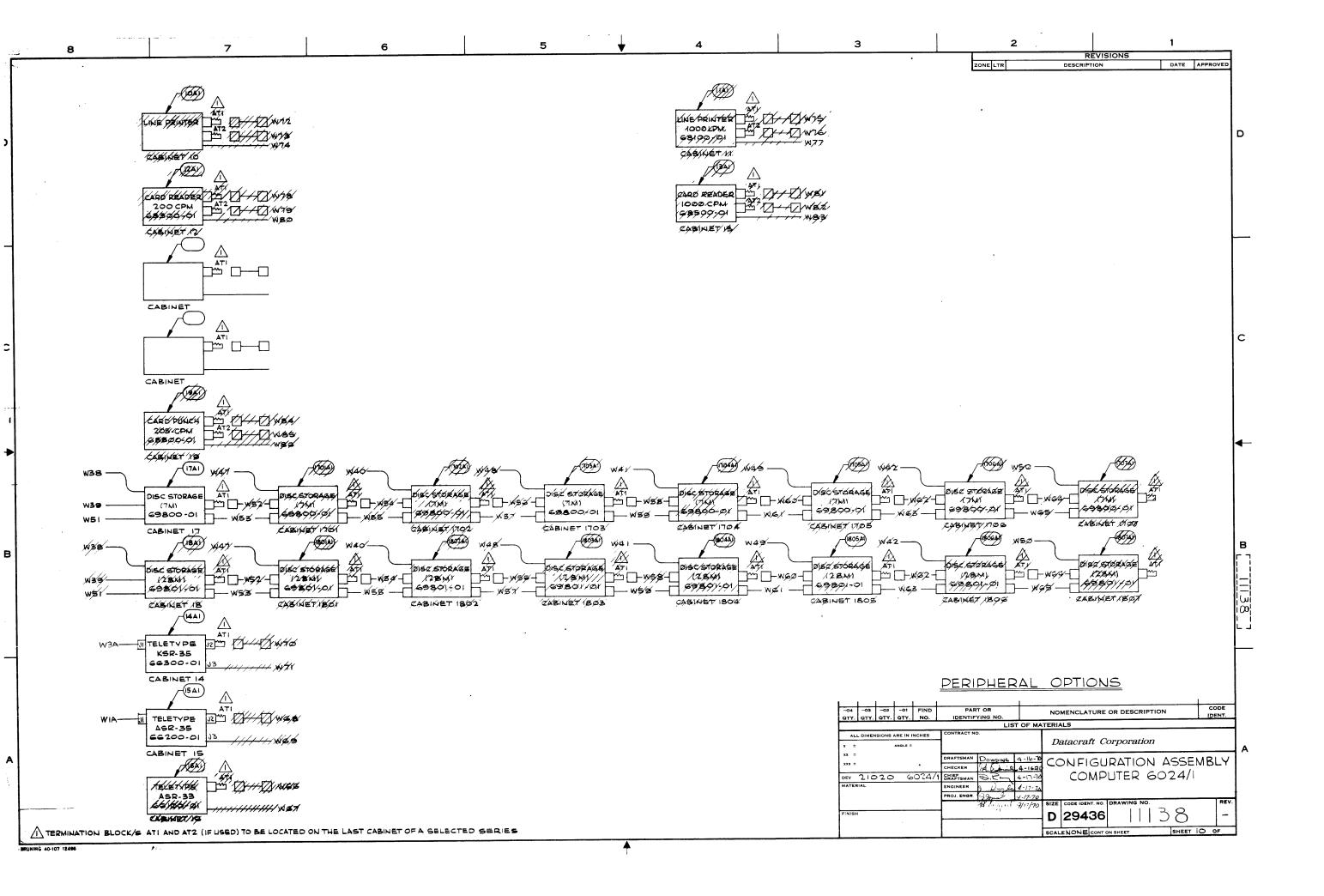
8 7 6 5 3 2 REVISION ZONE LTR DESCRIPTION DATE APPROVE TABLE I TABLE II TABLE III CAB. INTERCONNECTION CAB. INTERCONNECTION CHANNELS INTERRUPT CONNECTIONS CHAN CPU OR WIRE NO. PERIPHERAL PERIPHERAL CAB WIRE NO. FROM TO FROM WIRE NO. TO COLOR NOTES NAME NAME OR TERMINATION BLK NO. IJS WIA TTY ASR-35 ISAIJI A2-2A-82 RED CHAN O 889 0 P4 - 101 J4 - 101 W87 001L W29 $A\subset$ 156 WIB - 201 | - 201 -2A-81 BLK INPUT INTR IJ7 W2A - 102 -2A-22 RED CHAN O 26 13 -102 W88 IJE WZB - 202 | -202 ļ -2A-21 BLK OUTPUT INTR AEW PLI IAAIJI TTY KSR - 35 - 103 -44-82 RED CHAN I 108 11 -103 2 W89 1W114 IJIO W3B - 203 | - 203 -4A-81 BLK INPUT INTR IJII W4A -104 -44-22 RED CHAN 1 -104 W90 1W115 WBO 1112 W4B 33100 $\Delta \subset$ -204 -204 -44-21 BLK OUTPUT INTR TABLE IV IJI3 WSA **3J2** 3A5ATI TERM BLOCK -105 -105 -64-82 RED CHAN Z W9 I IW 117 1J14 W5B PRIORITY INTERRUPT GROUP I CONNECTIONS -205 -205 -64-81 BLK INPUT INTR 3J4 3A4ATI TERM BLOCK IJI5 WGA 3J3 7MB DISC CONT -106 -106 -GA-22 RED CHAN Z 14 TERM BLOCK 3A4AT2 W92 316 WIIS LEVEL TO COLOR 3J5 7MB DISC CON-IJIG WGB -206 -206 -64-21 BLK OUTPUT INTR **3**J7 DISC STORAGE O HJIT W7A W38 17A1J5 107 -107 -BA-82 RED CHAU 3 PI - 105 RED was 1W120 0 1J18 W78 319 W39 17A1J3 -207 -207 -8A-81 BLK INPUT INTR DISC STORAGE O BLK IJI9 WBA -ICB -IOB -84-22 RED CHAN 3 RED __ 2111 W40 -02AIJ5 ₩94 1W121 - 1 1J20 WAR -208 -208 -8A-21 BLK OUTPUT INTR -206 BLK 3J15 W41 O4AIJS 1121 | W9A 104-82 RED CHAN 4 -109 -107 RED -8-W95 W123 3J17 W42 OGALIS IJ22 W9B -209 - 209 -IOA-8+ BLK INPUT INTR BLK HJ23 WIOA 4 -104-24 RED CHAN 4 -110 -109 D≈D W96 1J24 WIOB -210 -210 - IOA-21 BLK OUTPUT INTR -209 BLK IJ25 WIIA 124-82 RED CHAN S -111 -111 -110 RED +0 3J28 W97 1J26 WHB -12A-81 BLK INPUT INTR - 211 - 211 -21C 1J27 W12A -112 -112 -12A-22 RED CHAU 5 RED ++ 5 1J28 W128 3JIO W47 OIAIJ5 - 212 - 212 -12A-21 BLK OUTPUT IUTR -211 BLK 1J29 WI3A -301 -301 -14A-8' RED CHAN 6 -303 RED 12 3112 W42 03AL15 W99 IW129 1130 W138 -401 -401 -144-81 BLK INPUT INTR -403 BLK 3116 w49 OSALIS IJ31 WI4A ISOLATION INTERFACE P4-505 WIIS -302 - 302 -14A-22 RED CHAN 6 -307 RED ISOLATION INTERFACE 15 13 07AIJ5 WIOG -W-5-0 1W130 132 W14B ISOLATION INTERFACE 3J18 -407 P4-605 - 402 14A-2+ BLK OUTPUT INTR BLK WCC INTR -402 AC W51 - 303 - 303 -IGA-82 RED CHAN 7 RED ASR - 35 ITAIJI DISC STORAGE O -310 P4-104 WIOI IW132 OIAIJ3 W52 - 403 IGA BI BLK INPUT INTR -410 P4 - 701 BLK INPUT INTR -403 -IGA-22 RED CHAN 7 -304 -304 -502 P4 - 102 RED ASR - 35 1801 W102 -SEW ILLAID W133 AIJ2 Sew -404 -404 164-21 BLK OUTPUT INTR -602 P4 - 202 BLK OUTPUT INTR OZAIJ3 W54 1702 -504 P4 - 105 RED KSR-35 - 305 -305 -ISA-82 RED CHAN & W103 W135 10 w91 PERIPHERAL CABINET- UNIT ASSIG. OZAIJI W55 OIAIJZ -405 -405 184-81 BLK INPUT INTR -604 P4 - 205 BLK INPUT INTR KSR - 35 PERIPHERAL CAB., NO. CHANNEL UNIT, NO OSALJS WSG 02A1J4 -306 -306 184-22 RED CHANE -505 P4 - 106 1703 W104 WOZ 1W136 1.1 -406 - 406 -184-21 BLK OUTPUT INTR -605 P4 -206 BLK OUTPUT INTR OBAILL WS7 OBAILE 1803 -204-82 RED CHAN 9 -307 - 307 -506 P4 - 103 PTF RED WIOS TTY 45R-35 OBAIJ4 15 0 0 GROUP I/LEVELS 04A1J3 W54 IWI38 12 1704 -407 - 407 -204-81 BLK INPUT INTR -606 P4 - 203 -507 P4 - 104 BLK INPUT INTR 804 OHAIJI WS9 -308 -204-22 RED CHAN 9 - 30 g O3AIJ2 RED PTP WIOE PTR 1 GROUP I/LEVEL IZ OPW OSAIJS WES -04A|J4 -204-21 BLK OUTPUT INTR -408 -408 -607 P4-204 BLK OUTPUT INTR -224-82 RED CHAN 10 RED TMB DISC -309 -309 -508 P4 - III OSAIJI 04AIJ2 1 GROUP I/LEVEL 13 -608 P4 - ZII. - 400 -409 -224-81 BLK INPUT INTR BLK INPUT INTR OGAIU3 WG2 OSALJ4 900 900 900 900 -310 -310 -22A-22 RED CHAN 10 -509 P4 -503 REL ISOLATION INTERFACE TTY KSR-35 Z IW142 0 GROUP I/LEVELIO WIOS EIIW OGAIJI WG3 -410 O541.12 -410 22A-21 BLK OUTPUT INTR -609 P4-603 BLK INPUT INTR OCAIJ4 24A-82 RED CHAN II 1707 OTALJ3 WG4 -311 - i O I RED TMB DISC CONT 2A4 5 0 WIOS 1W144 GROUP I/LEVEL 14 244-81 BLK INPUT INTR 807 -411 -411 -201 BLK I LIAFO WG5 OGAIJ2 -312 -312 244-22 RED CHAN H -102 REC ISOLATION SPECIAL 13 0 GROUP I/LEVEL 15 WHO 1W145 IGAIJ2 INTERFACE EQUIP 412 24A-21 BLK OUTPUT INTR BLK - 412 -202 16 IGAIJ3 W67 -50+ -501 264-82 RED CHAN IZ -103 RED -264-81 RIK INPUT INTR 15A1J2 TERM BLOCK - 60+ -601 -203 BLK -502 -502 -26A-22 RED CHAN IZ - 104 RED ELIA51 WG9 19 -602 -602 -264-21 BLK OUTPUT INTR -204 14A1J2 441AT1 TERM BLOCK -503 284-82 RED CHAN 13 -503 - 510 RED 14 WII3 IW150 20 14A1J3 -603 28A-81 BLK INPUT INTR W71 -603 -610 IOAIJ2 W79 -504 284-22 RED CHAN 13 - 504 RED WH4 IW 151 21 -604 -604 -28A-21 BLK OUTPUT INTR -611 BLK HOALJ4 W73 505 -505 A-22 RED ABC WCC -512 RED WIIS W/149 22 A UNIT NO. ASSIGNED TO THE SPECIFIED CHANNEL 10AIJ5 -W74 - 605 A-21 BLK PRIORITY I -605 -612 BLK IIAIJ2 W15 A-22 RED ABC WCC -506 -500 -413 RED ASSIGNED OPERATING CHANNEL FROM TABLE I WH6 A-21 BLK PRIORITY 2 -606 -- 606 PI- 613 BLK HAIJ4 W76 A FIRST THRU LAST CABINET DESIGNATION (S) -507 - 507 A-22 RED ABC WCC HAIJS W77 -607 -607 A-21 BLK PRIORITY 3 TYPE PERIPHERAL SELECTED 12AIJ2 -508 -508 A-22 RED ABC WCC WHE NOMENCLATURE OR DESCRIPTION PIN NO. FOR CHANNEL SELECTED INTERRUPT PER TABLE III,
PERIPHERAL EQUIPMENT PER TABLE II OR CUSTOMER REQUIREMENT
SWIRE NO. FROM TABLE II, III, OR CUSTOMER REQUIREMENT 12AIJ4 W79 -608 -608 A-21 BLK PRIORITY 4 QTY. QTY. QTY. QTY. IDENTIFYING NO. IDENT. -509 - 509 A-22 RED ABC WCC LIST OF MATERIALS 1241J5 W80 WHA - 609 A-21 BLK PRIORITY 5 -609 ALL DIMENSIONS ARE IN INCHES WSI-CHANNEL OR CUSTOMER EQUIPMENT SELECTED FOR LEVEL (SEE P L SHEETS 7 & 8) 13AIJ2 Datacraft Corporation -510 A-22 RED ABC WCC -510 ANGLE ± IBAIJ4 WB2 W120 A CARD PREFIX PER CHANNEL PRIORITY IN TABLE I ONLY IF ABC WCC INT 15 15EE (SEE IG BIT OPTIONS, P/L SHEETS 14-27)
WIRE NO. PER CHANNEL PRIORITY IN TABLE I ONLY IF ABC WCC A-21 BLK PRIORITY 6 -610 -610 ISALIS WAS -5++ CONFIGURATION ASSEMBLY -51+ A-22 RED ABC WCC W121 HECKER 14-16-7 ISAIJ2 W84 -6! A 21 BLK PRIORITY 7 -611 INT IB USED (SEE IG BIT OPTIONS, PL SHEETS 14-27)

A ENTER OR COMPLETE CONNECTOR DESTINATION

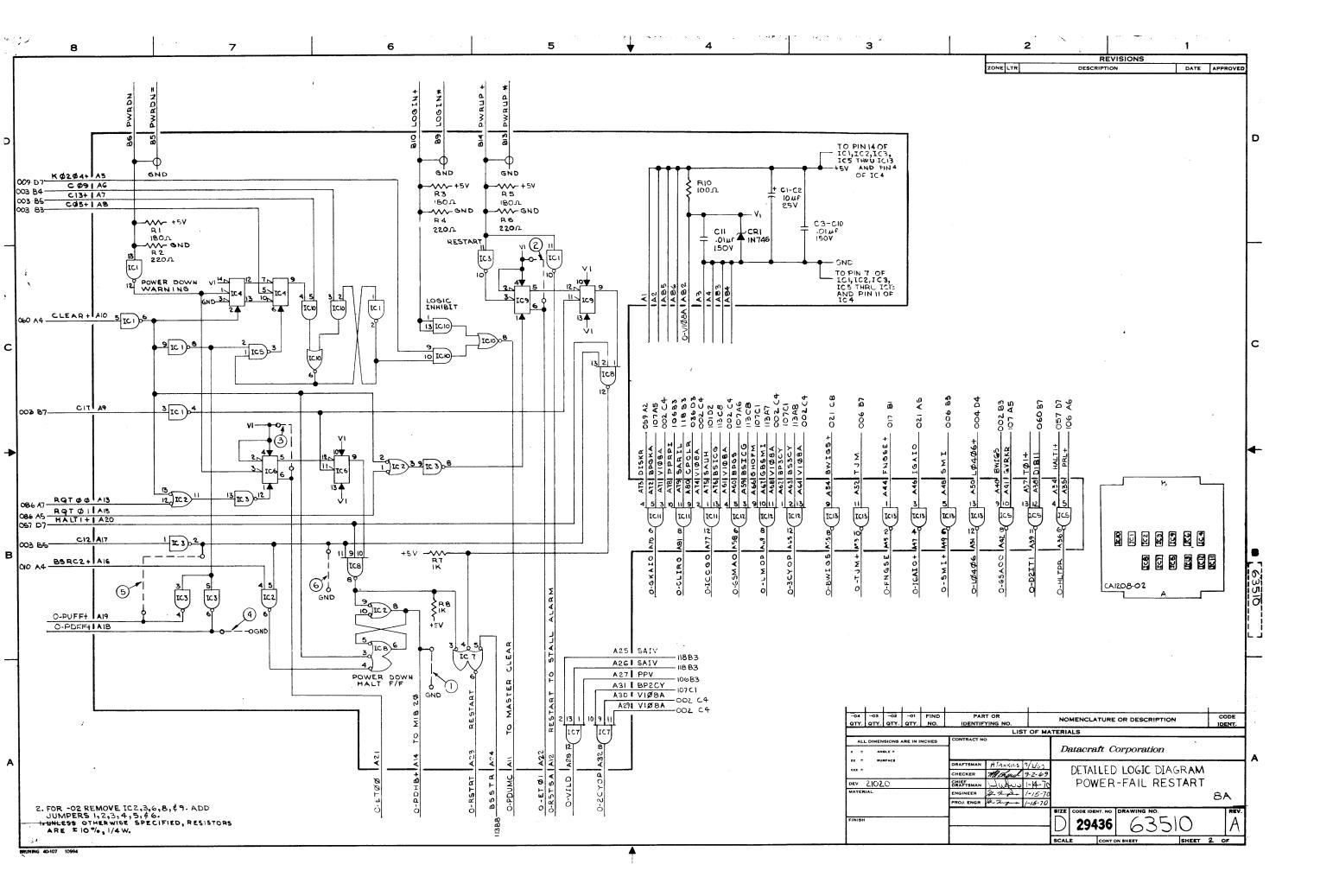
(SEE PL SHEETS 28-37) DEV 21020 GC24/ CHIEF DRAFTSMAN SAC COMPUTER 6024/I -512 -512 A-22 RED ARC WCC 19A1J4 W85 W122 ENGINEER 9 4 2 -612 -G12 A-21 BLK PRIORITYS SELECT CAB. NO. & ENTER CAB. PREFIX TO "FROM" COL THEN ENTER

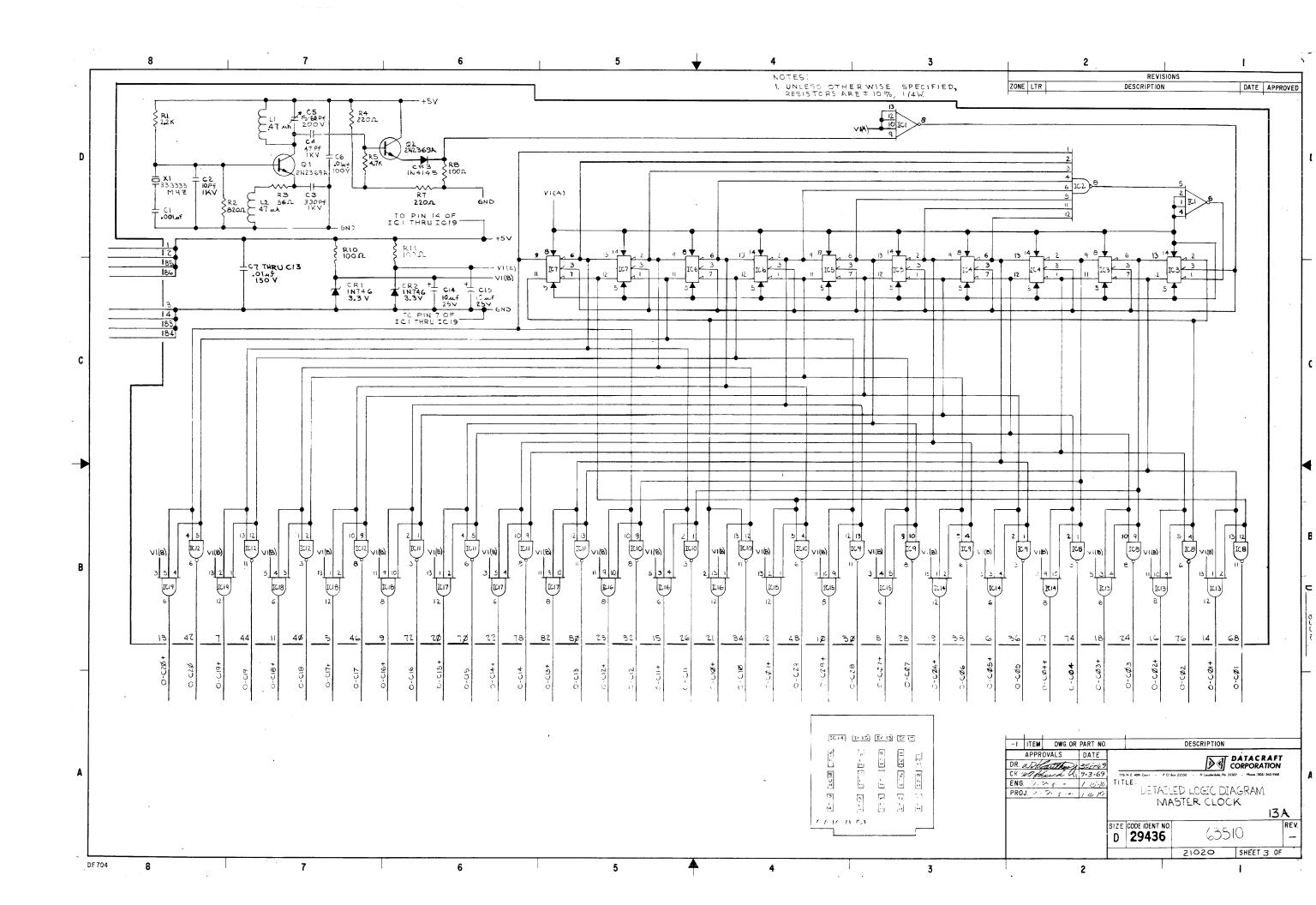
CAB. PREFIX TO ATI IN "WIRE NO." COL. ONLY IF USED (SEE PL SH 30-33)

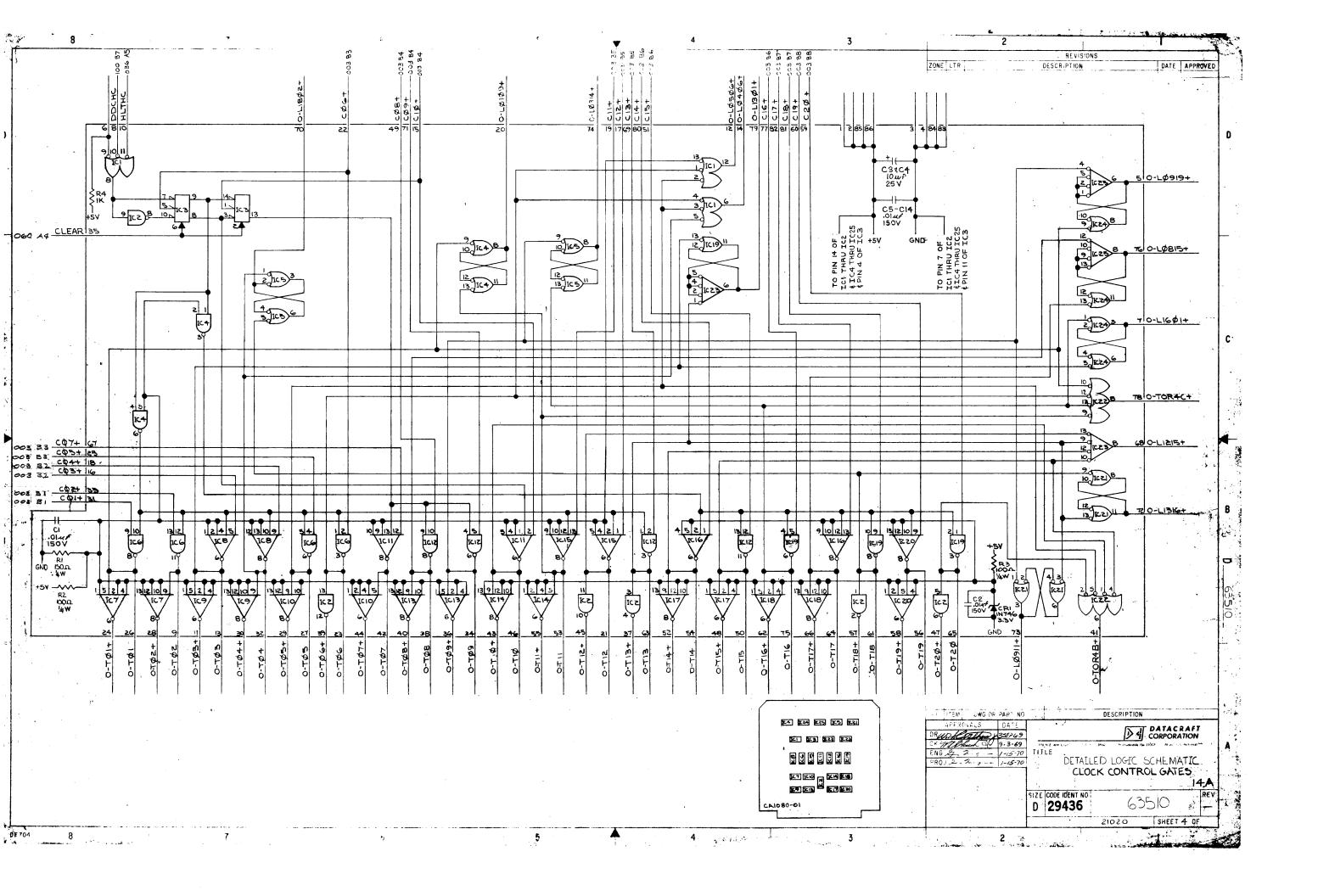
DESTINATION CONNECTOR ON SELECTED PERIPHERAL 19AIJ5 W86 PROJ. ENGR W123 P4-G13 J4-G13 -413 -413 A-22 RED ABC WCC A2- A-21 BLK PRIORITY 9 D 29436 (SEE P L SHEETS 28-37) A CUSTOMER SELECTED ABC PRIORITY SCALE NONE CONT ON SHEET 10 SHEET 9

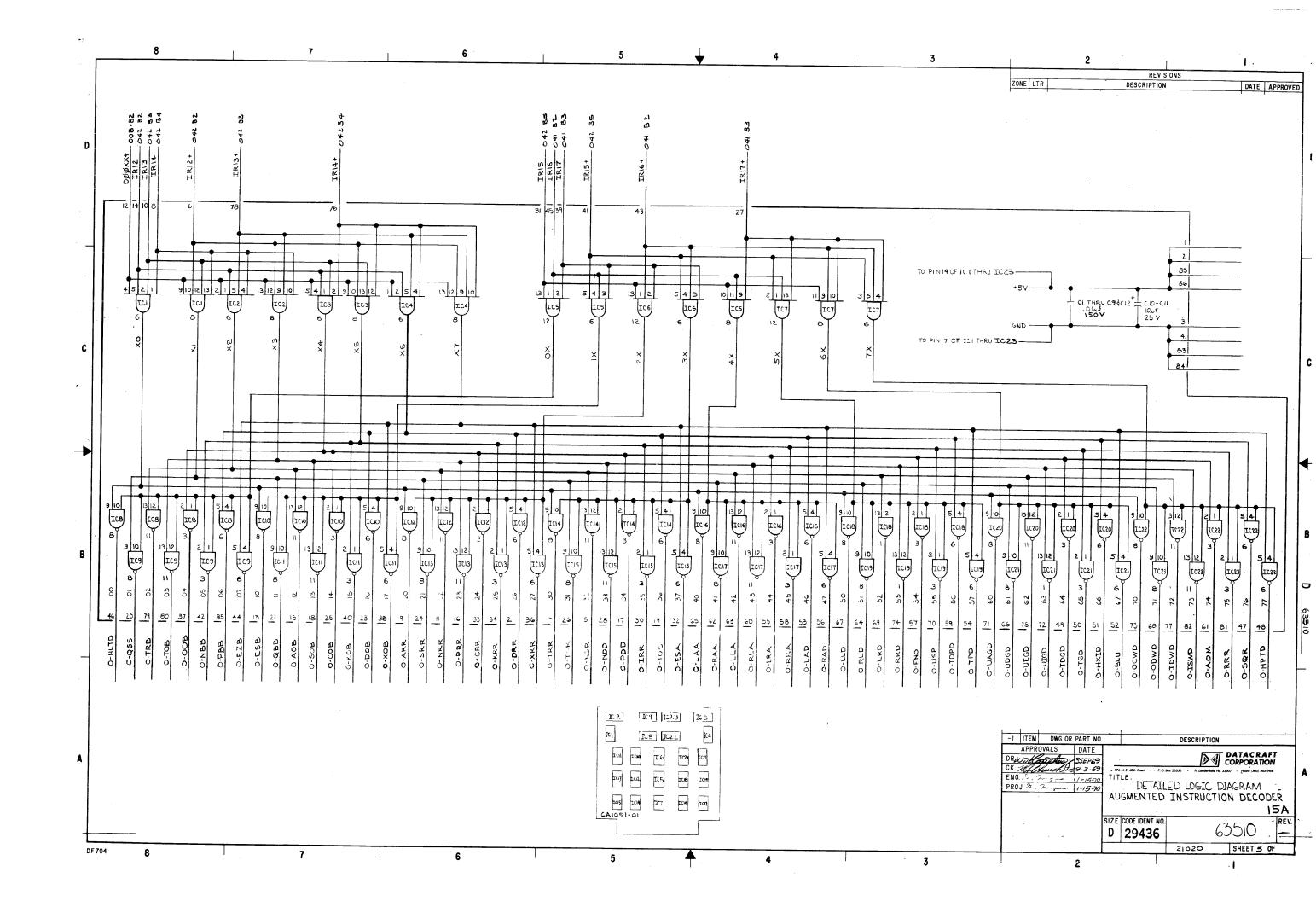


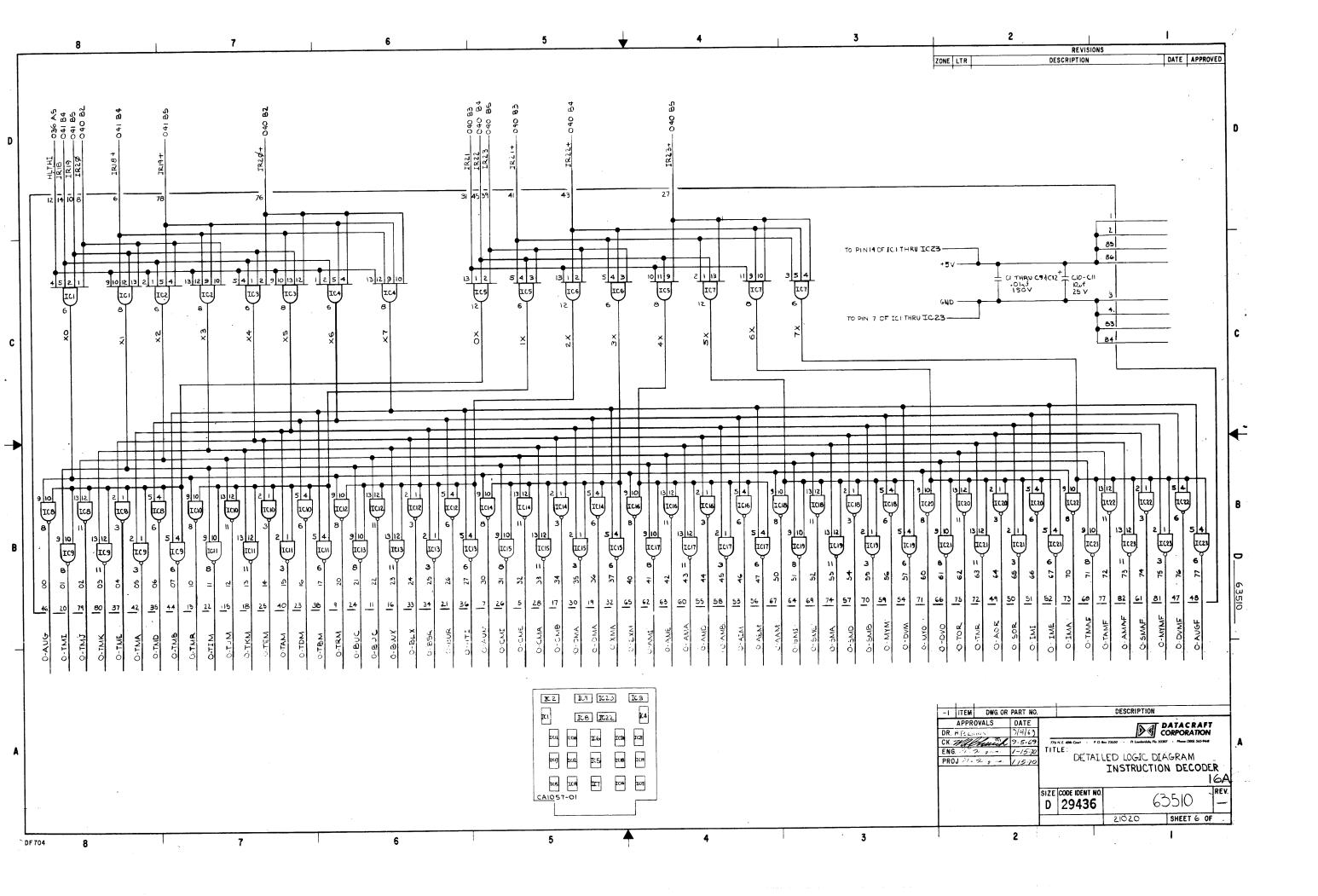
8 1 REVISIONS DESCRIPTION DATE APPRO SEE SHEET 121 FOR REVISION STATUS OF SHEETS CARD LOC. ΙA 2 **A** 3 A 4 A 5 A 6 A 7 A 8 A 9 A IO A IIA 12 A 13A 16A 17A 18A 194 20A 21A 22A 23A 24A 25A 26A 27A 28A 29A 30A 31A CARD [A] 1149-01 1150-01 1148-01 1149-01 1150-01 1148-0 1208.02 1667.0 1071-0 1080-01 1057-01 1057.0 1085.01 1098-01 1086.01 1091-01 10820 1066-01 1063-01 1109-01 1108-0 1094-01 103.0 1110-01 107-0 1106.011105-0 MATRIX MOR Y MATRIX X MATRIX MOR Y MATRIX RIVERS DRIVERS DRIVERS DRIVERS DRIVERS POWER MASTER CLOCK MEMORY BRANCH AUGMENTED INSTRUCTION ONTROL I/0 € MEMORY BRANCH CONDITION ACCESS CONTROL REGISTER RR ¢ Byte T¢C ULTIPLY INDEX FAIL RESTART PROC. CLOCK CONTROL INSTRUCTION DECODER GATES DECODER CYCLES INST. T & C CARD CONTRO STROBE CONTROL MEM 7 & & \$ SA GATES ATOR LATOR GENERATORIDECODER ICONTROL SQÜARF INST. TYPE GATING ETROBE MEM 7 & & GATING MEM 5 è 6 \$ STROBE STROBE CONTRO 1 HOO-3 CKTS AVAIL PAGE 088 087 090 089 091 092 002 107 003' 004 005 006 007 ೦೦೩ 009. 010 011 012 013 014 015 016 017 018 019 020 021 REMARKS \triangle \triangle Δ 13 <u>/3</u>\ CARD LOC. ΙB 2 B 3 B 4 B 5B 6B 7·B 8 B 9 B IO B ПВ 12B 13B 14B 15B 16B 17B 18B 19B 20 B 21B 22B 23B 24B 25B 26B 27B 28B 29B 30B 31B CARD [A 1149-01 1150-01 1062-01 1067-01 1061-01 1065-01 1065-01 1065-011069-011070-011171-01 1129-01 1129-01 1199-01 1052-01 1114-01 104.0 1083-0 1101-01 1111-01 1113-01 1663-01 1664.01 1665-01 1666-0 YMATRIX X MATRIX KMATRIX MOR MOP Y MATRIX BOOTSTRAP MEMORY MEMORY UPPER LOWER PROGRAM PROGRAM ADDE ESSINTERVAL INTERVAL NTERVA ZERO START INTERRUPT DRIVERS DRIVERS DRIVERS DRIVERS MEM MEM & SA MEM 13 & 4 GATES | & 2 MATRIX STALL BOOTSTRAP BOOTSTRAP BOOTSTRAP RIVERS DRIVERS DRIVERS DRIVERS TEC DECODER ADDRESS DATA DATA
PGC & REGISTER REGISTER DATA PROTECT PROTECT TIMER T & C BUS TRAP TIMER BITS CARD TIMER FF TAPE DETECTOR STOP CONTROL ALARM DISC CARD READER MEM BITS 6-15 E INST. ΕξA CONTROL ADDRESS & Y BITS BUS MATRIX 16-23 TYPE GATES TAPE BITS BITS 0-7 Ė 4,5, МЕМ SIGN COMPARATOR COMPARATOR MEM | € 2 8-15 H00-3 CKTS AVAIL PAGE 093 094 095 022 023 024 113 025 026 027 028 030 029 031 104 105 099 096 098 032,033 106 097 034 035 036 037 118 114 115 116 117 REMARKS 4 4 4 <u>/5</u>\ <u>√16</u>/ 6 6 /7\ **/8**\ /8\ <u>/8\</u> 6 ∕⋾ 囪 CARD LOC. ıc 20 30 40 5C 60 7 C 80 90 100 HC 120 13 C 140 15C 160 170 18C 190 200 210 220 230 240 250 2 60 270 28C 290 30C 310 CARD 1093-01 1661-01 10.96.03 10.96.03 10.96.01 10.96.02 10.64.01 1064-01 1064-01 NO. 1064-01 1064-0 1064.0 1669.01 1669.01 1669.01 1669-01 1668-01 1072-0 1053-02 1053-01 1055-02 1053-01 1053-01 1055-011076-01 1053-01 1053-03 1055-0 SAU SAU MI BUS MI BUS MI BUS MI BUS MI BUS TR BUS BIT BIT SHIFT BIT MIEE/FACE INTERFACE INTERF BIT BIT ADDED CCESS INTERFAC ADDER ROCESSOR PROCESSOR PROCESSOR PROCESSOR PROCESSOR COUNTER SLICE SLICE ADDER INPUT SLICE SLICE ADDER ADDER INPUT INPUT BUS BUS BITS 12-15 BITS 8-11 SLICE SLICE ADDER ADDER INPUT BUS BUS BITS 4-7 BITS 0-3 ADDER \$ ADDER GATES OUTPUT \$ MPY BUS BITS \$ ADDER CARD DECODE I BITS 18.23 BITS G-11 BITS TYPE BUS BITS 16-23 BUS BITS 16-19 BITS 20-23 BITS 12 - 15 BITS 8 - 11 BITS 4-7 BITS 0 - 3 REGISTER 31TS 20-23 BITS 16-19 HOO -CKTS AVAIL 150-1 PAGE 100 101 102 103 038 039 040 041 042 043 044 045 046 108 109 110 111 112 047 048 049 050 051 05 Z 053 054 055 056 REMARKS <u>/i4\</u> <u>/is\</u> /3 CARD LOC. ١D 2 D 3 D 4 D 5 D 6 D 7 D 8 D a D IOD HD 12 D 13D 14D 17D 15D 16 D 18D 1 **9** D 20D 2 ID 2 2 D 23D 24D 2 5 D 2 6 D 270 28D | 29D 30D 3 I D CARD [A 1081-01 1081-01 1081-01 100 0-1601 10-1601 10-1601 1068.0 1068-01 1068-01 1068-01 1068-01 1068-01 1068-01 1068-01 1068-01 1068-01 1068-01 1068.0 169-01 1112-01 1168.0 1068-01 1068-01 1068-01 1068-01 1068-01 1068-02 1068-02 1068-01 CONTROL CONTROL CONTROL ONTROL PRIORITY PRIORITY PRIORITY PRIORITY PRIORITY PRIORITY PRIORITY PRIORITY PPINPIT PRINCITY Y PRIOR ITY PRIO PRIORITY PRIORITY PRIORITY PRIORITY PRIORITY EXECUTIVE EXECUTIVE PANEL PANEL PANEL PANEL INTERRIDAT INTERPLIATINTERPLIAT NTERRUPT INTERRUPT INTERRUPT INTERRUI TINTERRUP ITERRUF INTERFACE INTERFACE INTERFACE CABLE CABLE CABLE INTERFACE INTERFACE CABLE CABLE CABLE INTERFACE INTERFACE CABLE CABLE CABLE CABLE CABLE CARD GROUP I IGROUP∭IGROUP ∏ ROUP I GROUP III GROUP II GROUP T TERMINATORS TERMINATORS TERMINATORS LEVELS LEVELS LEVELS LEVELS LEVELS LEVELS 20-23 20-23 16-19 16-19 16-19 12-15 LEVELS LEVELS LEVELS TYPE LEVELS BITS 16 . 23 BITS 8-15 BITS 0-7 GROUP I GROUP II GROUP III 20-23 CKTS AVAIL HIO-I H21-1 .H21-1 057 058 059 060 061 PAGE 069 $\Gamma\Gamma$ 078 079 070 062 071 063 080 072 064 081 073 065 082 074 066 083 075 067 084 076 068 085 086 REMARKS 12 10 <u>/1</u>\ 13 6 12 <u>/15</u>\ LIST OF MATERIALS 6 PROGRAM RESTRICT OPTION. IF THE OPTION IS NOT USED PRIORITY INTERUPT GROUP III OPTION. IF GROUP III IS USED, 2015-01 16 EXTRA MEMORIES CHANGE 1067 TO A DASH ALL DIMENSIONS ARE IN INCHES 1607-04 GOES INTO 22B.(15B, 16B, 22B, 30D) GOES INTO THE UNUSED LEVELS SLOTS. (70,80,110,140,170,200,230,260) 2,3,4,5,6,7,0R8 WHEN 2,3,4,5,6,7,0R 8 MEMORIES ARE USED Datacraft Corporation 5 BOOTSTRAP OPTION. IF THE OPTION IS NOT USED 1607-06 10 PRIORITY INTERUPT GROUP I OPTION, IF THE OPTIONAL LEVELS 15 SAU OPTION. IF THE OPTION IS NOT USED 1607-08 DEAFTSMAN BIRD 10.3-69 GOES INTO 78.(78, 288, 298, 308, 318) ARE NOT USED 2015-01 GOES INTO 100,130, 160,250,280. (100,130,160,250,280) GOES INTO 2C.(2C, 3C, 4C, 29D) DETAILED LOGIC DIAGRAM CHECKER Manual 10-10-6; CHIEF BRAFTSMAN Haday 1-14-70 ENGINZER & 2 1-15-70 4 MEMORIES 3 & 4 INTERFACE . (18,28,38) STALL ALARM OPTION. IF THE OPTION IS NOT USED 1607-07 ABC OPTION. IF THE OPTION IS NOT USED 1607-03 DEV 21020 6024/I MAINFRAME GOES INTO 278.(276,290) GDES INTO IC.(IC) BIT PROCESSOR OPTION . IF THE OPTION IS NOT USED /8\ INTERVAL TIMER OPTION. IF THE OPTION IS NOT USED 1607-01 1607-05 GOES INTO !IA.(11A, 13C, 14C, 15C, 16C, 17C) POWER FAIL - RESTART OPTION. FOR THE OPTION USE 1208-01 ROJ. ENGR. &, 2 -15-76 GOES INTO 208 (188, 198, 208, 290) MEMORIES 5 \$ 6 INTERFACE (44,54,64) N 8A. (8A 30D) ADDRESS TRAP OPTION. IF THE OPTION IS NOT USED 1607-02 /2 PRICRITY INTERUPT GROUP I OPTION. IF GROUP II IS USED, 2015-01 29436 63510 /I MEMORIES 7 & B INTERFACE . (IA, 2A, 3A) GOES INTO 17B. (17B, 29D) GUES INTO THE UNUSED LEVELS SLOTS. (GD,90,120,150,180,210,240,270)

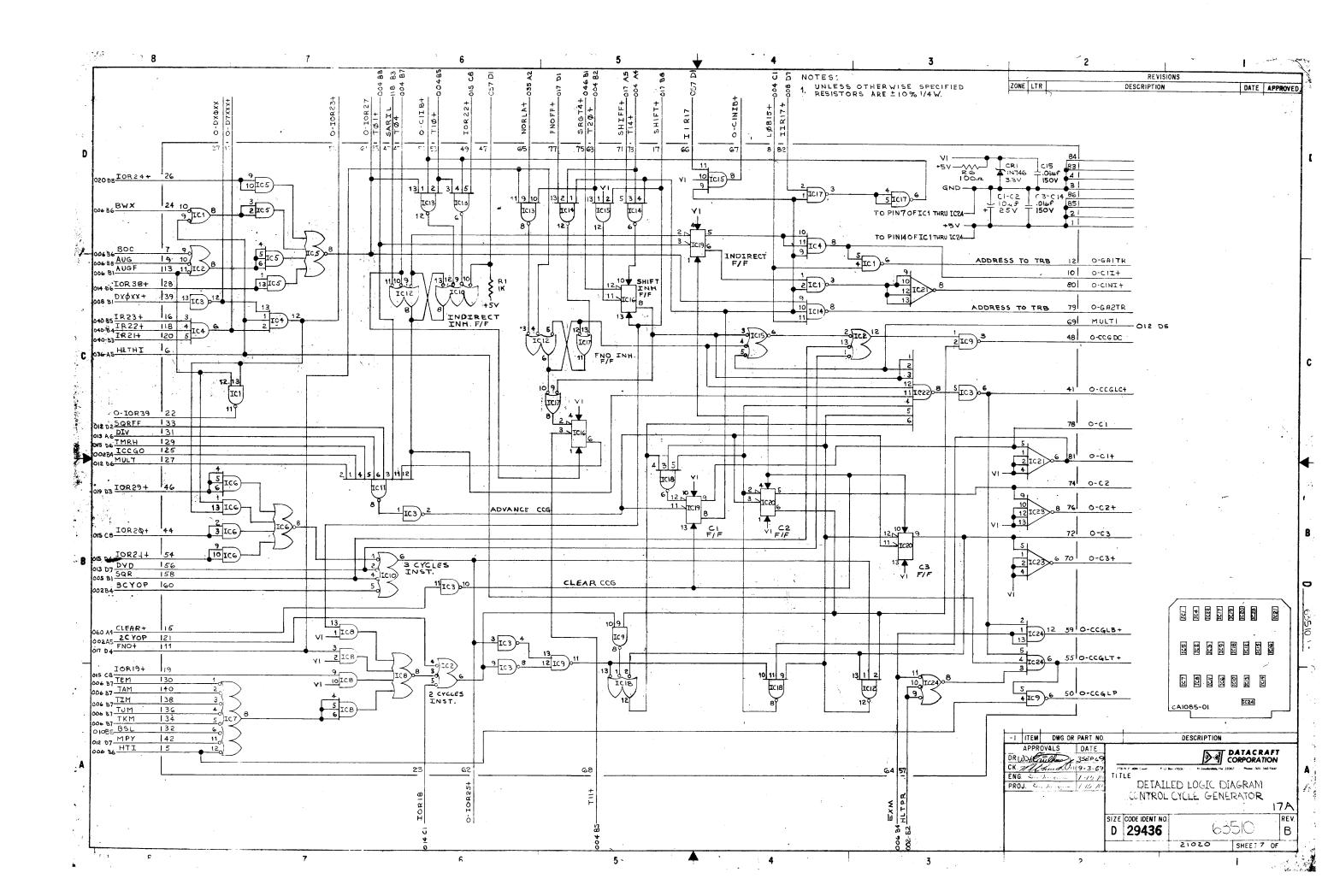


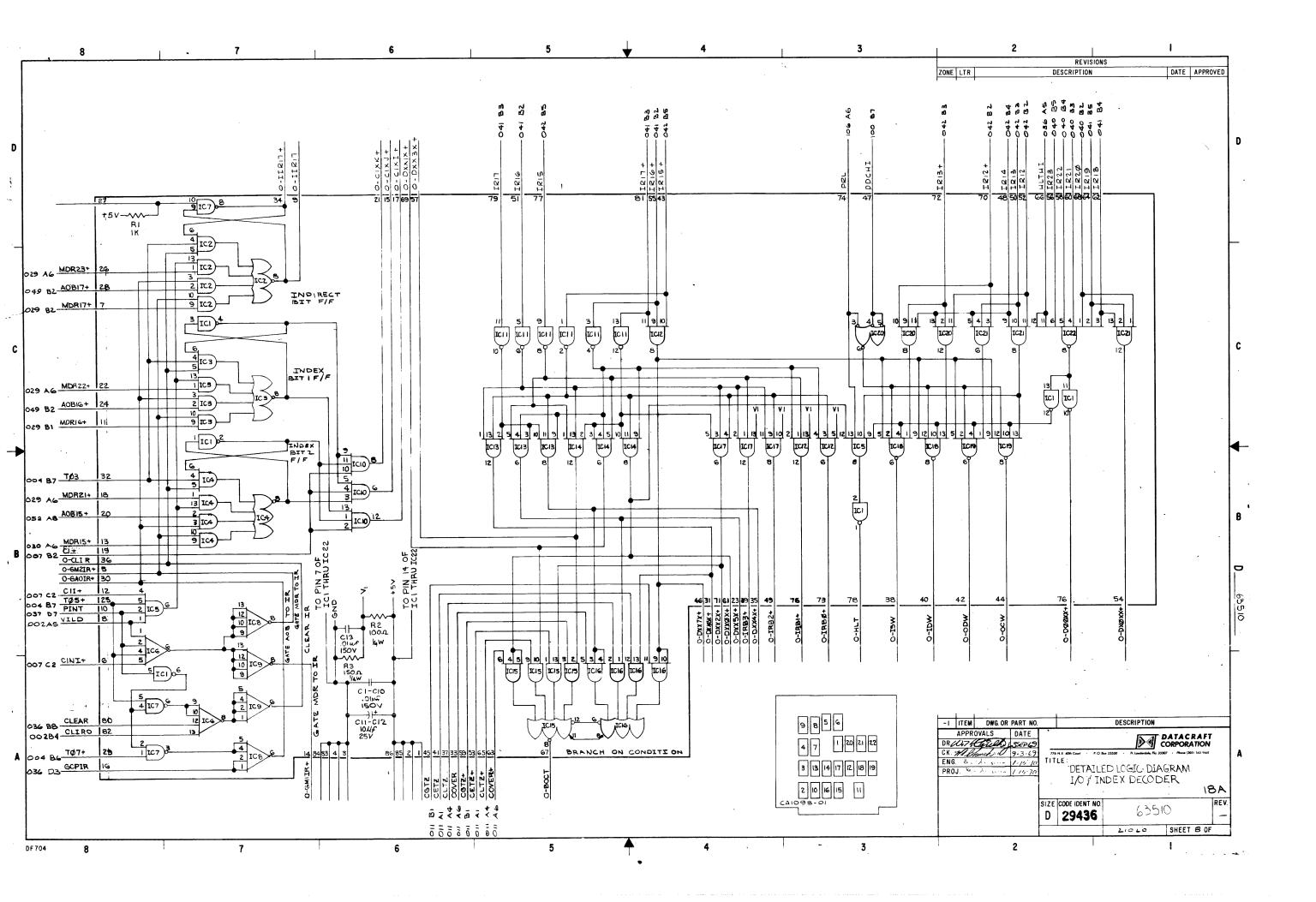


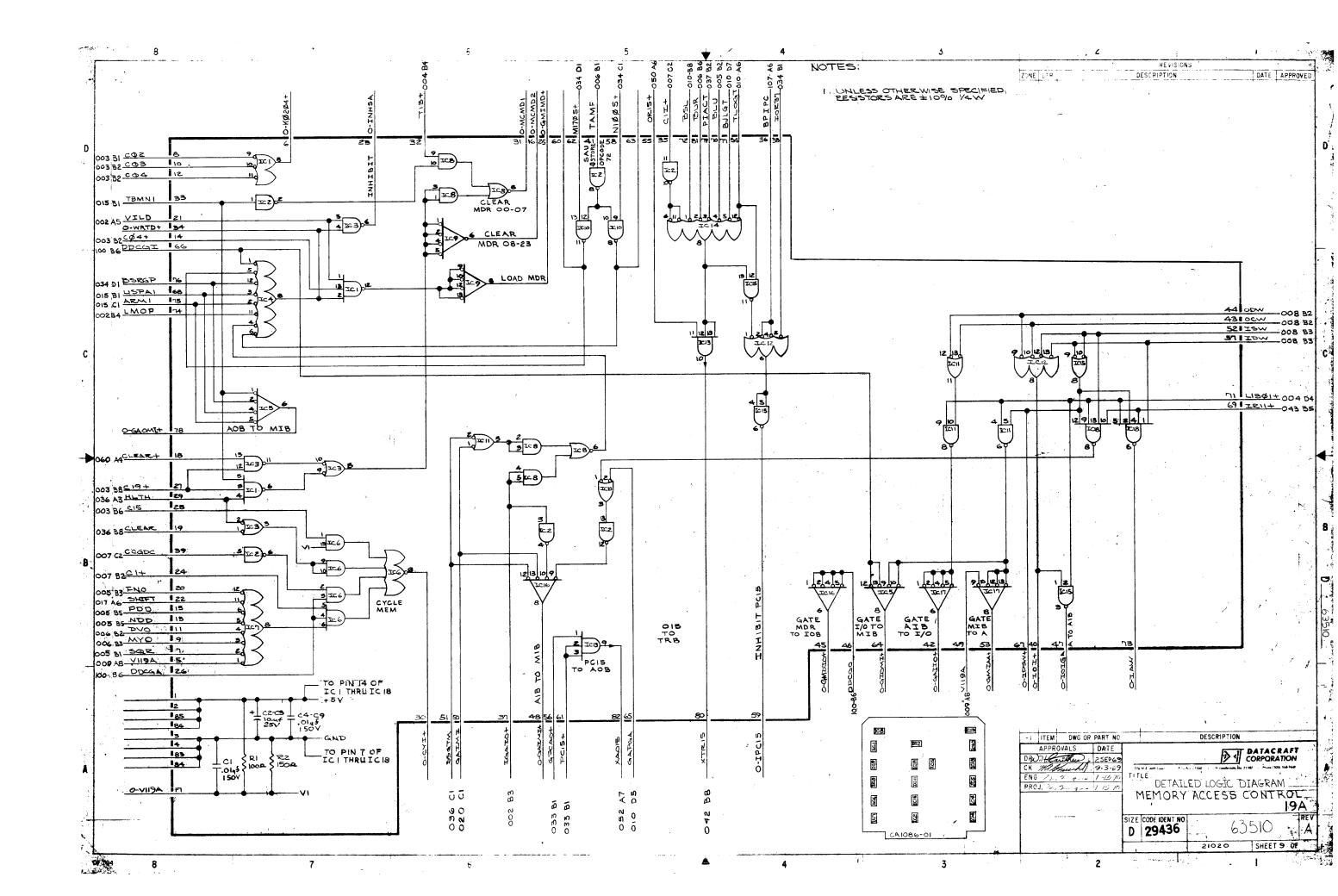


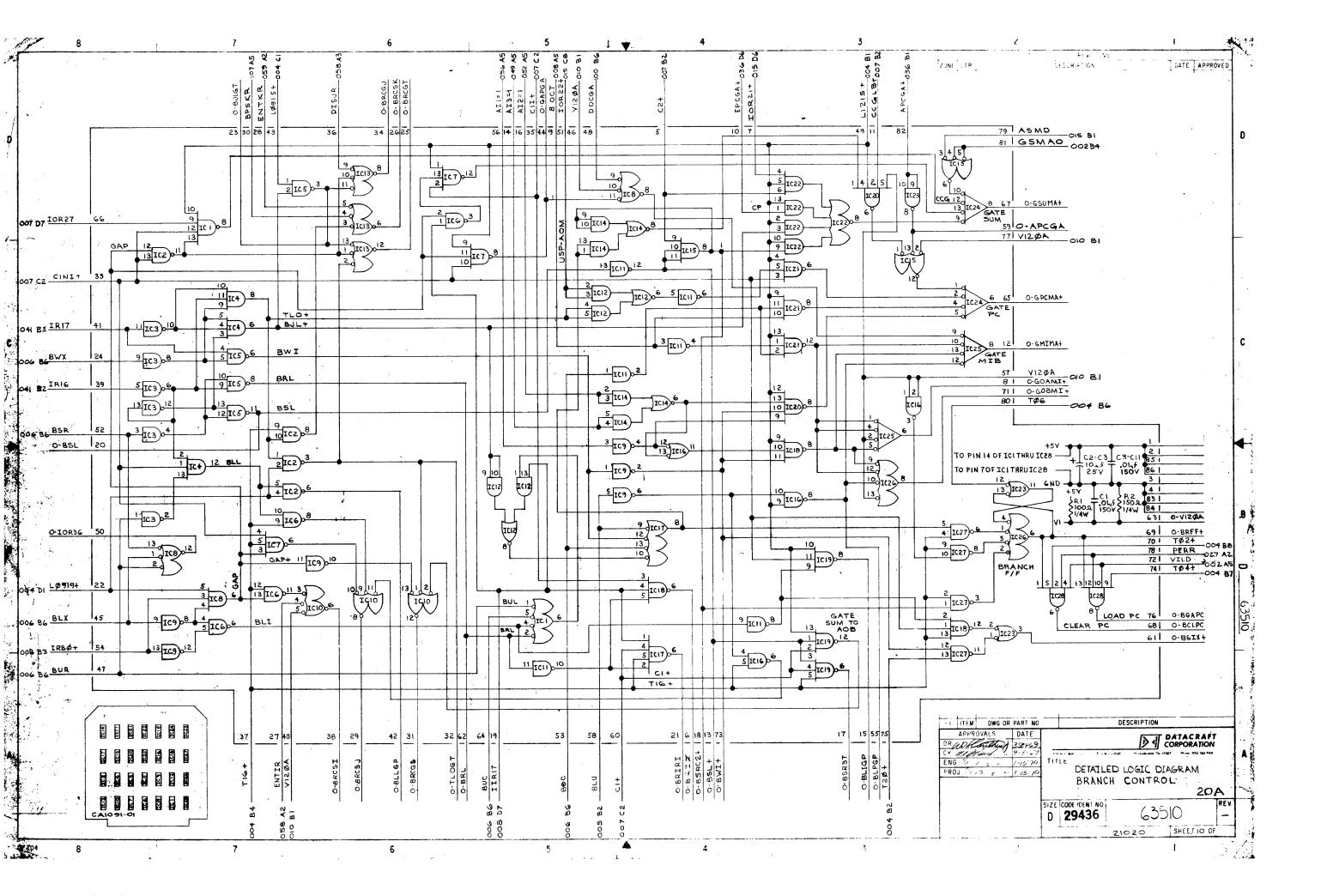


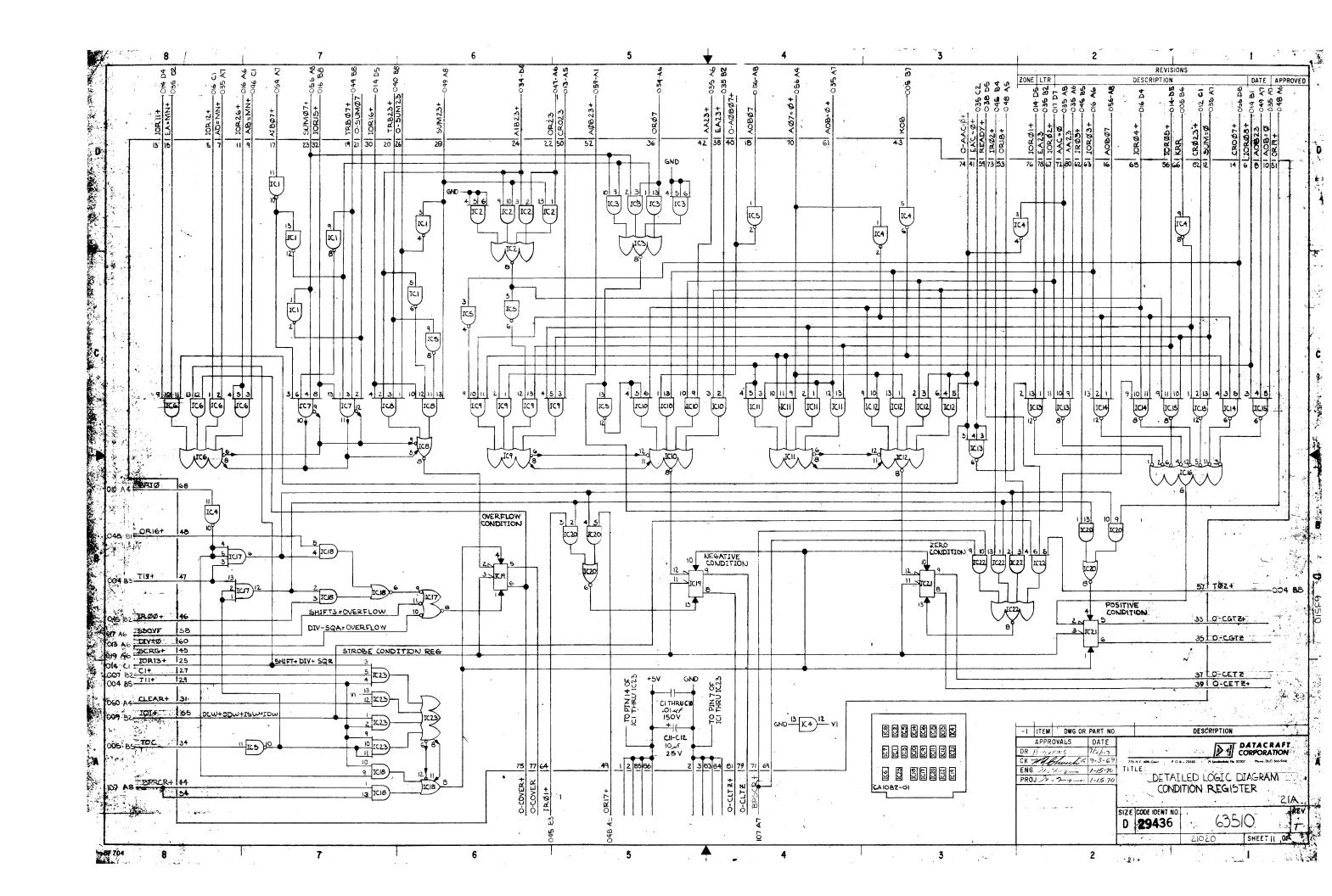


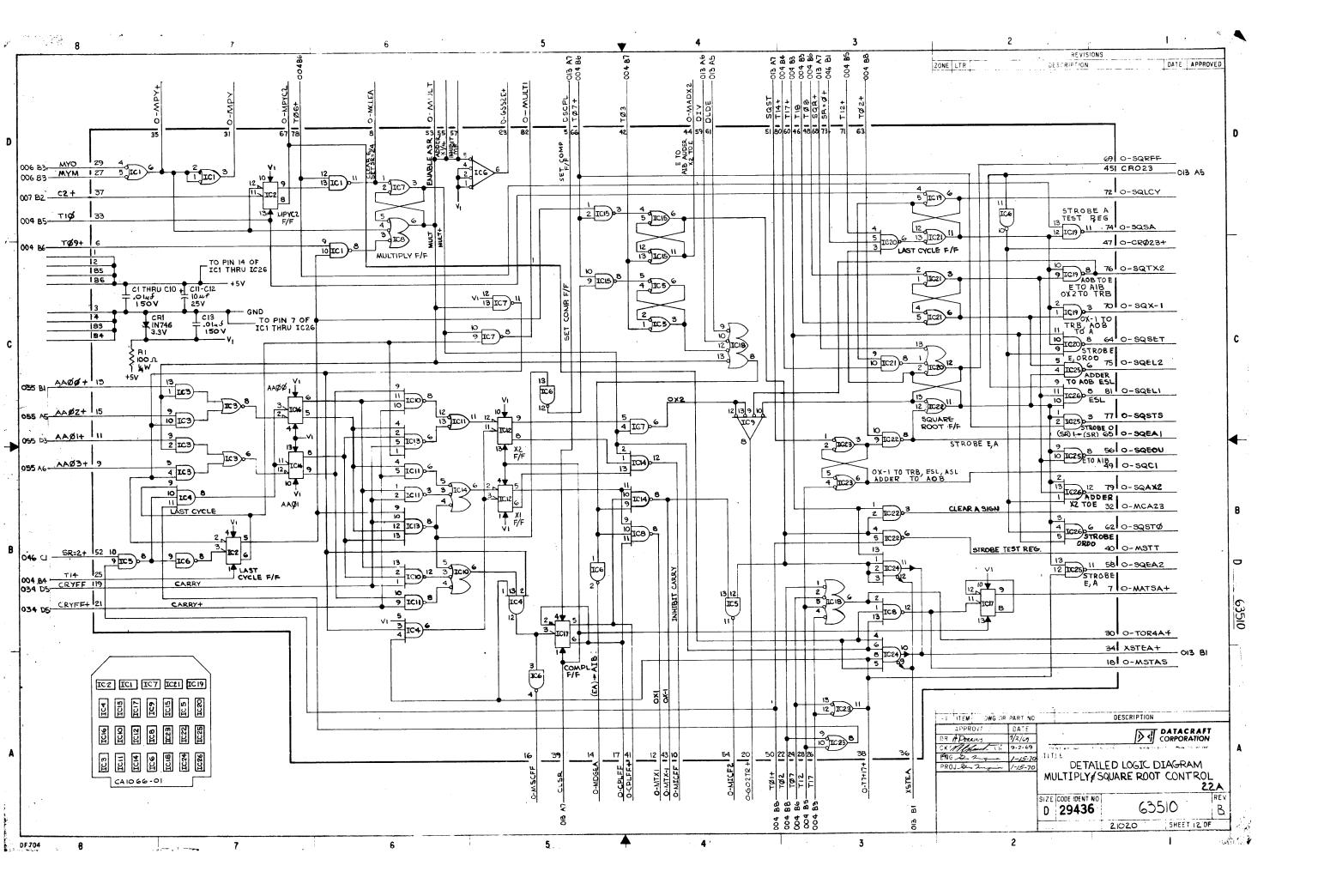


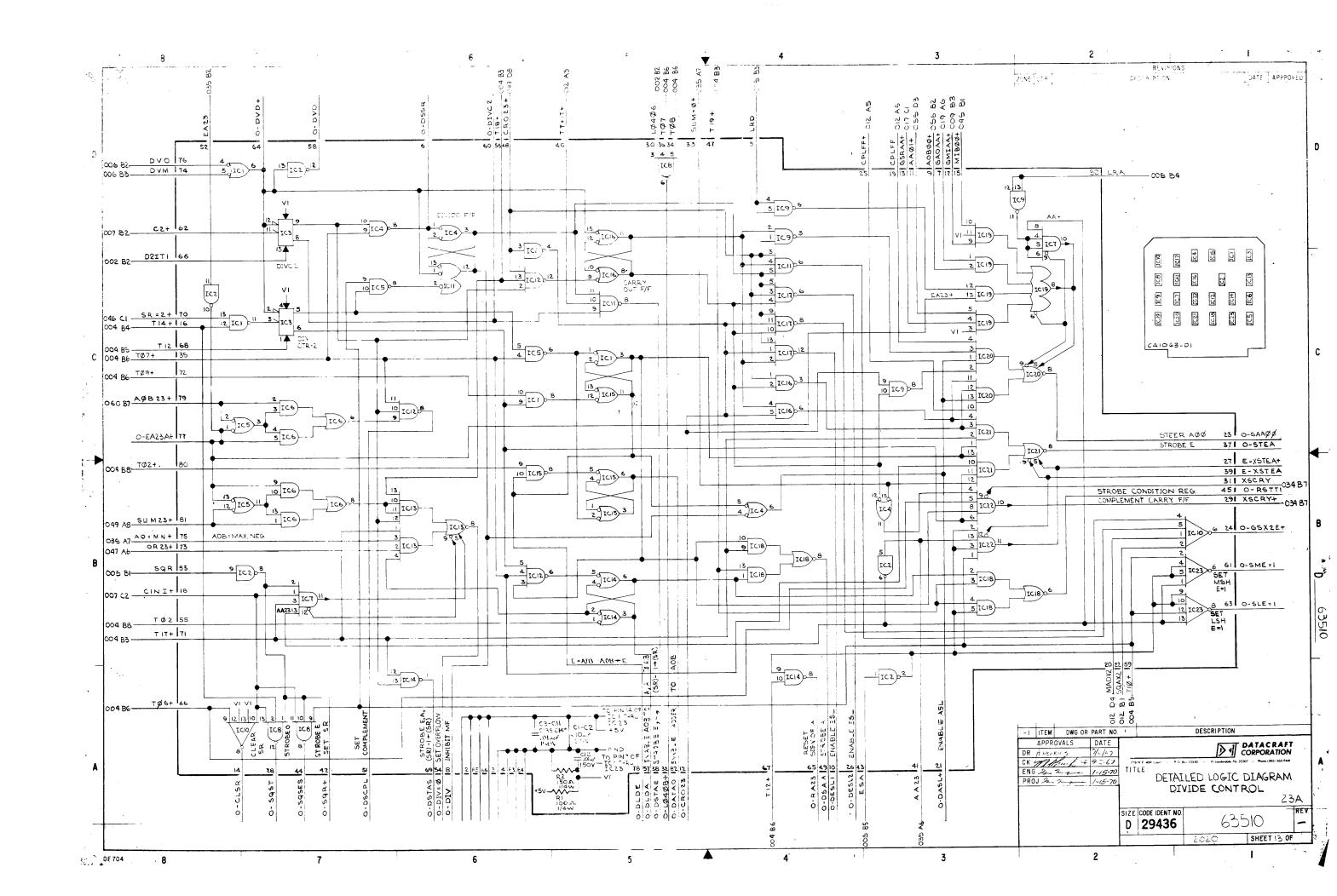


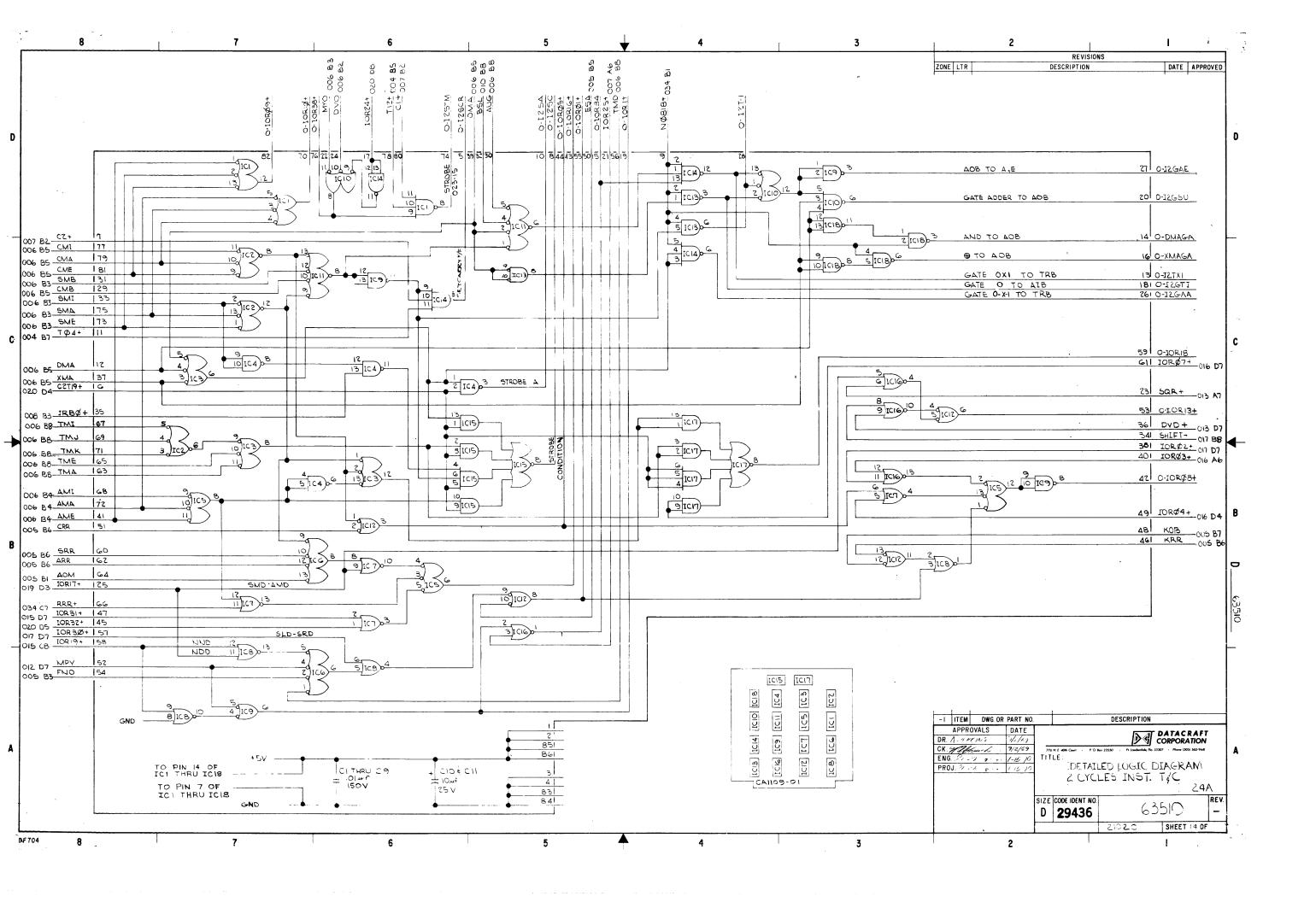


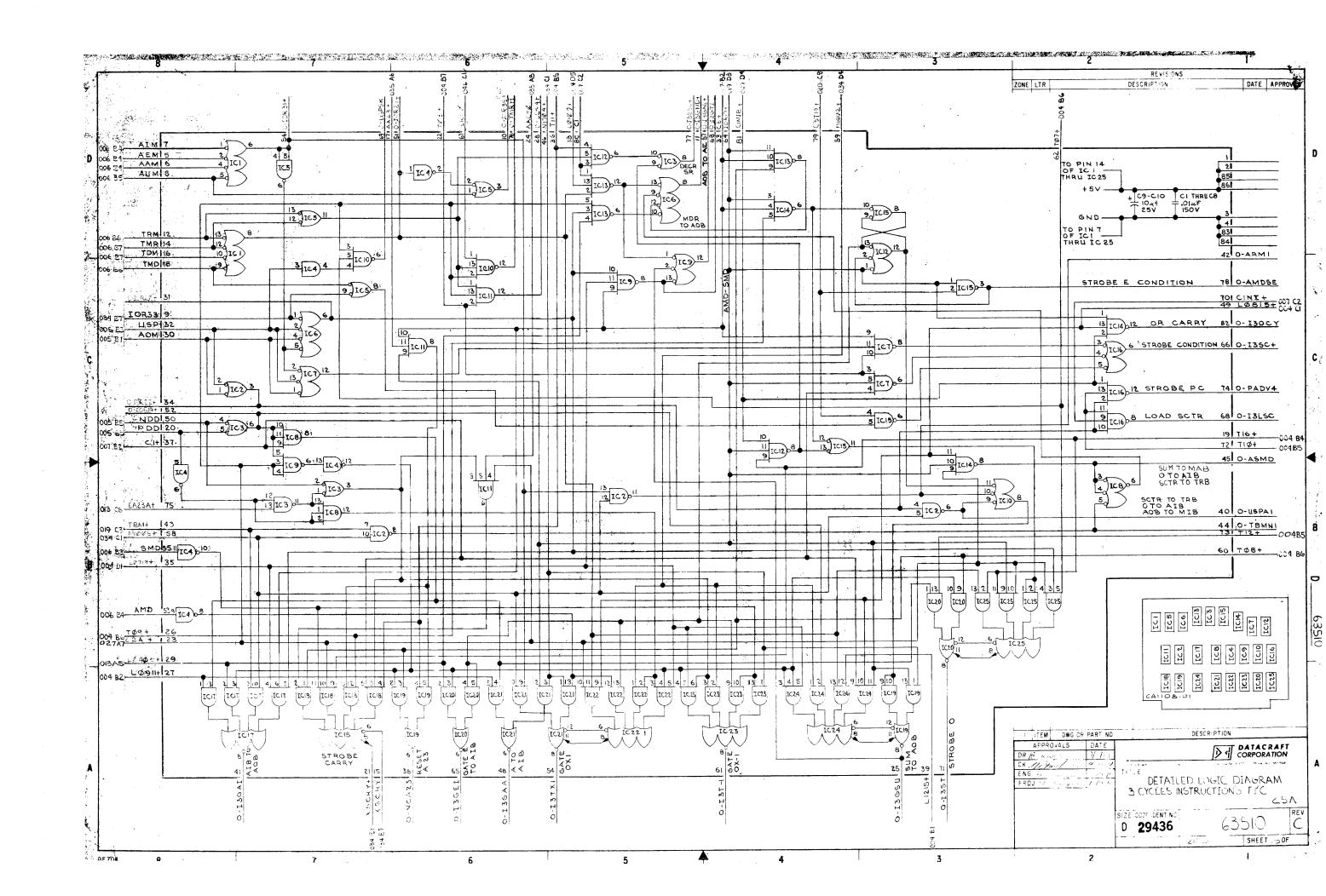


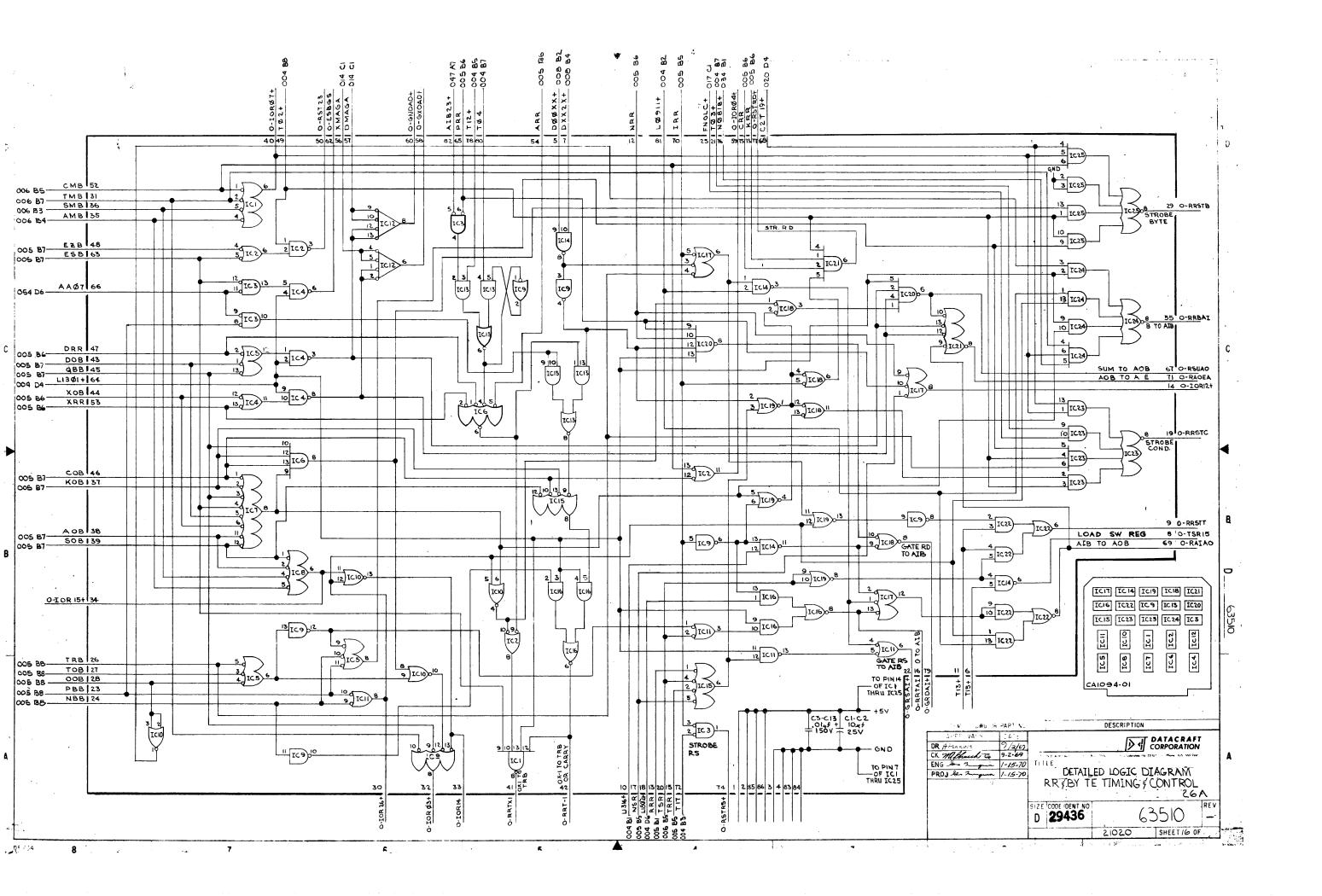


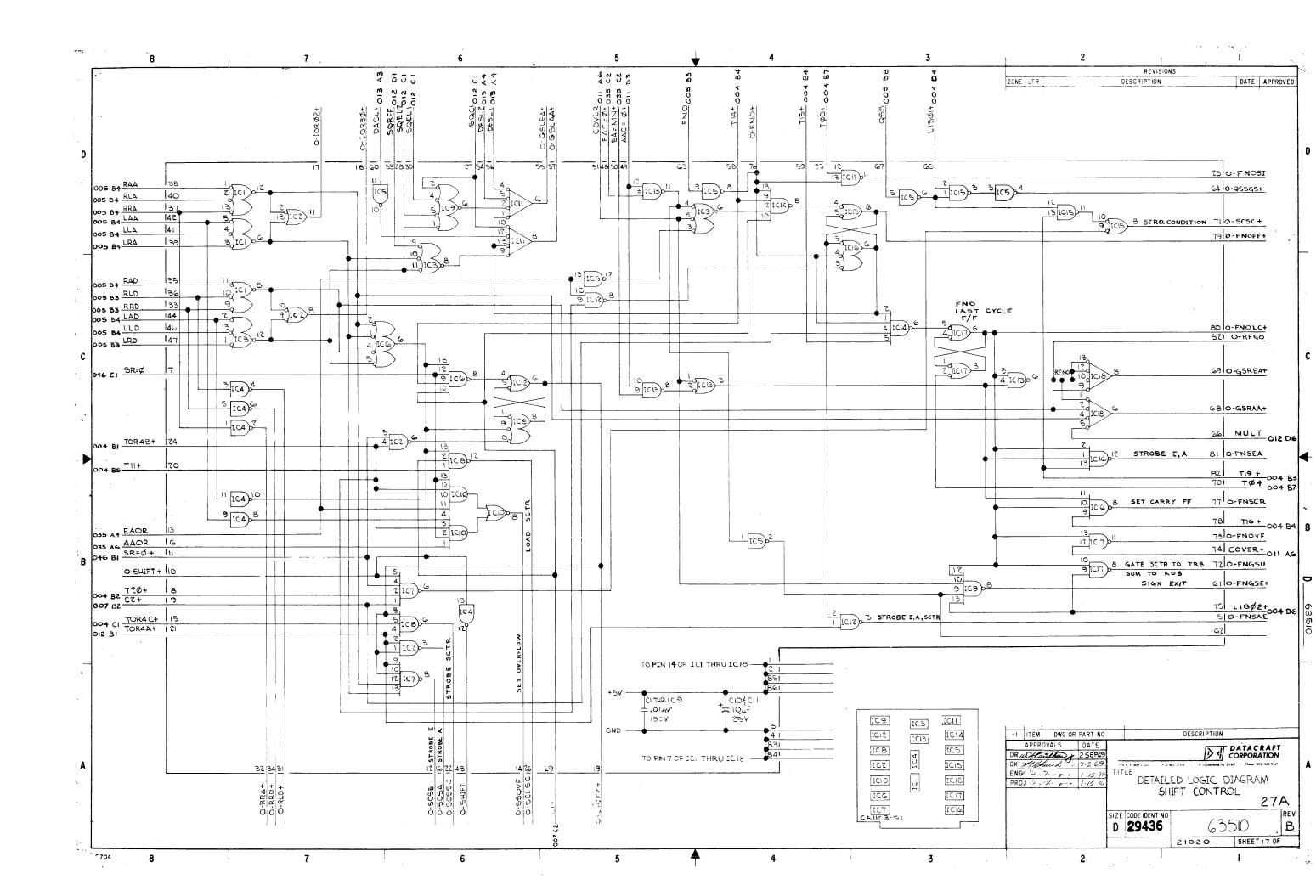


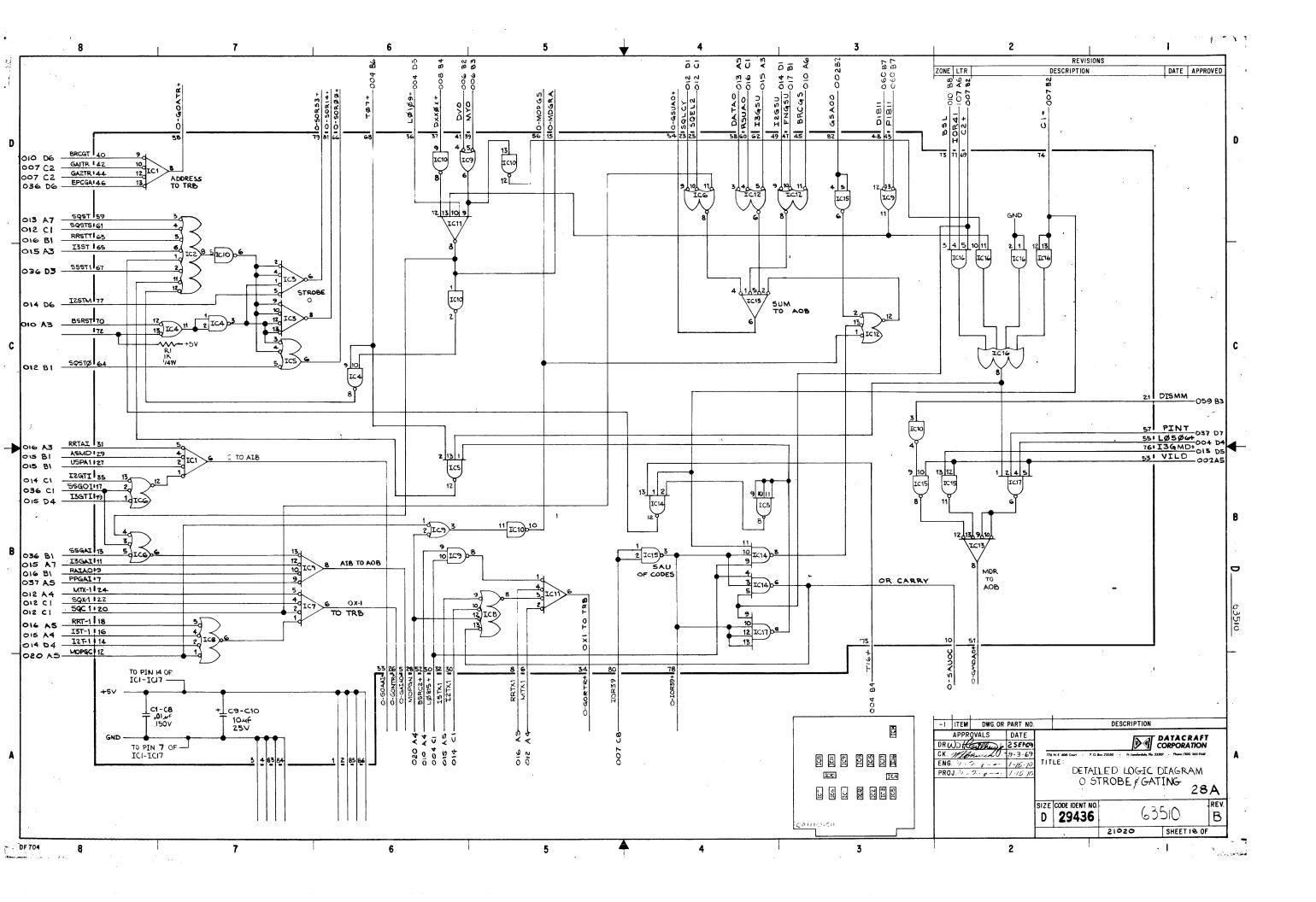


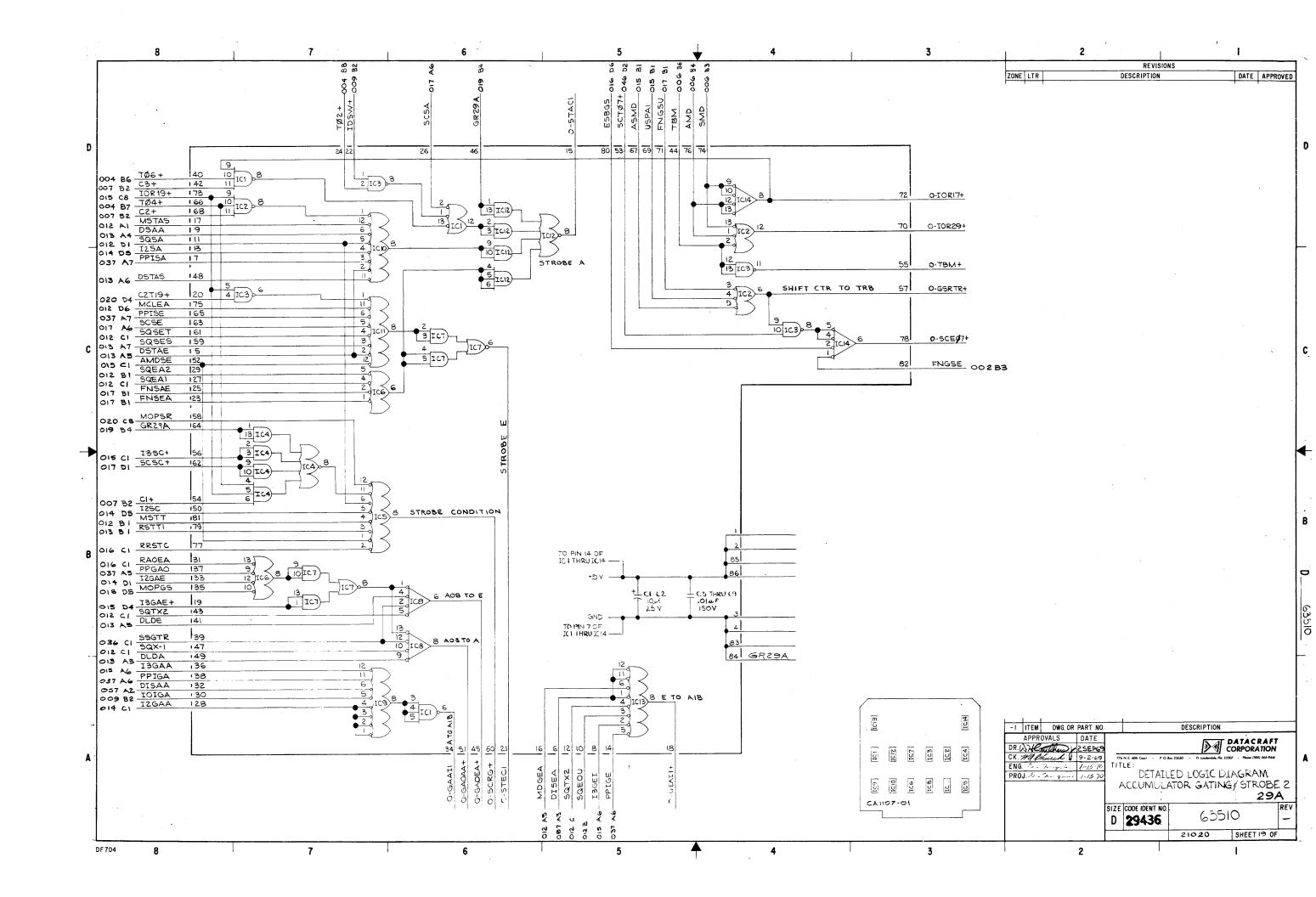


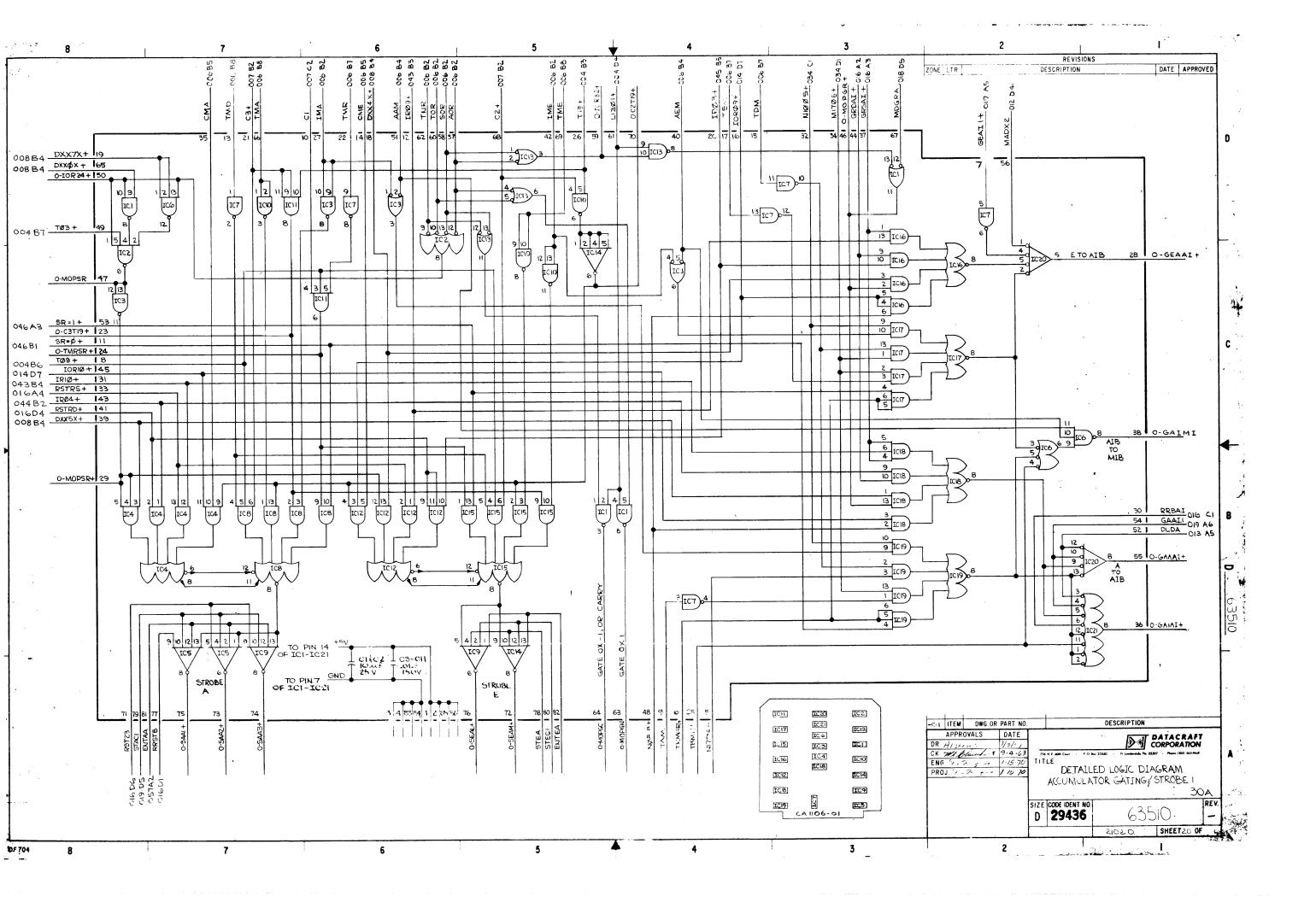


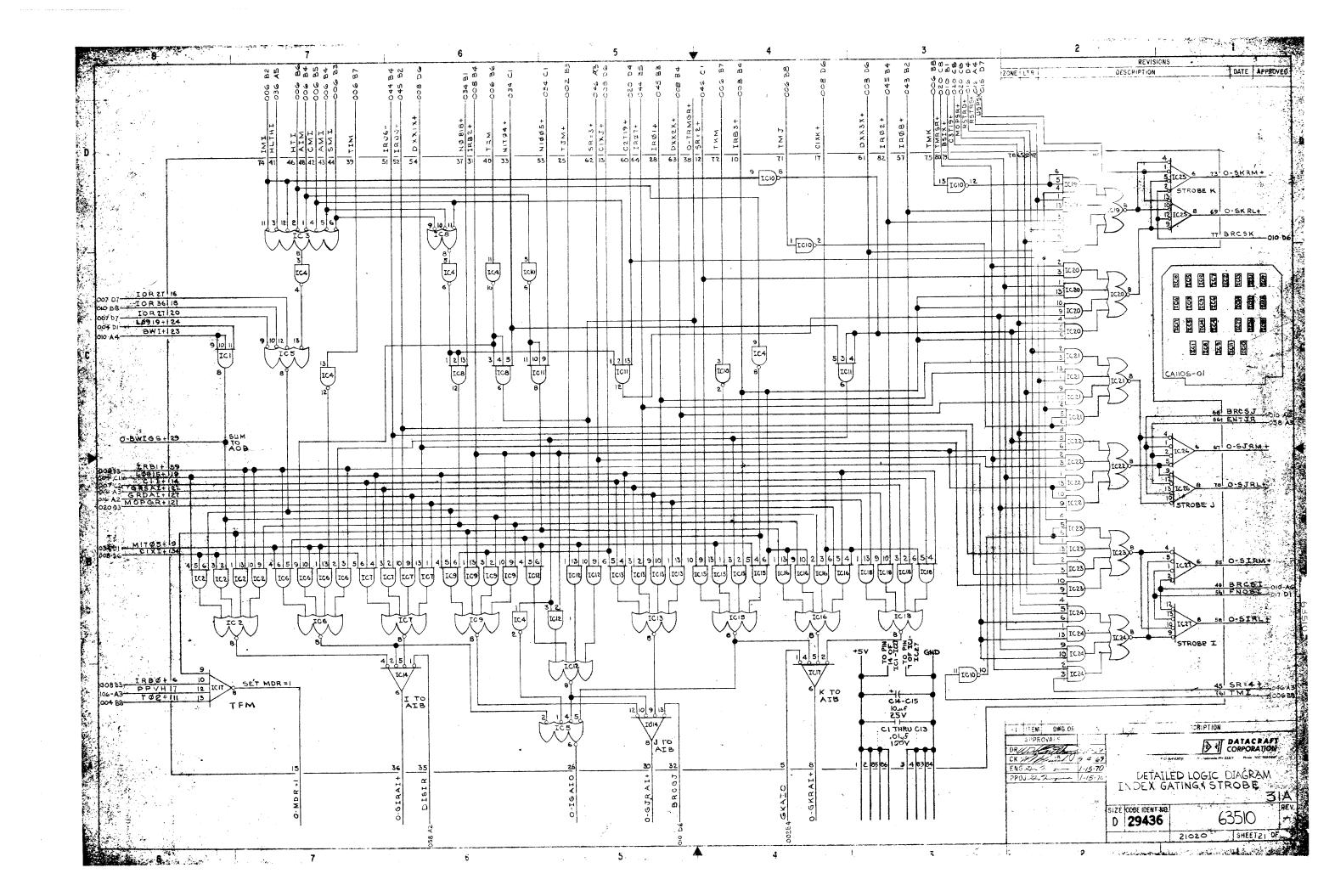


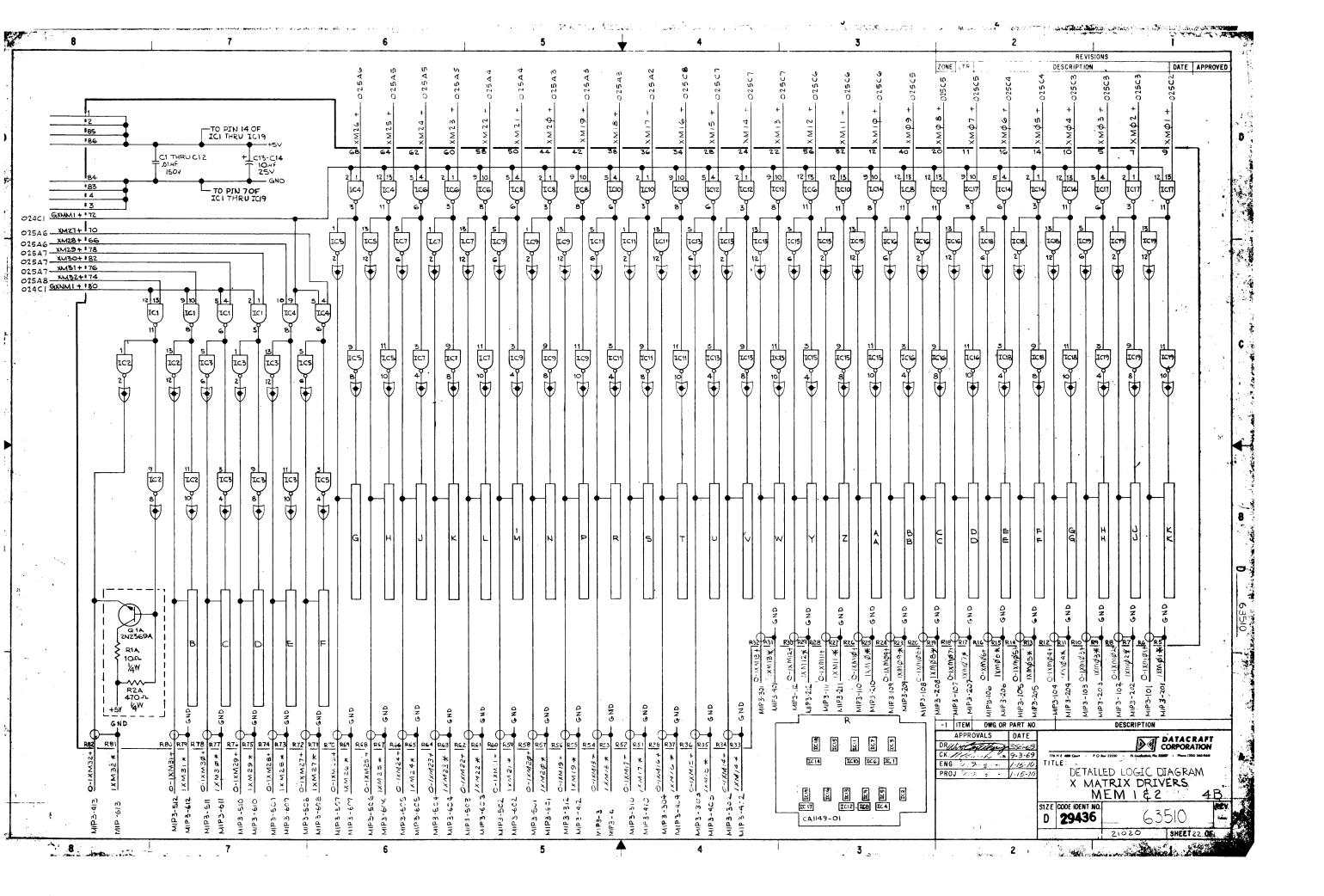


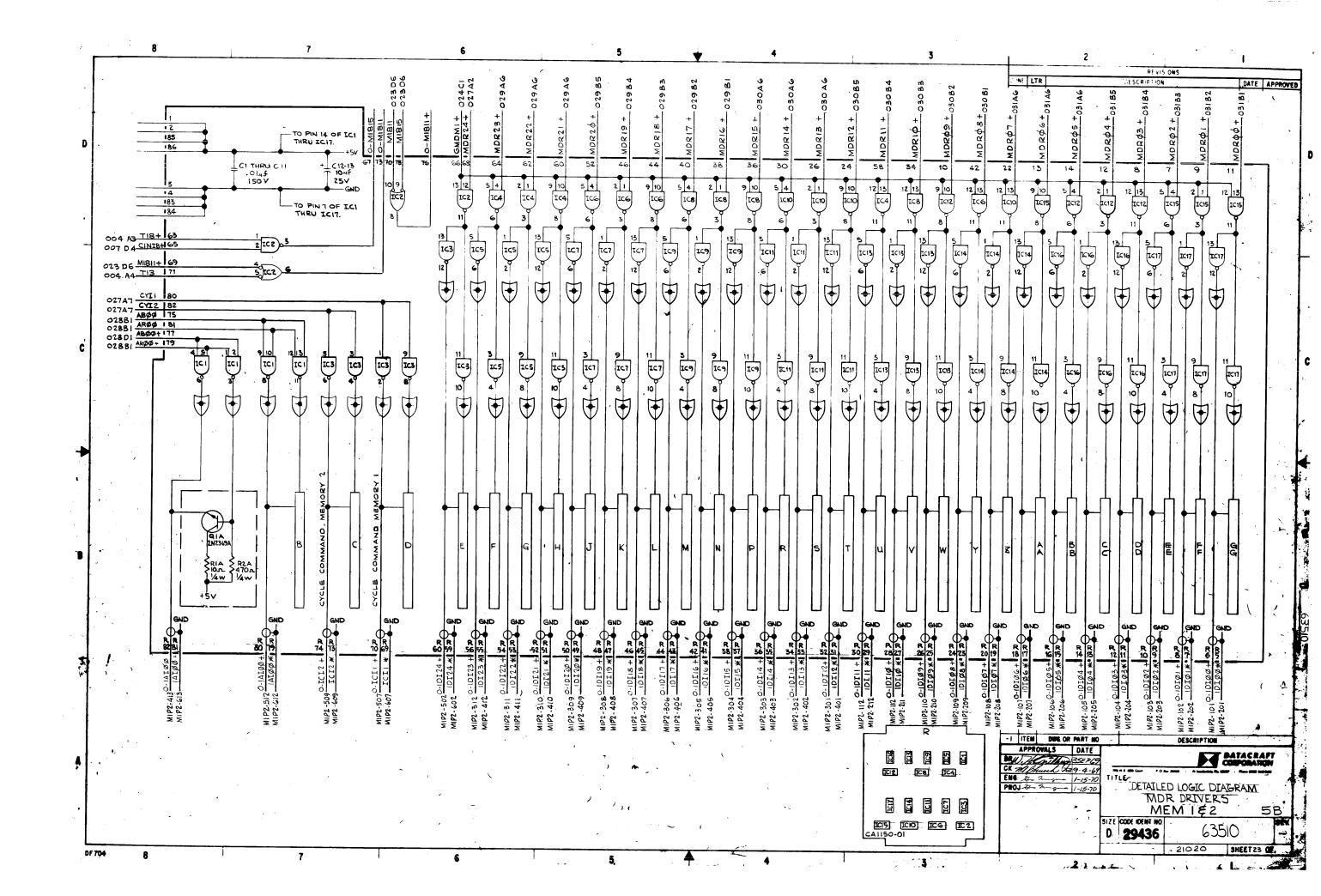


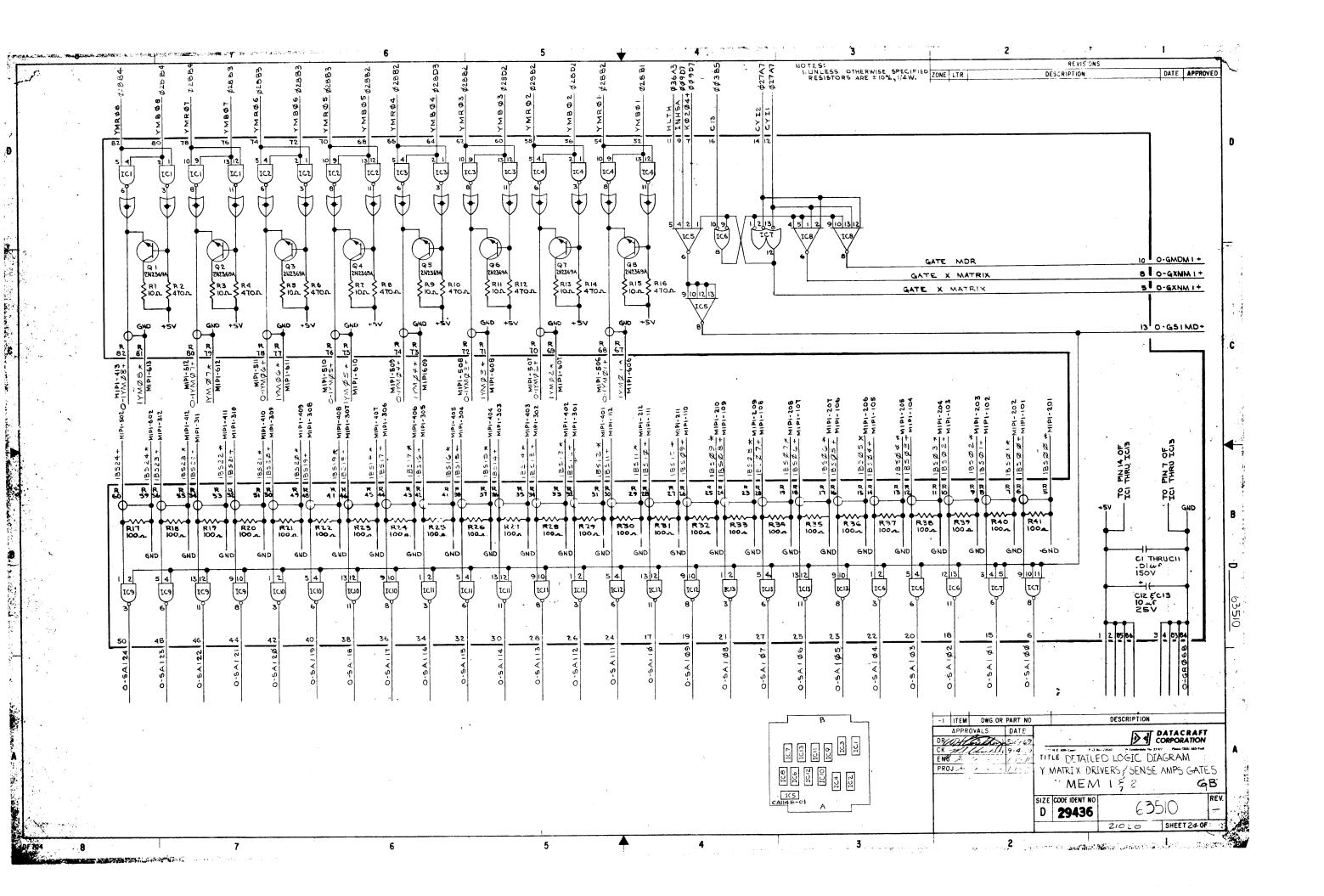


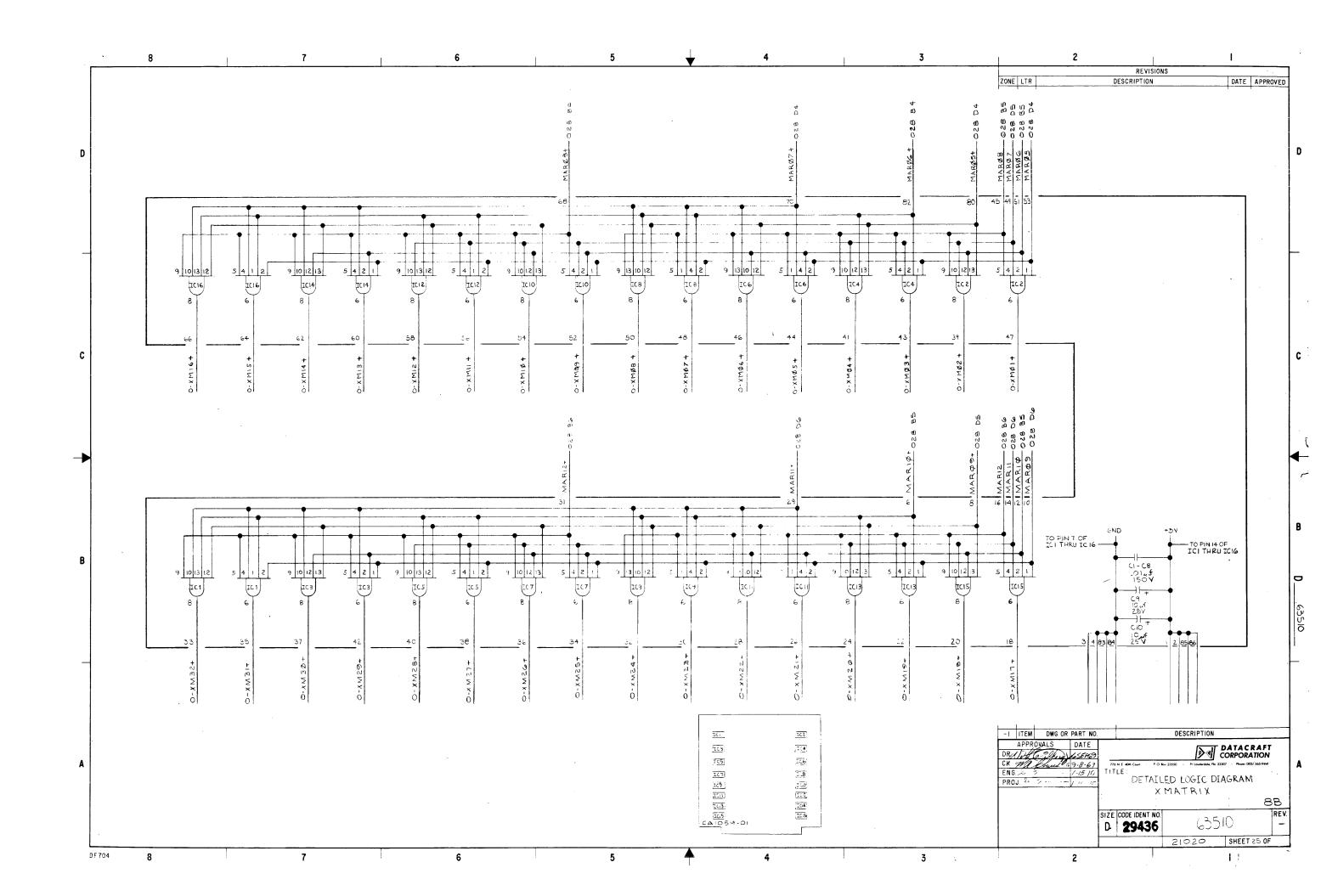


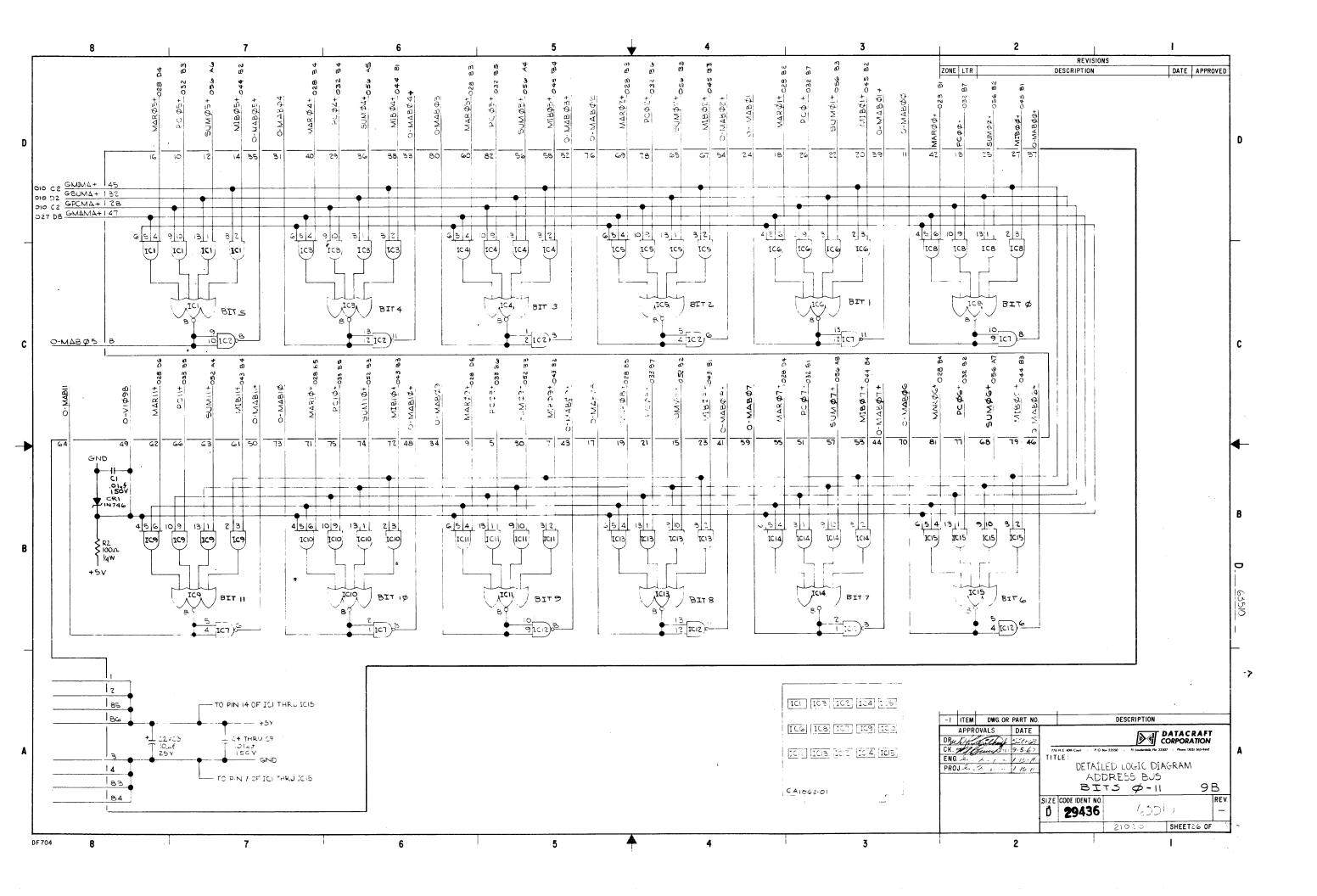


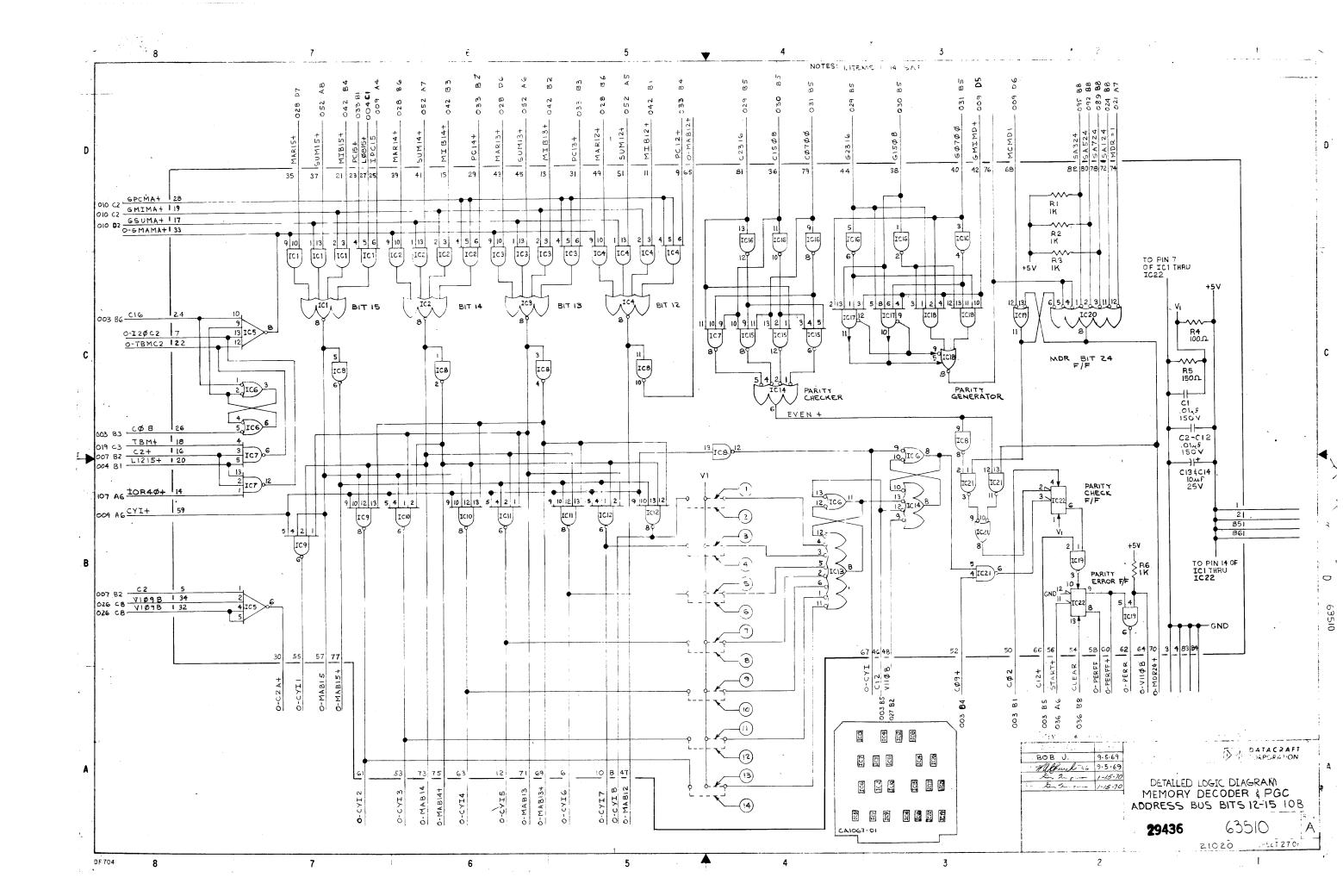


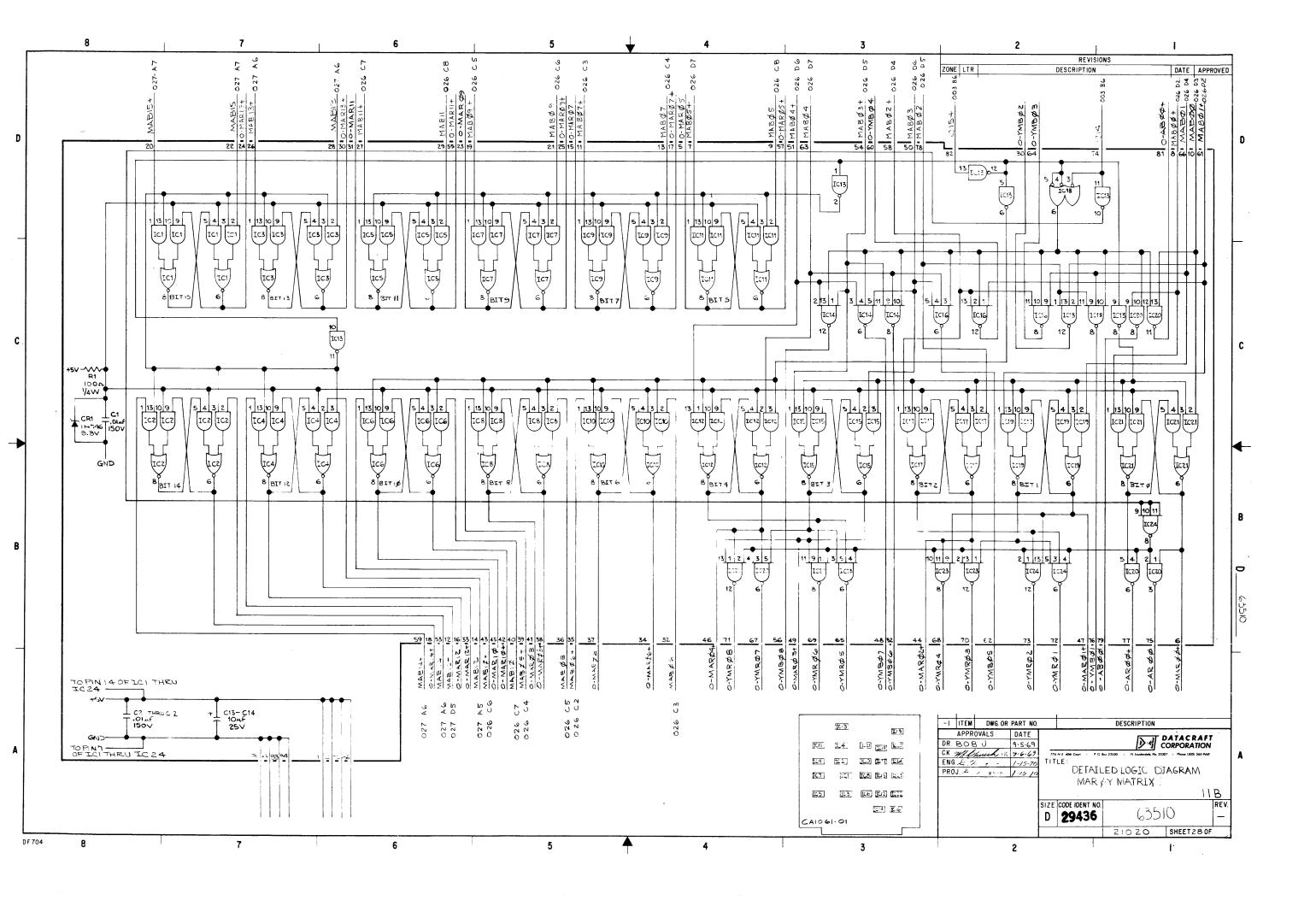


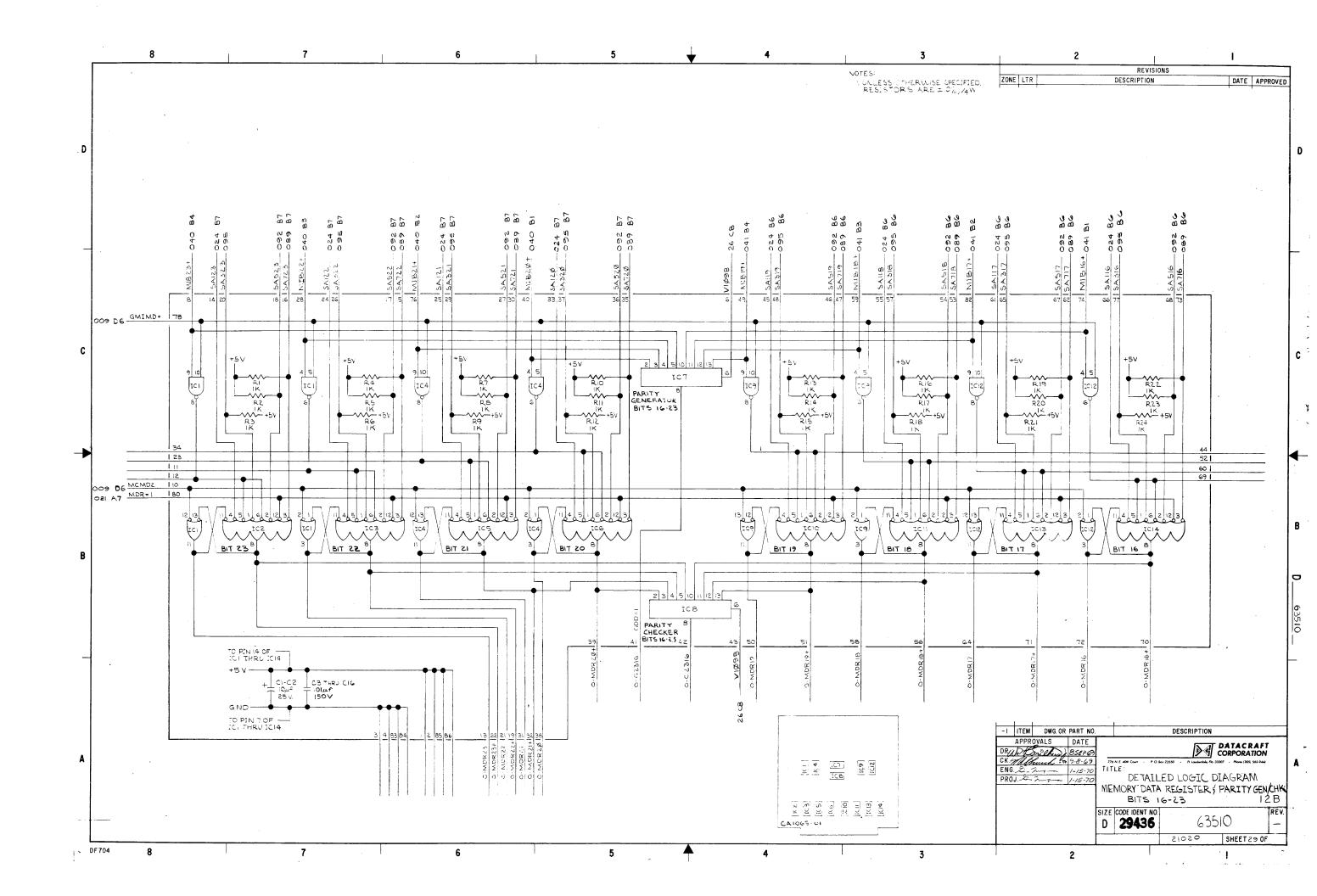


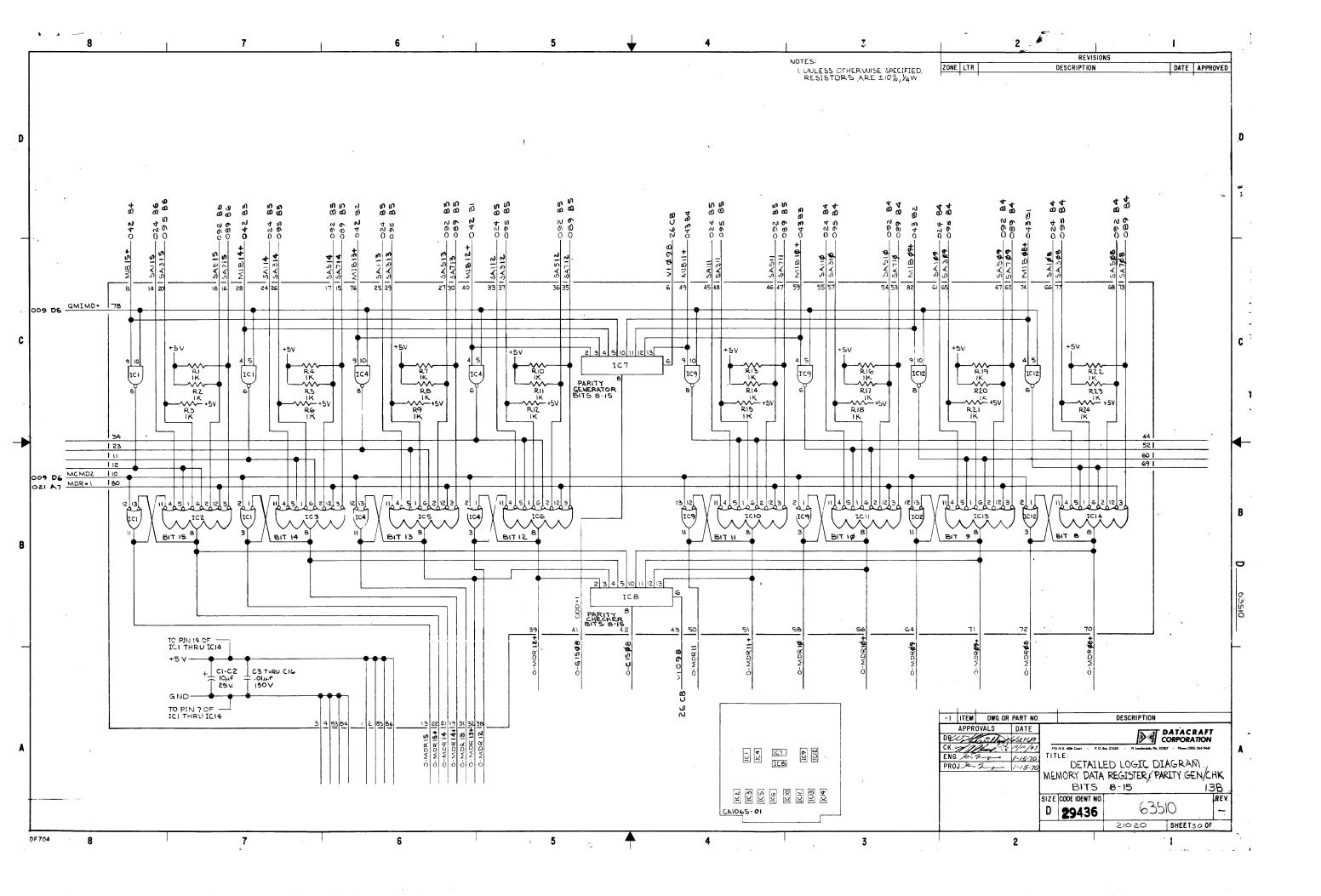


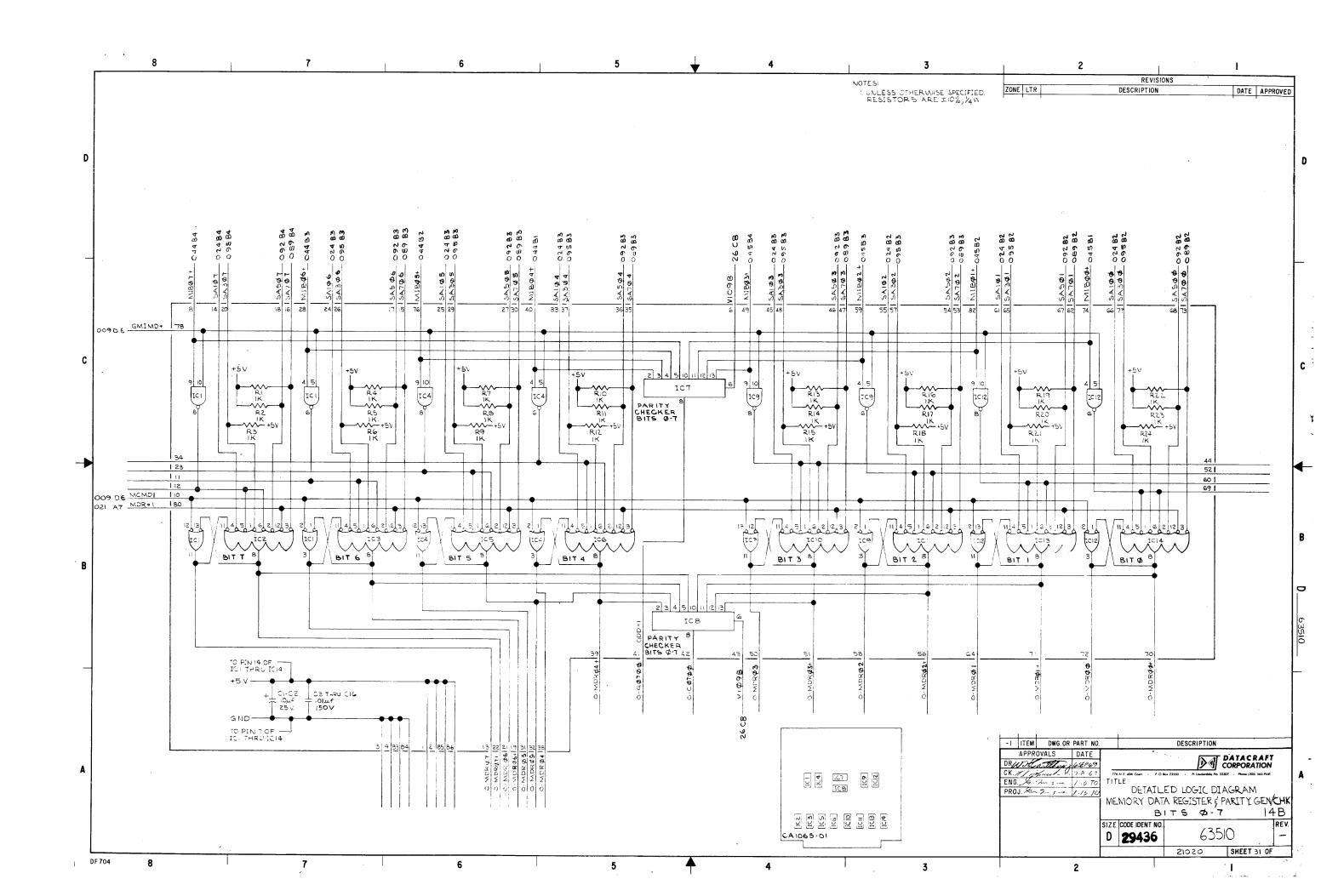


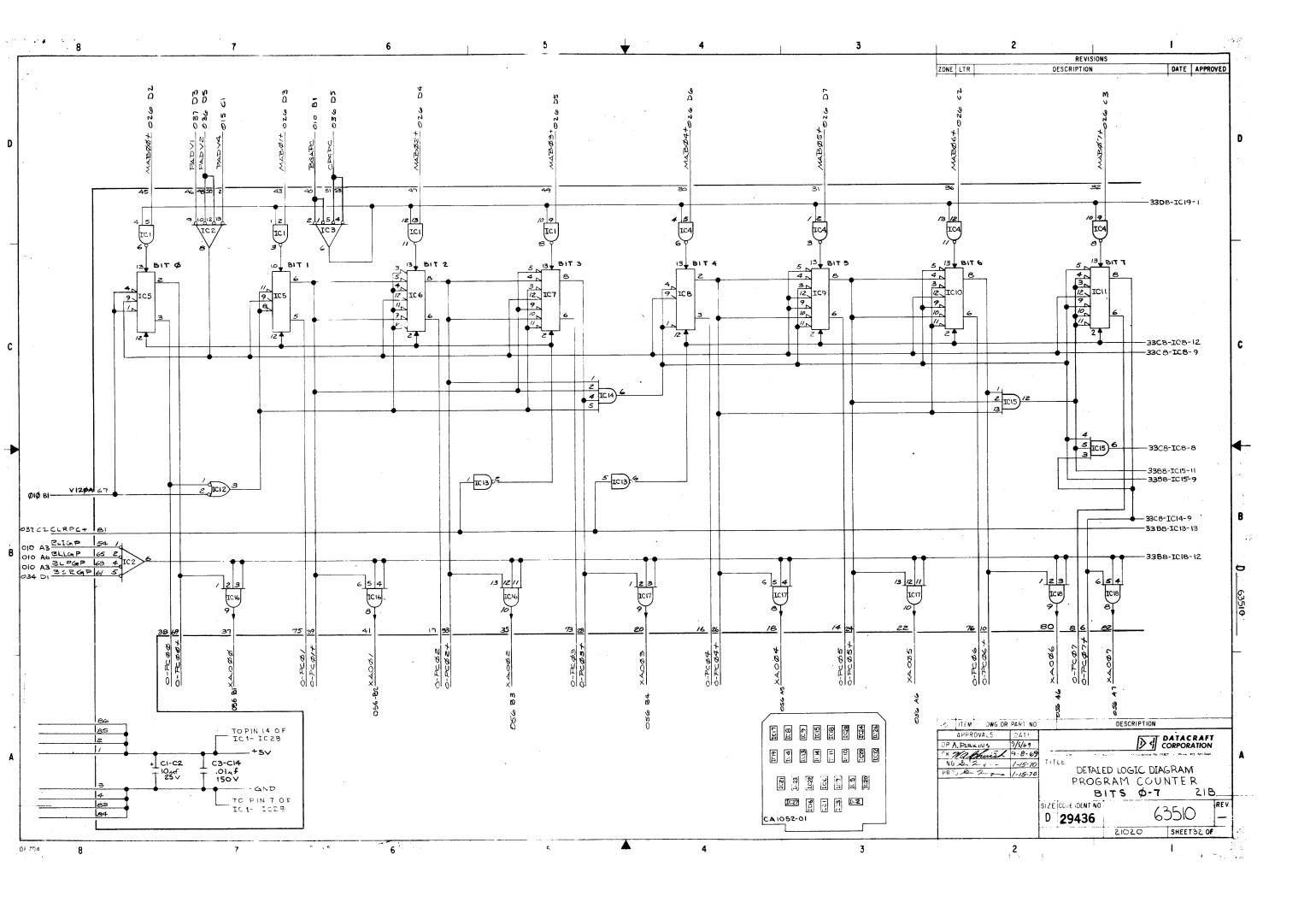


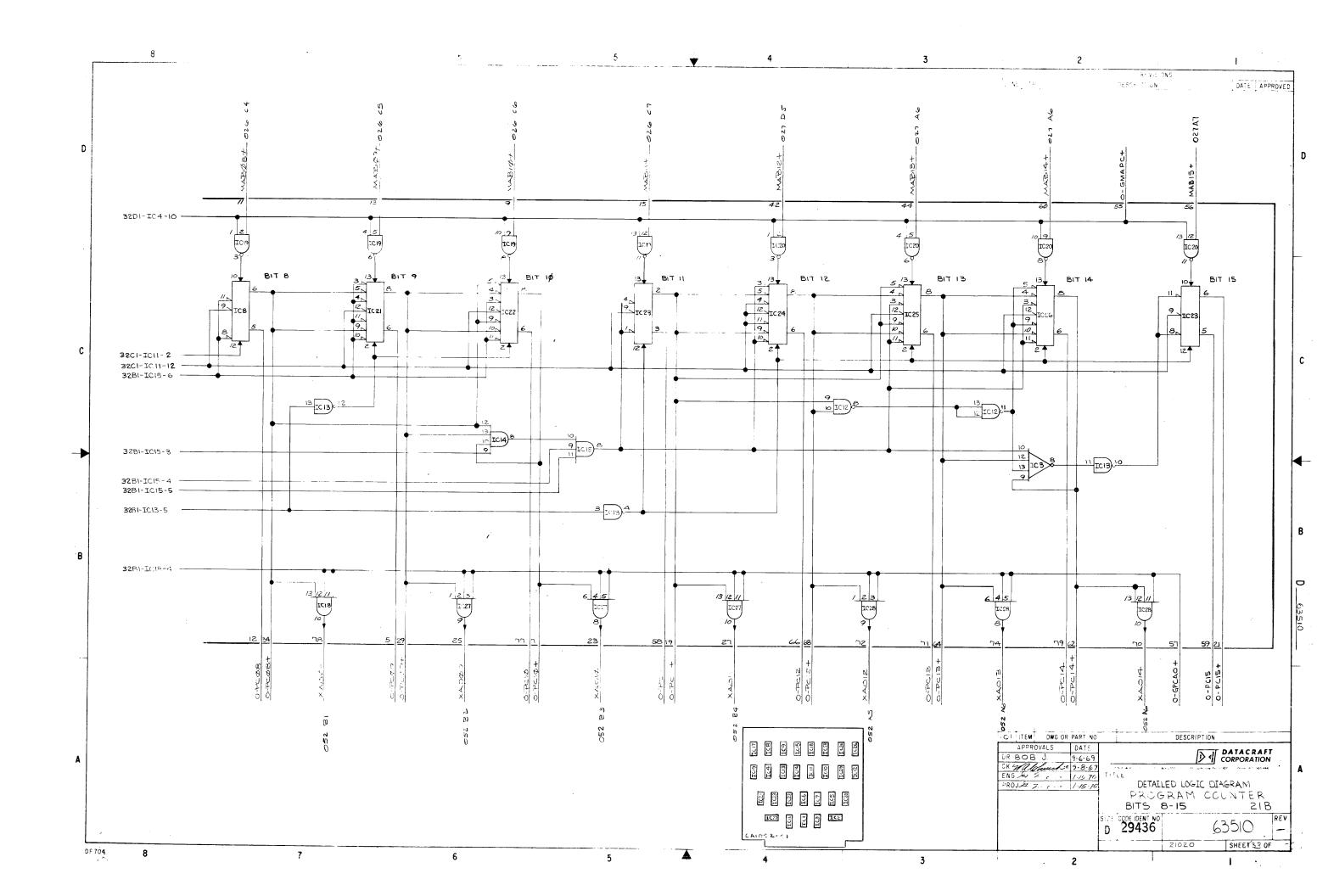


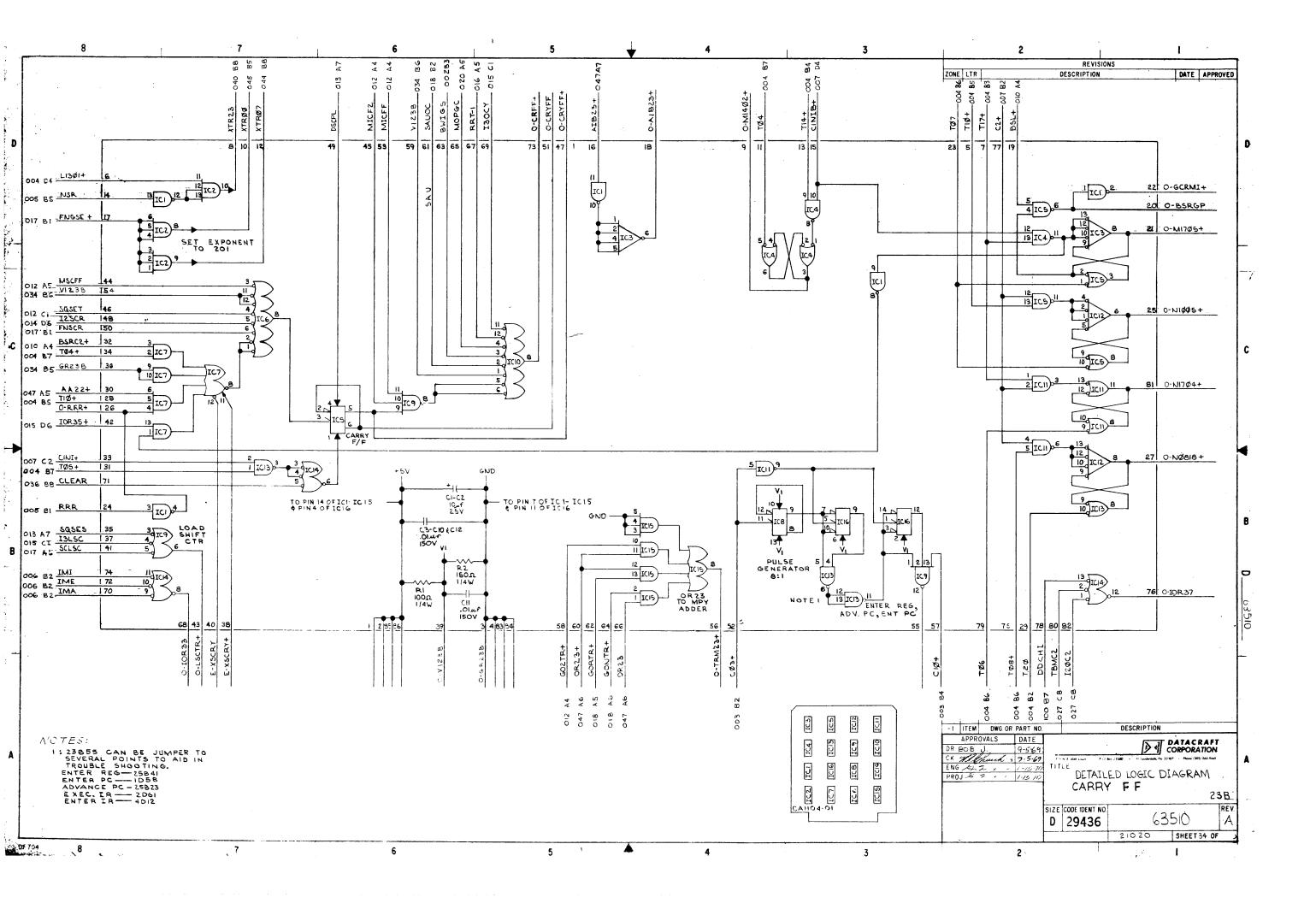


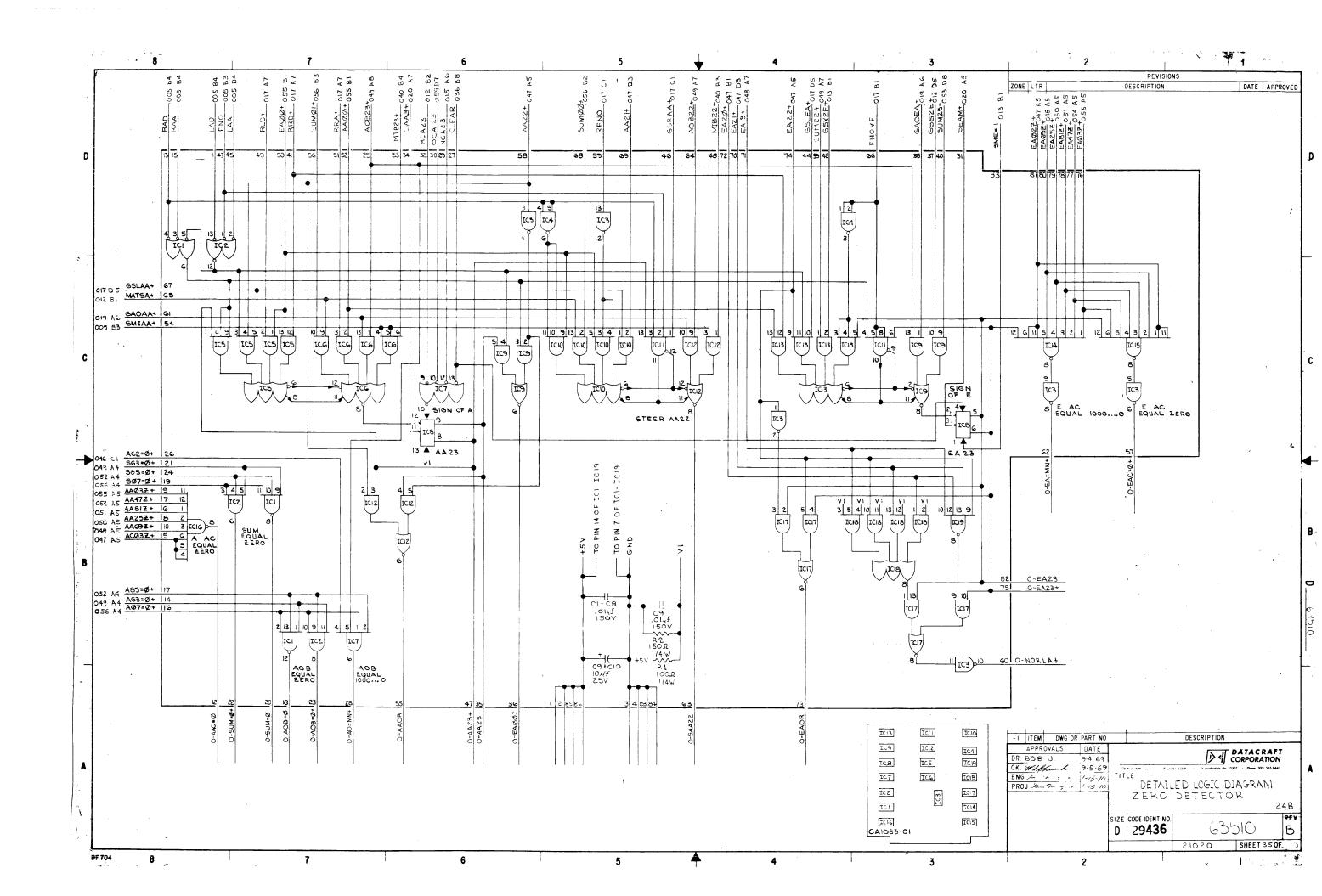


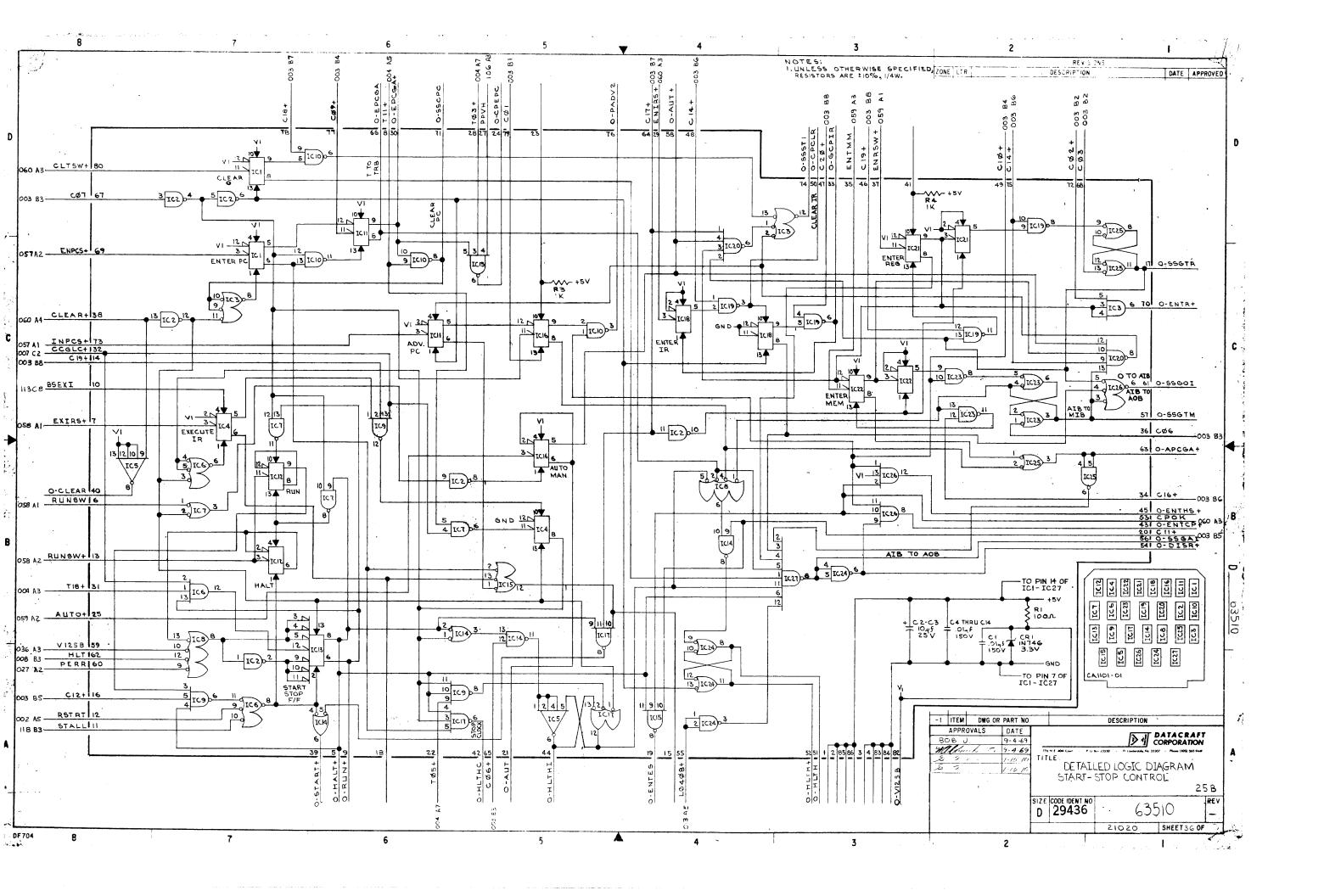


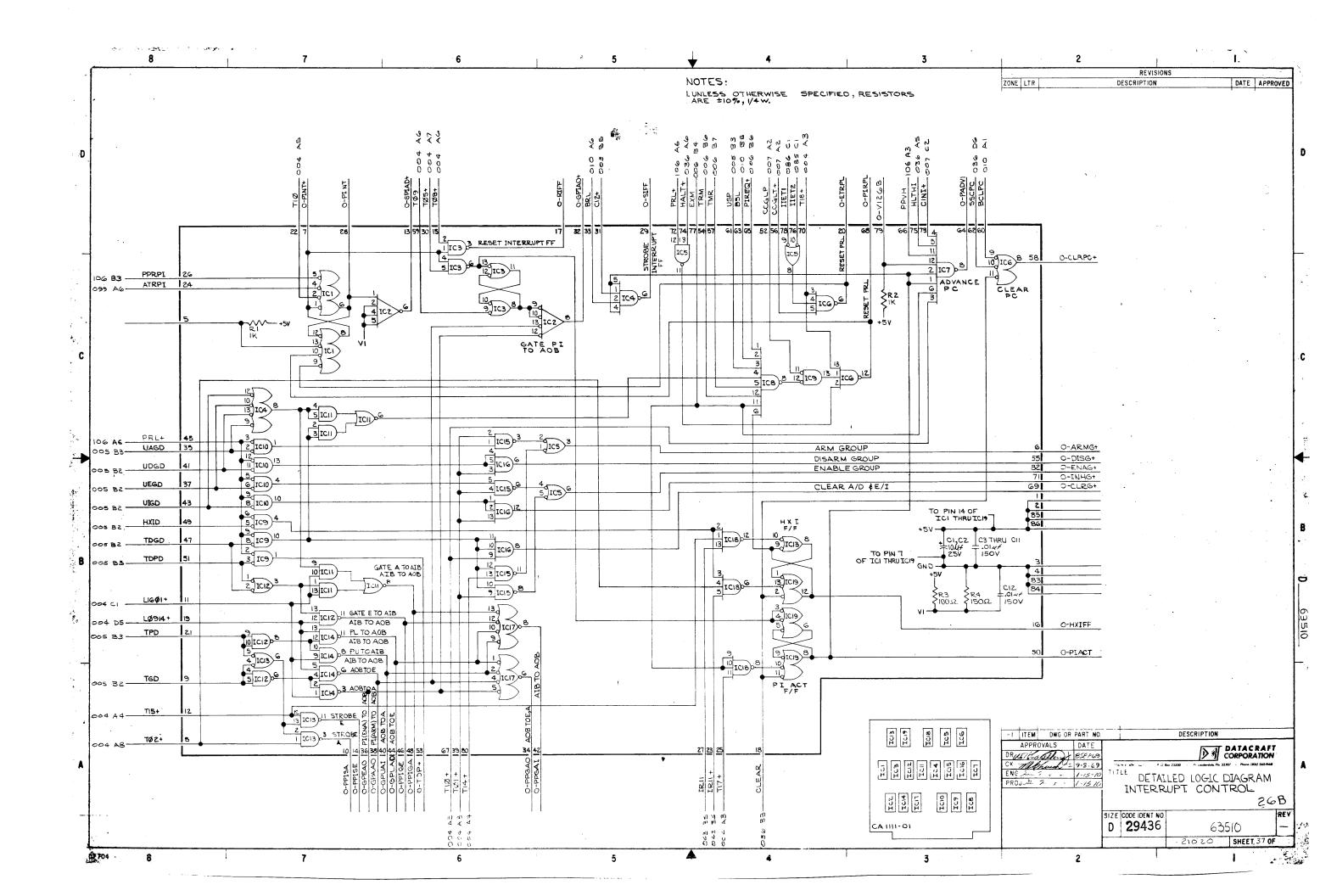


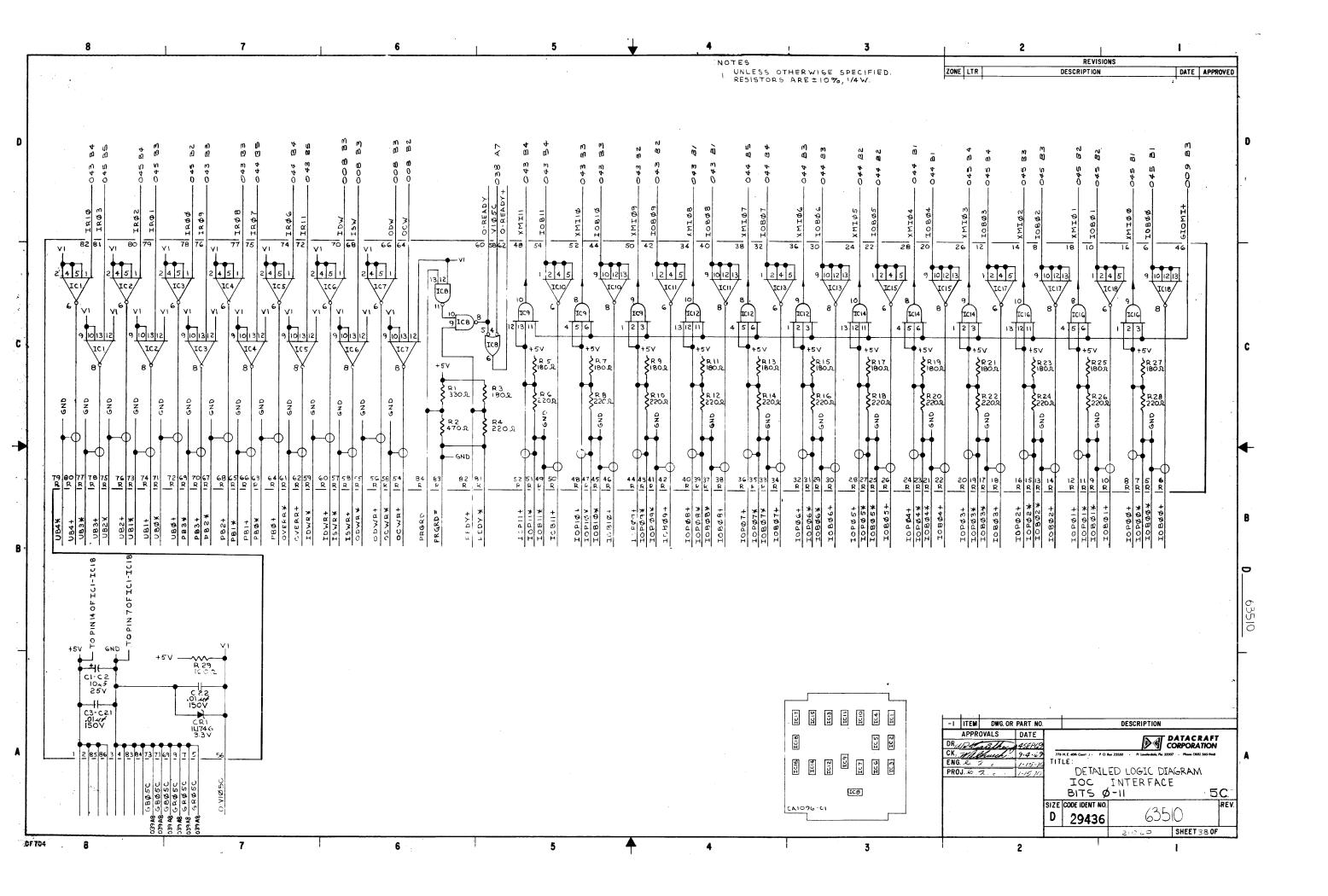


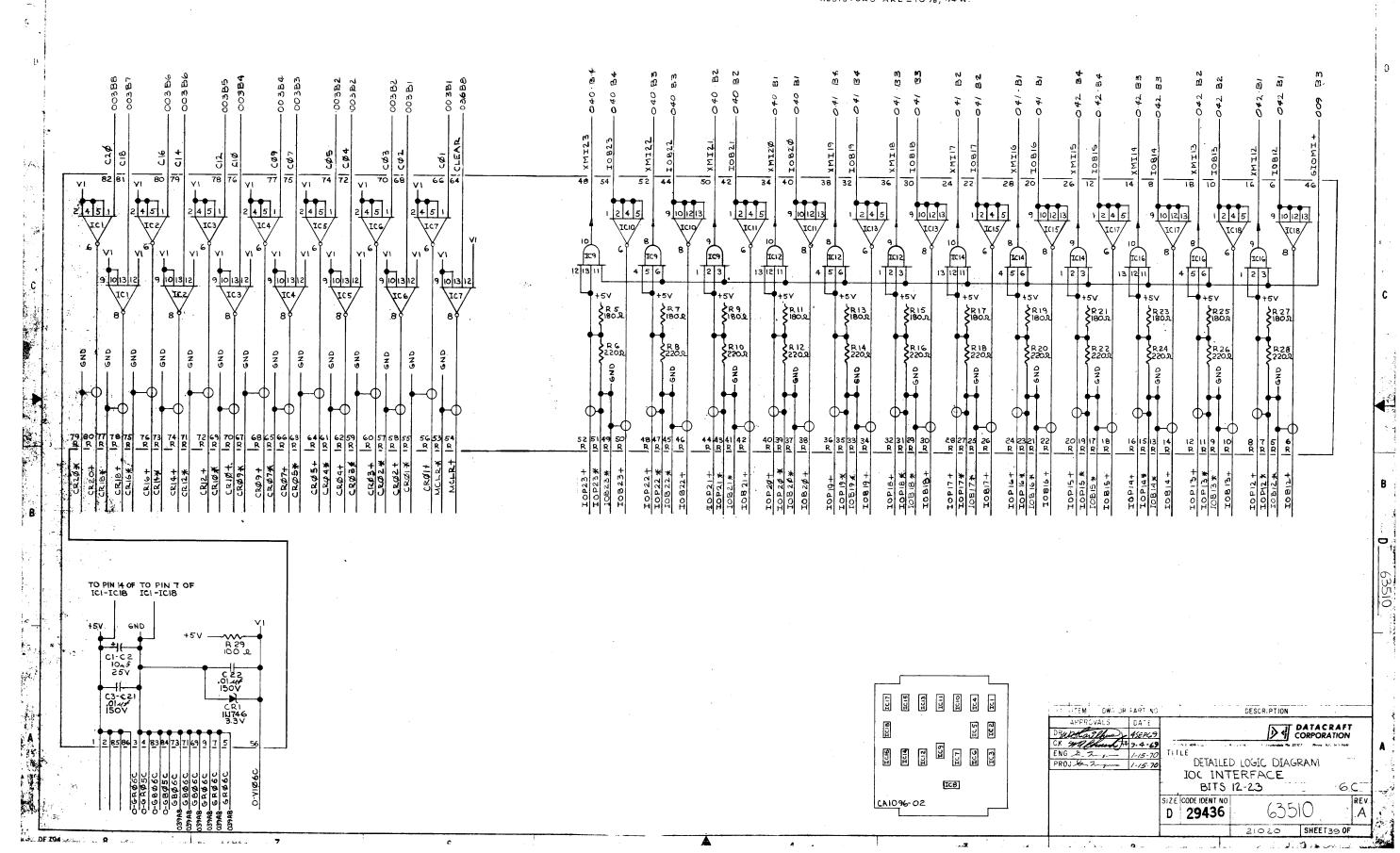


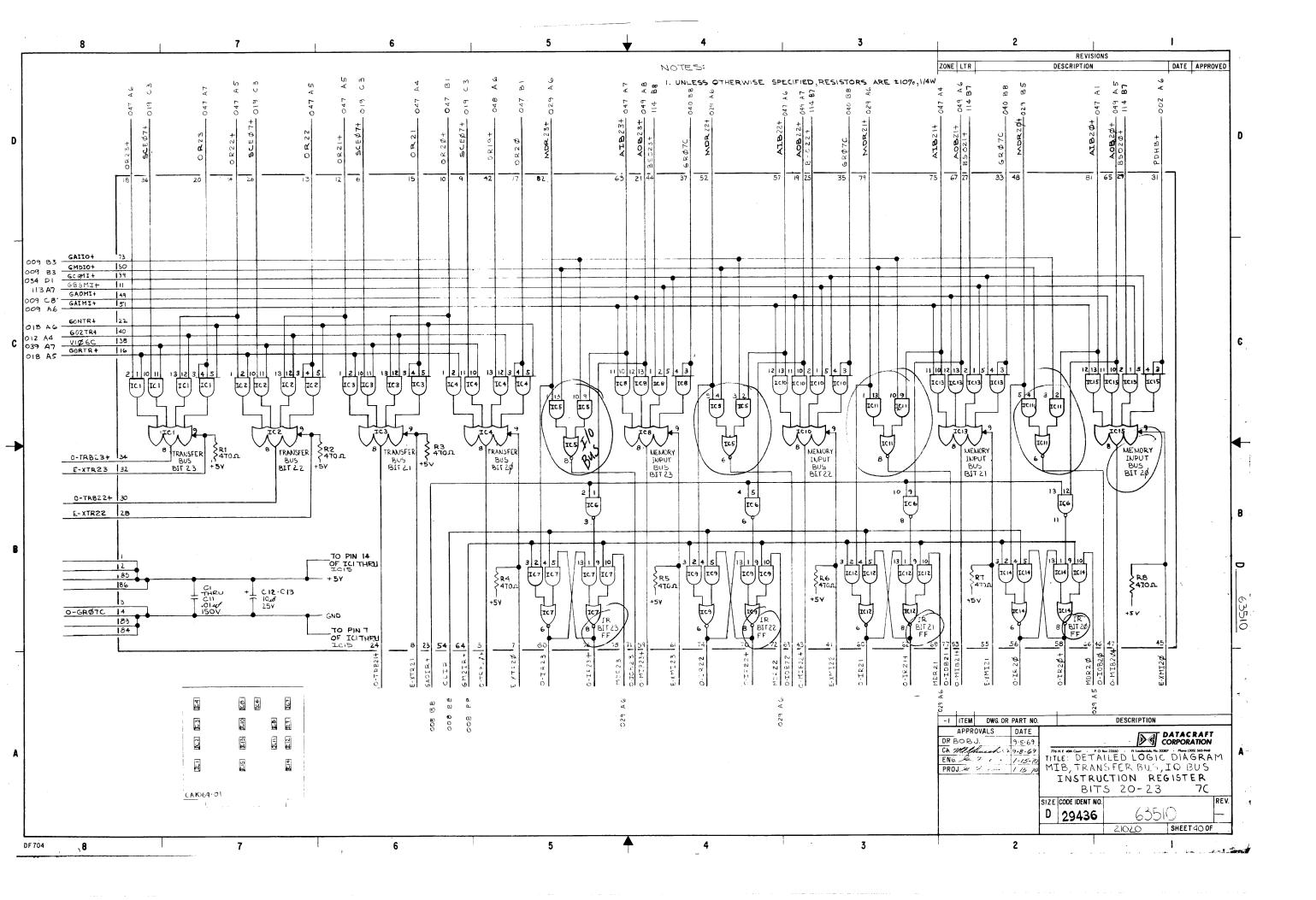


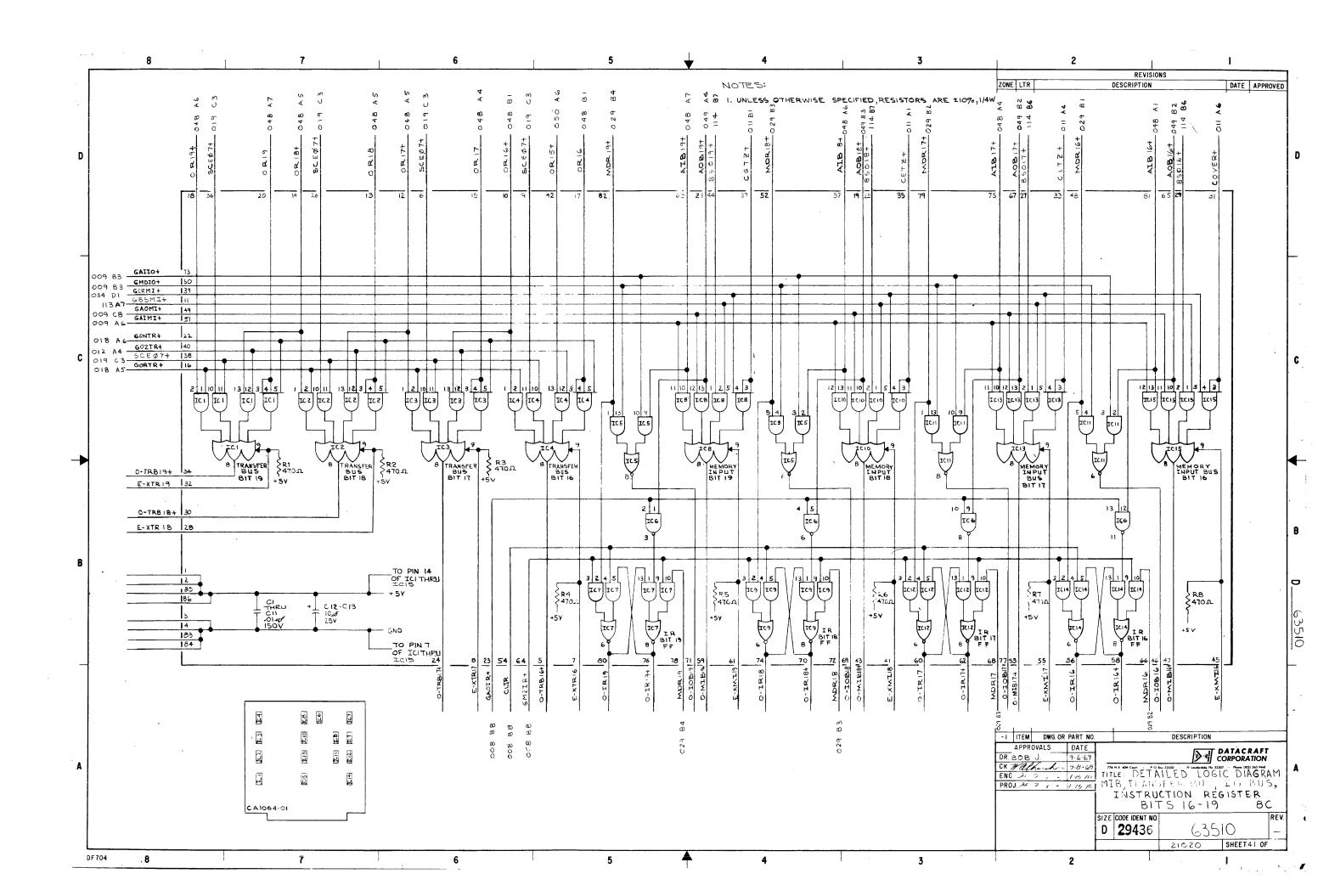


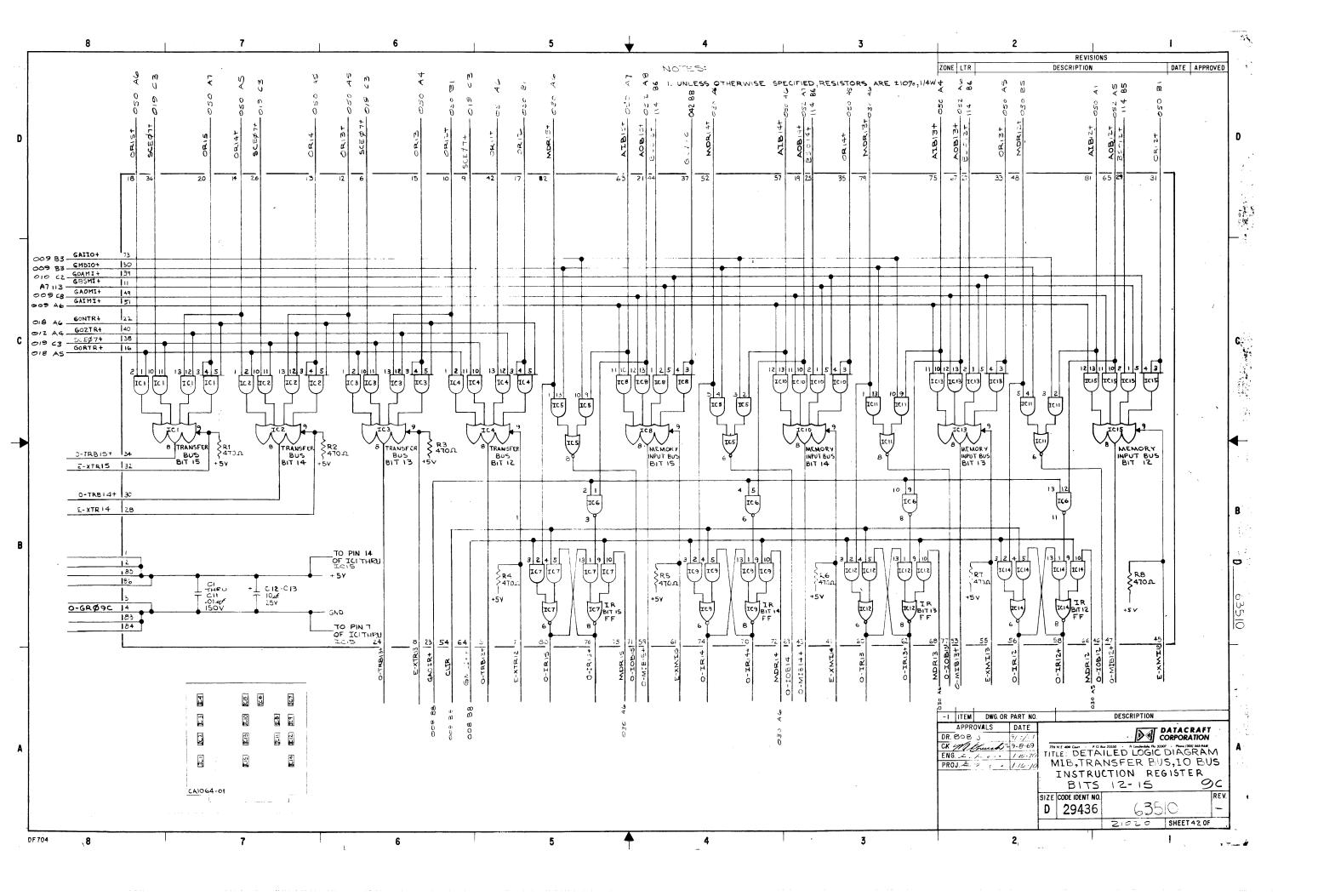


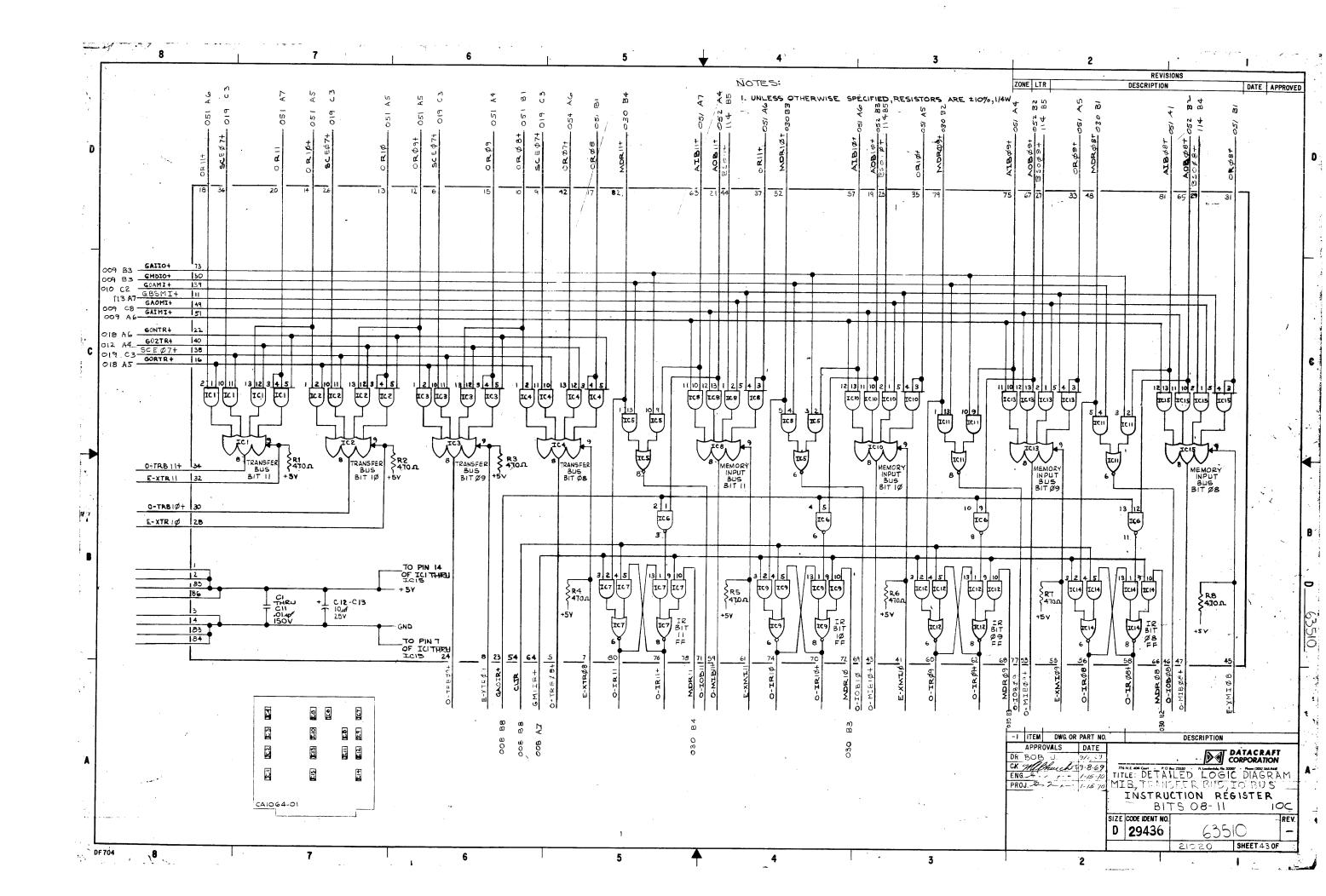


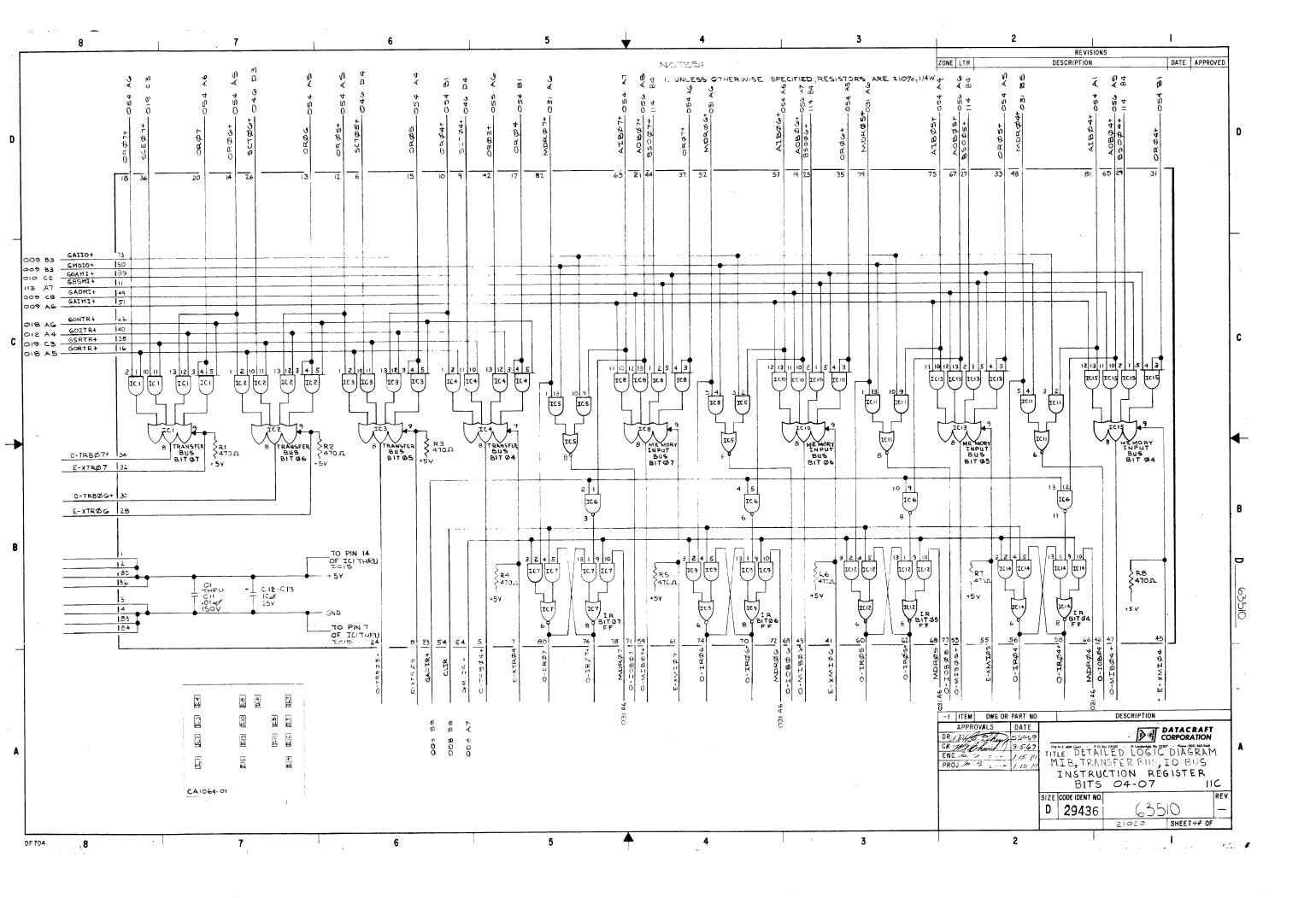


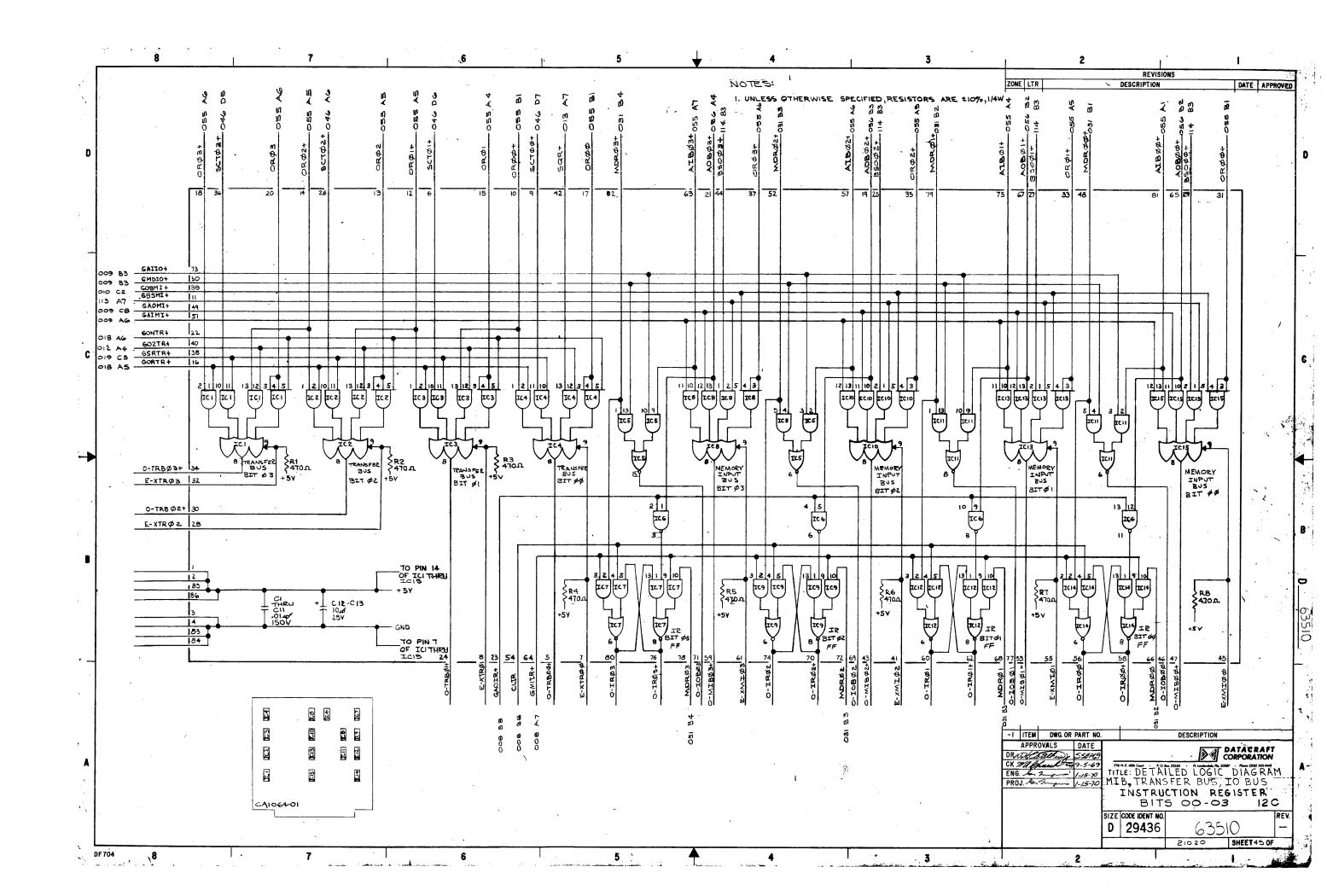


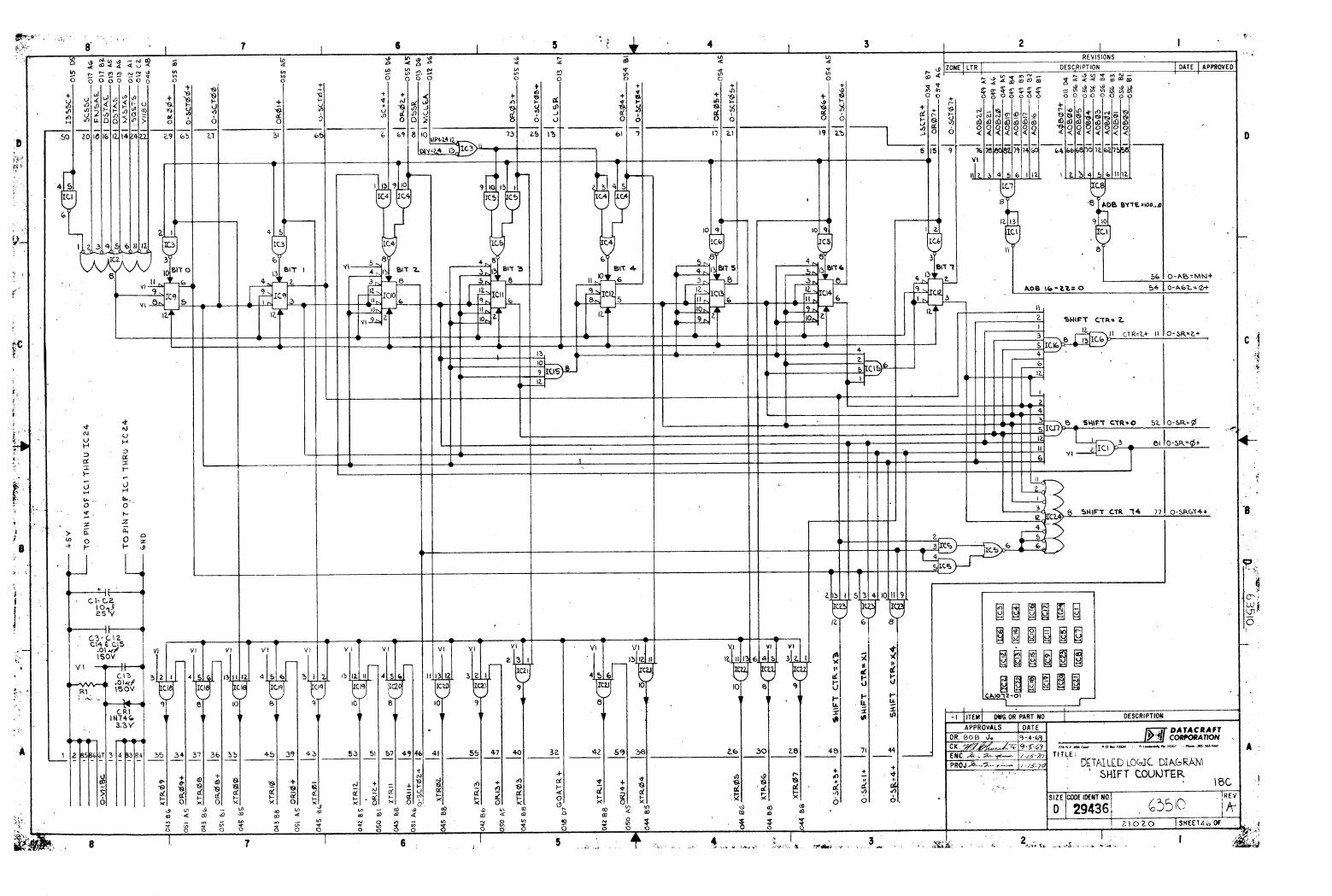


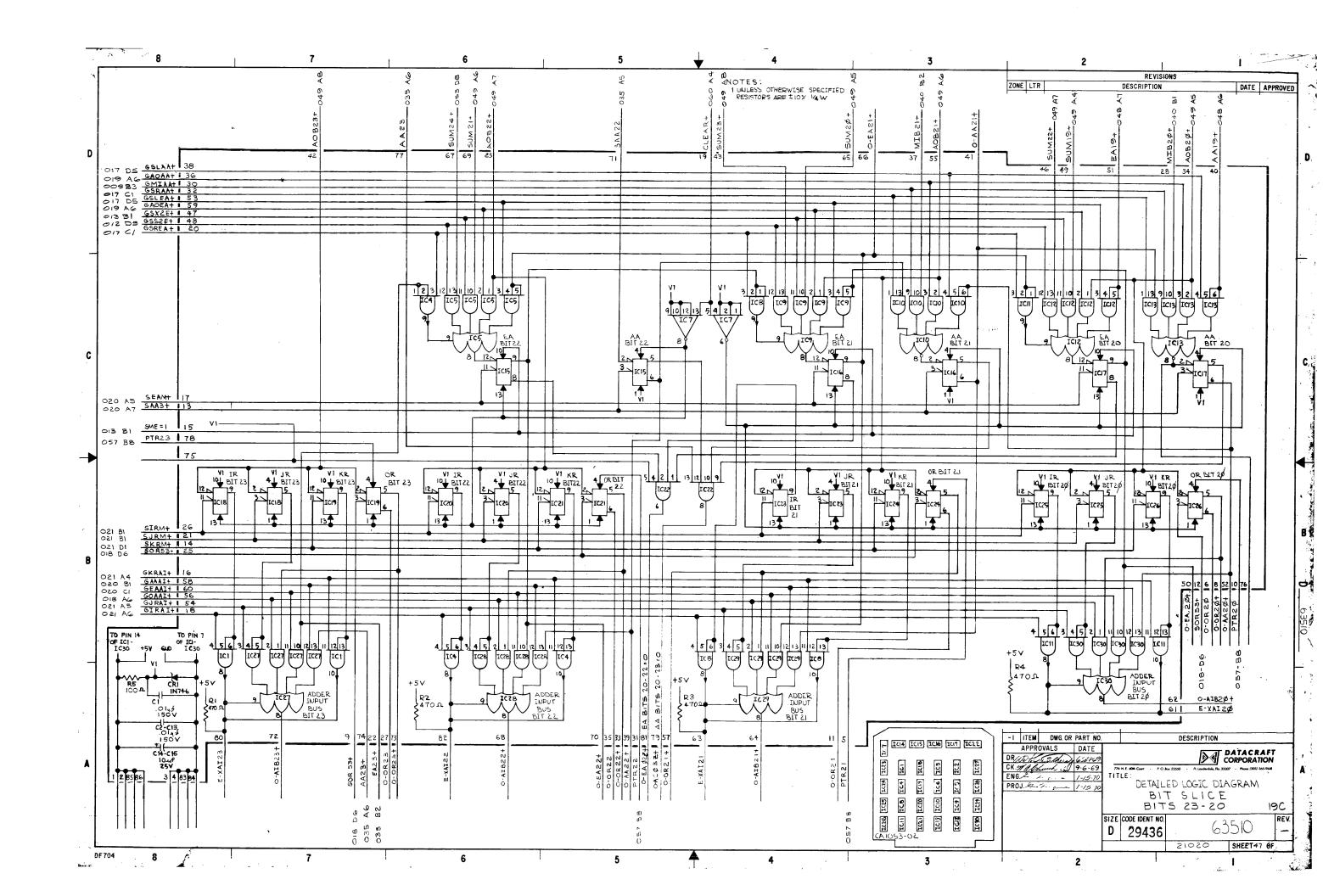


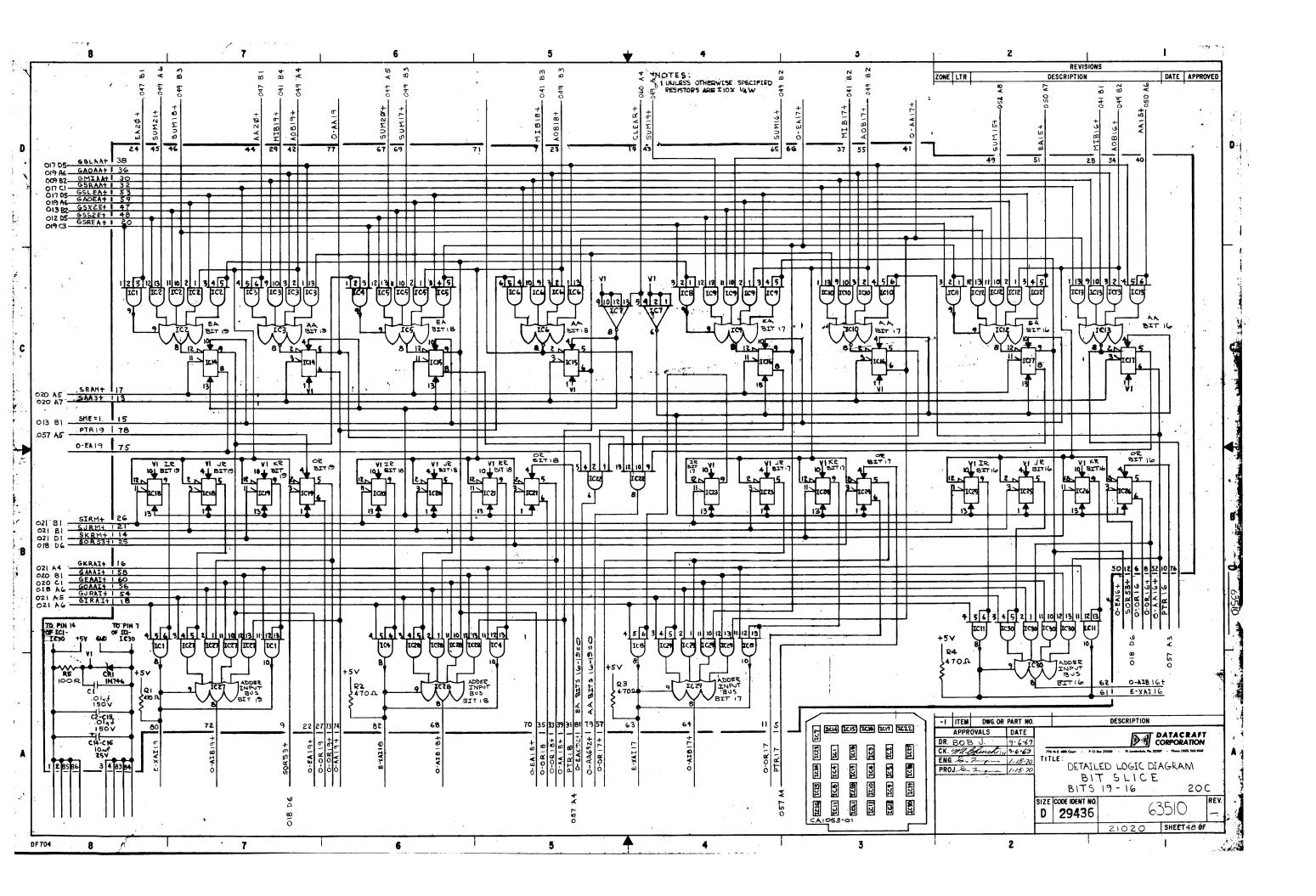


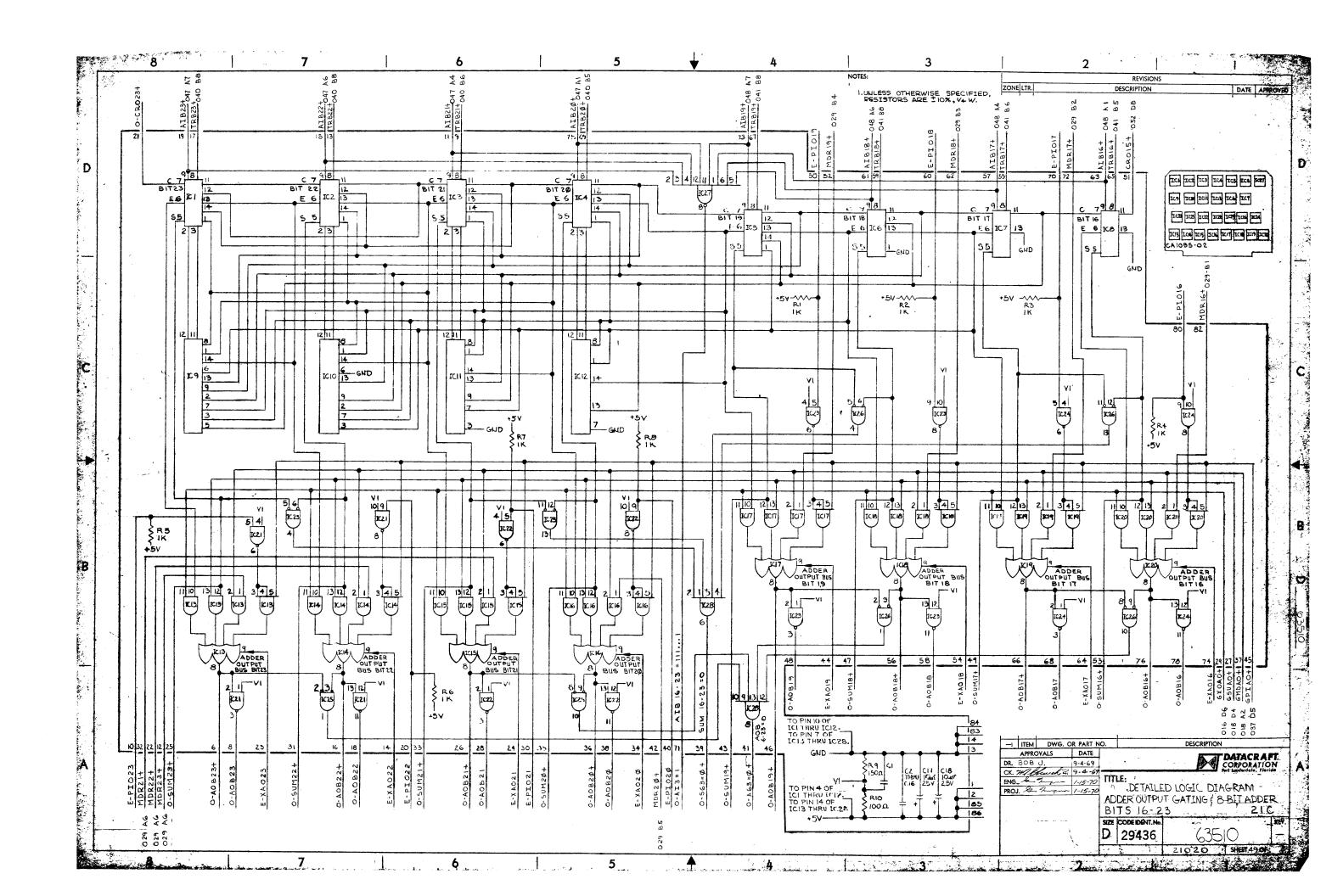


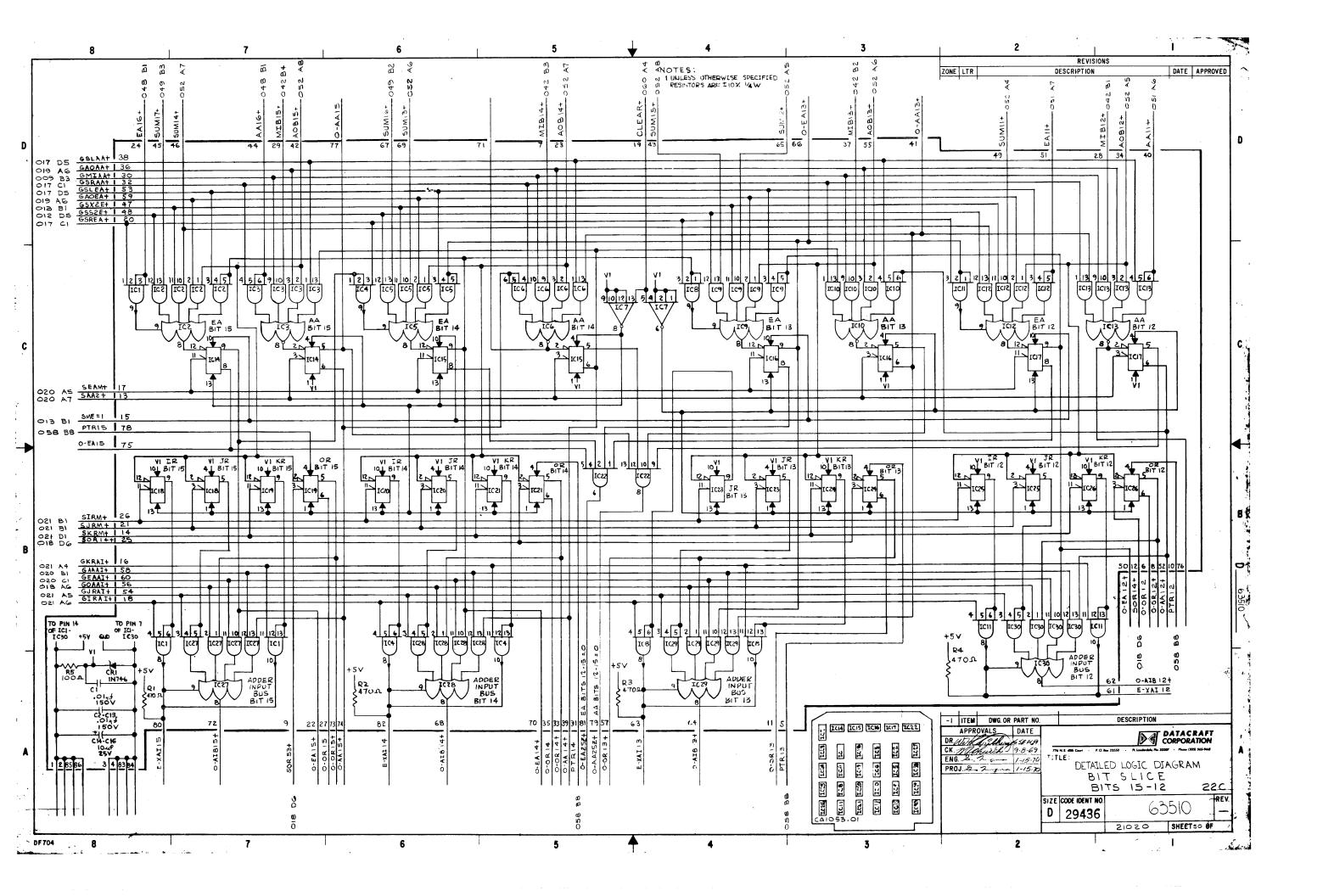


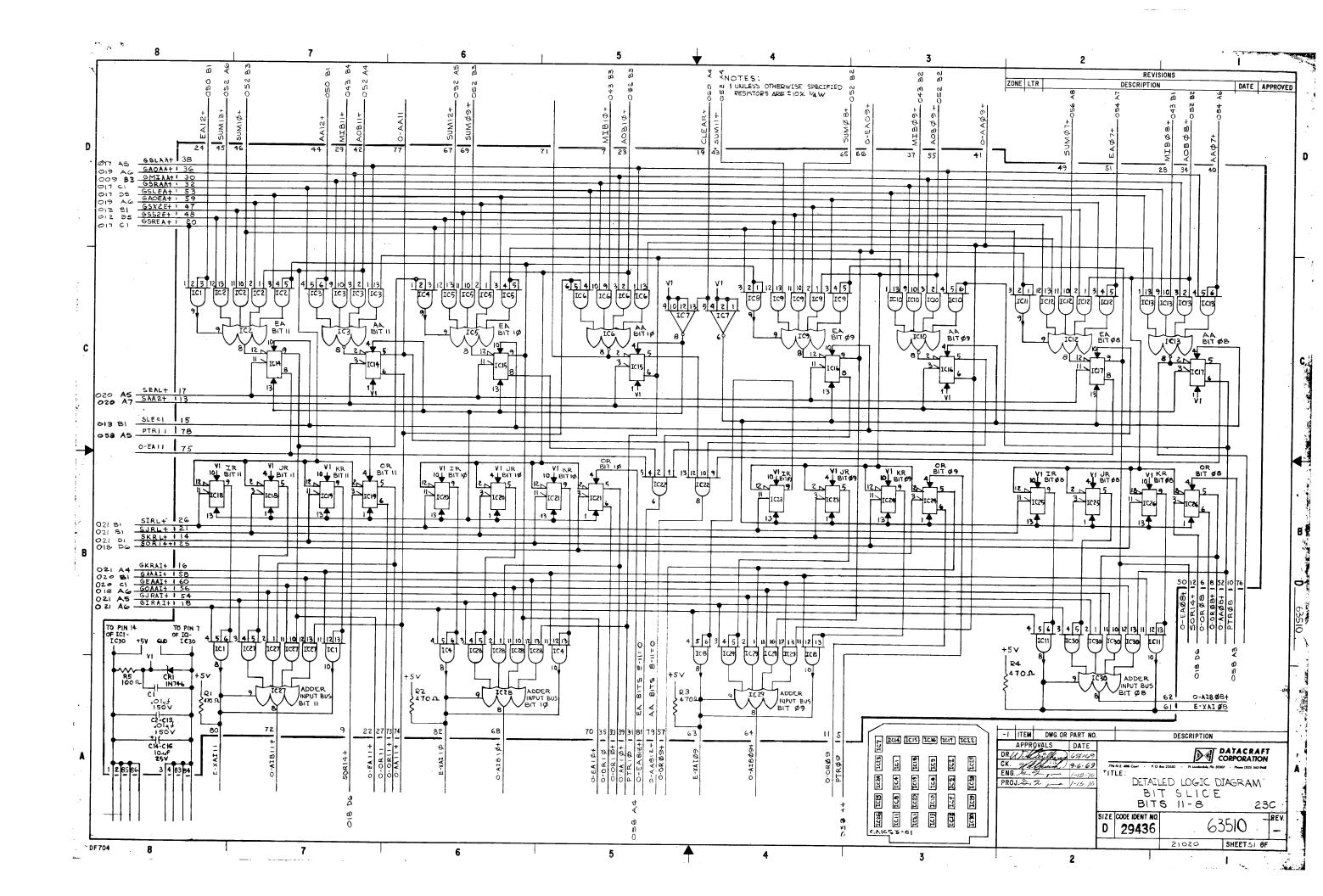


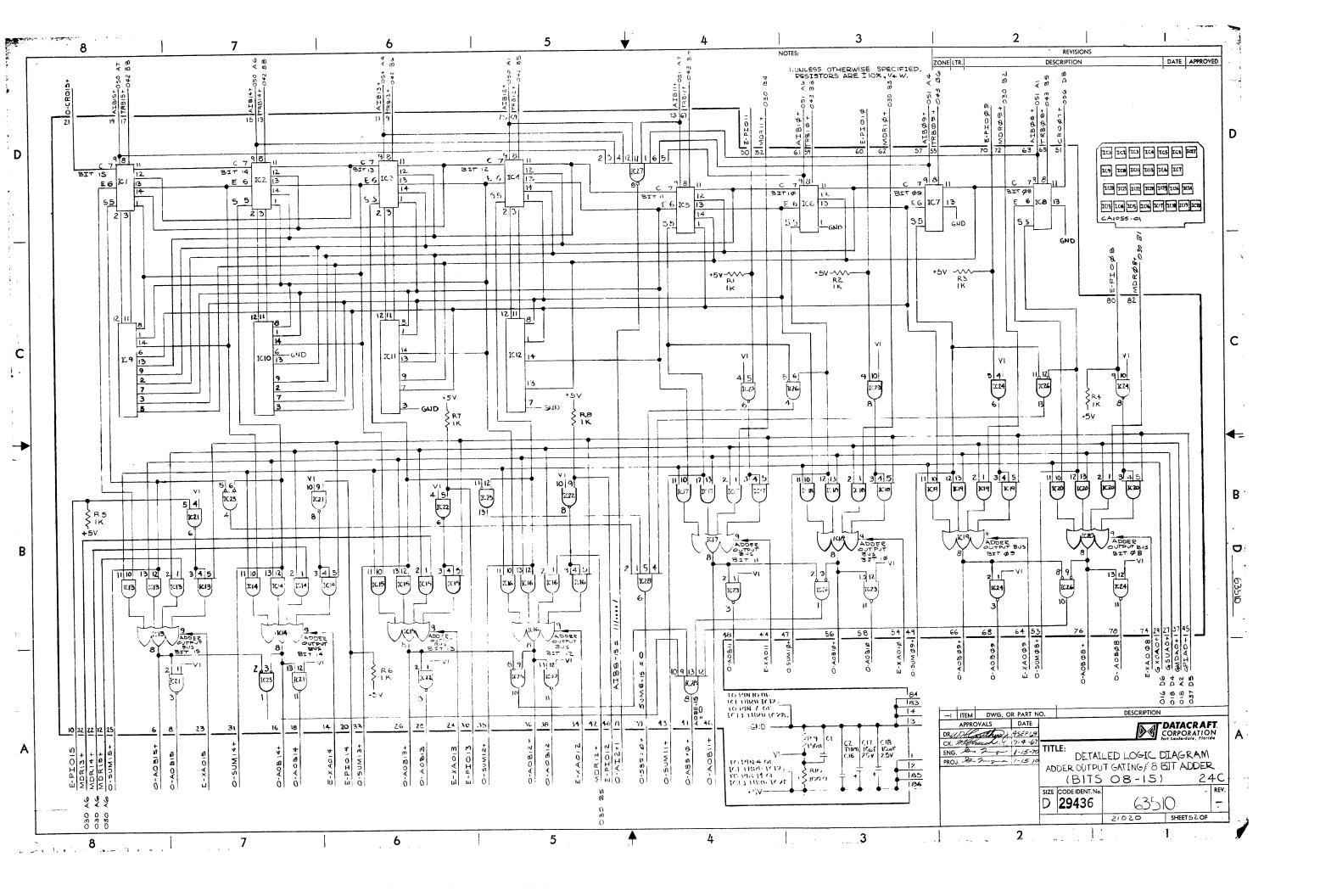


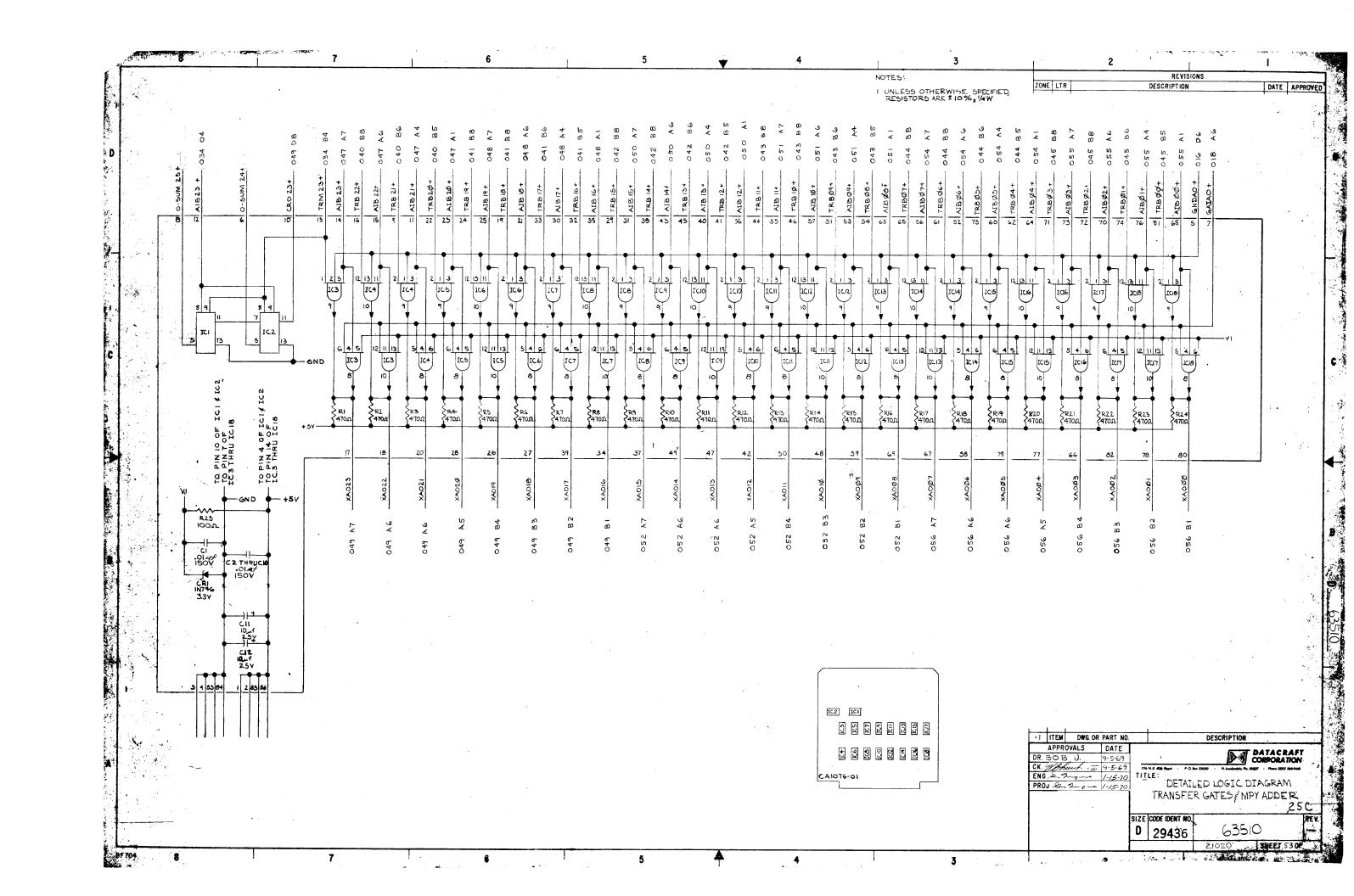


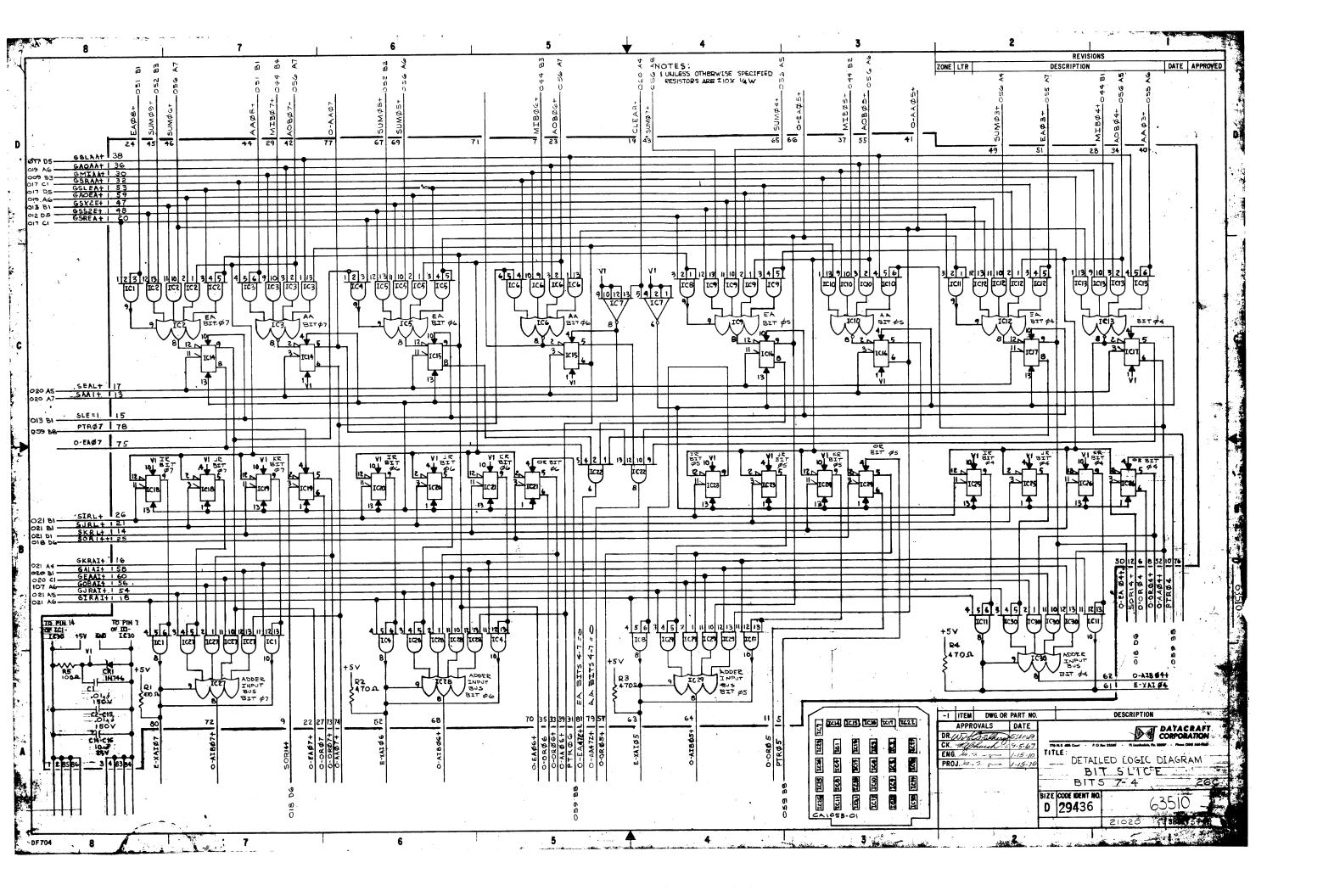


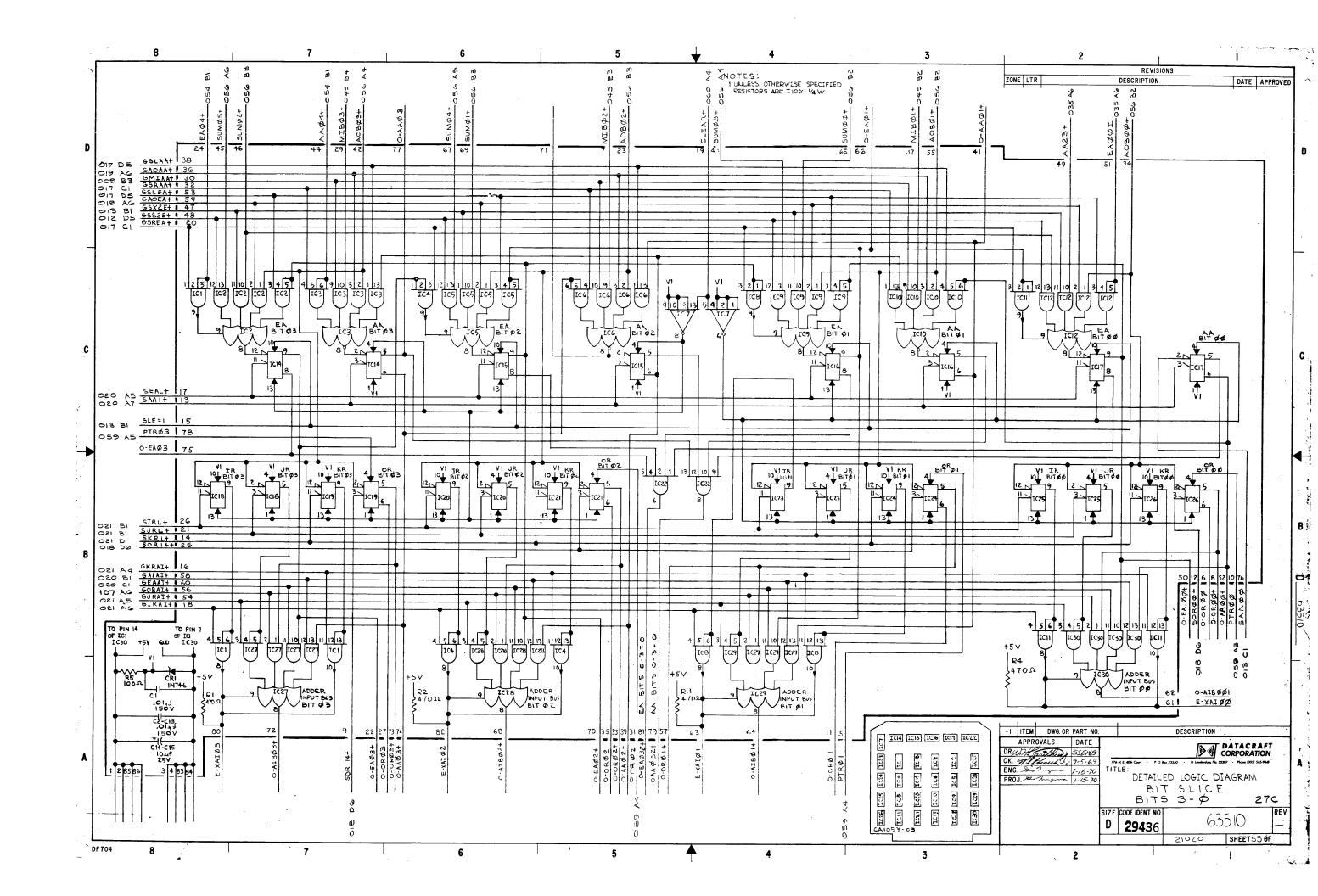


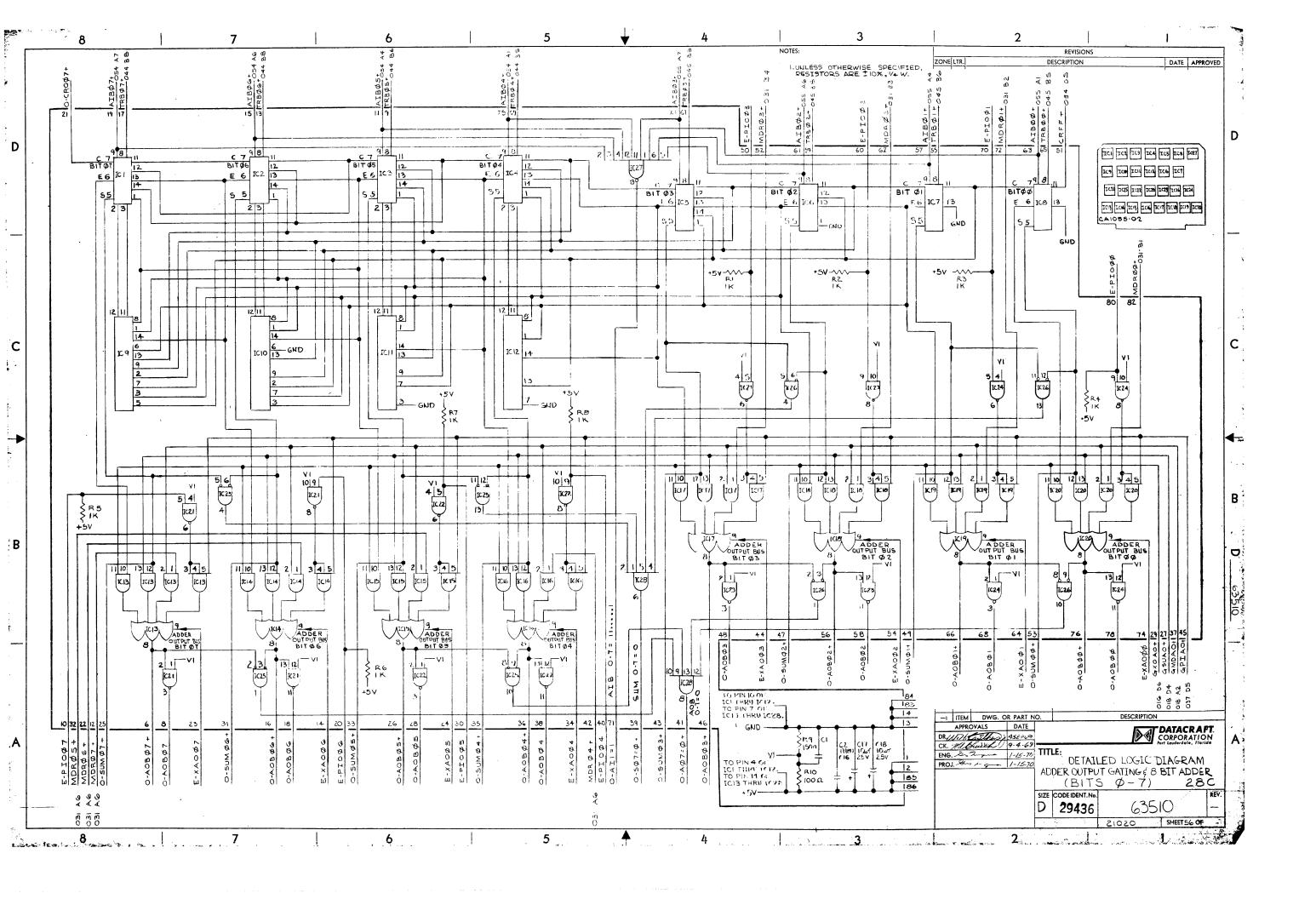


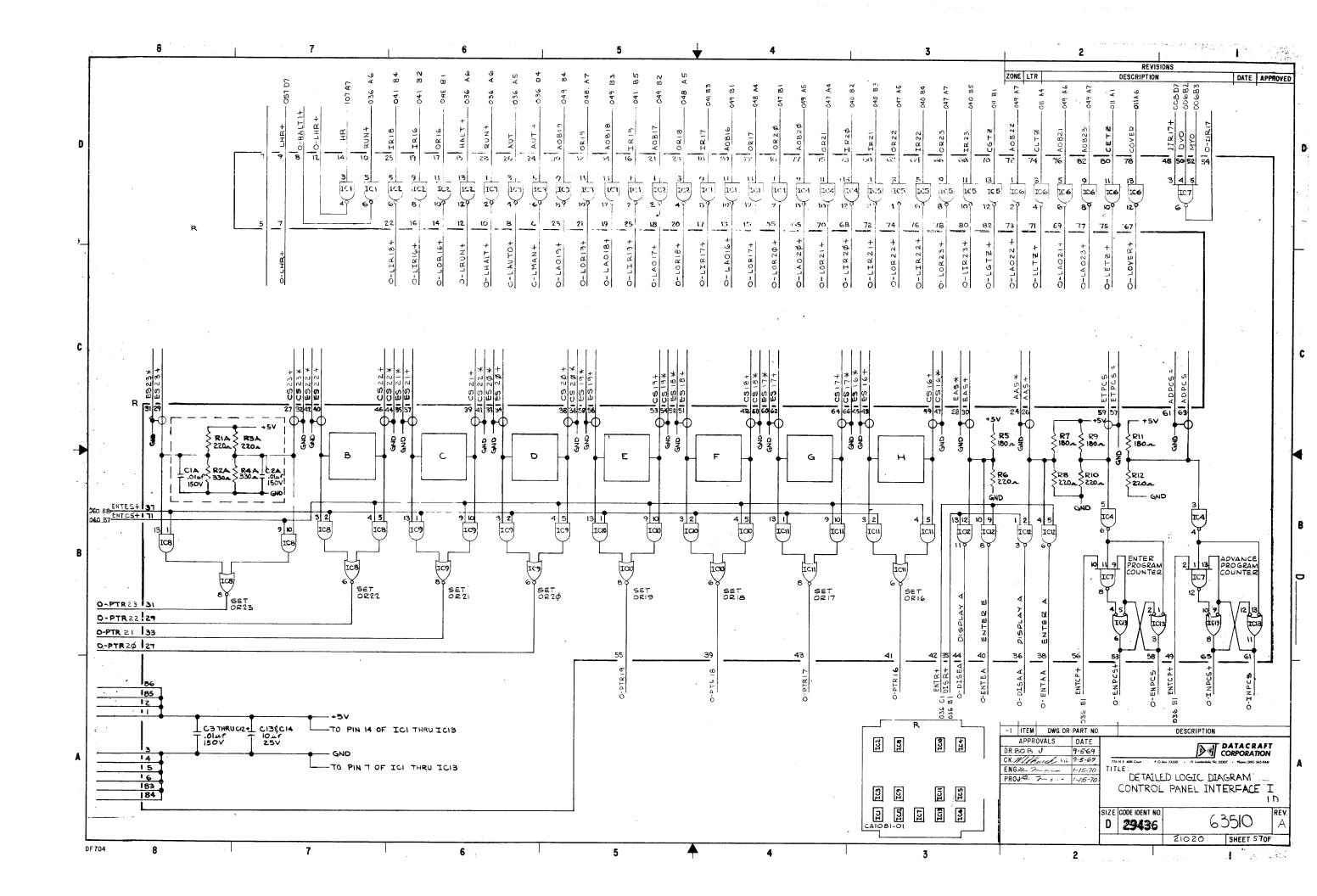


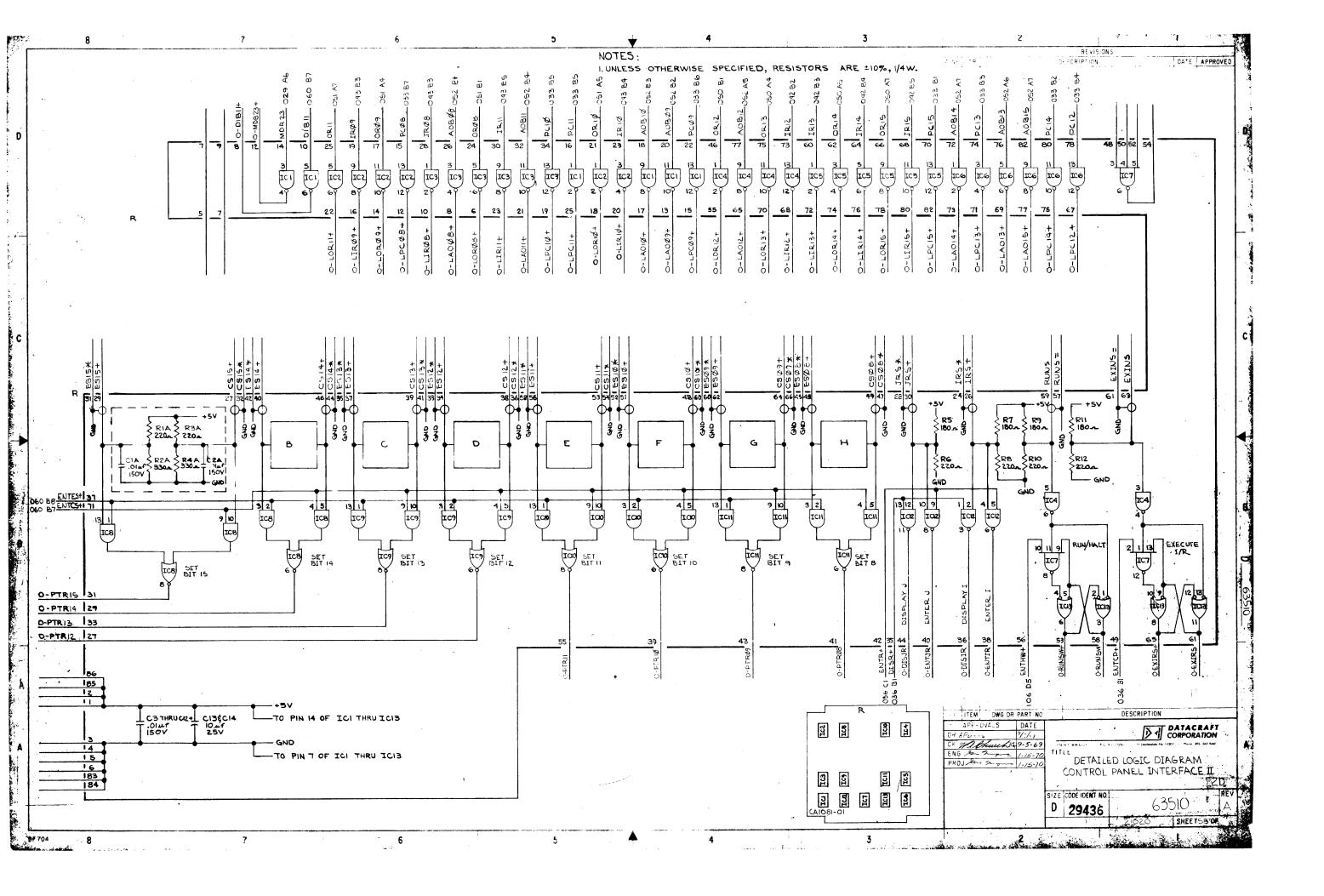


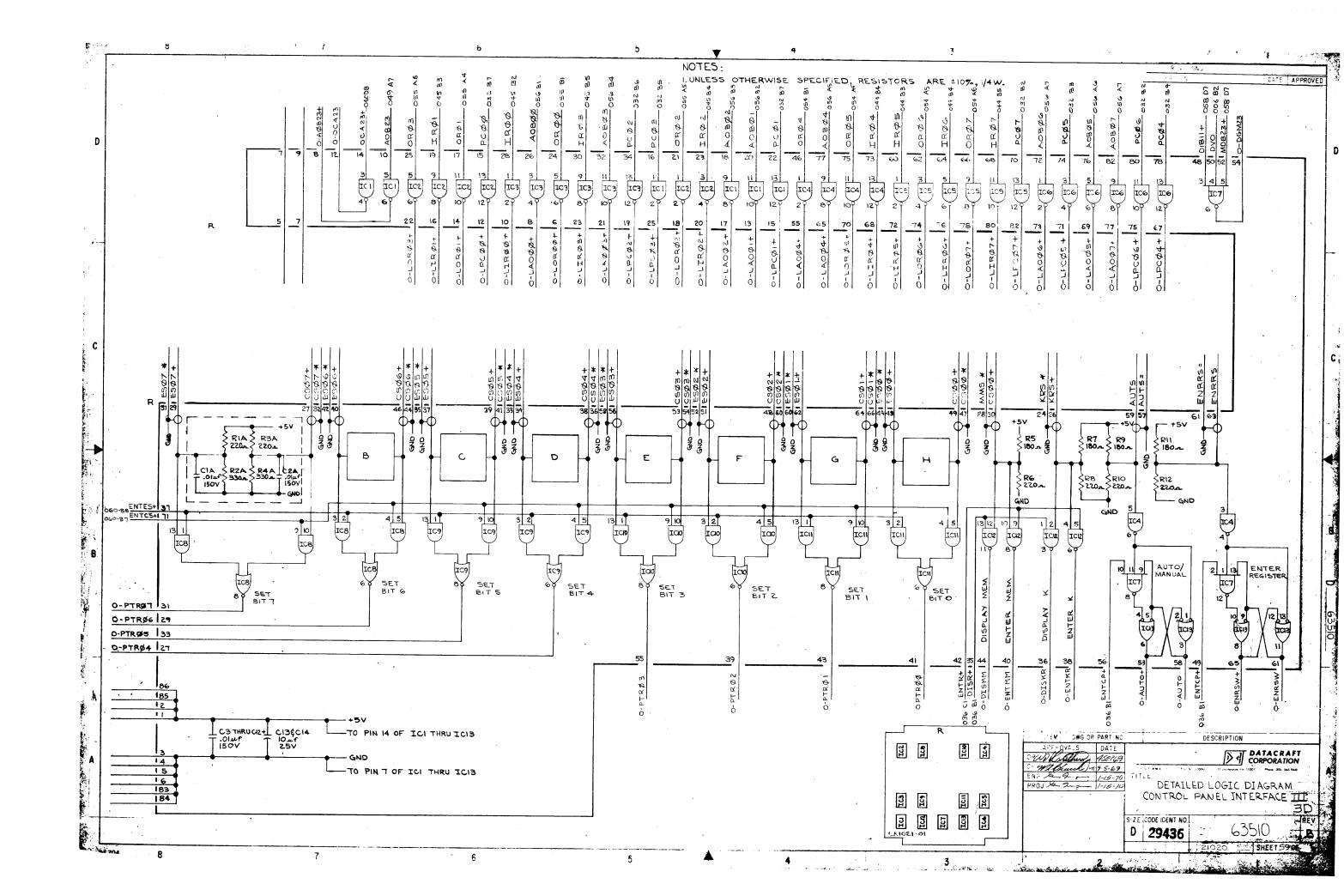


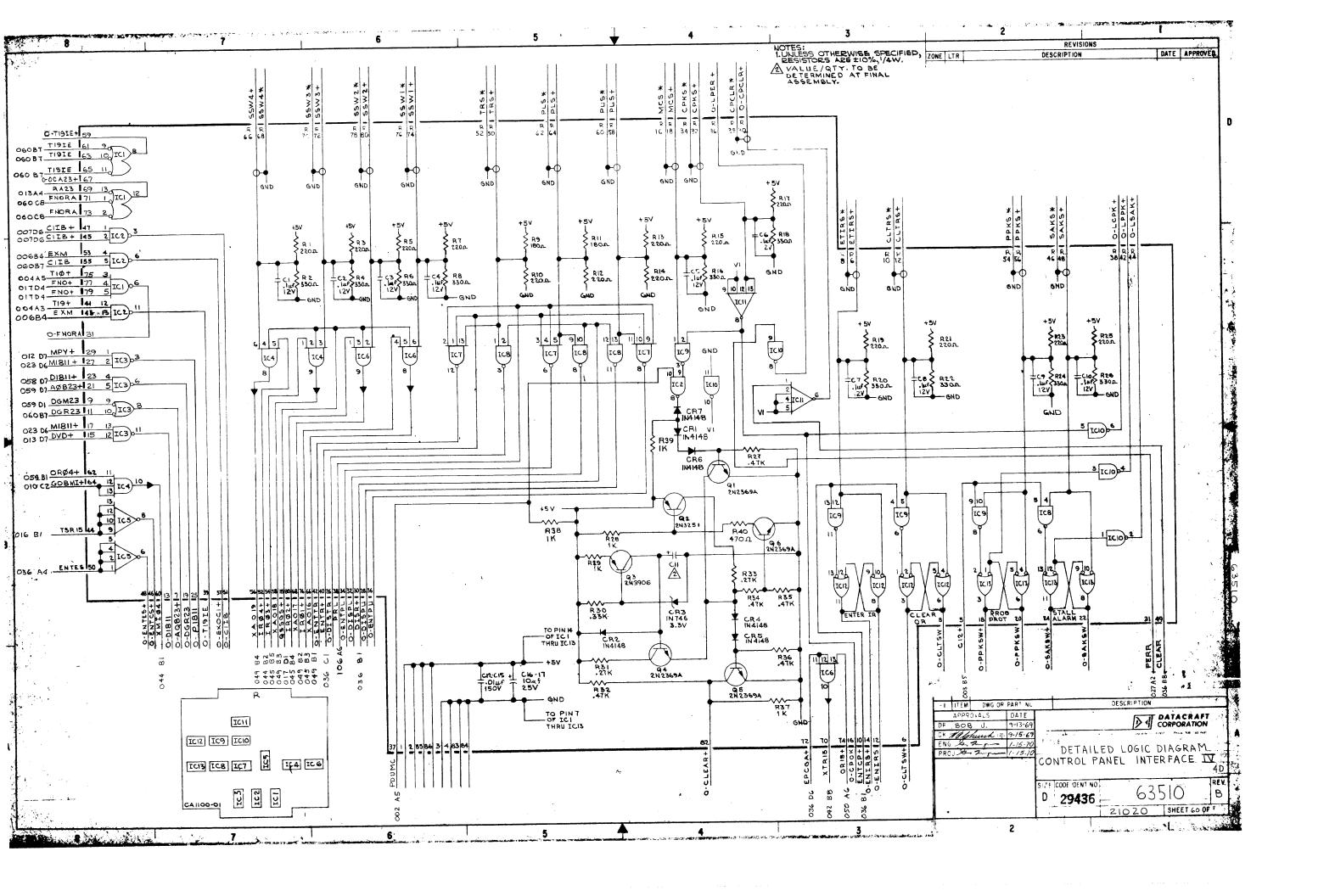


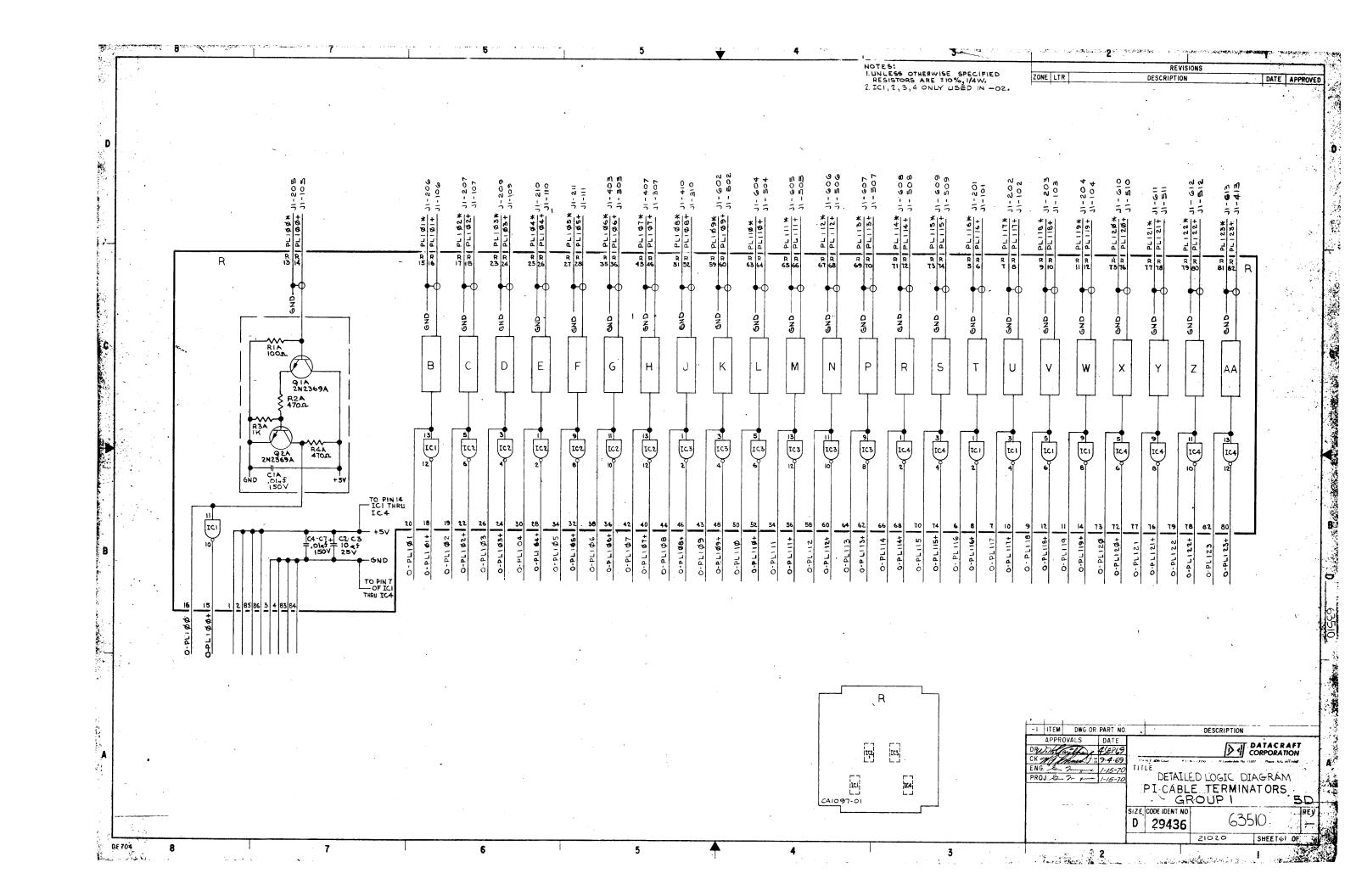


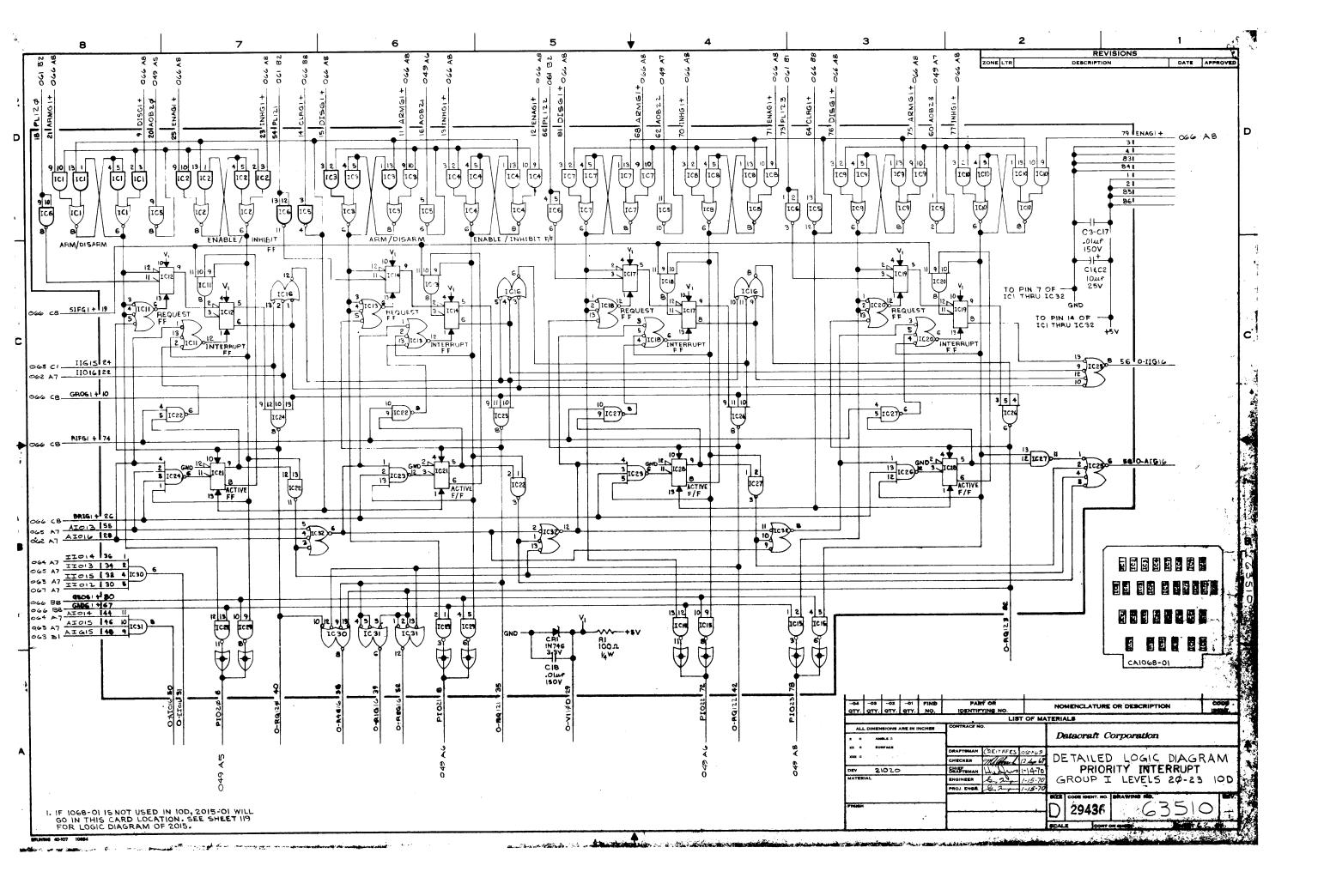


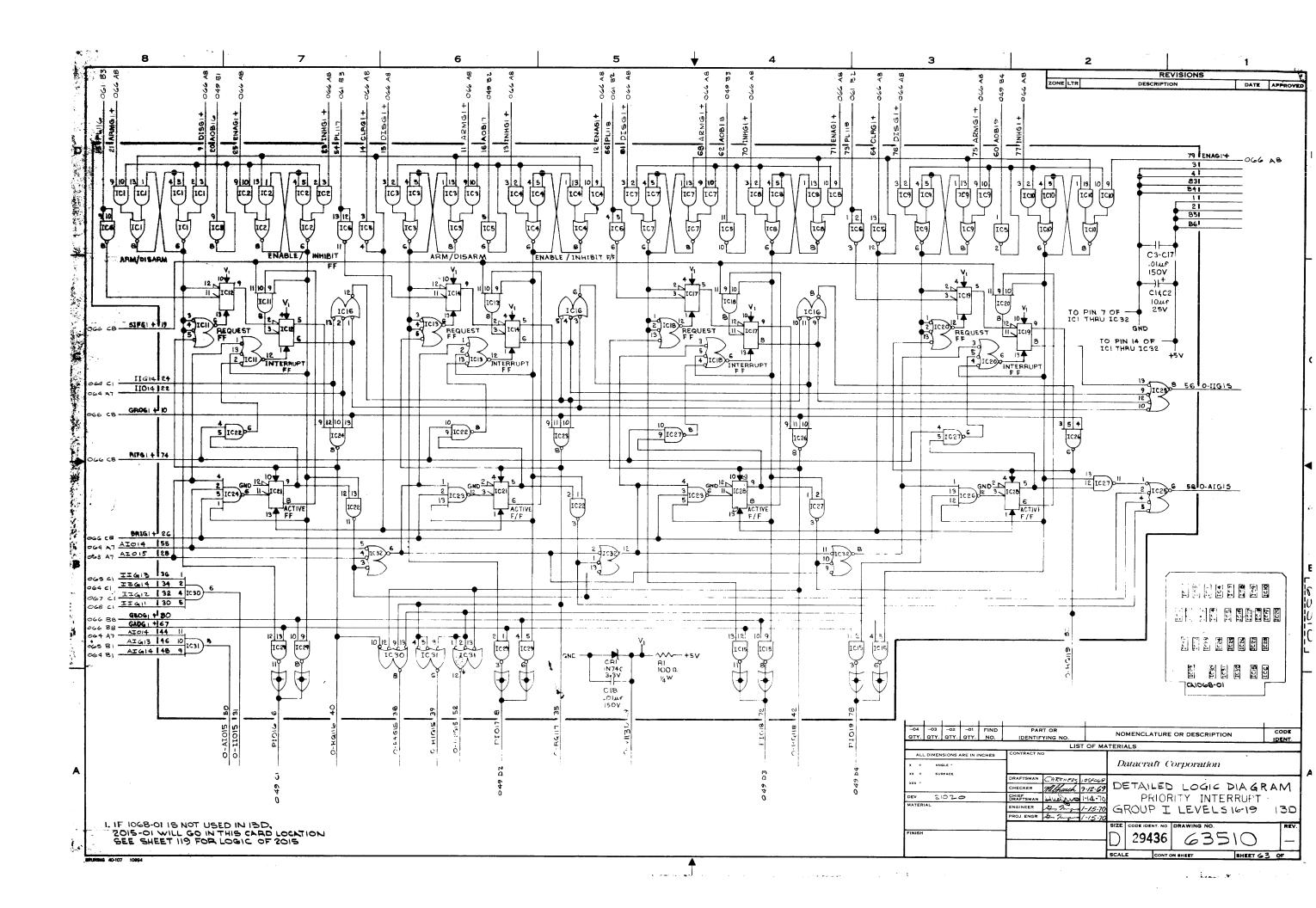


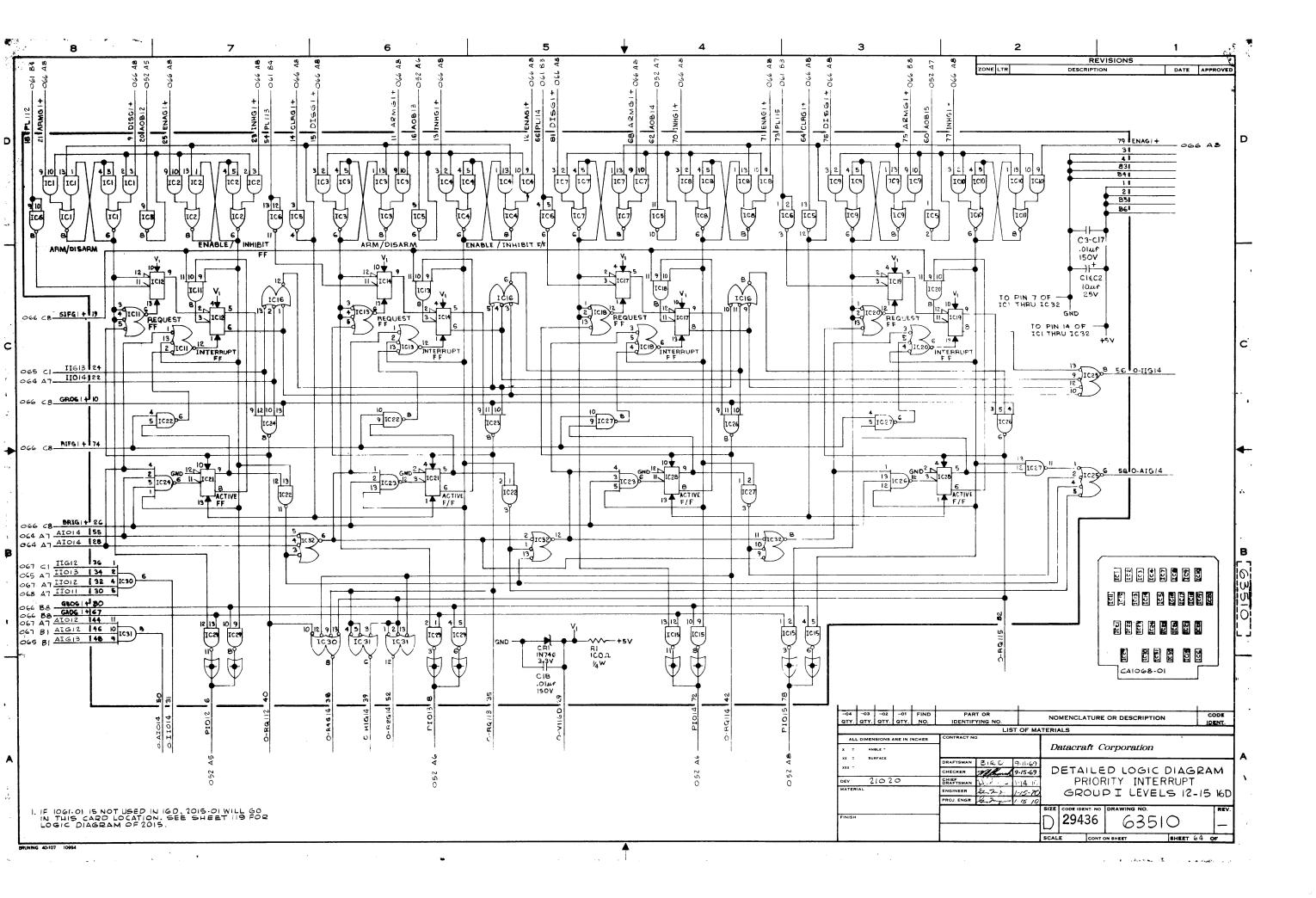


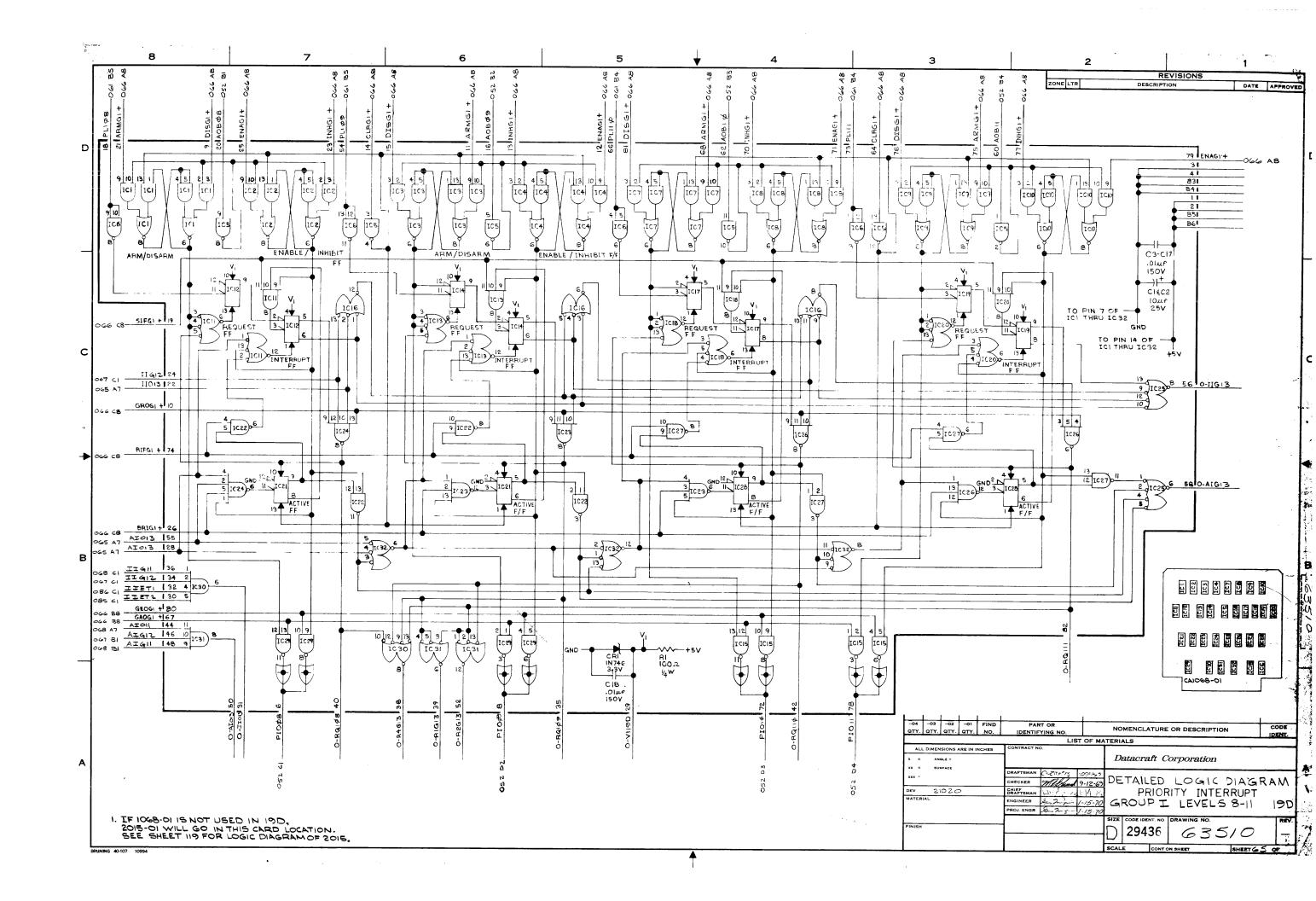


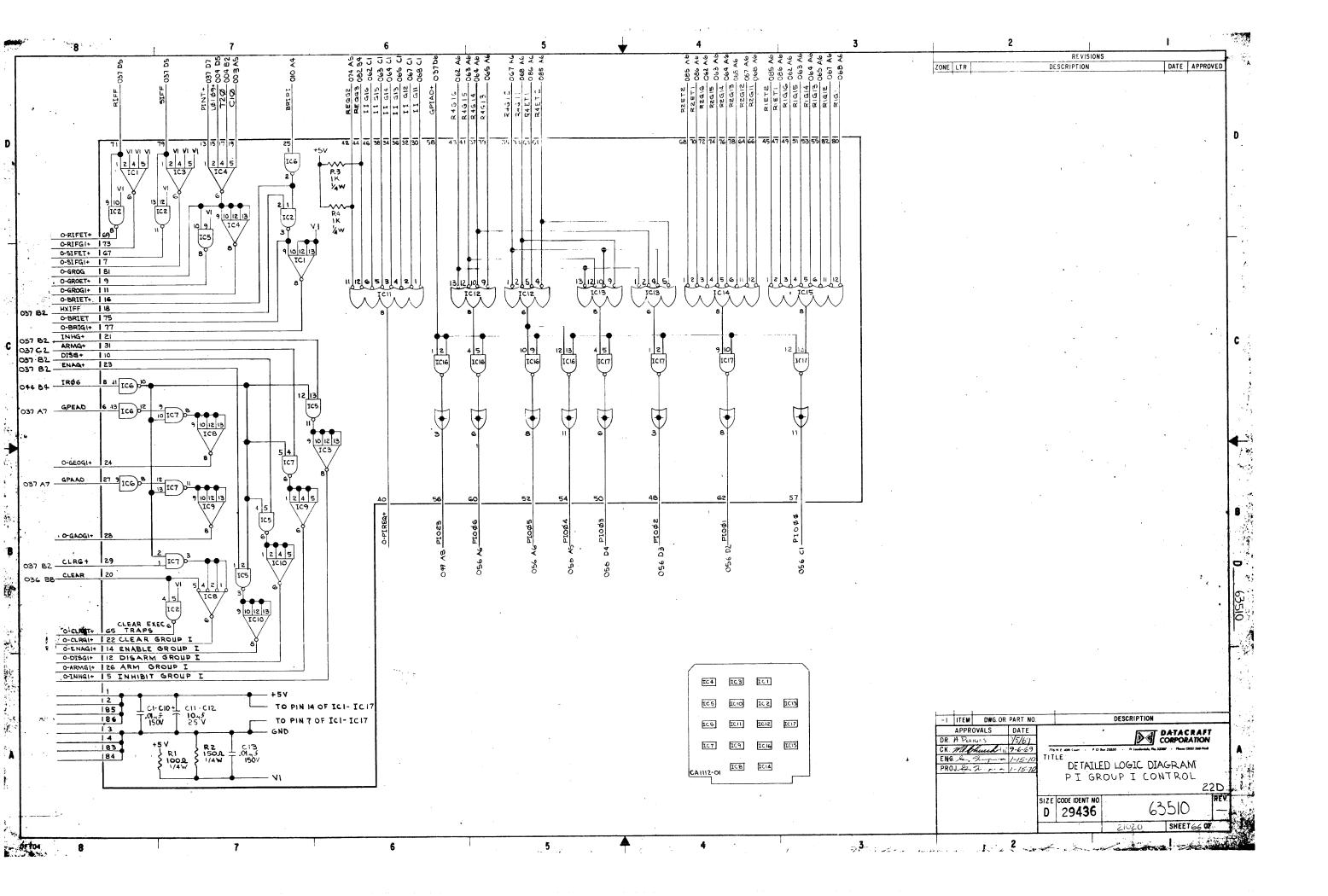


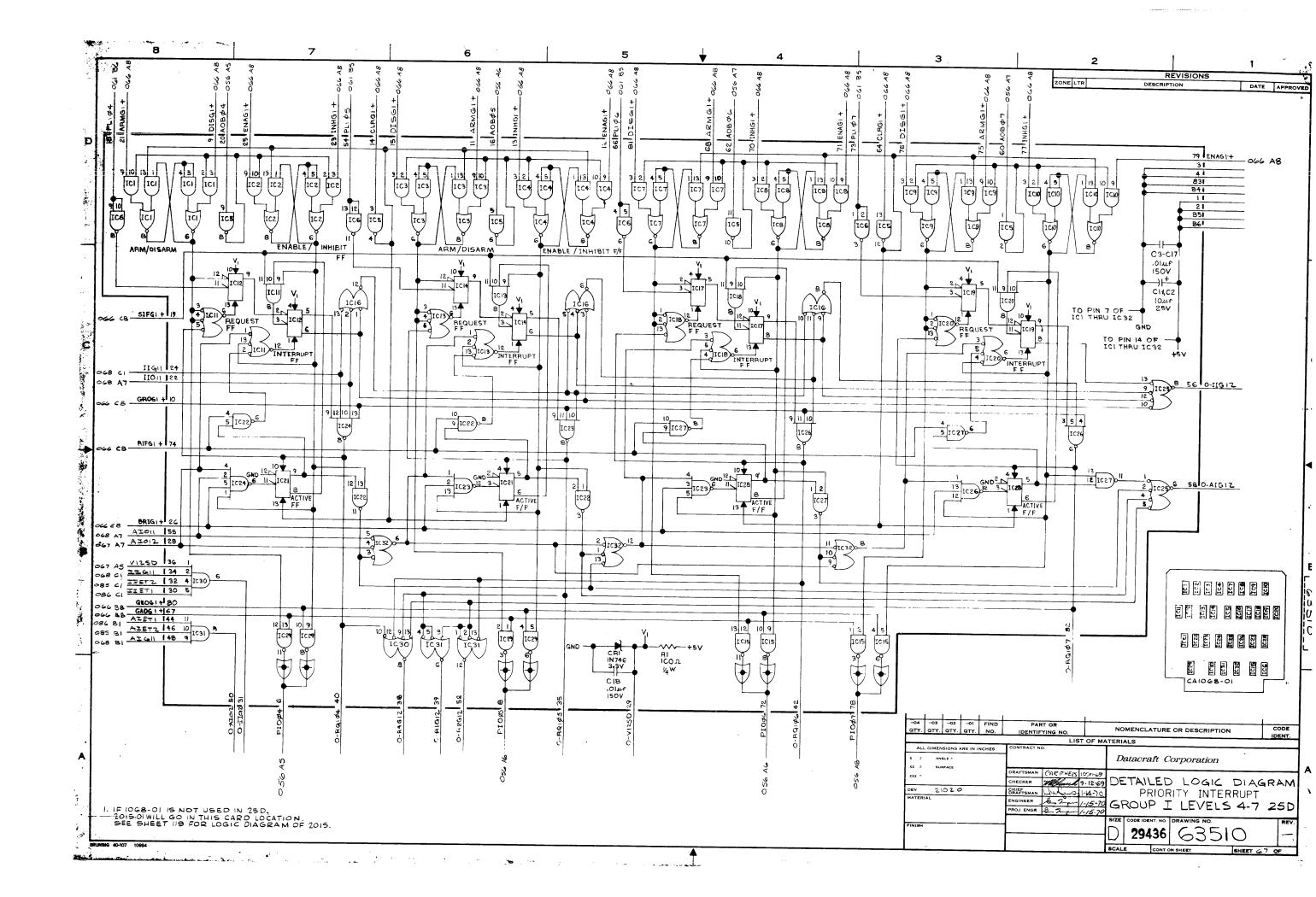


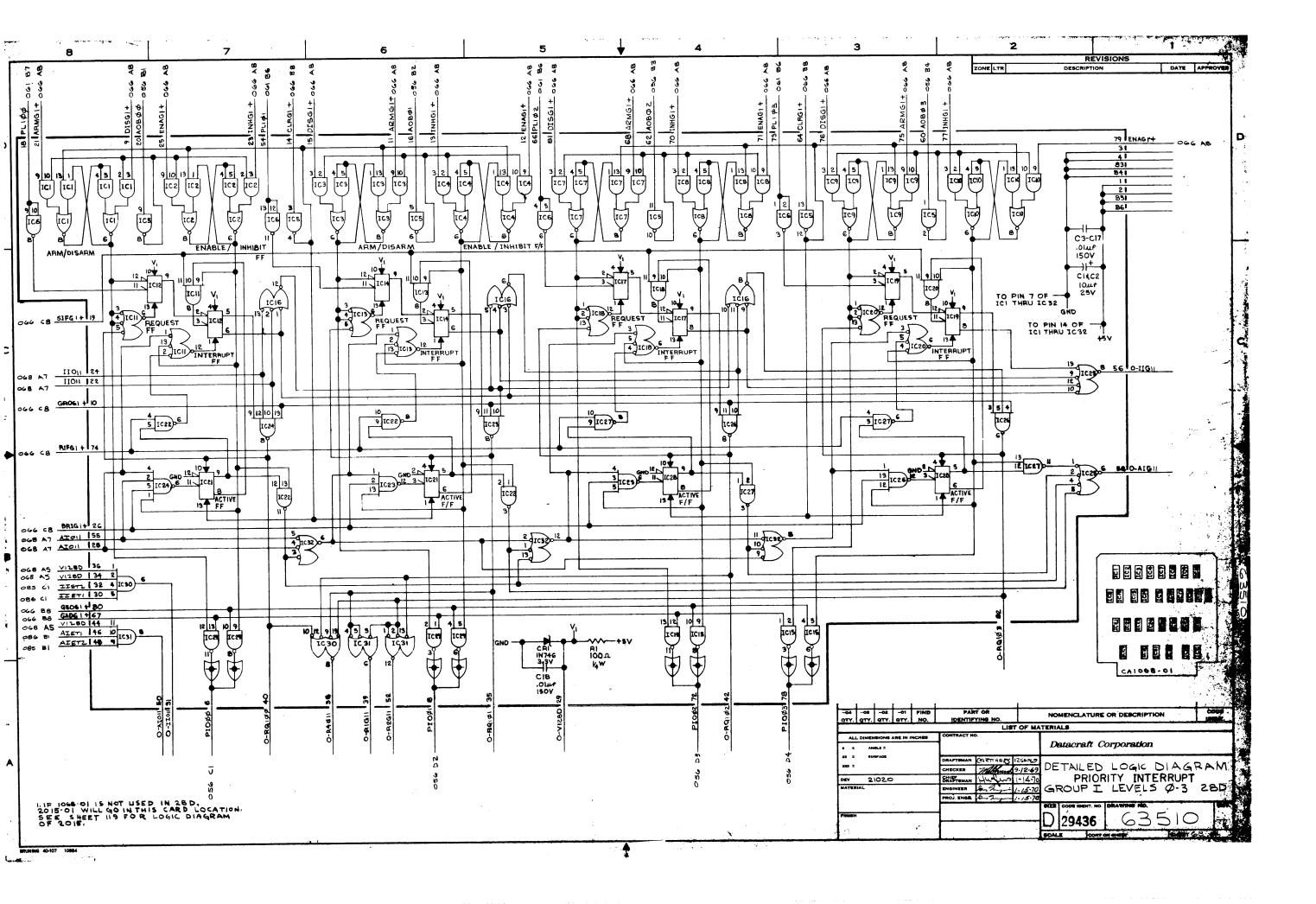


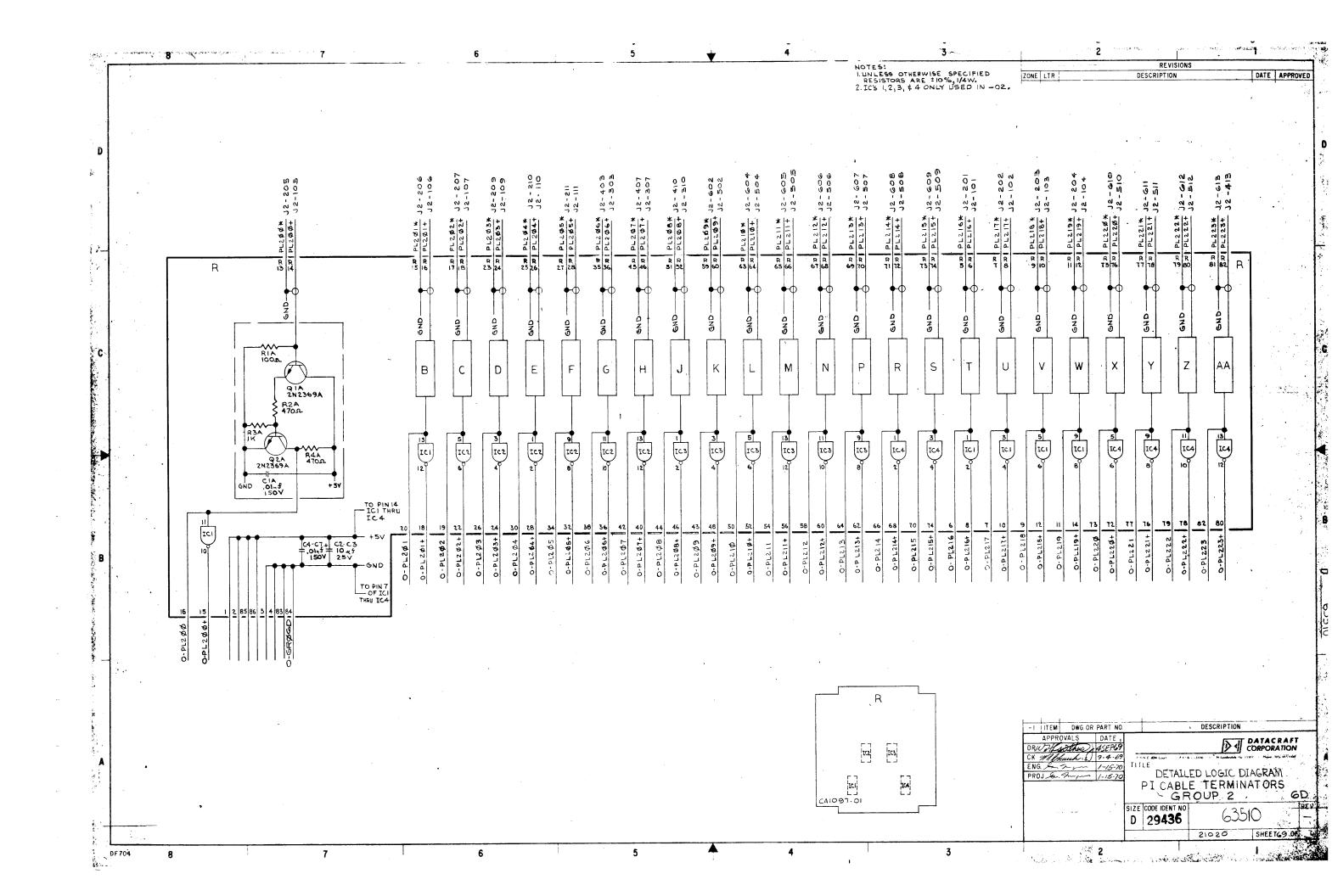


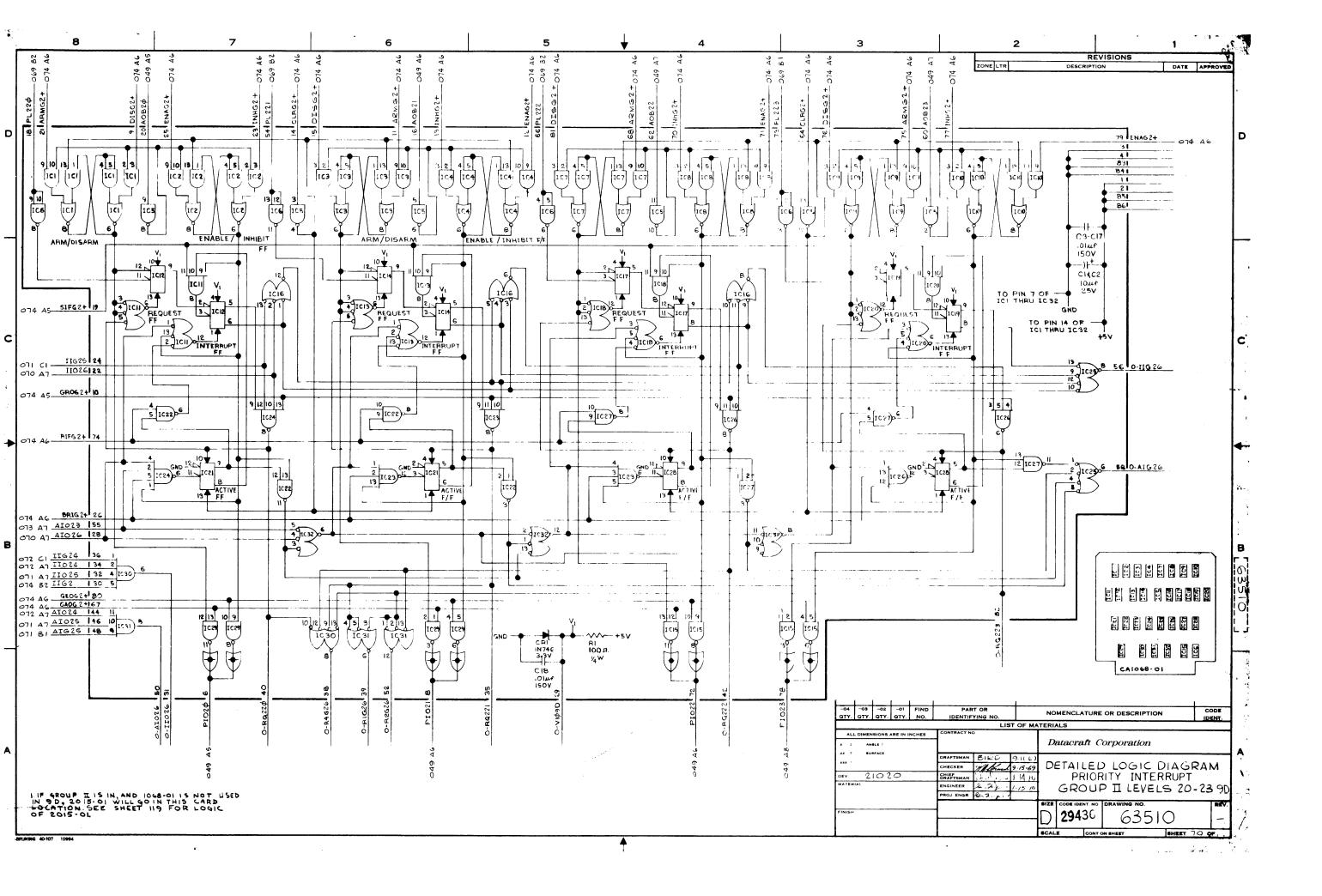


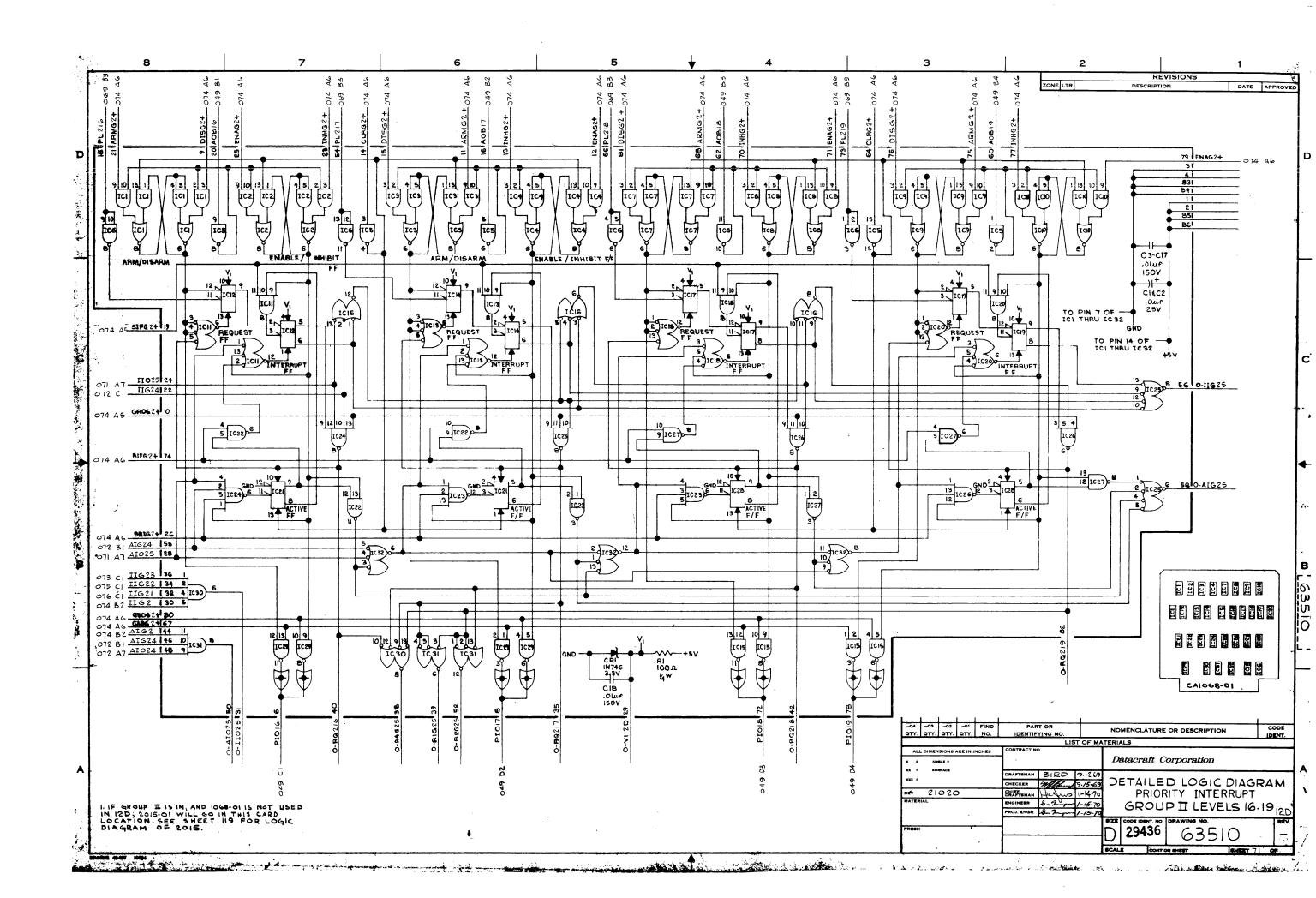


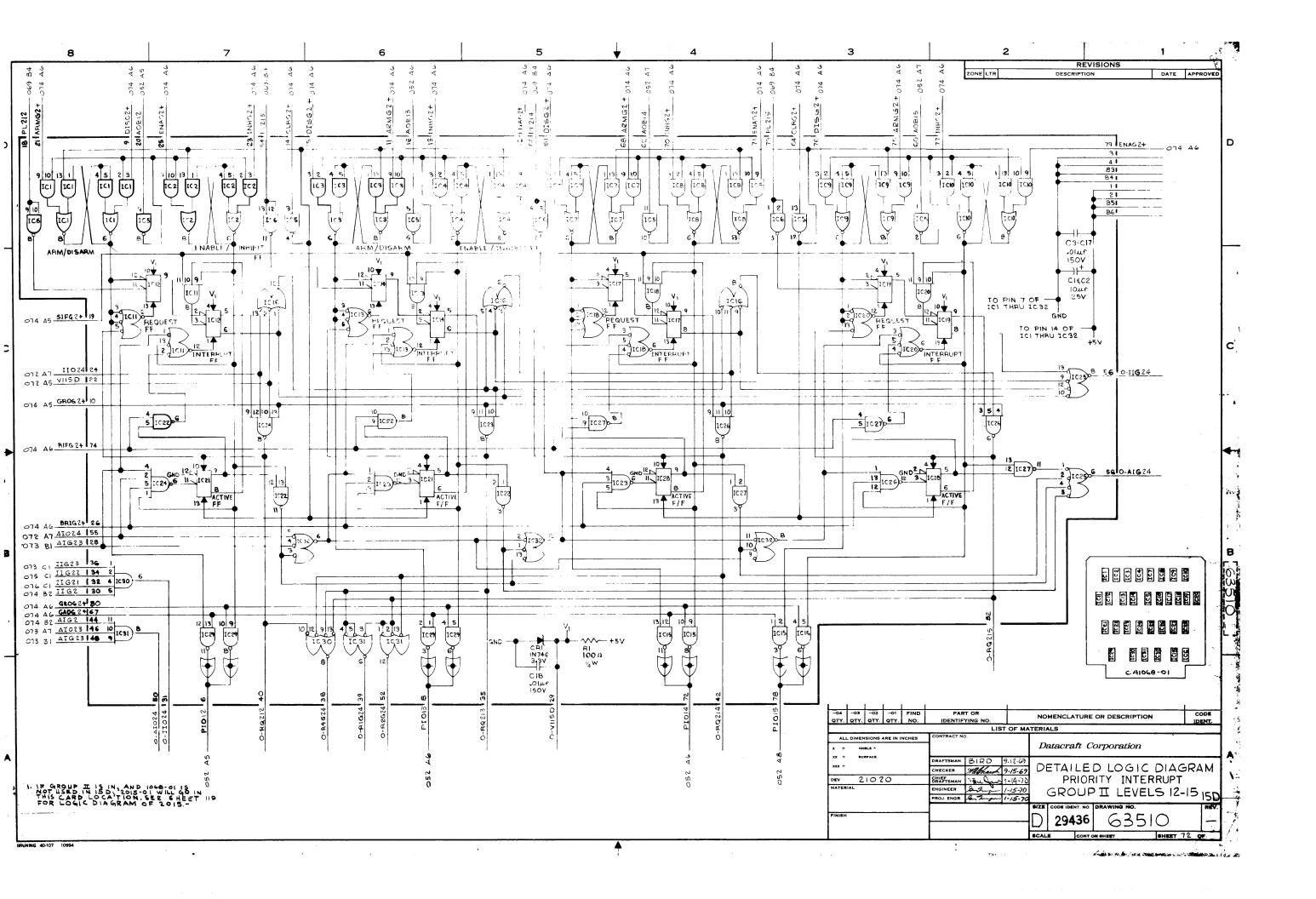


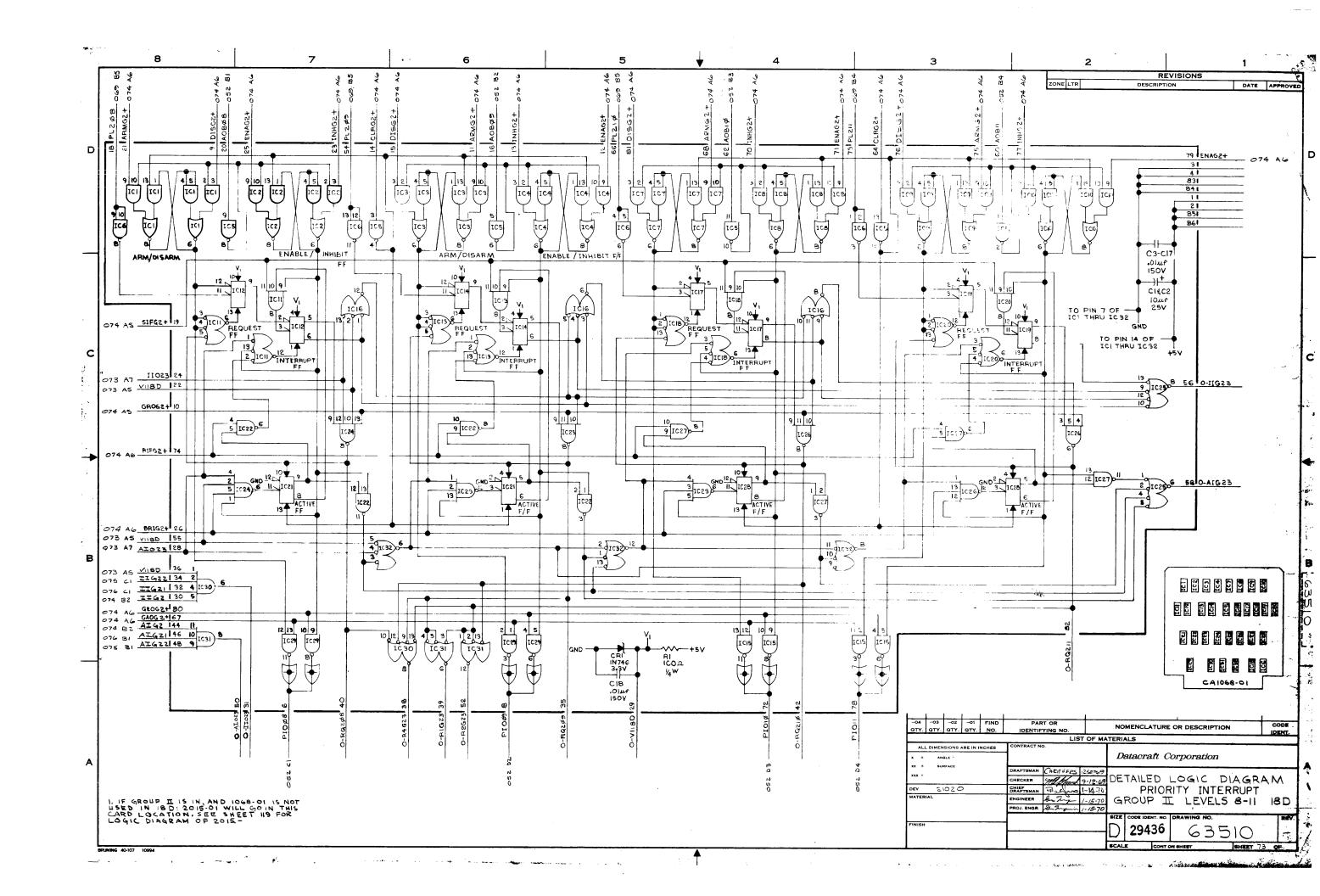


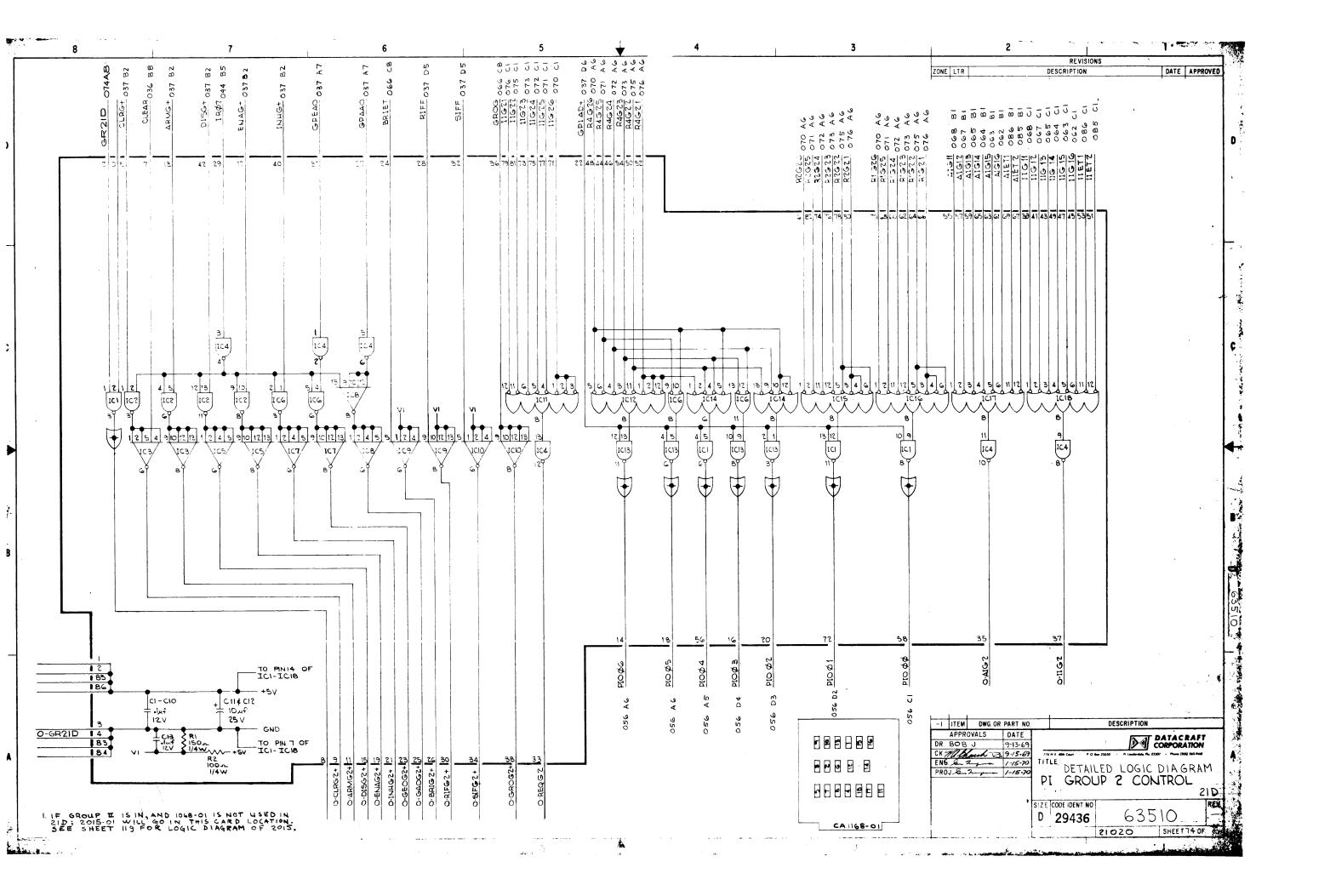


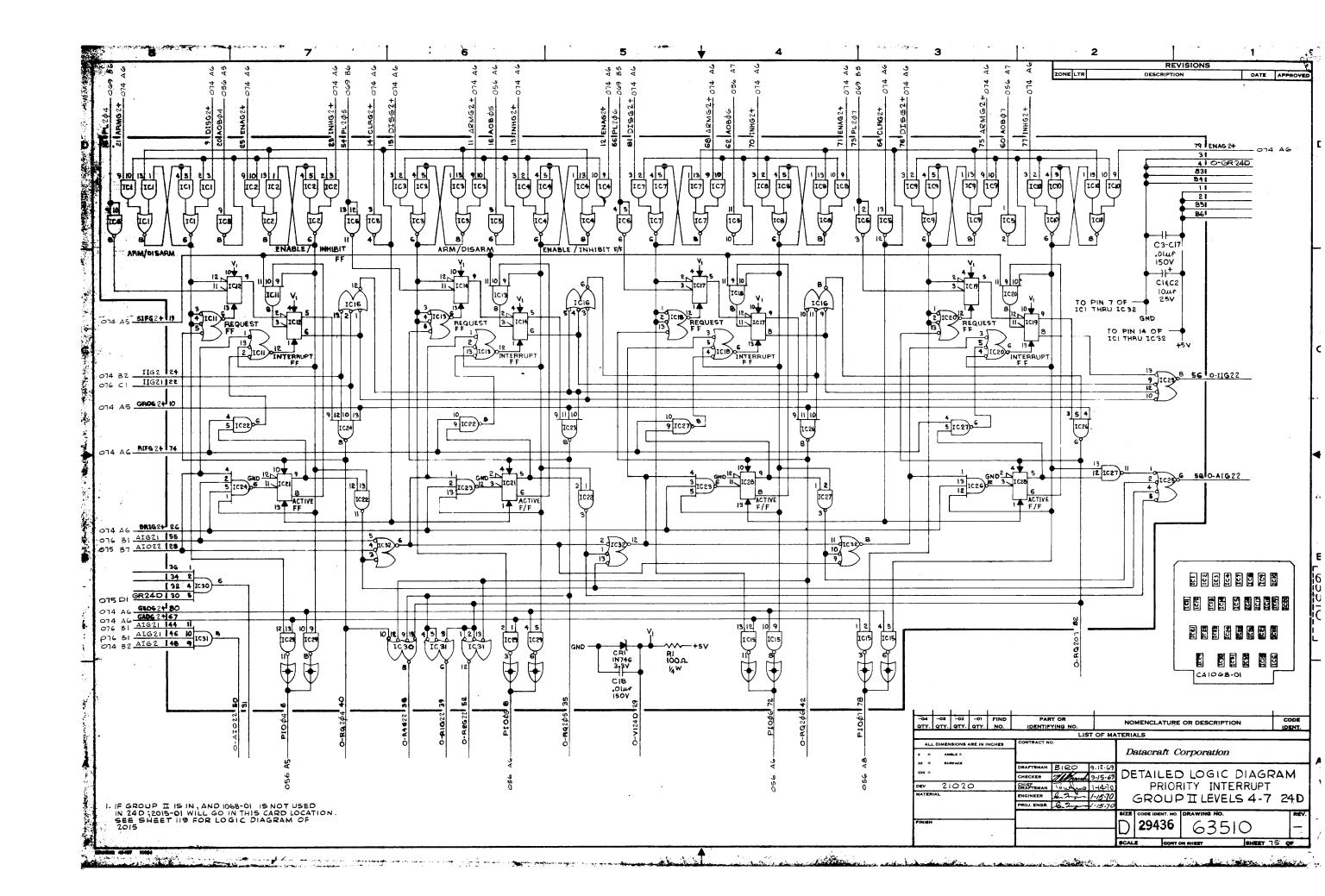


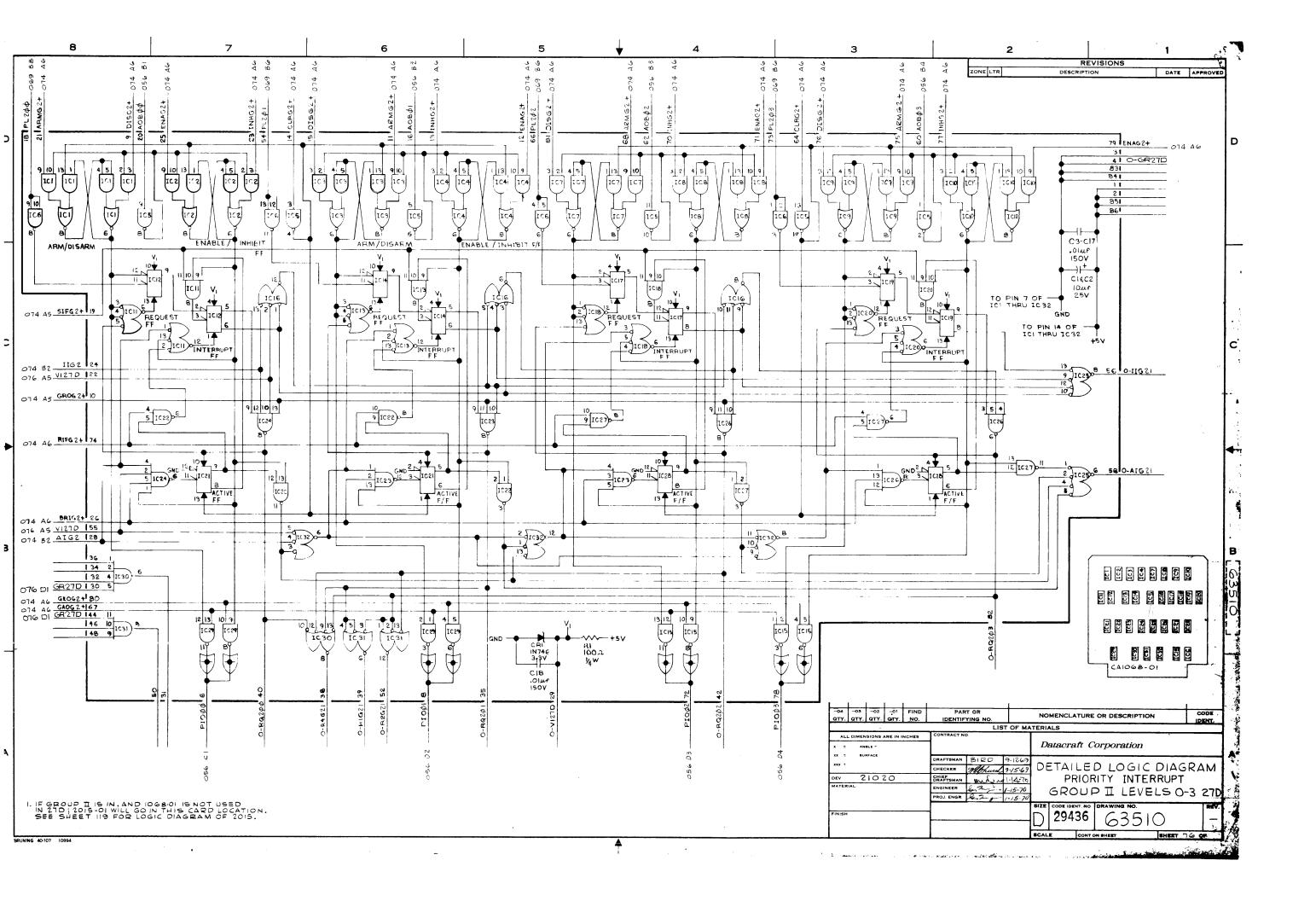


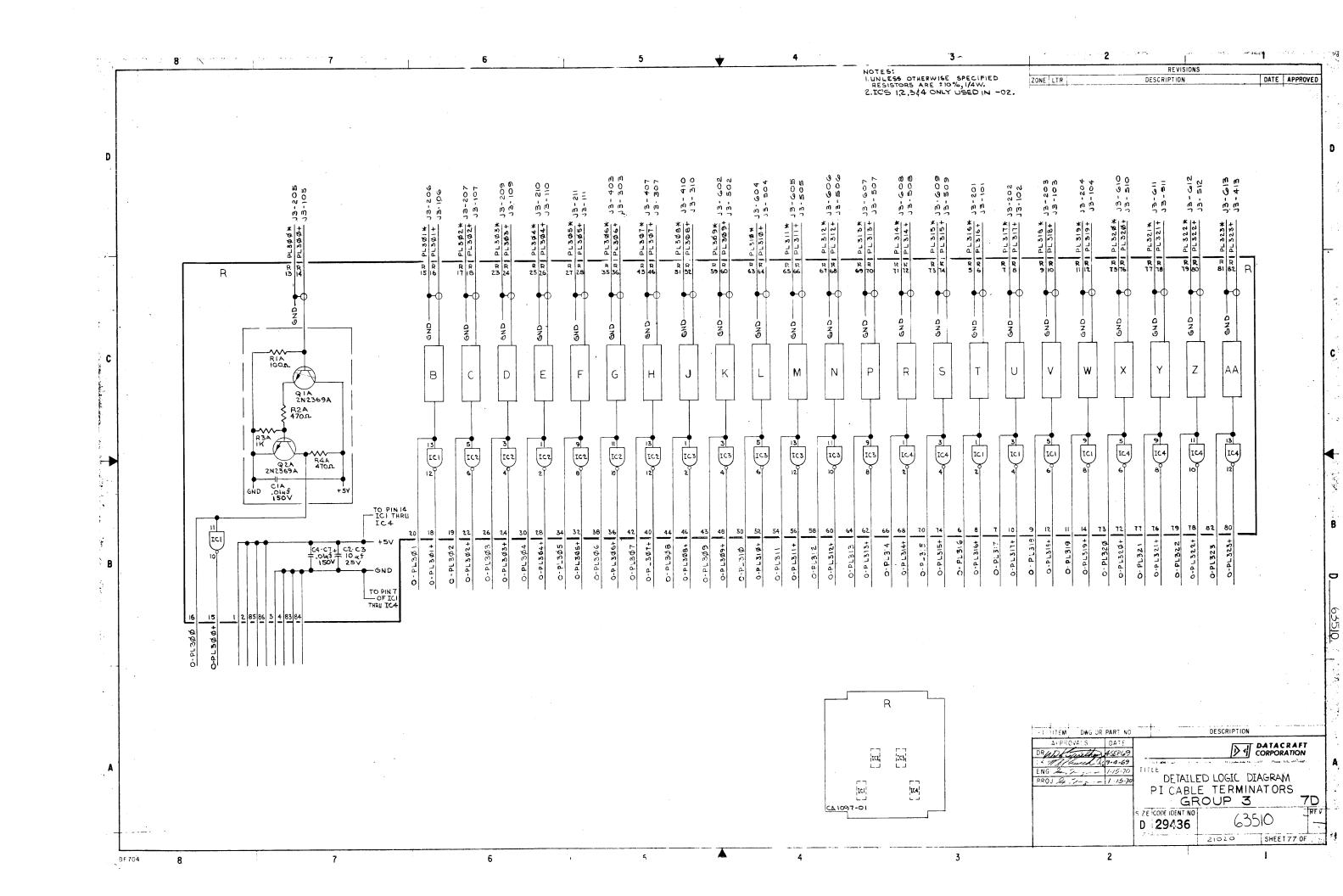


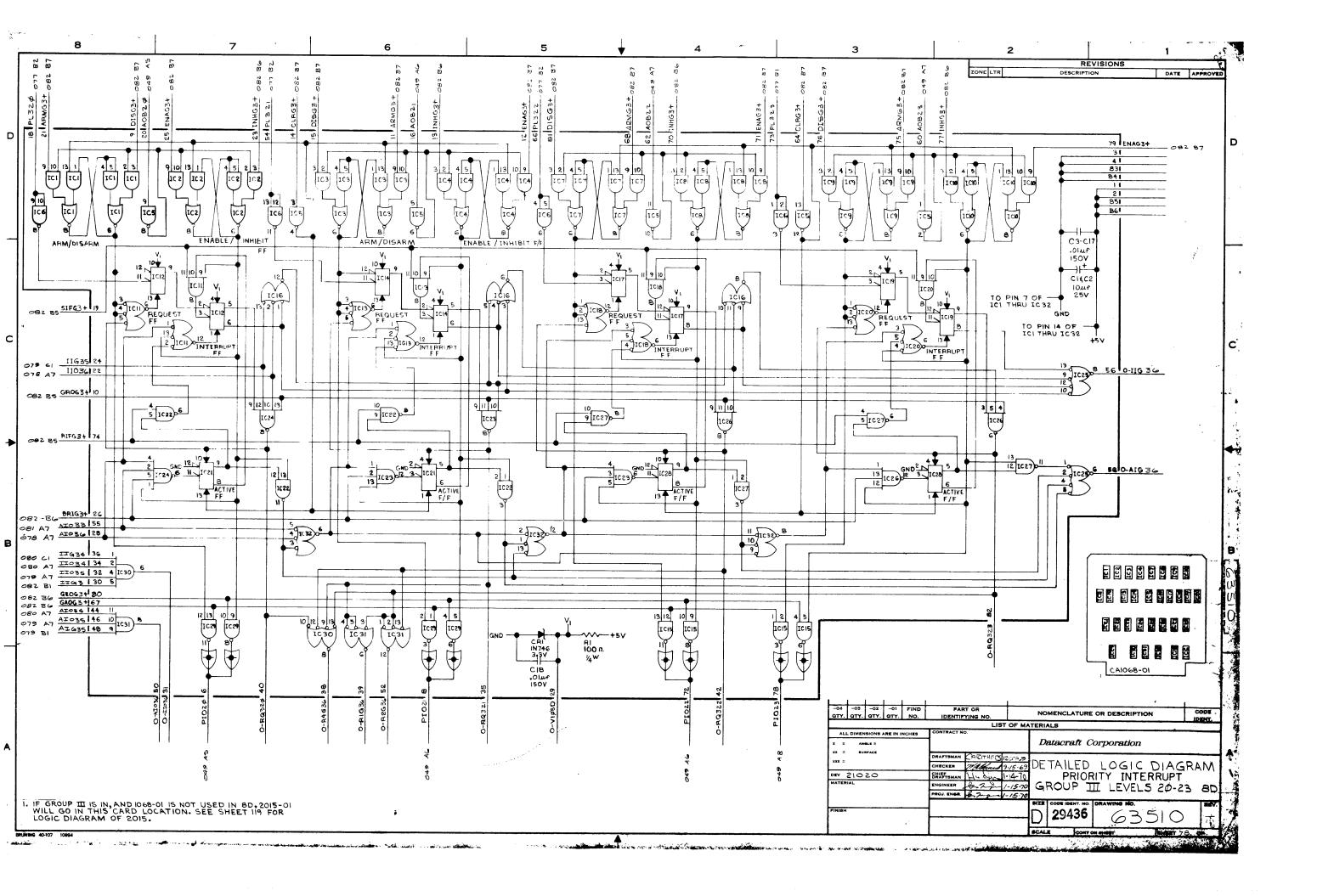


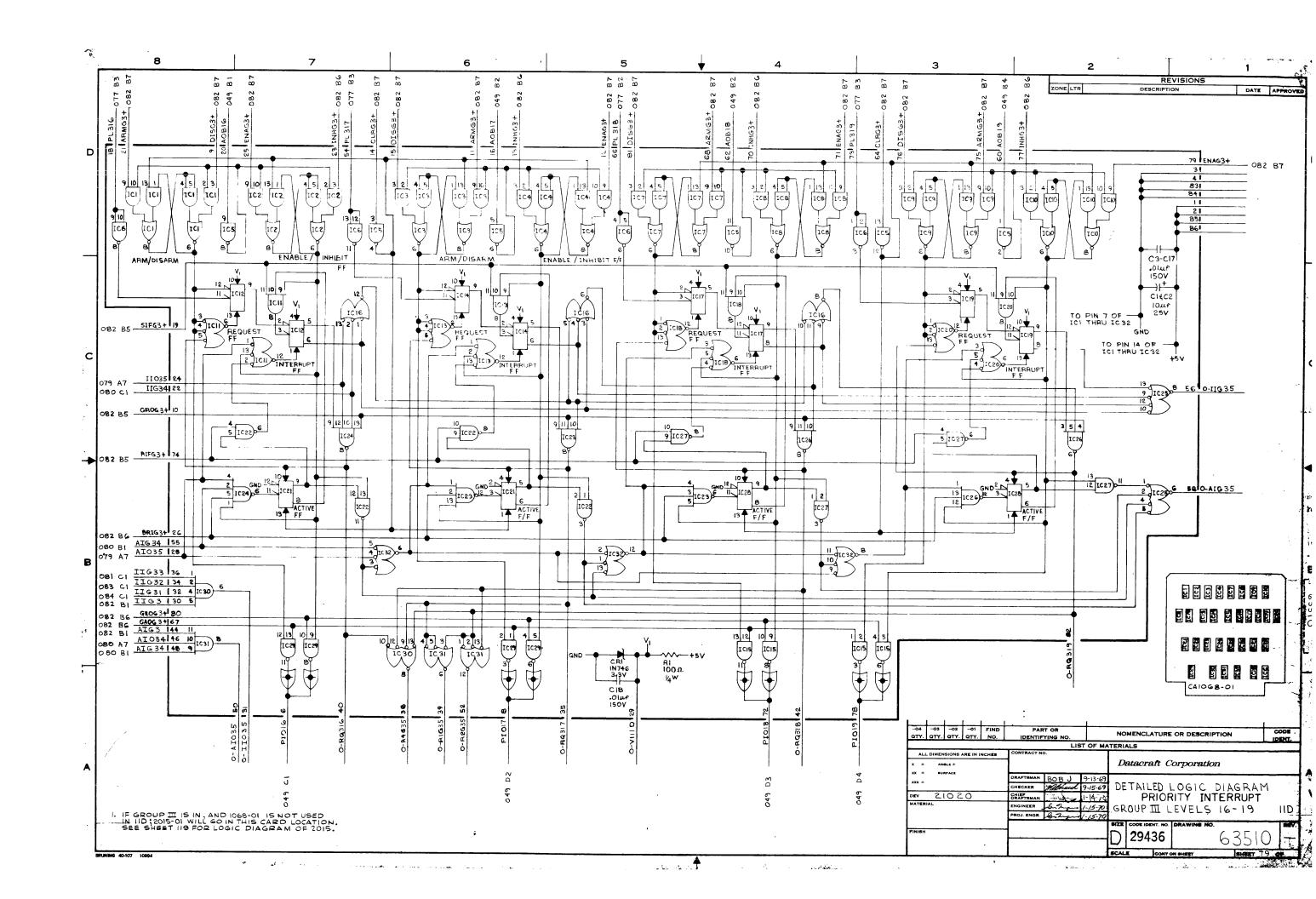


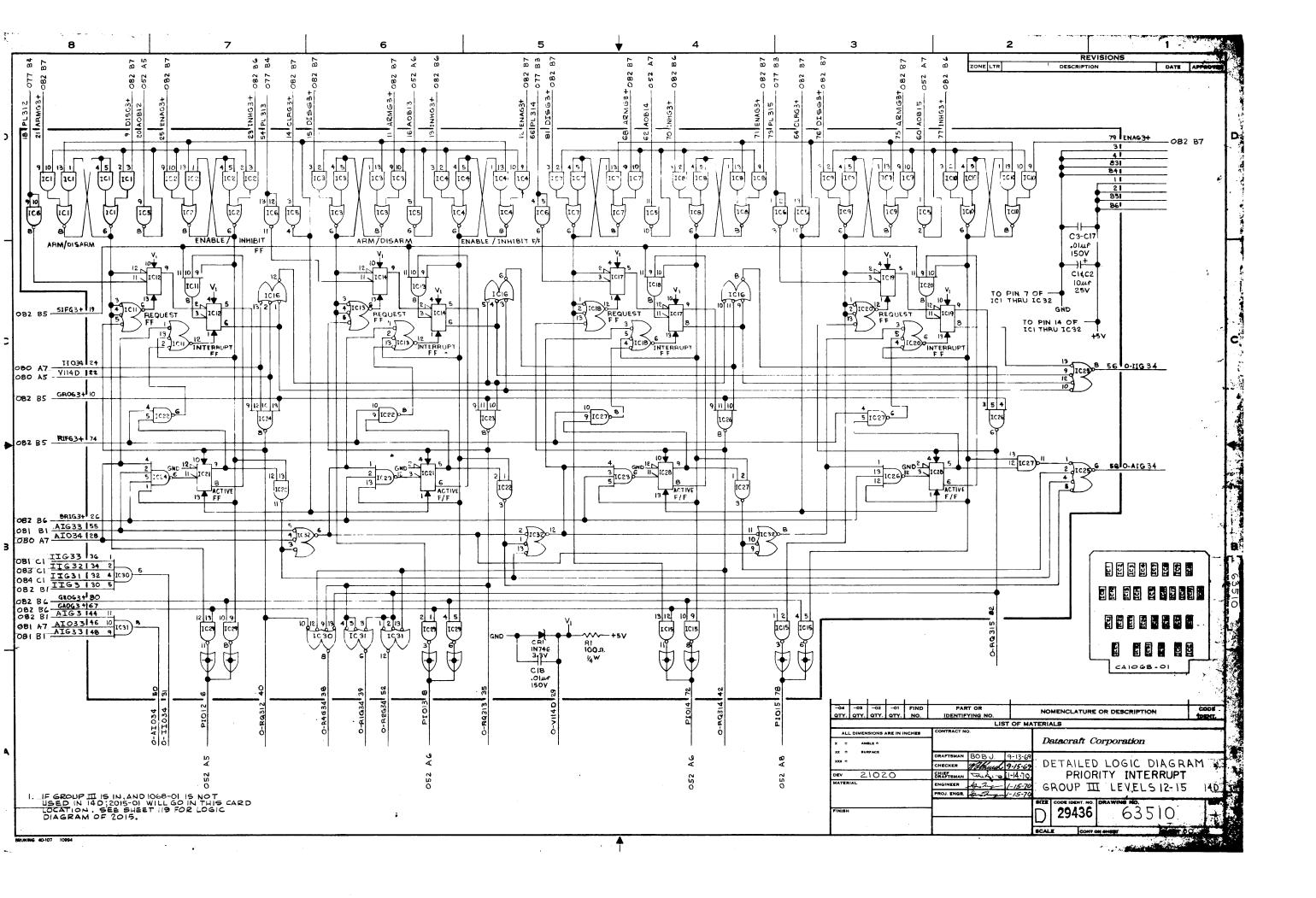


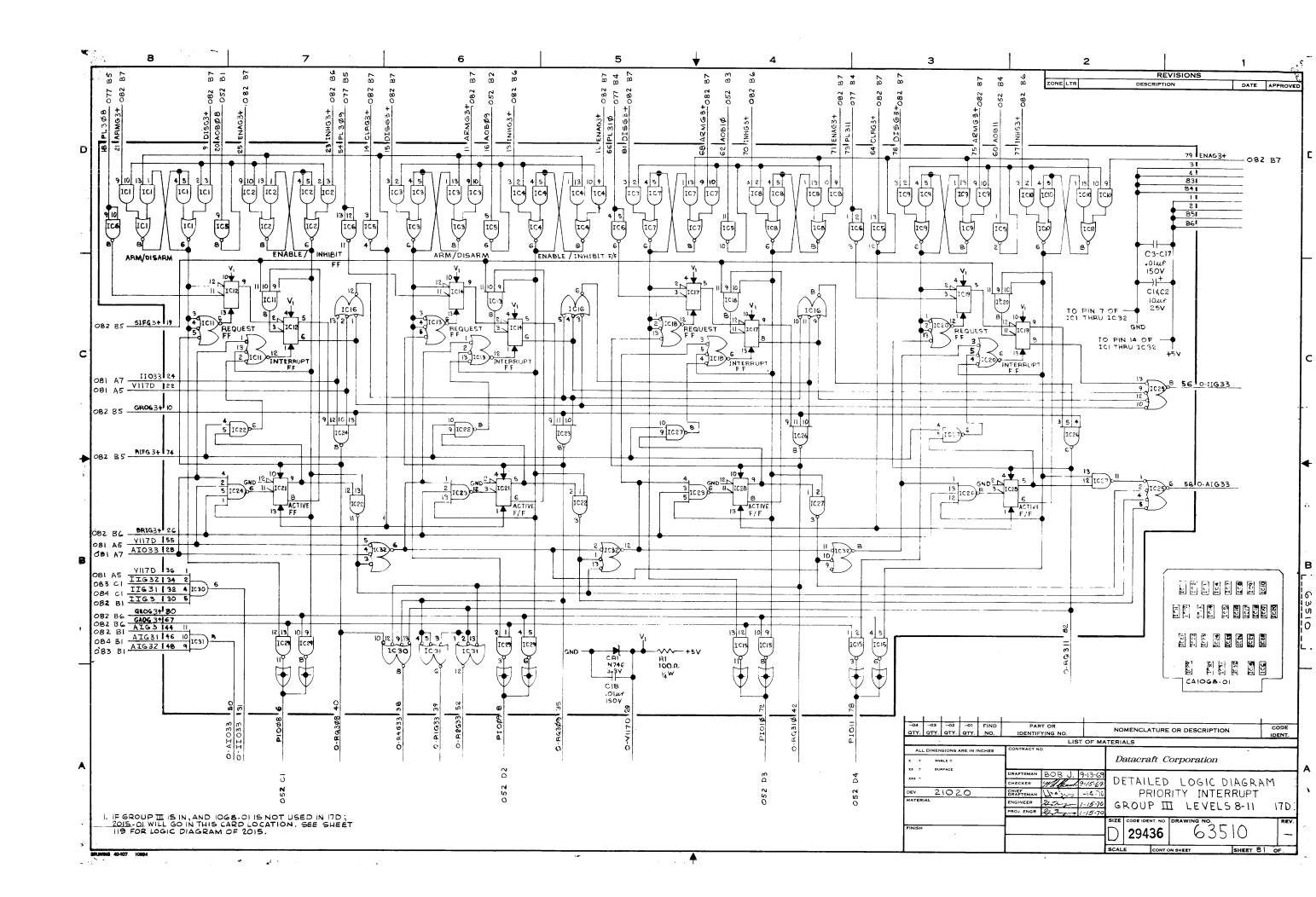


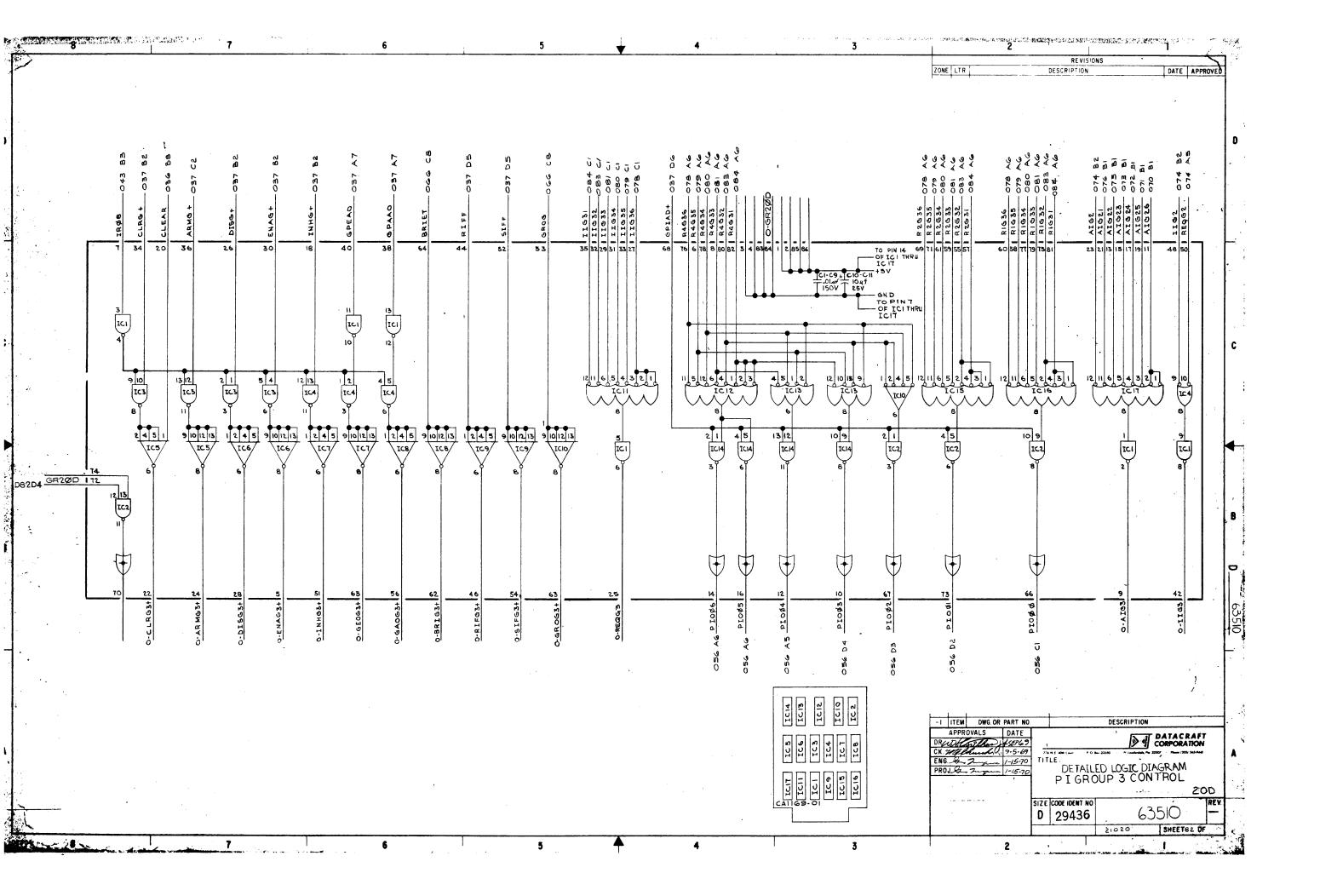


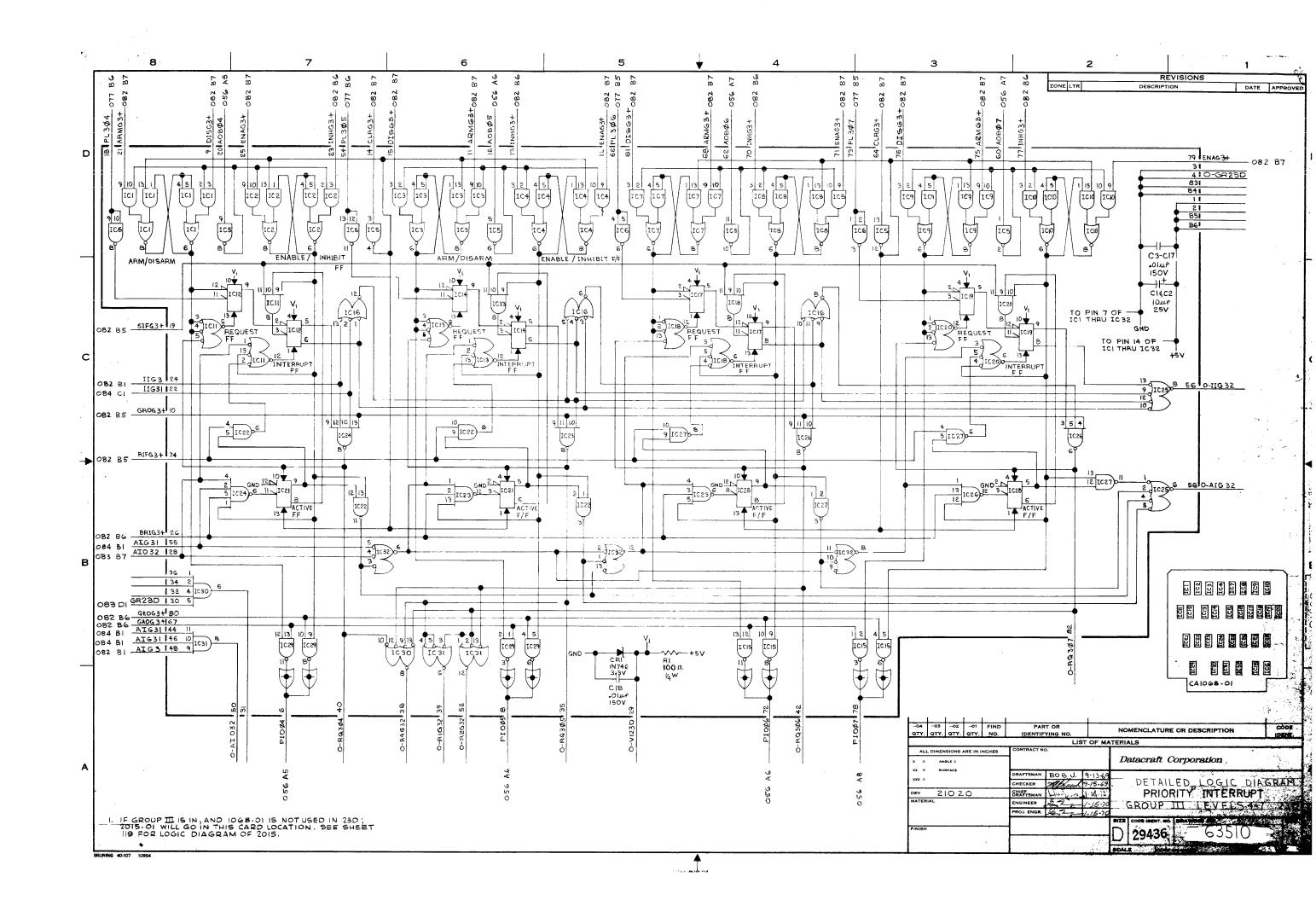


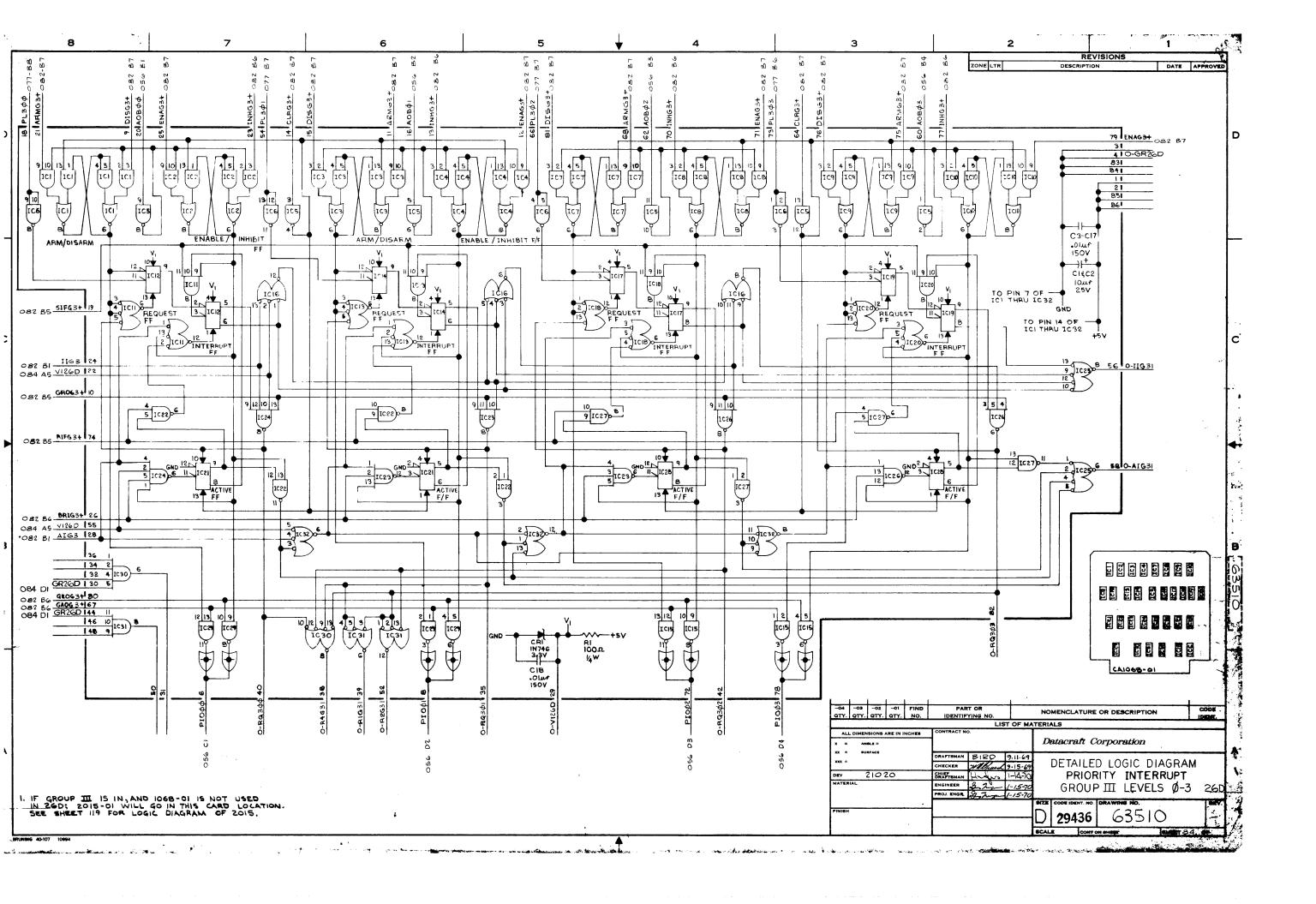


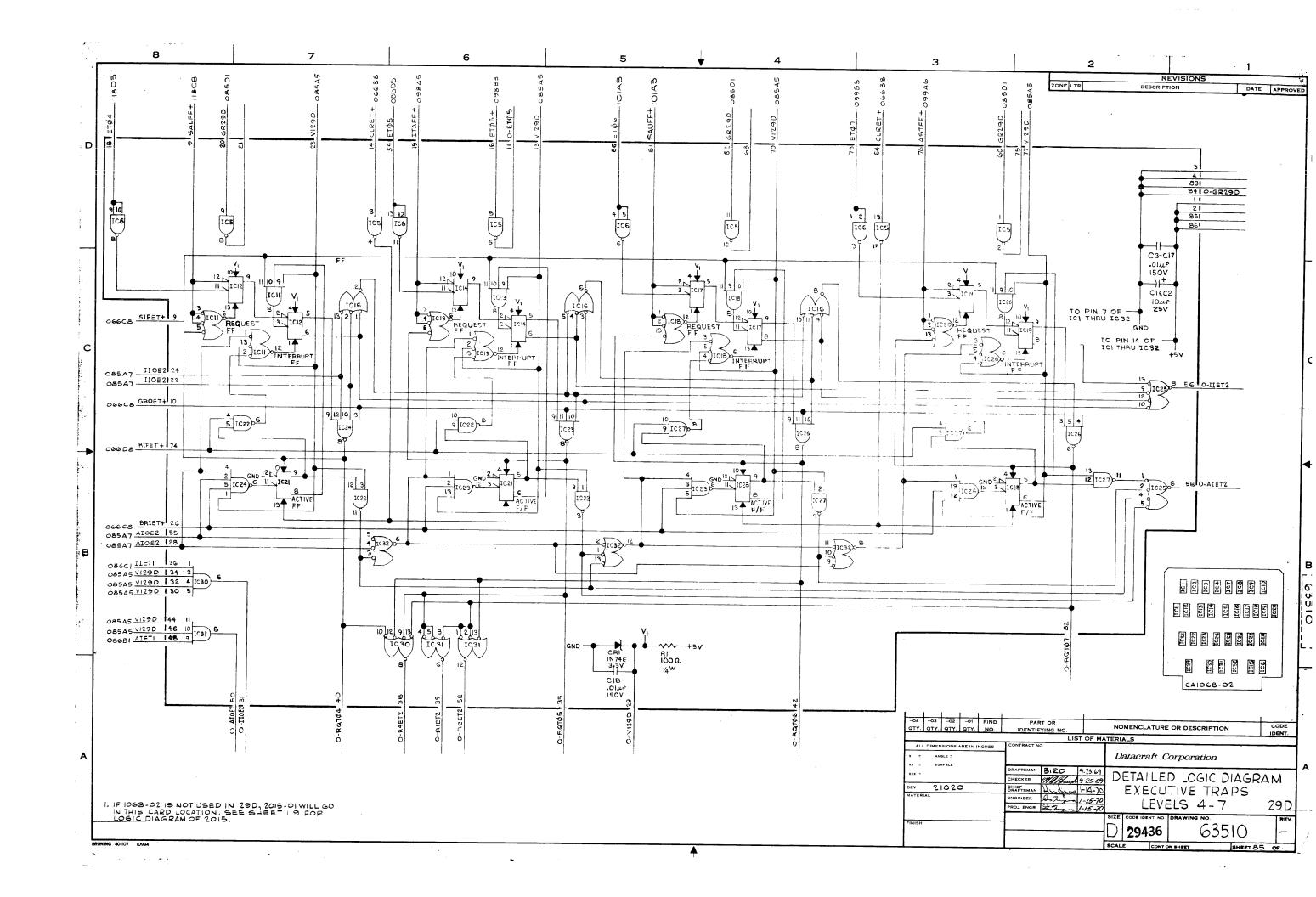


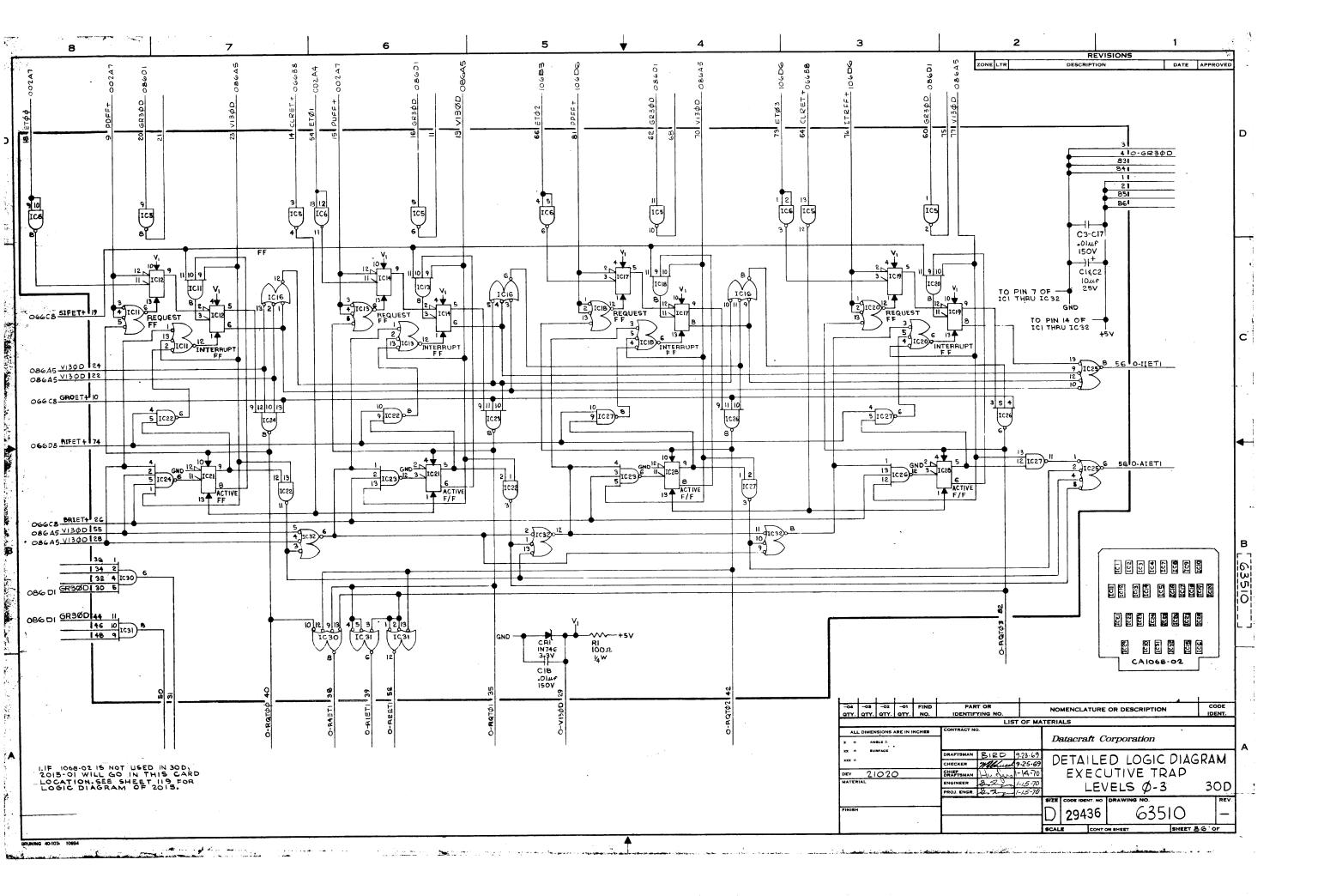


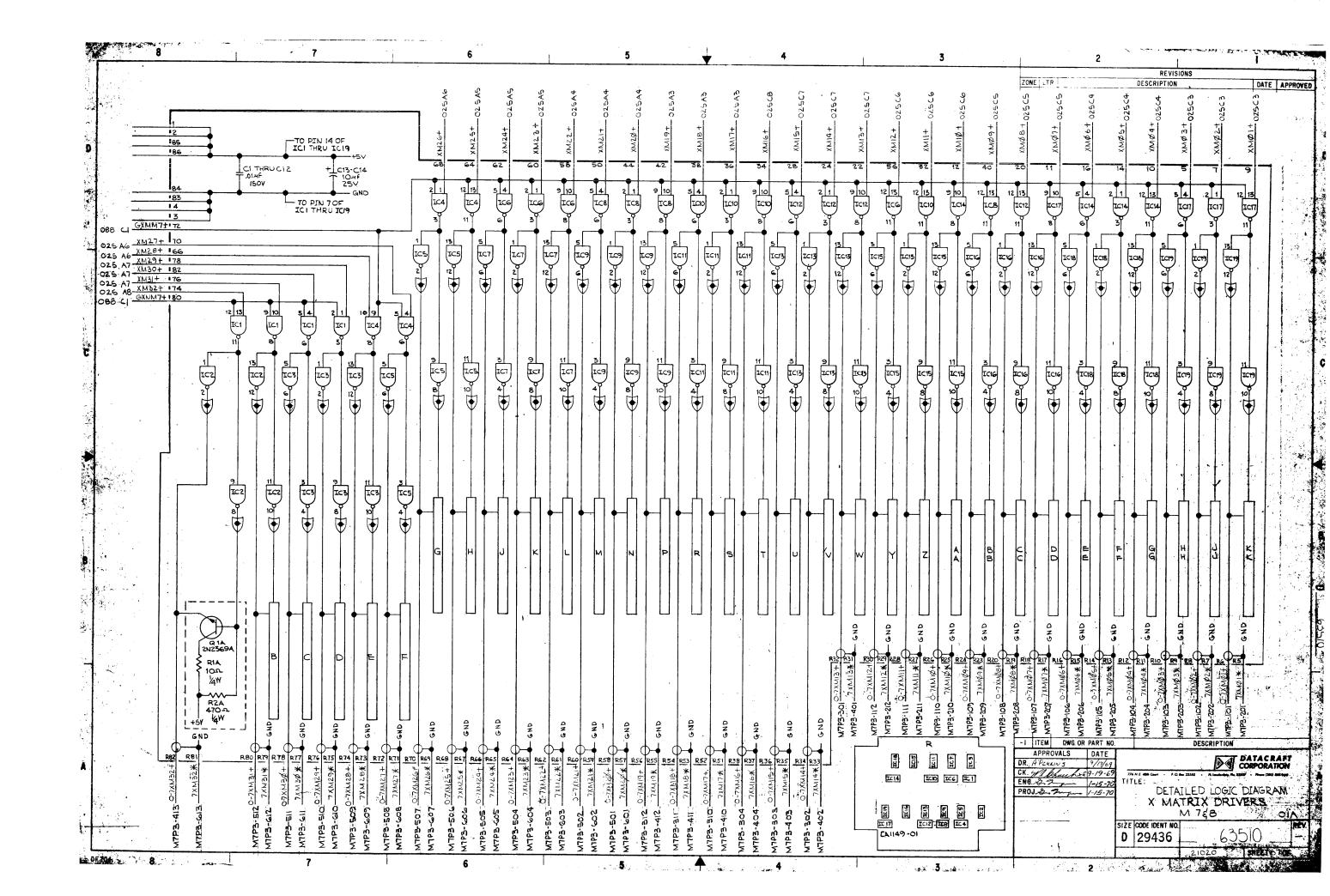


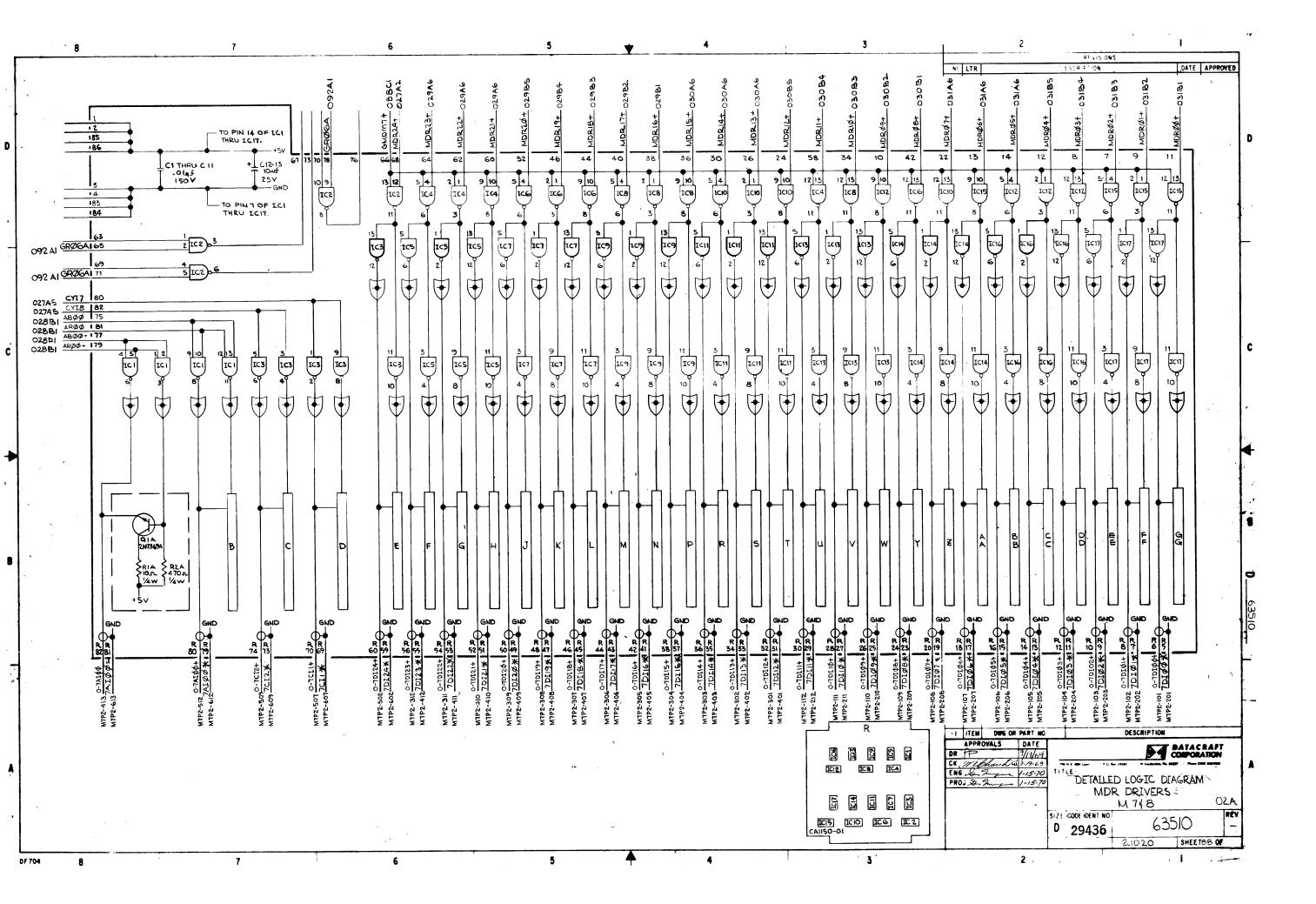


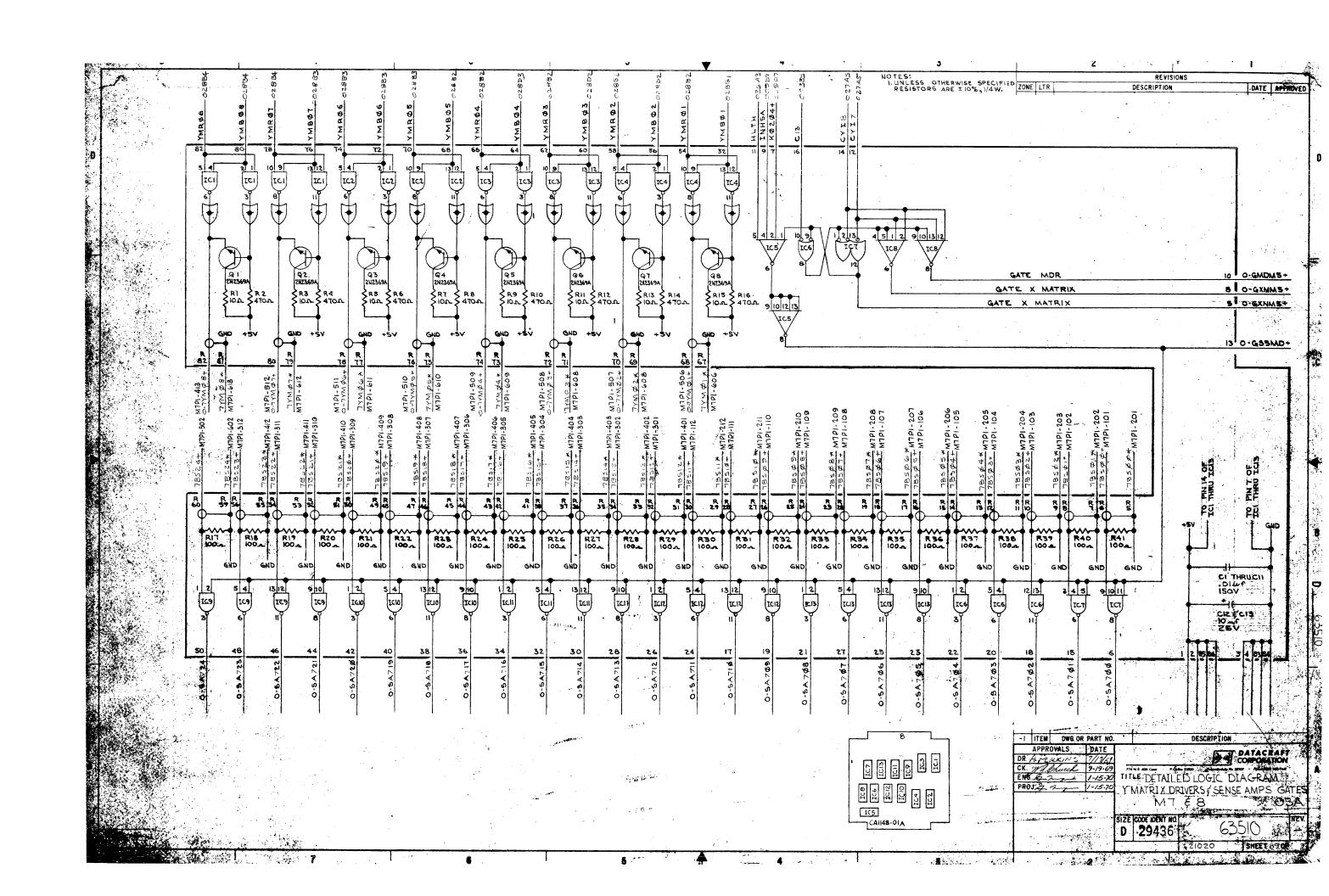


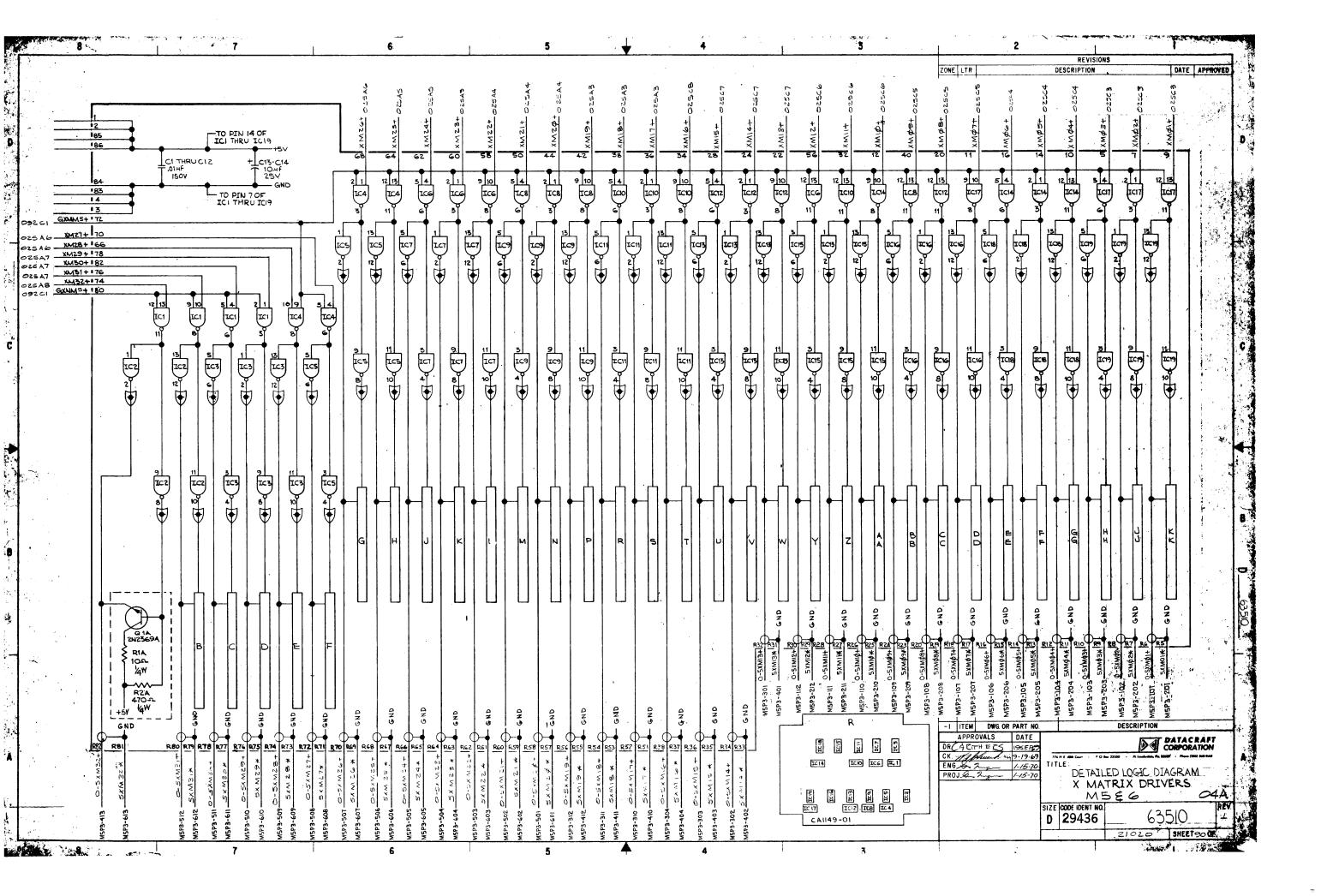


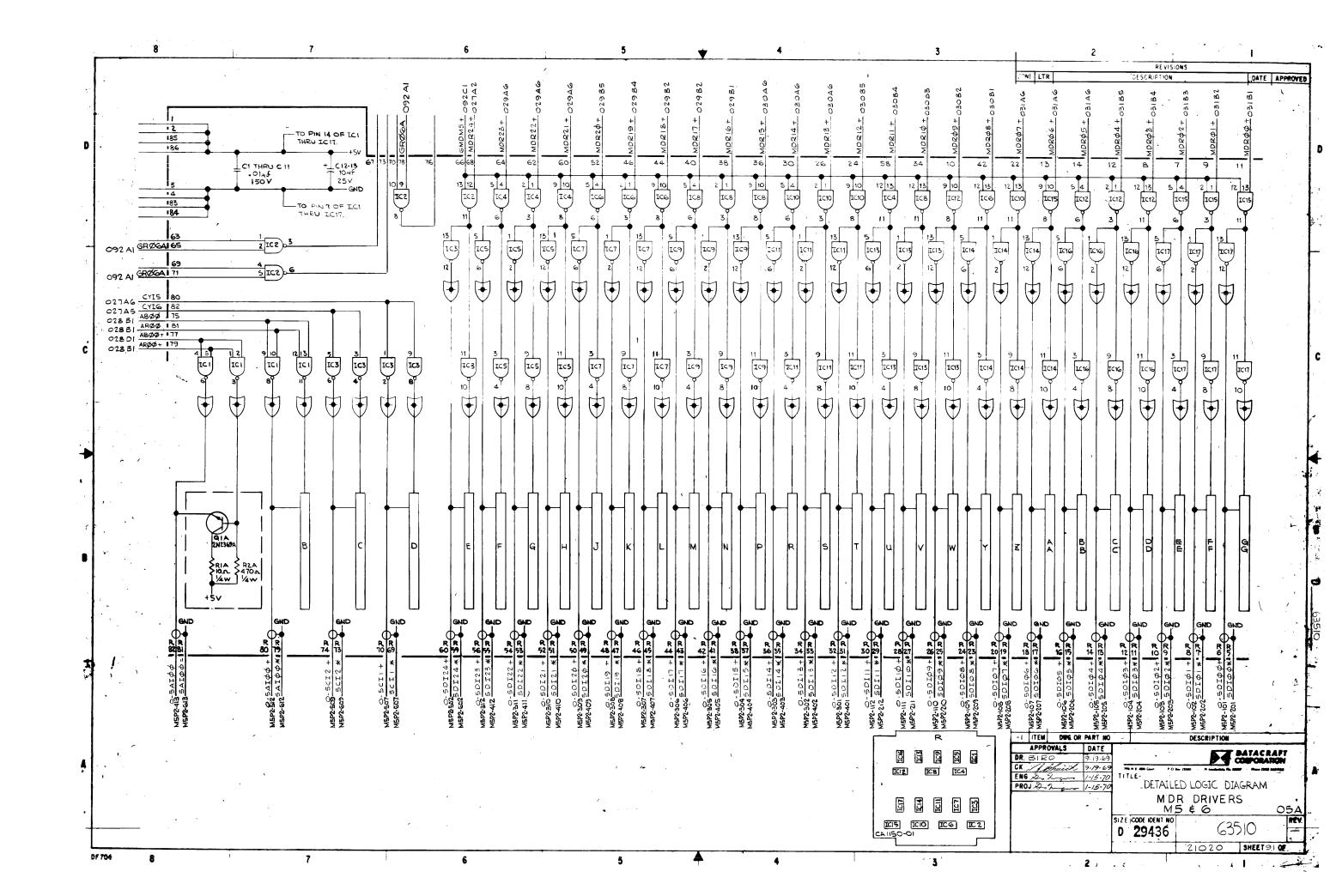


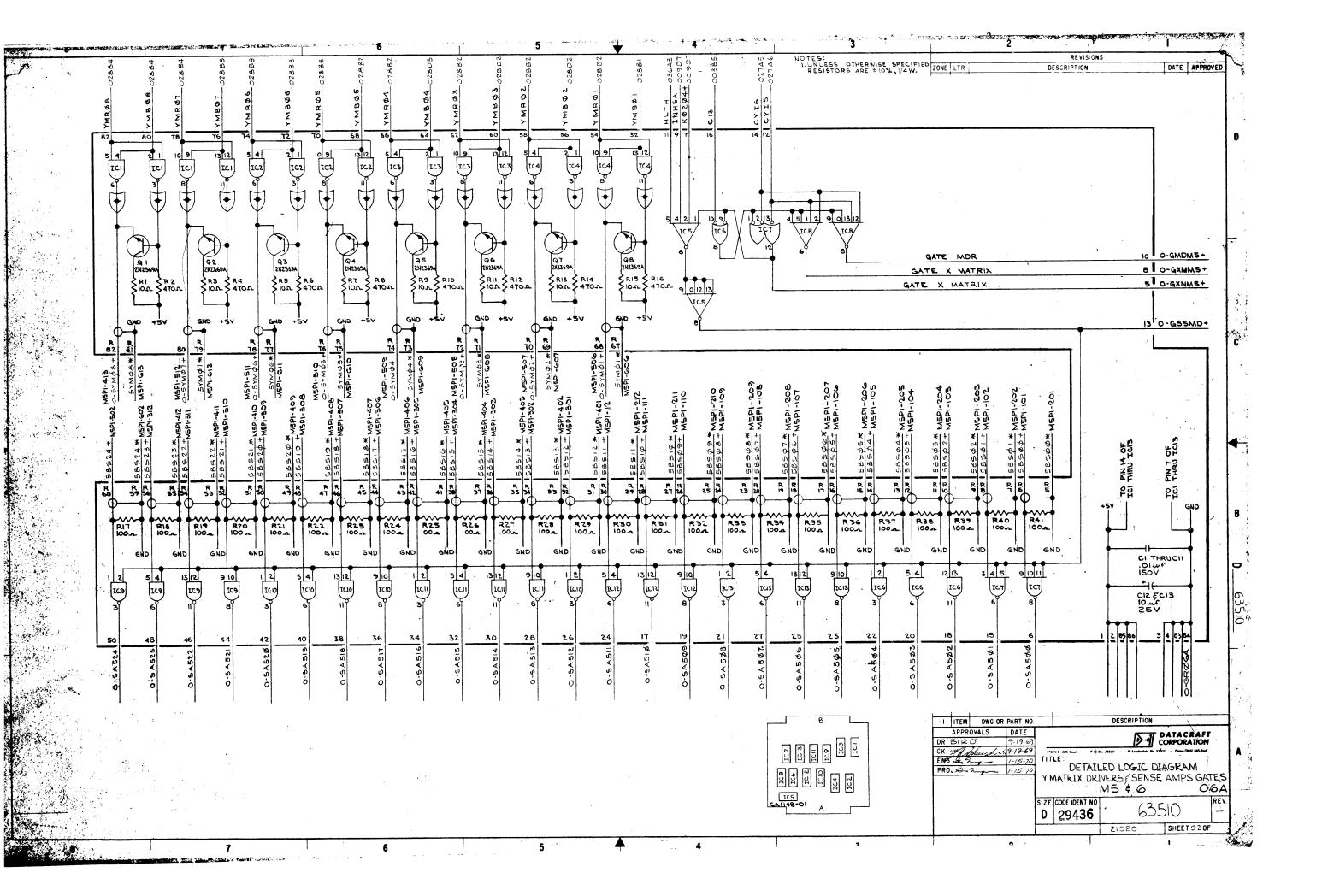


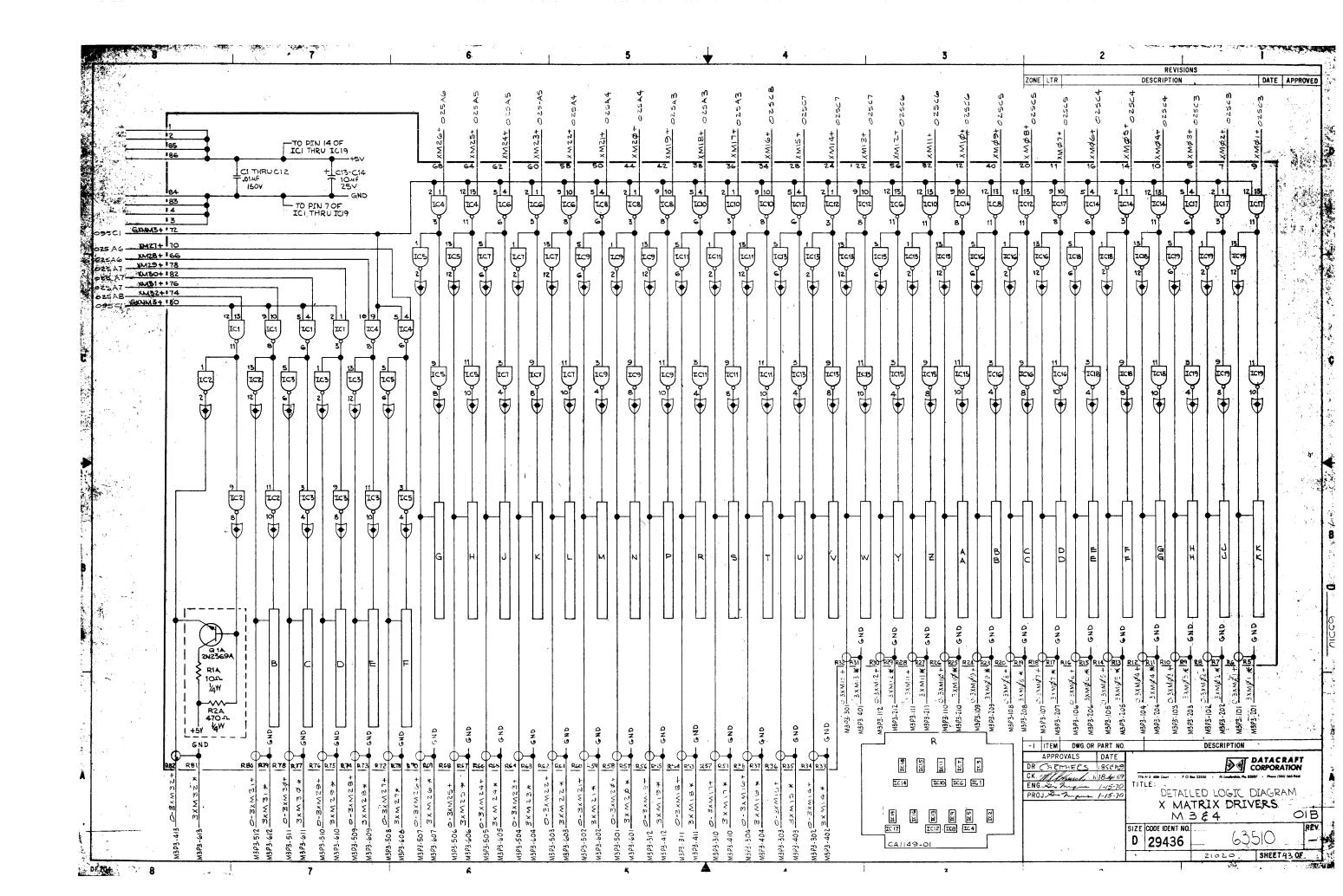


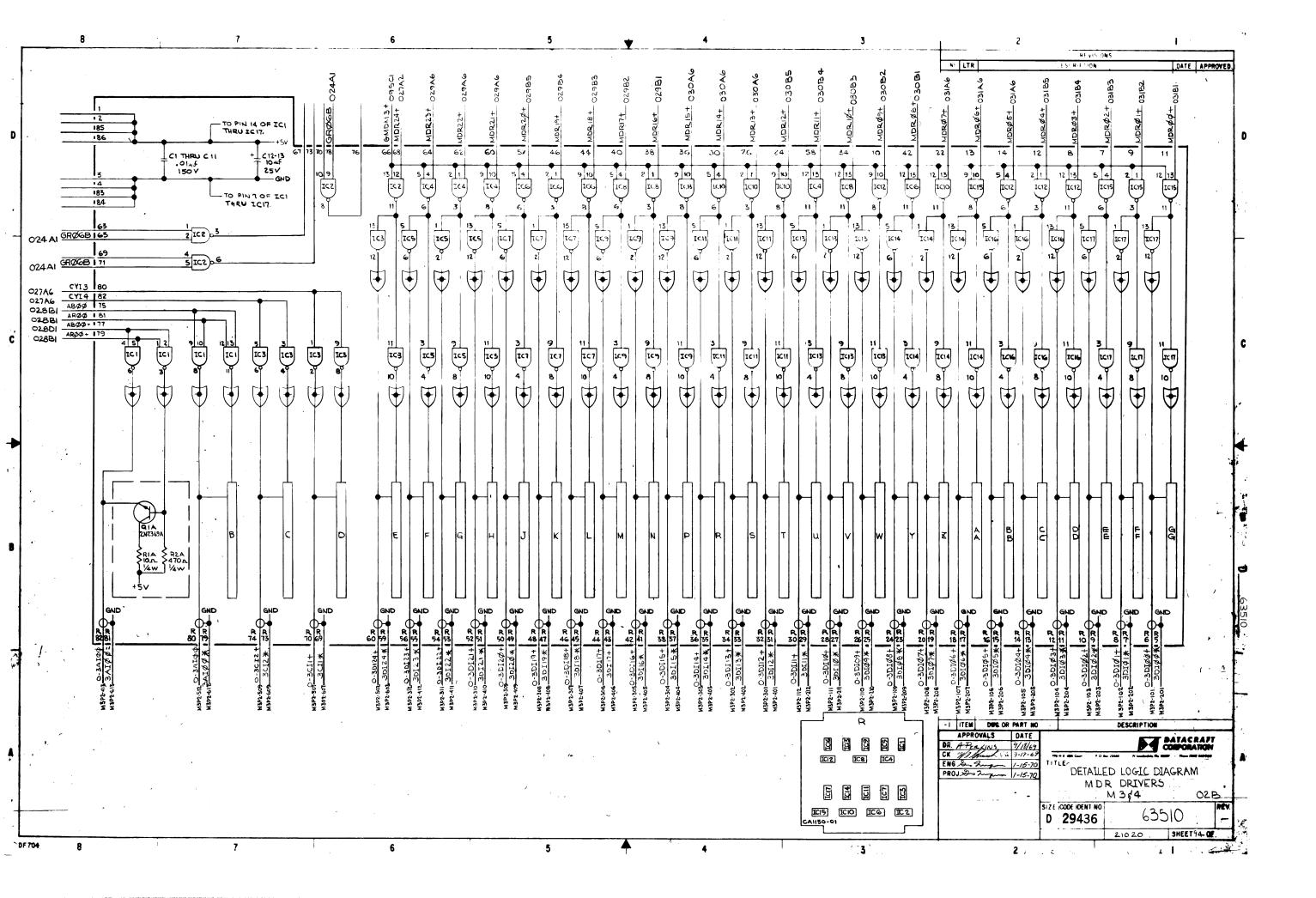


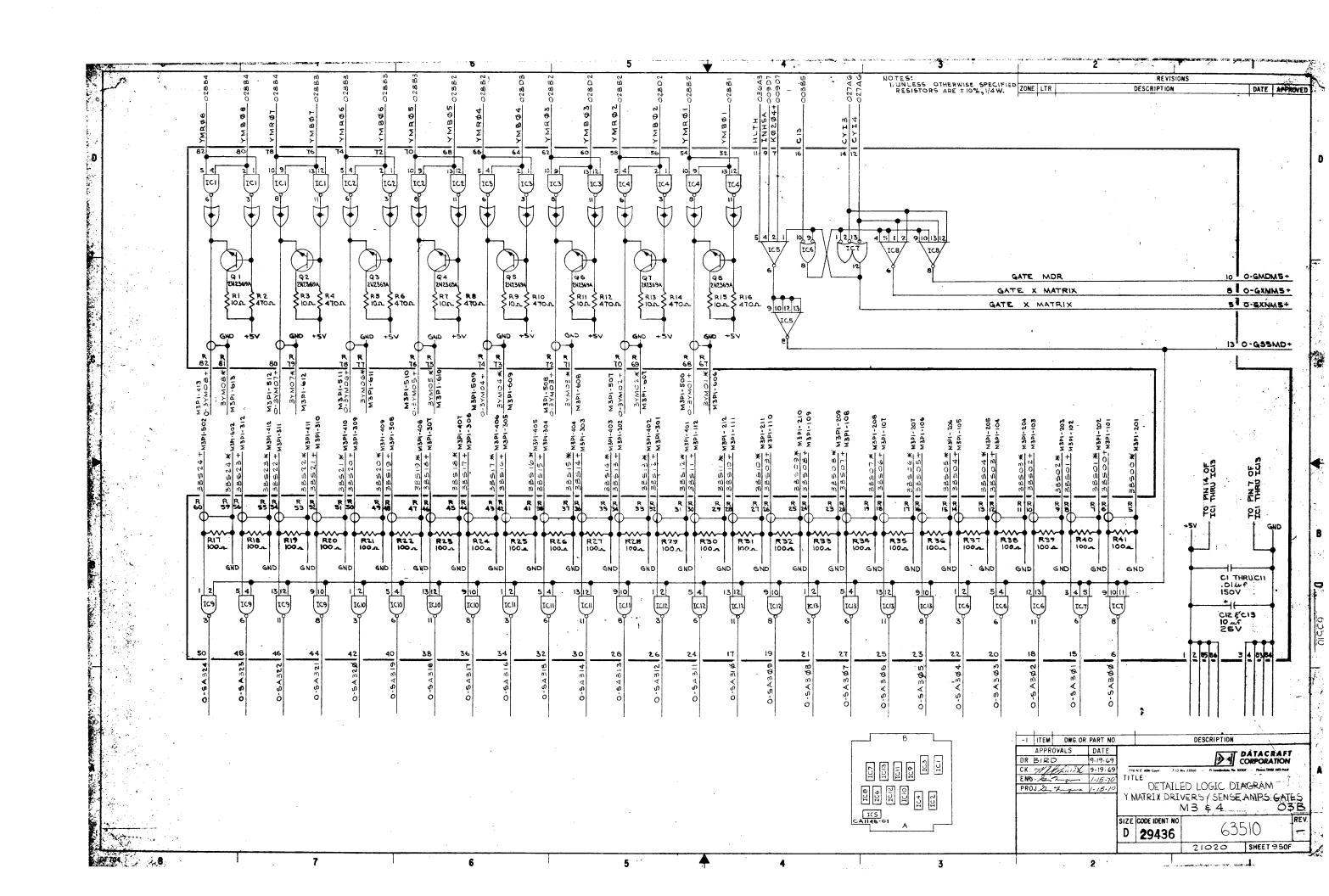


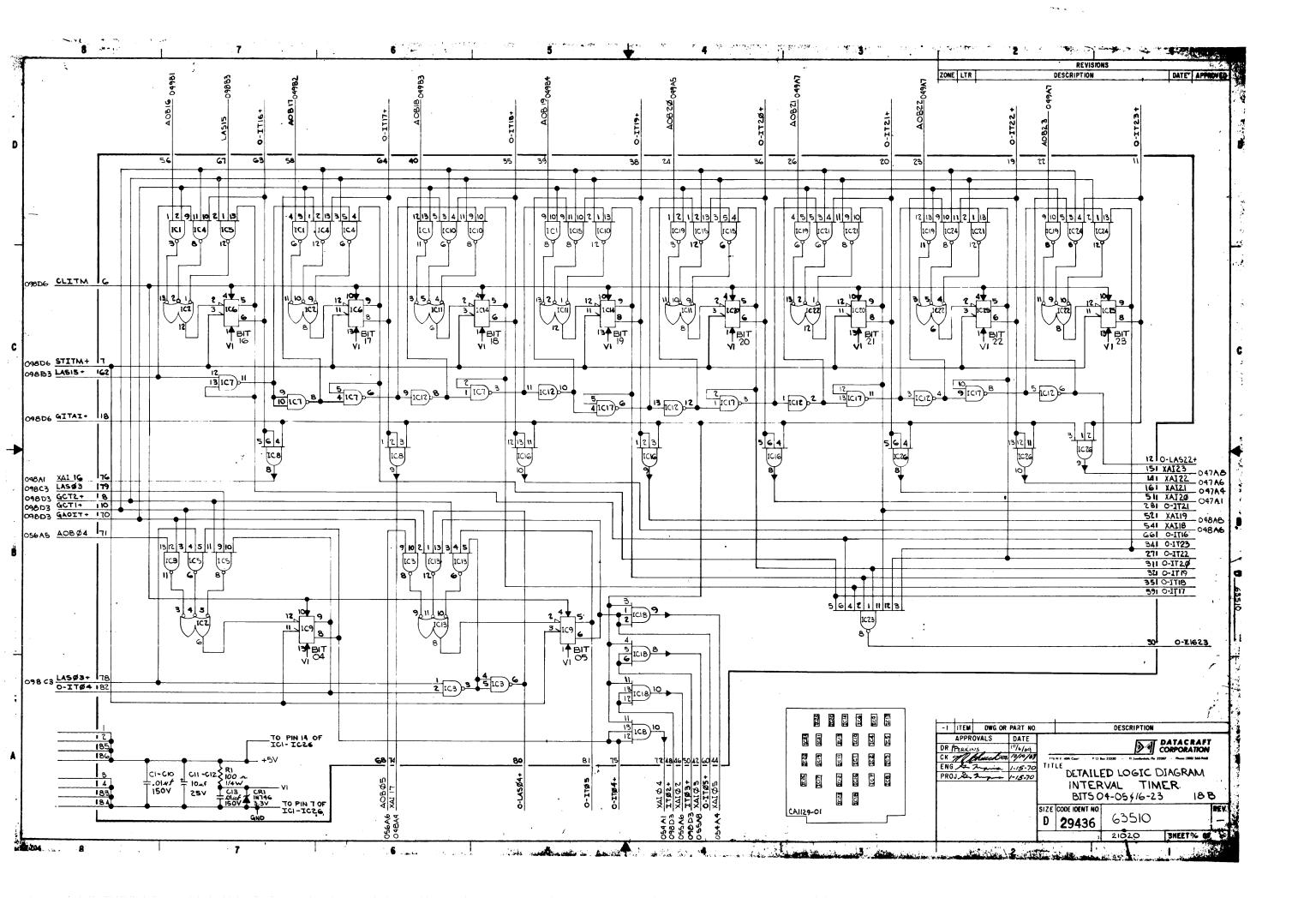


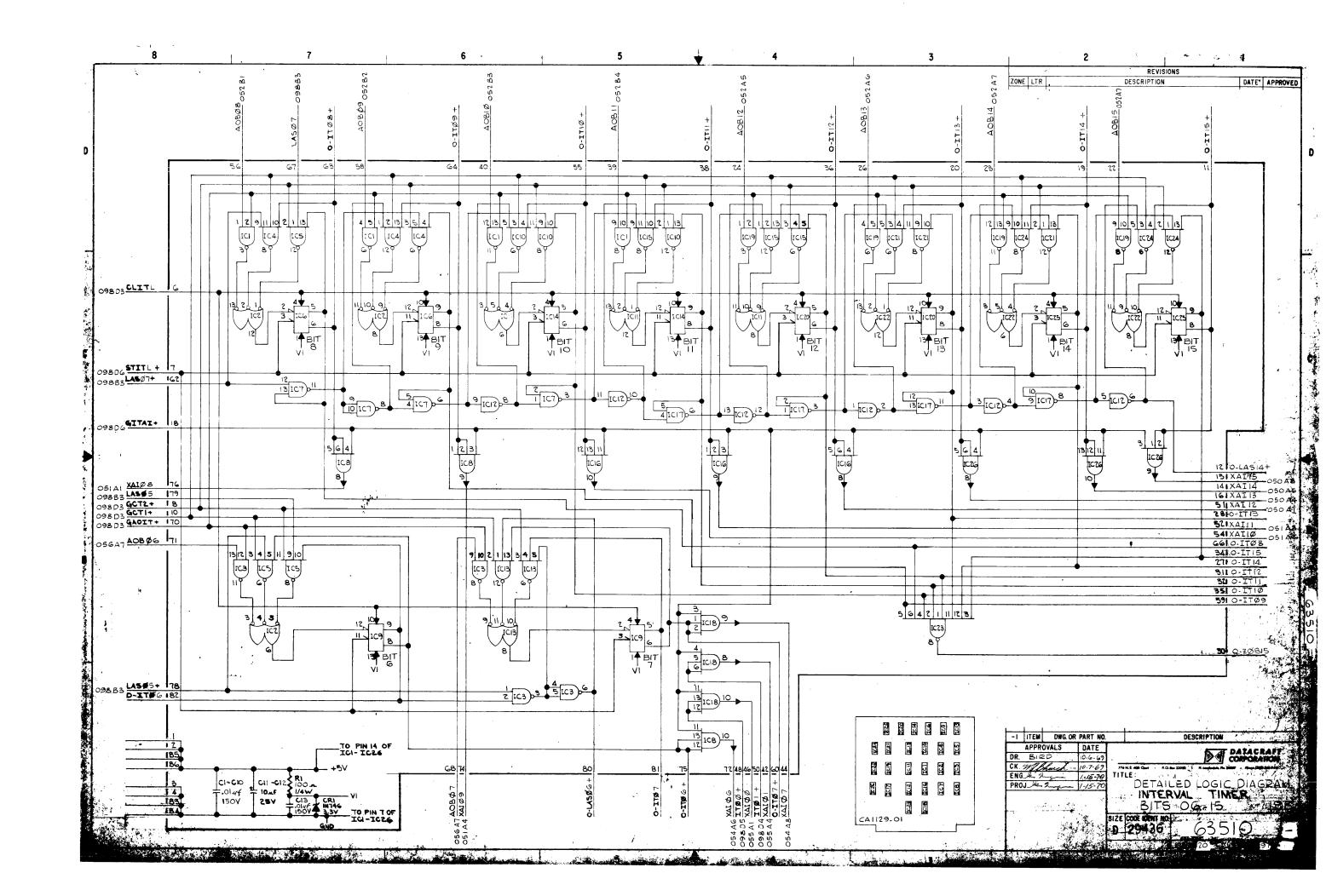


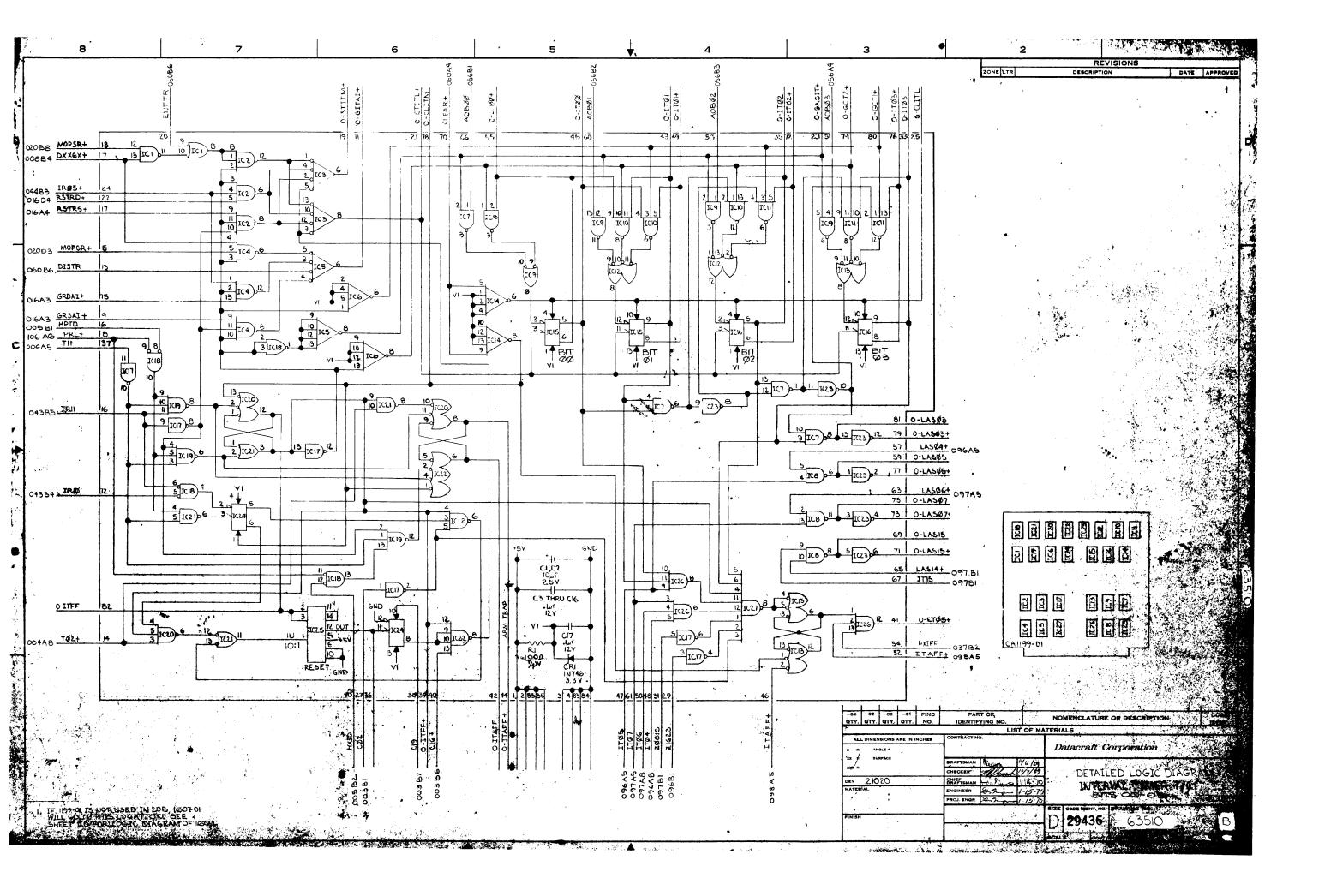


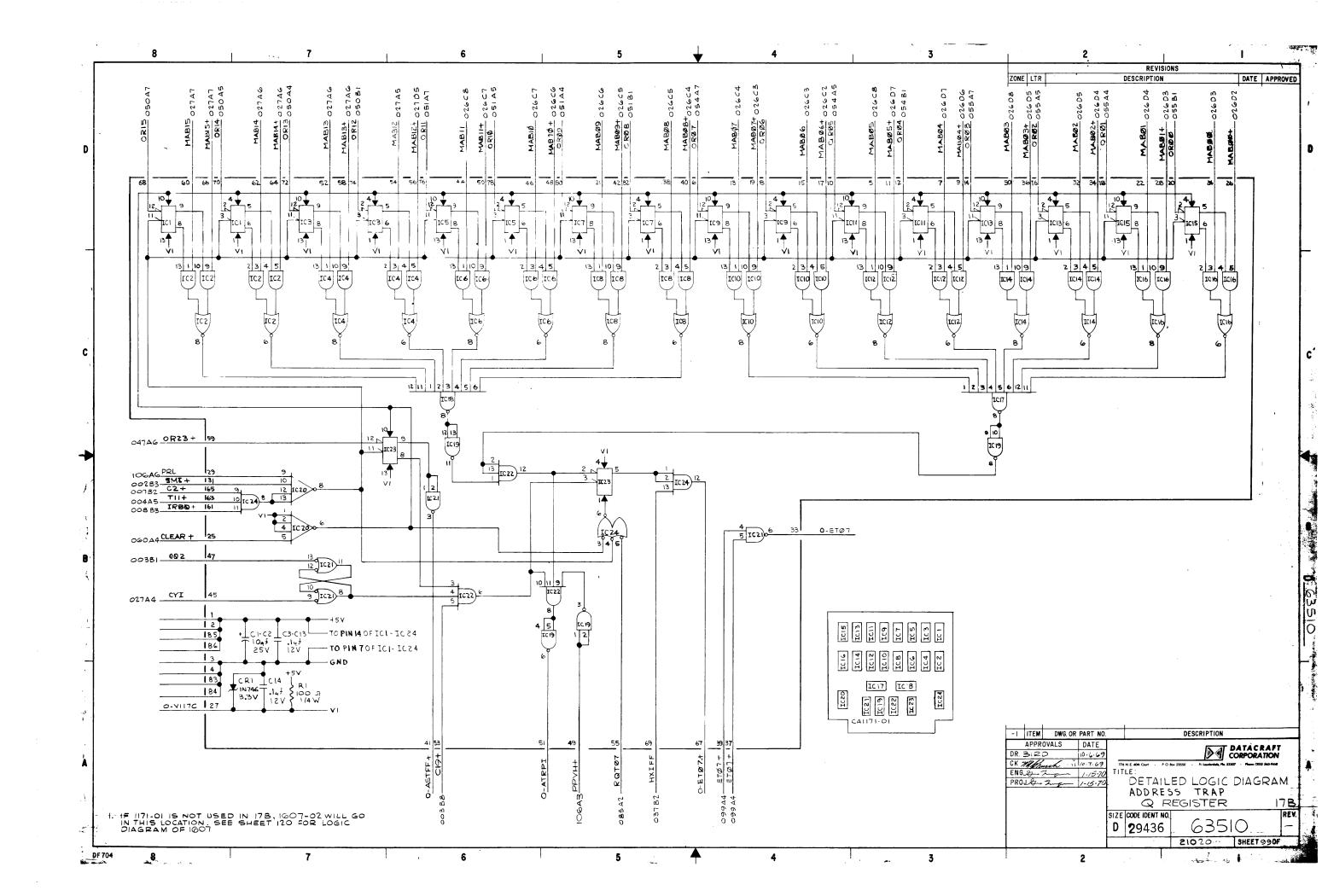


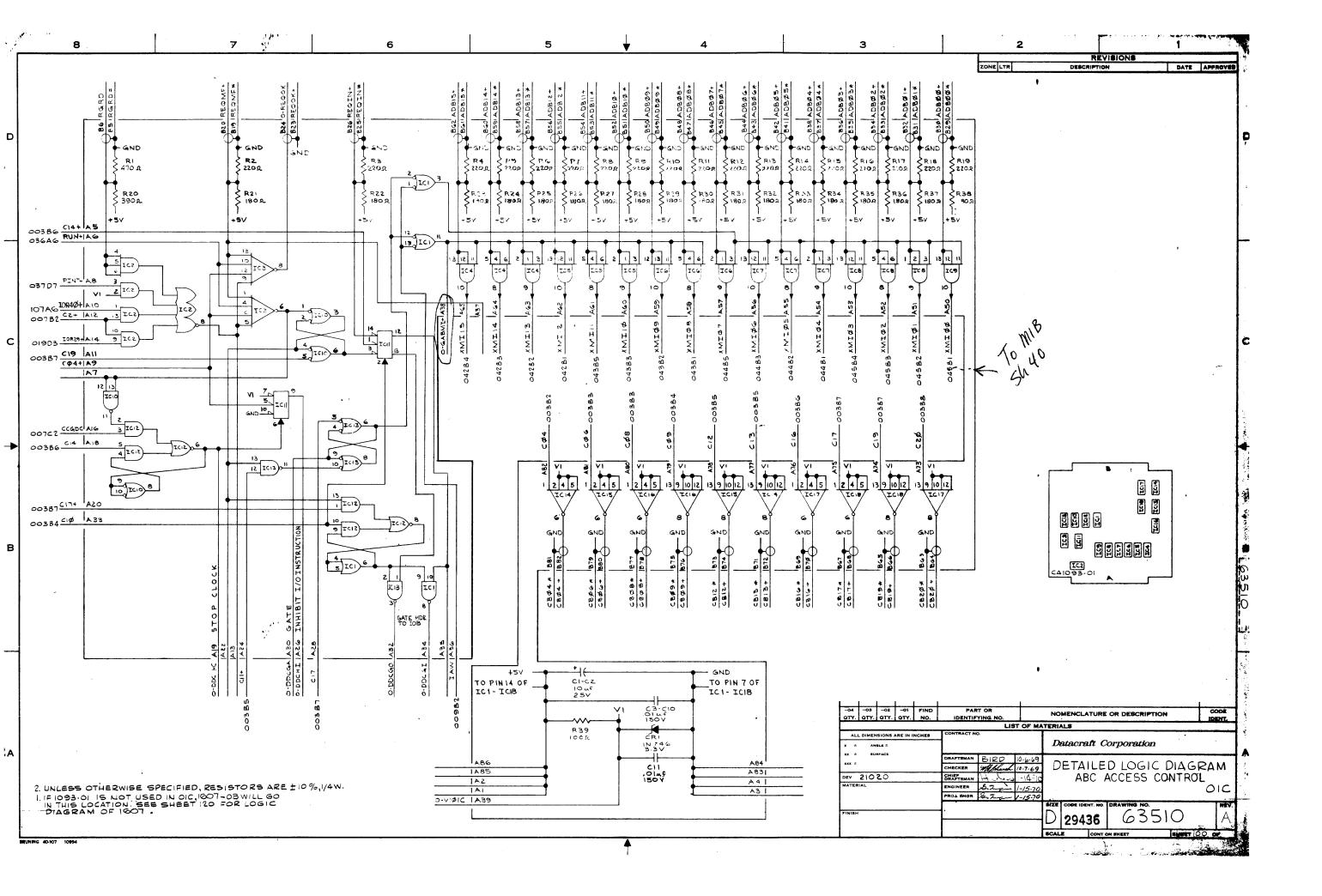


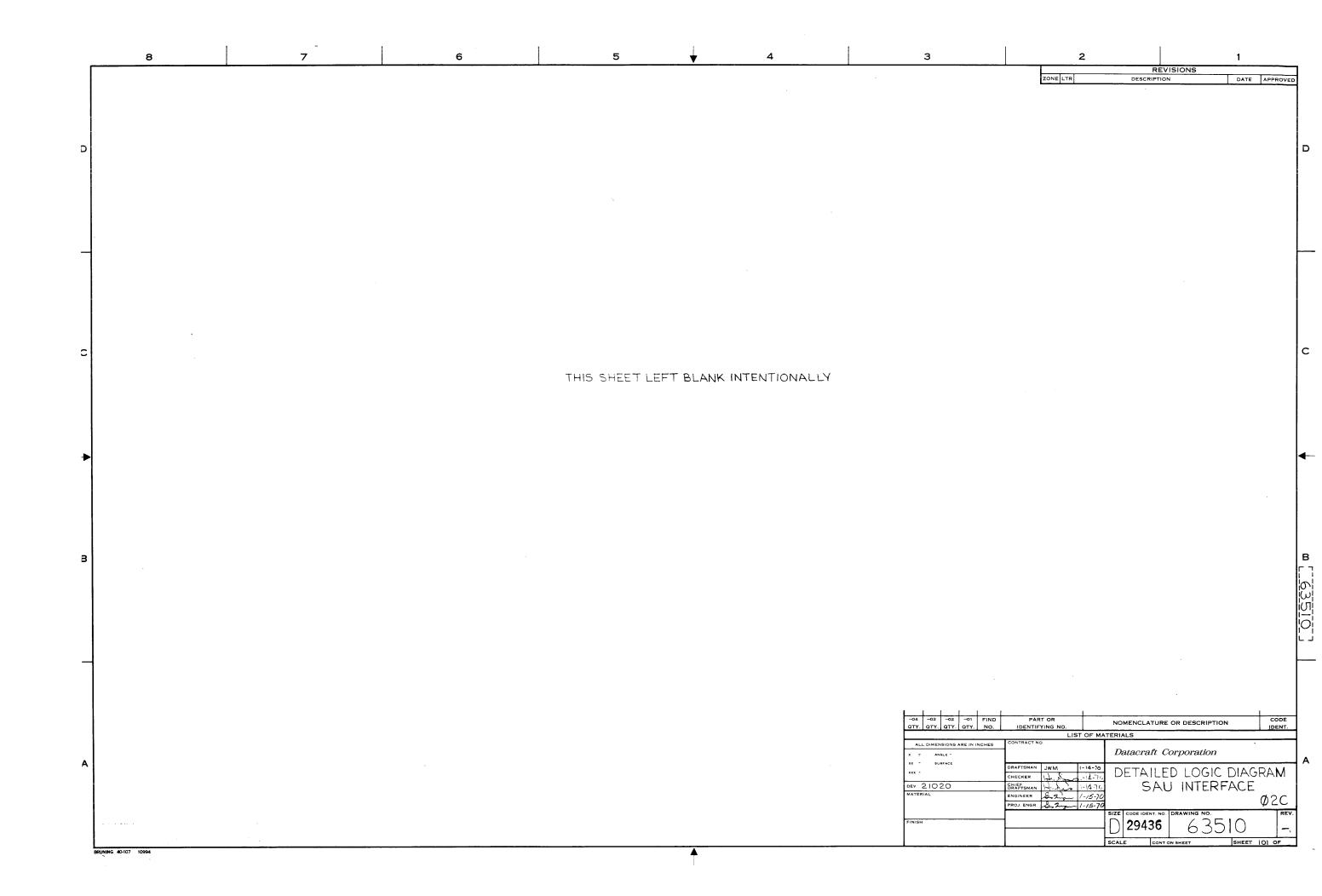


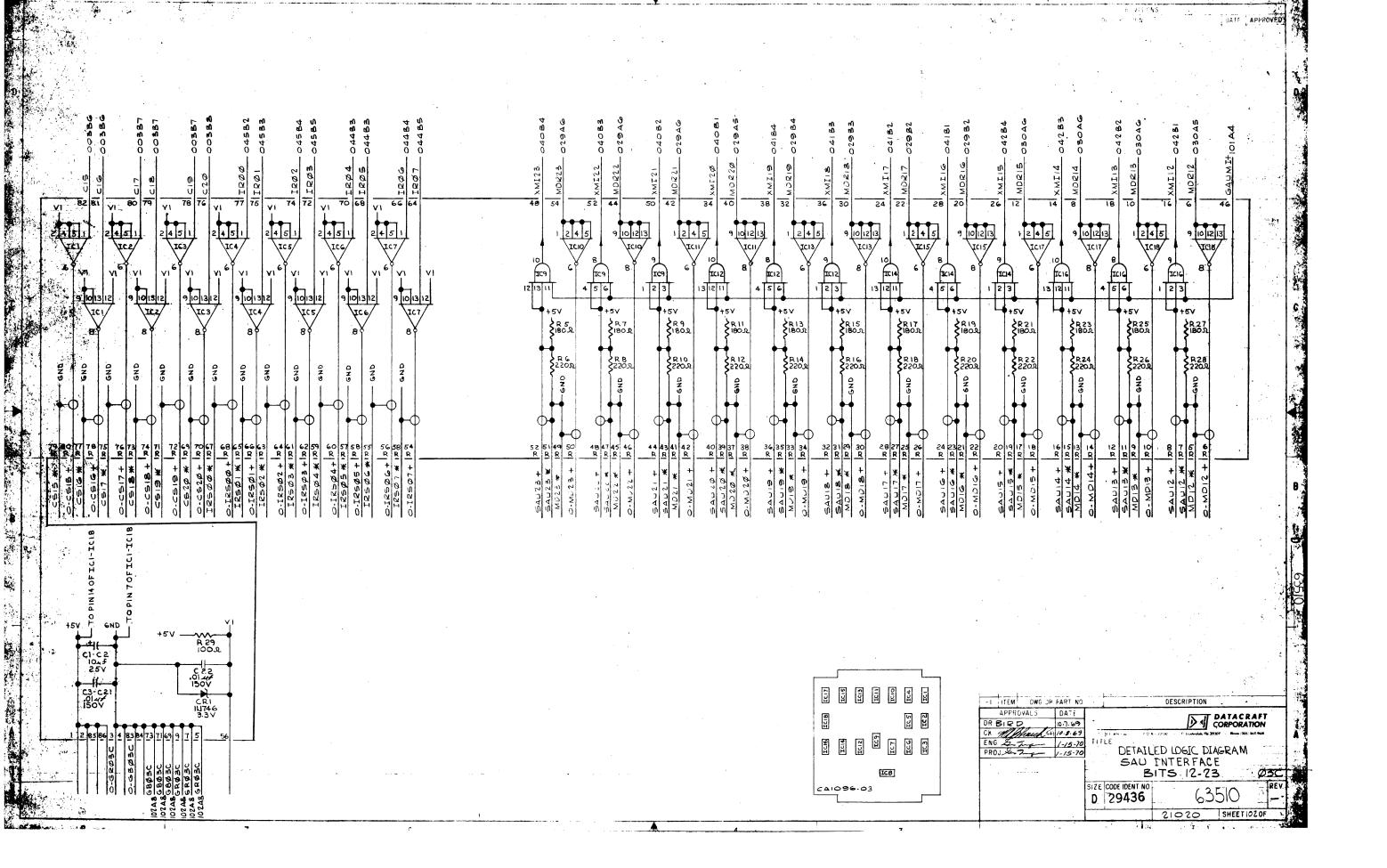












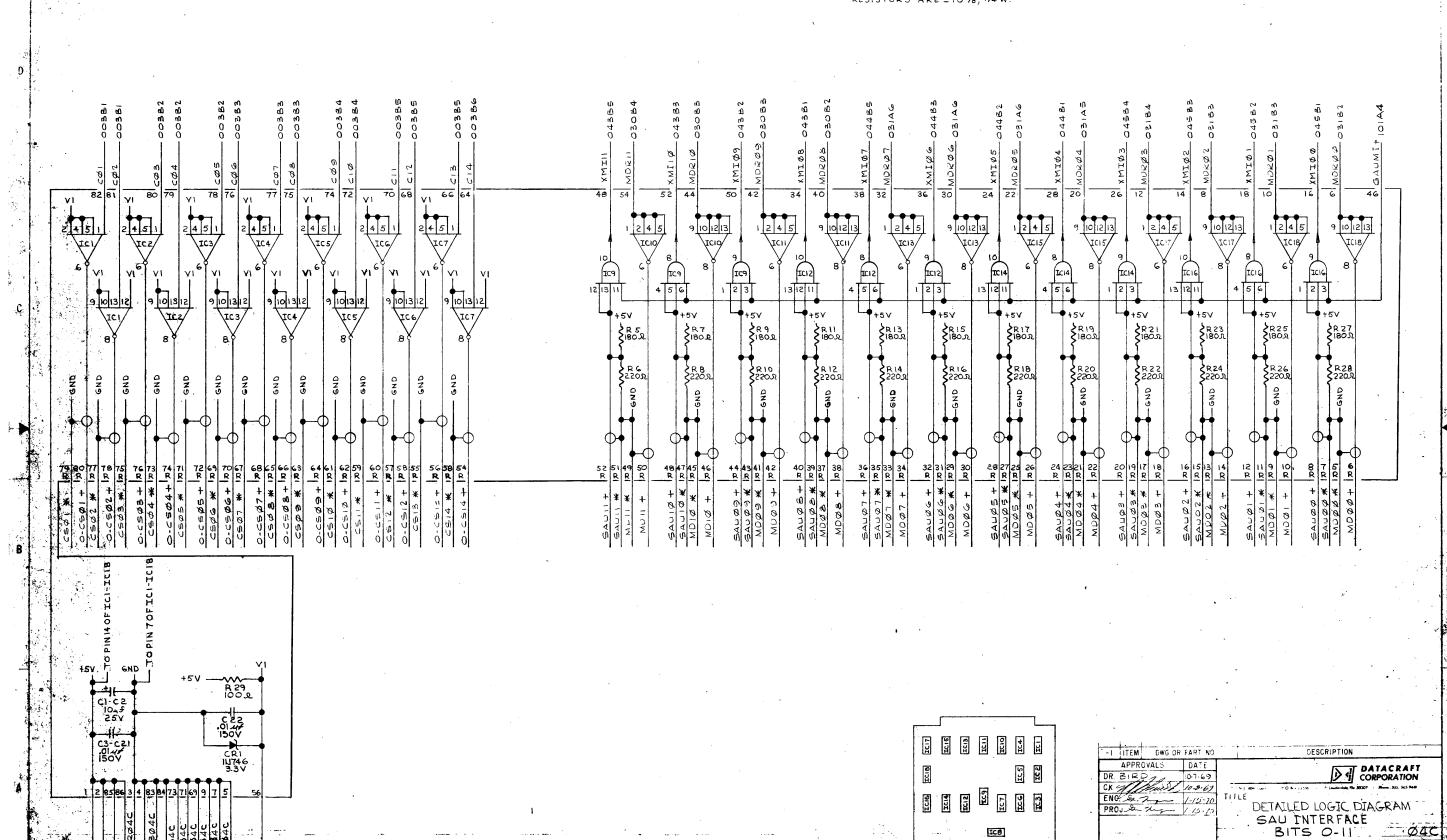
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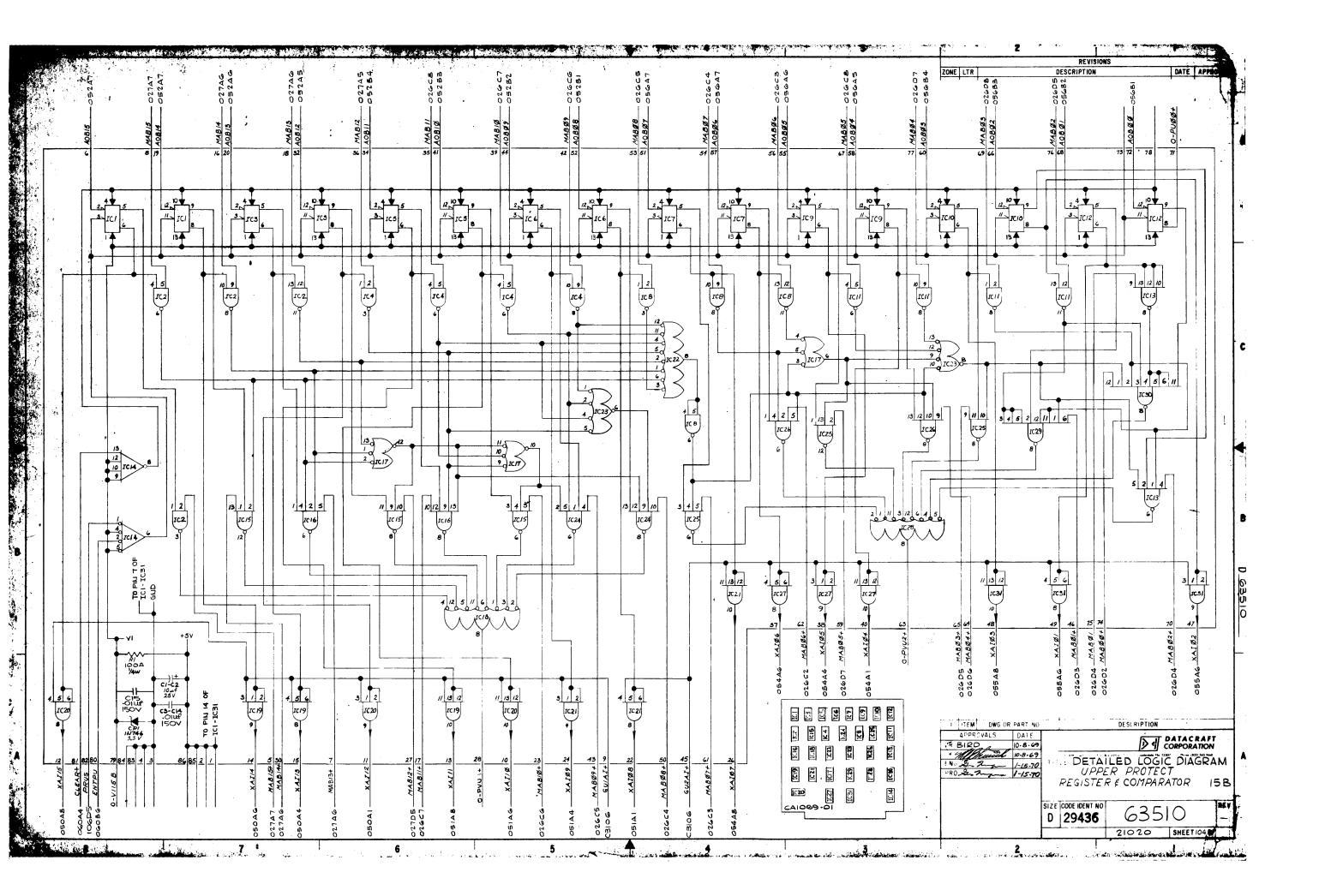
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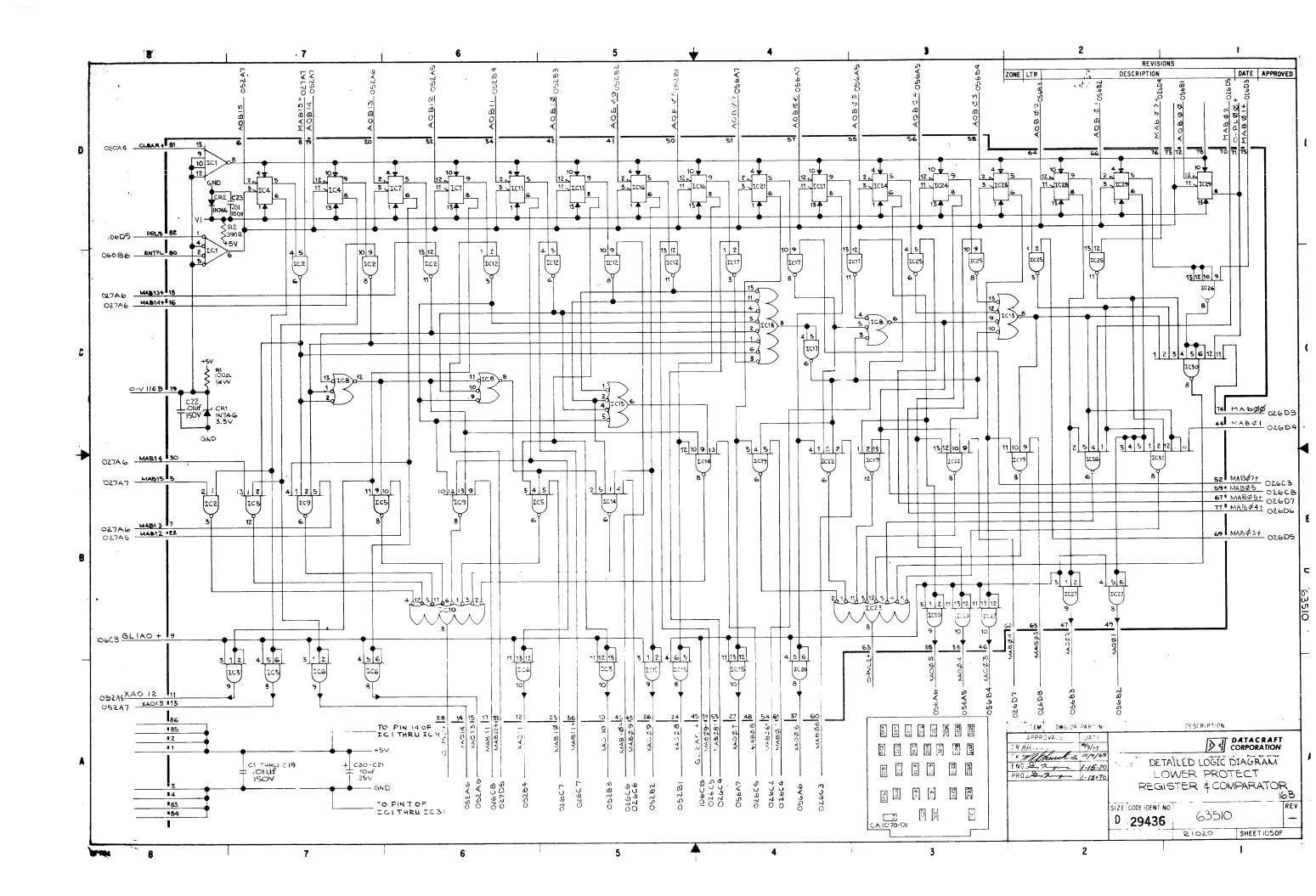
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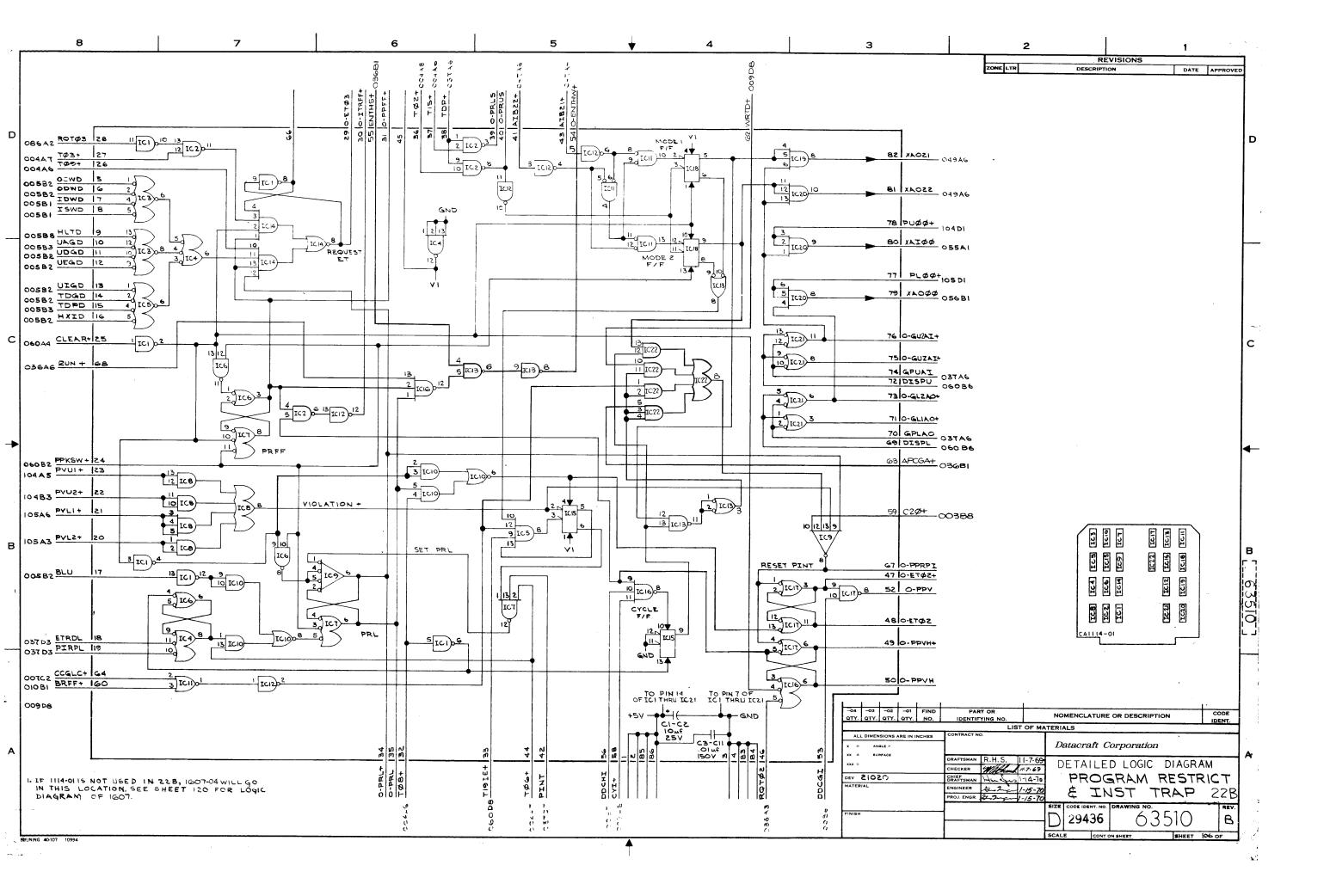
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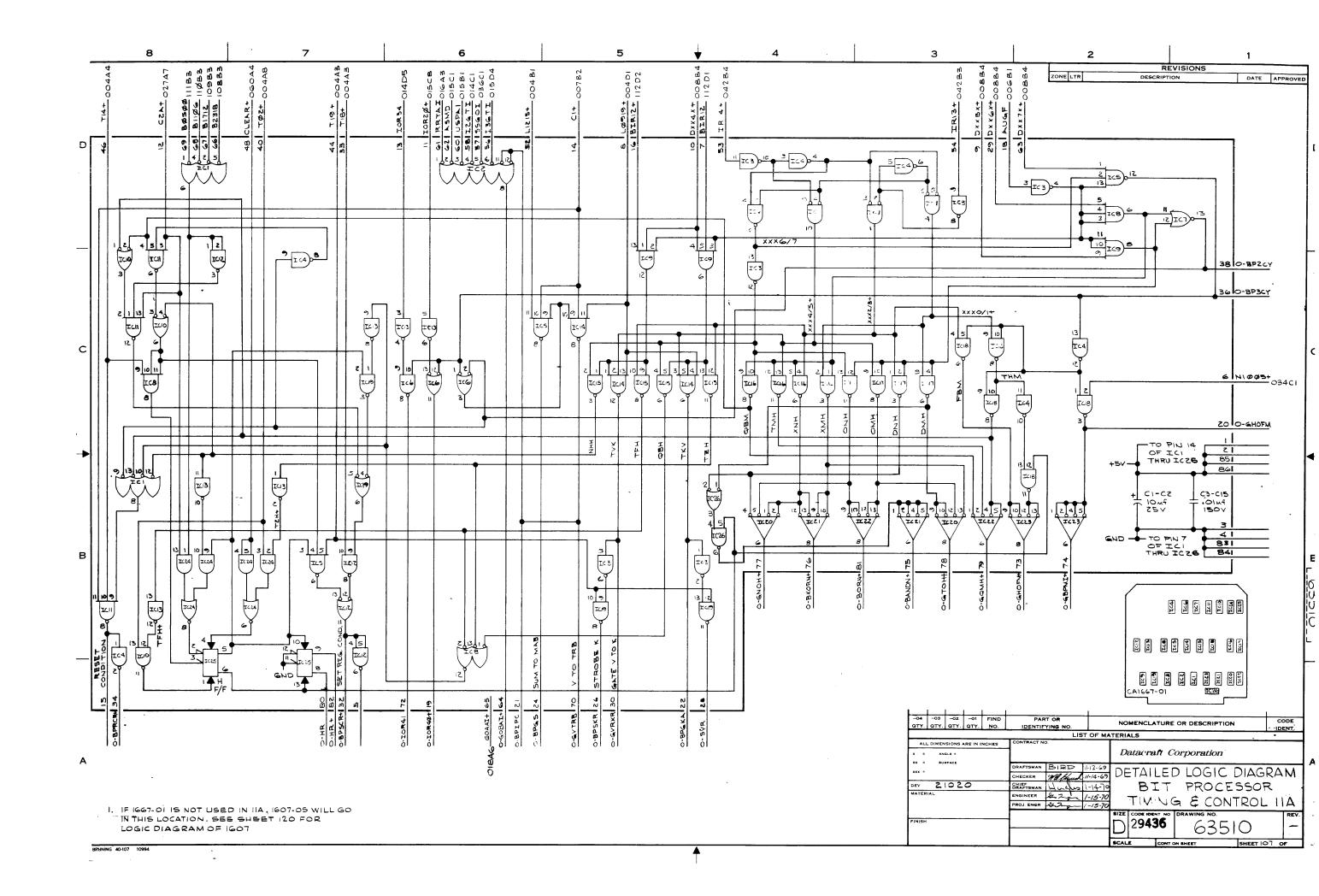


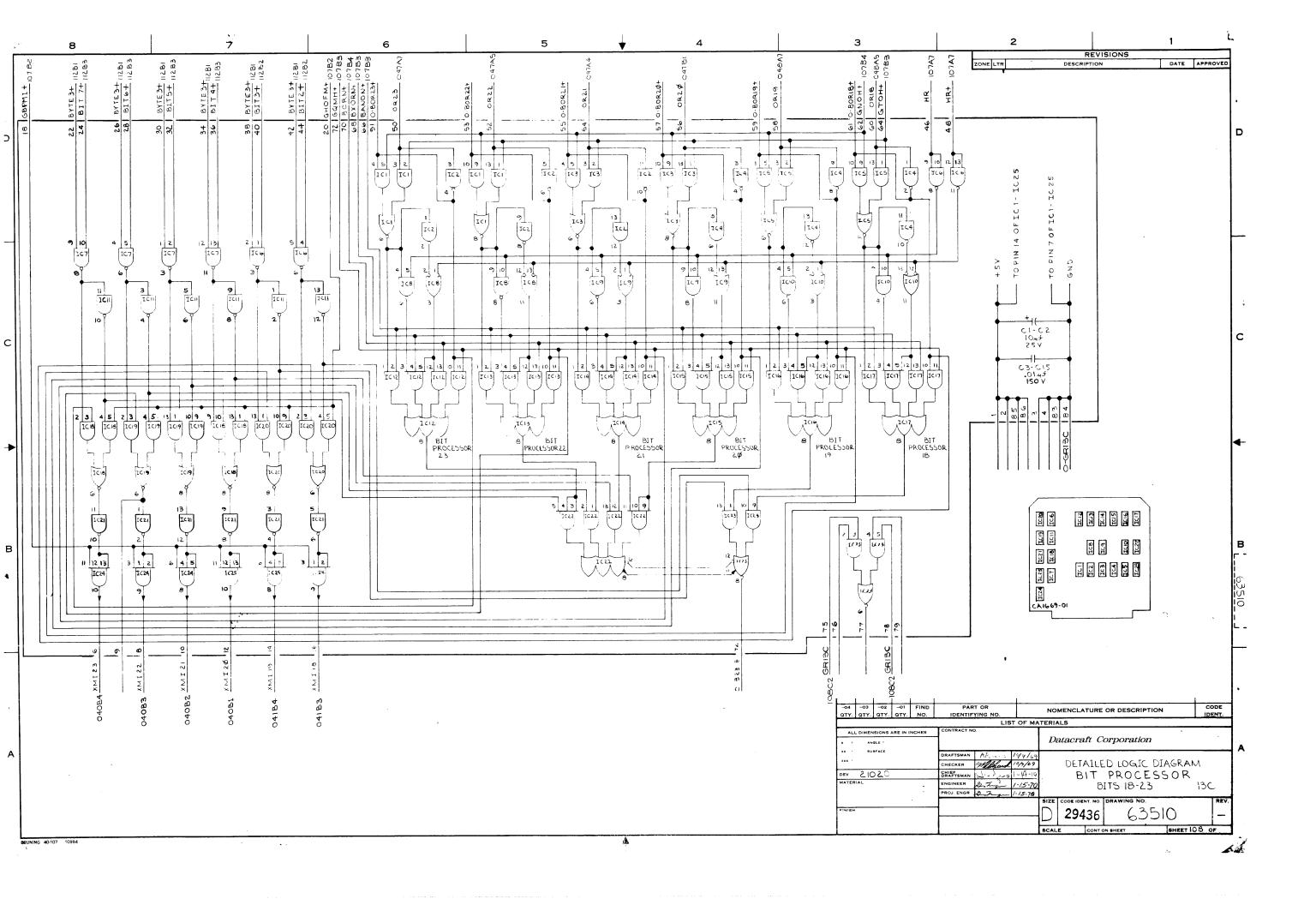
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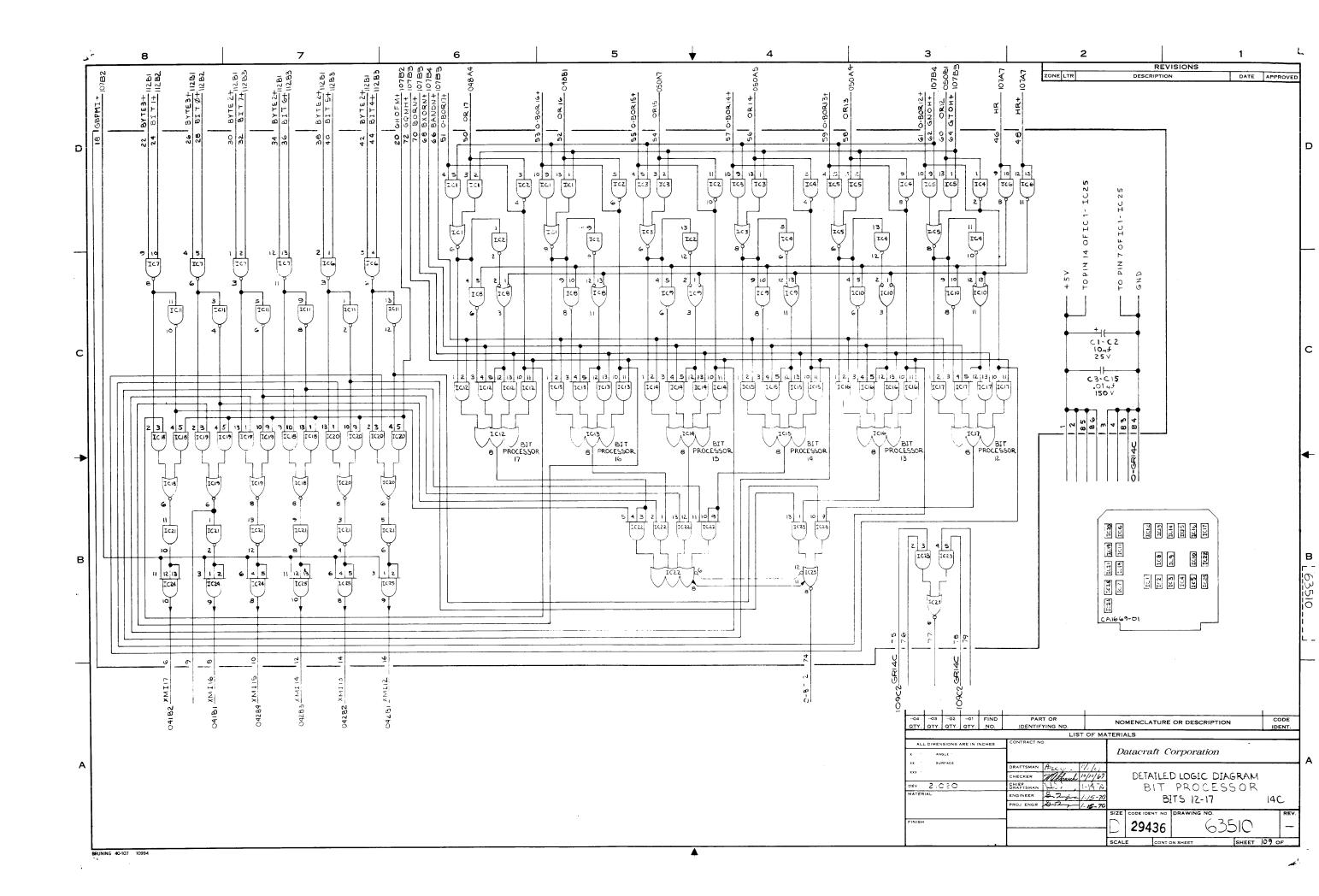


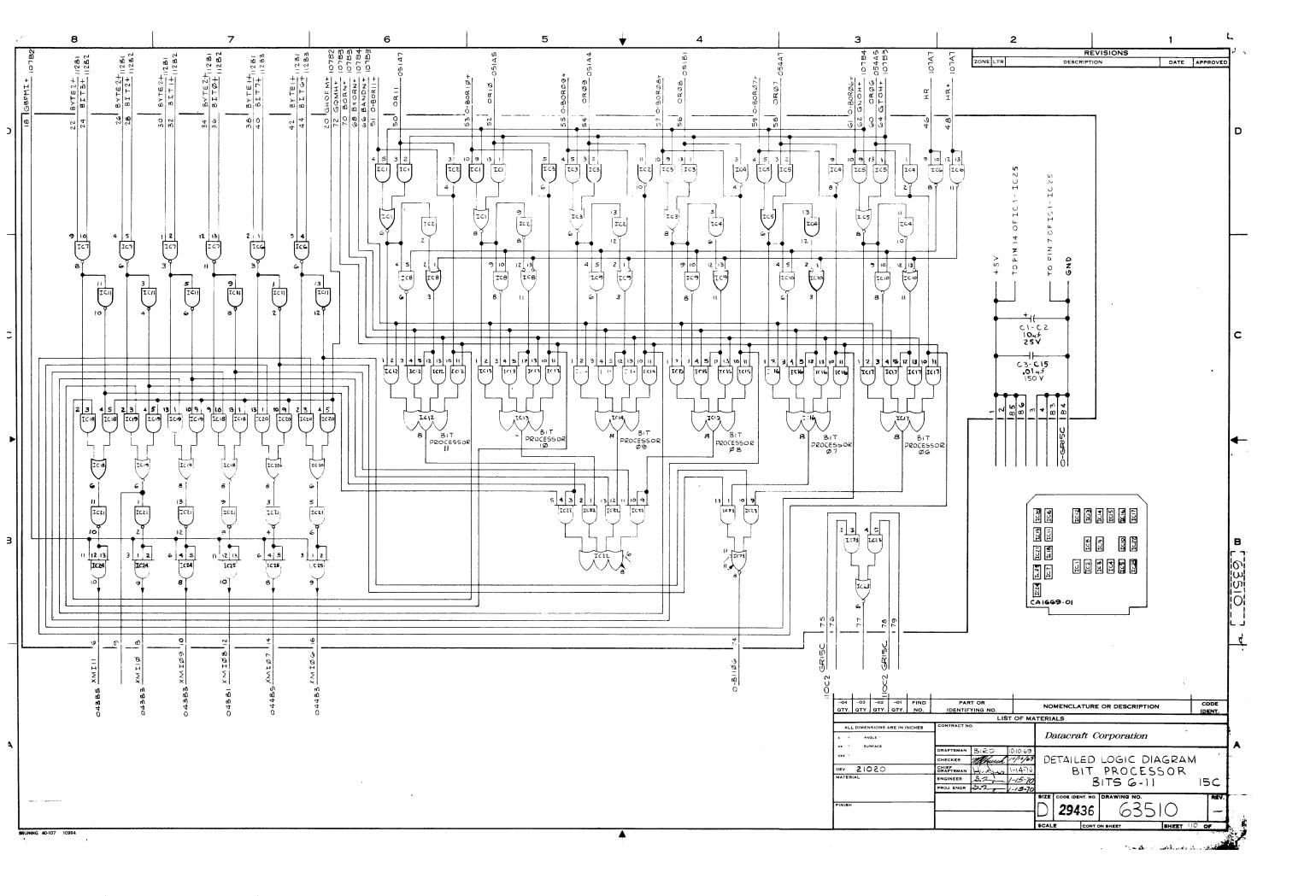


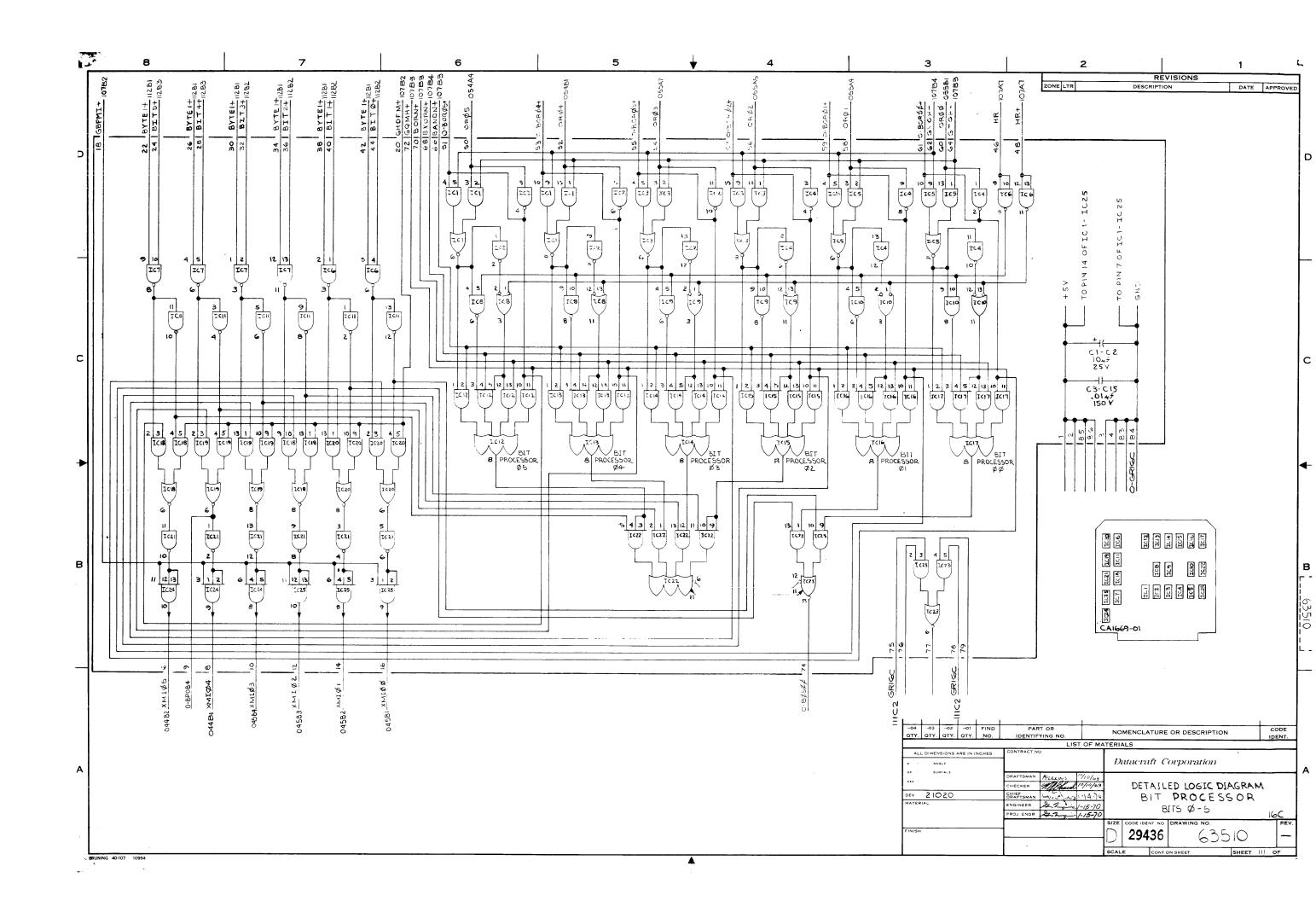


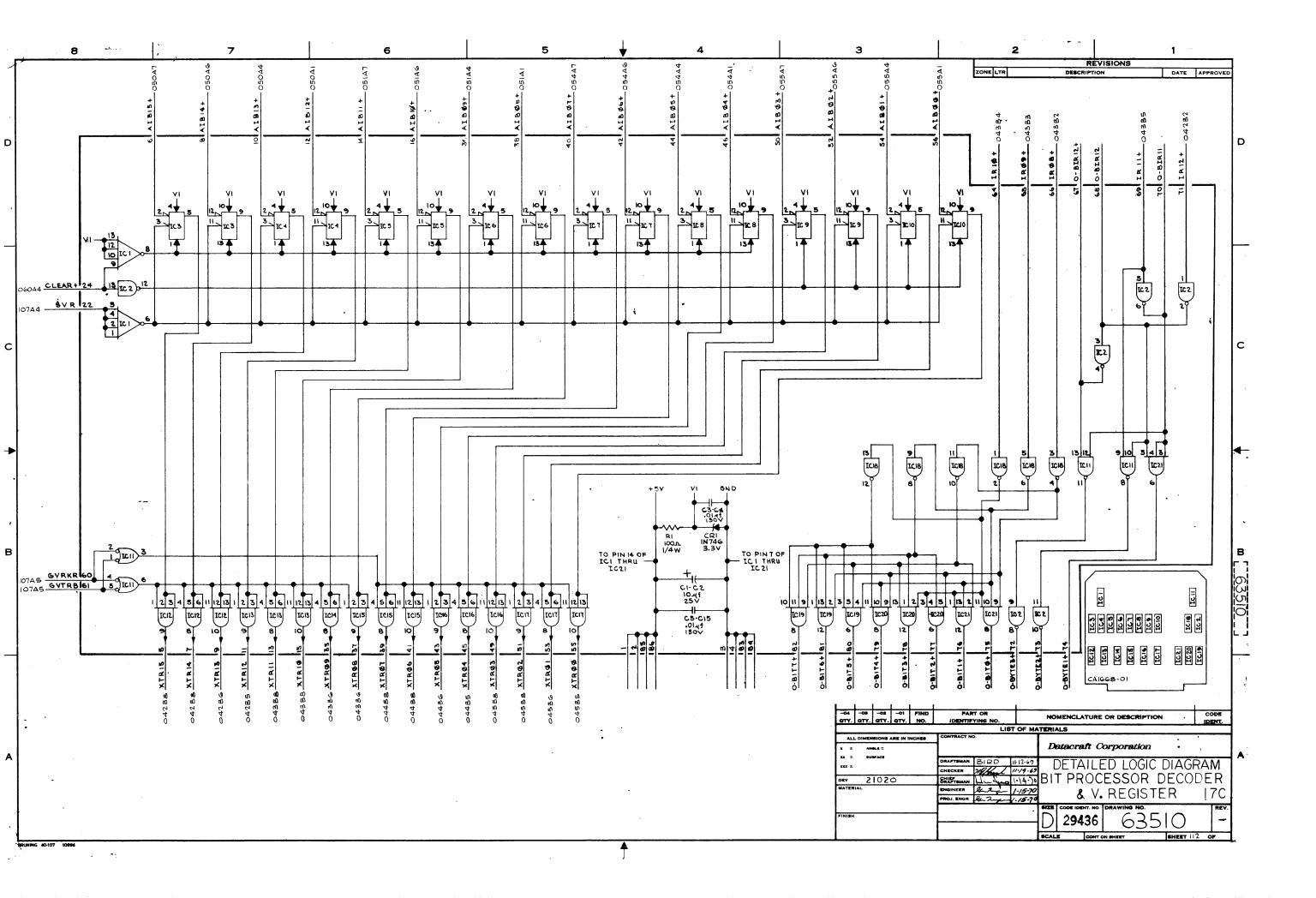


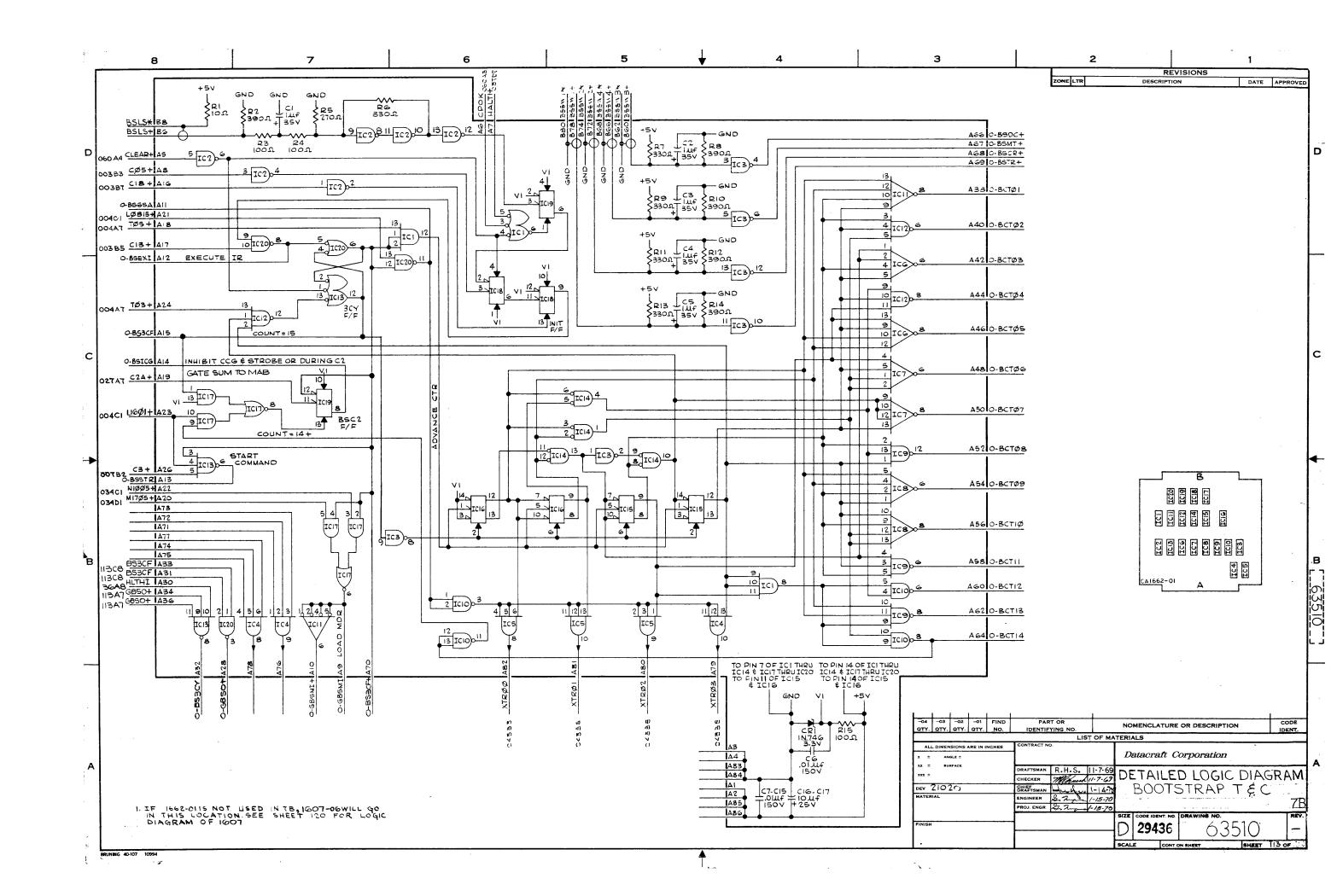


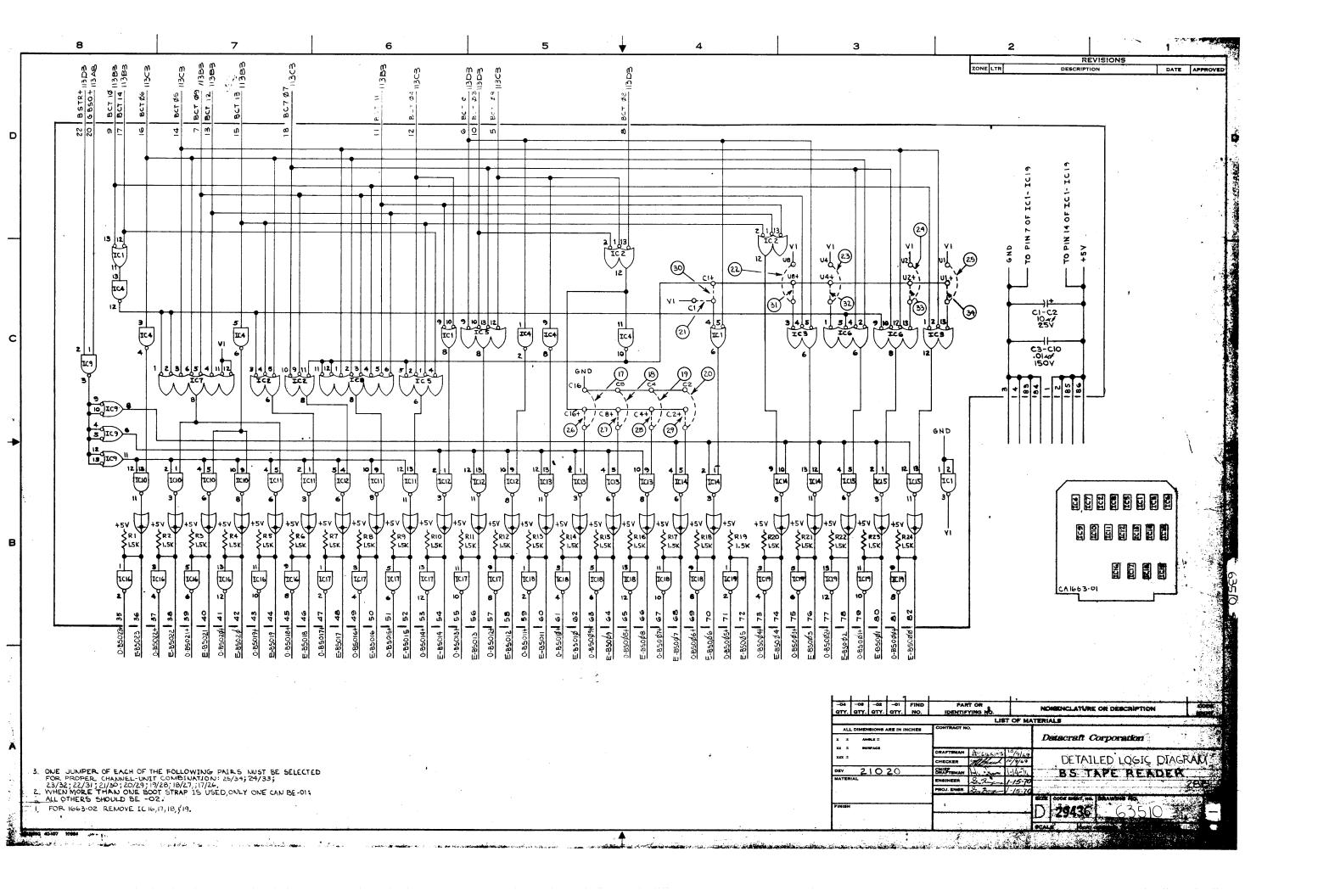


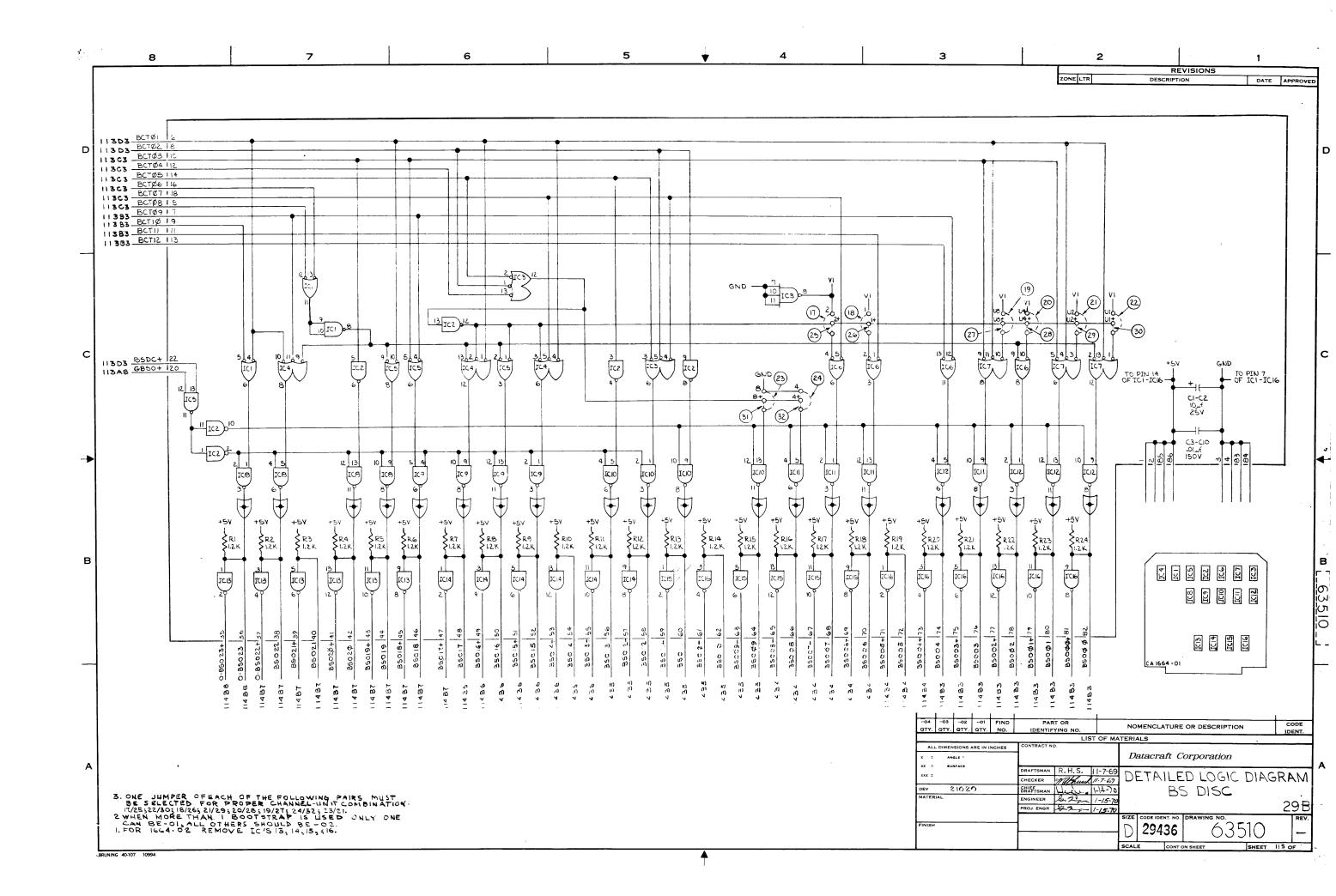


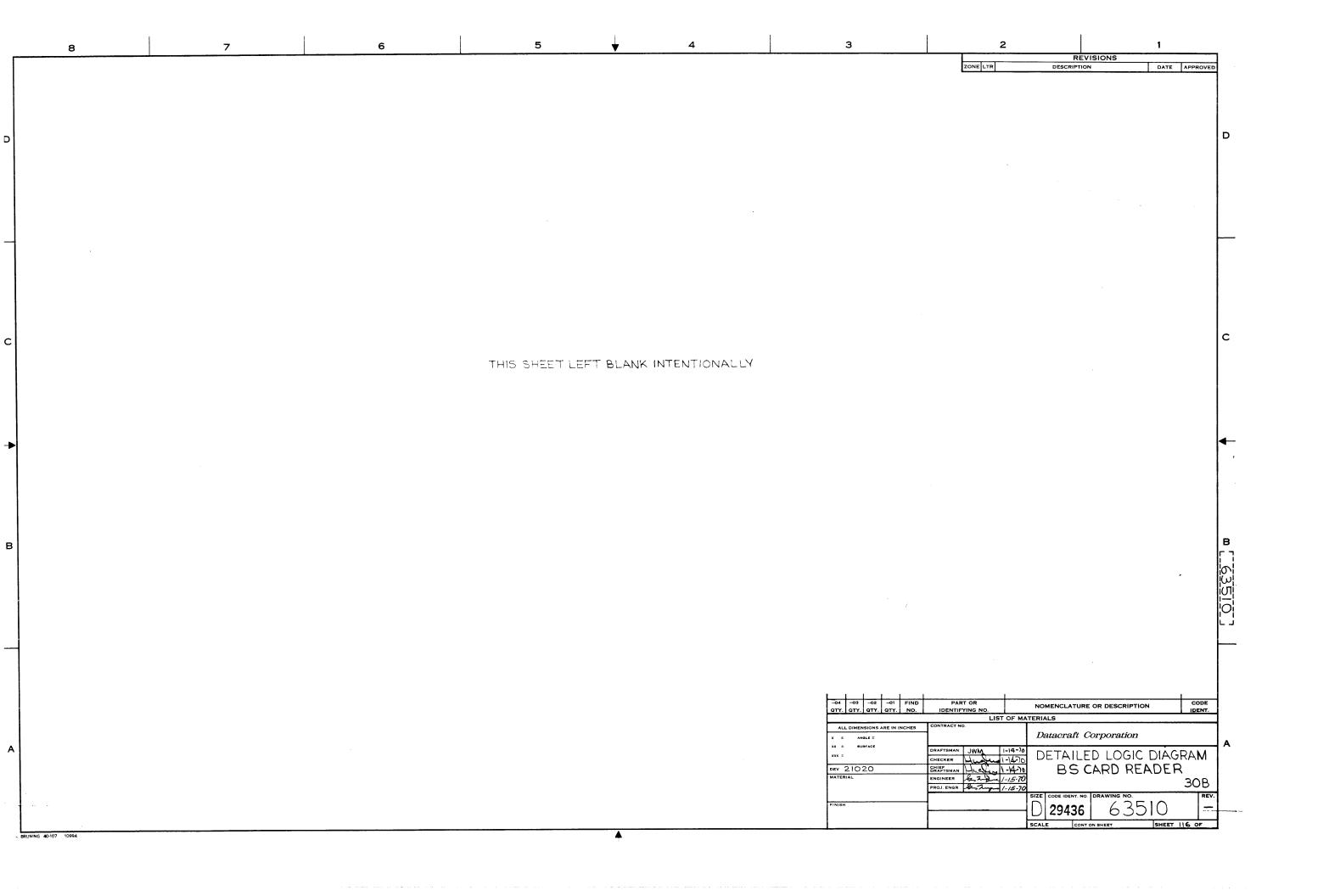


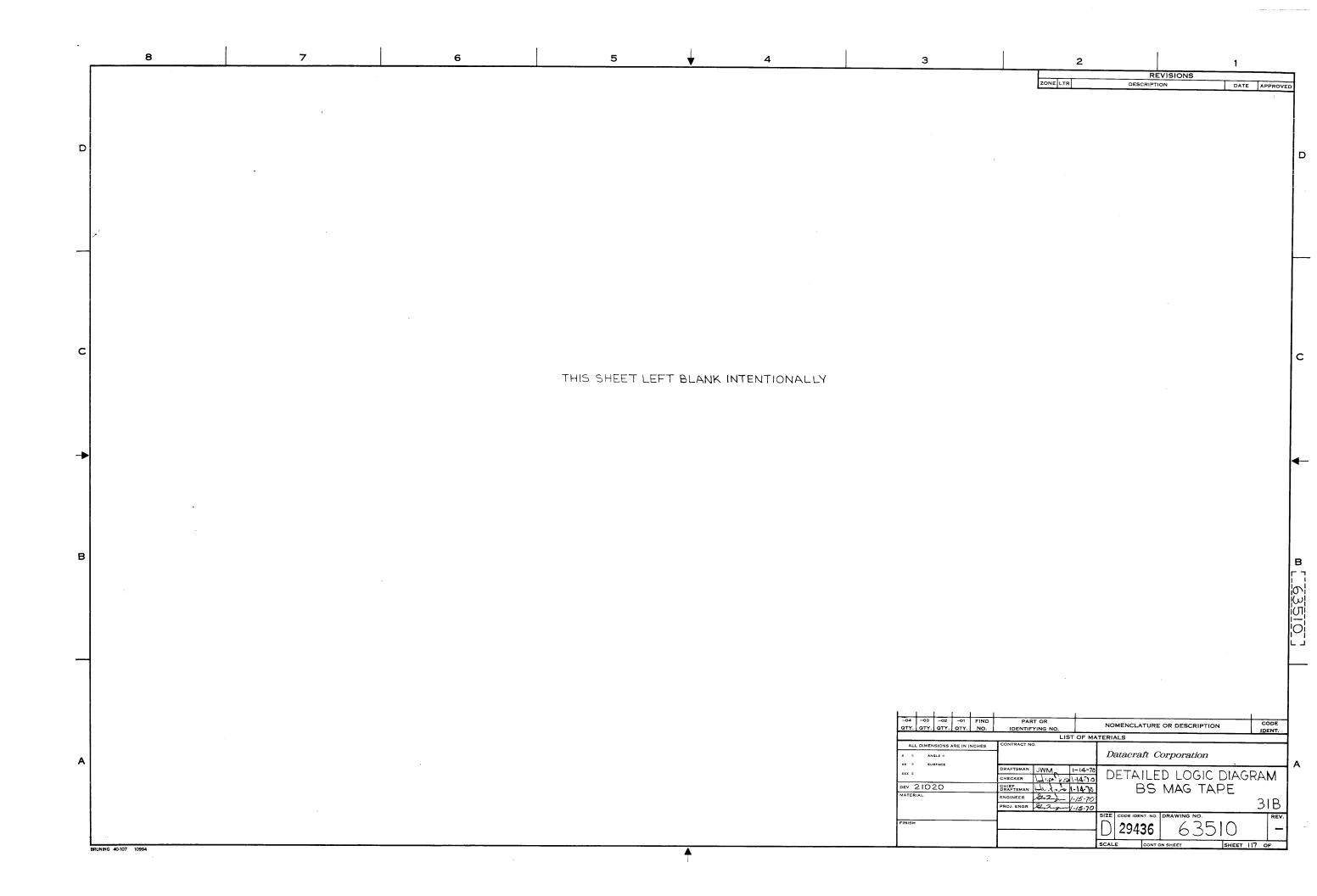


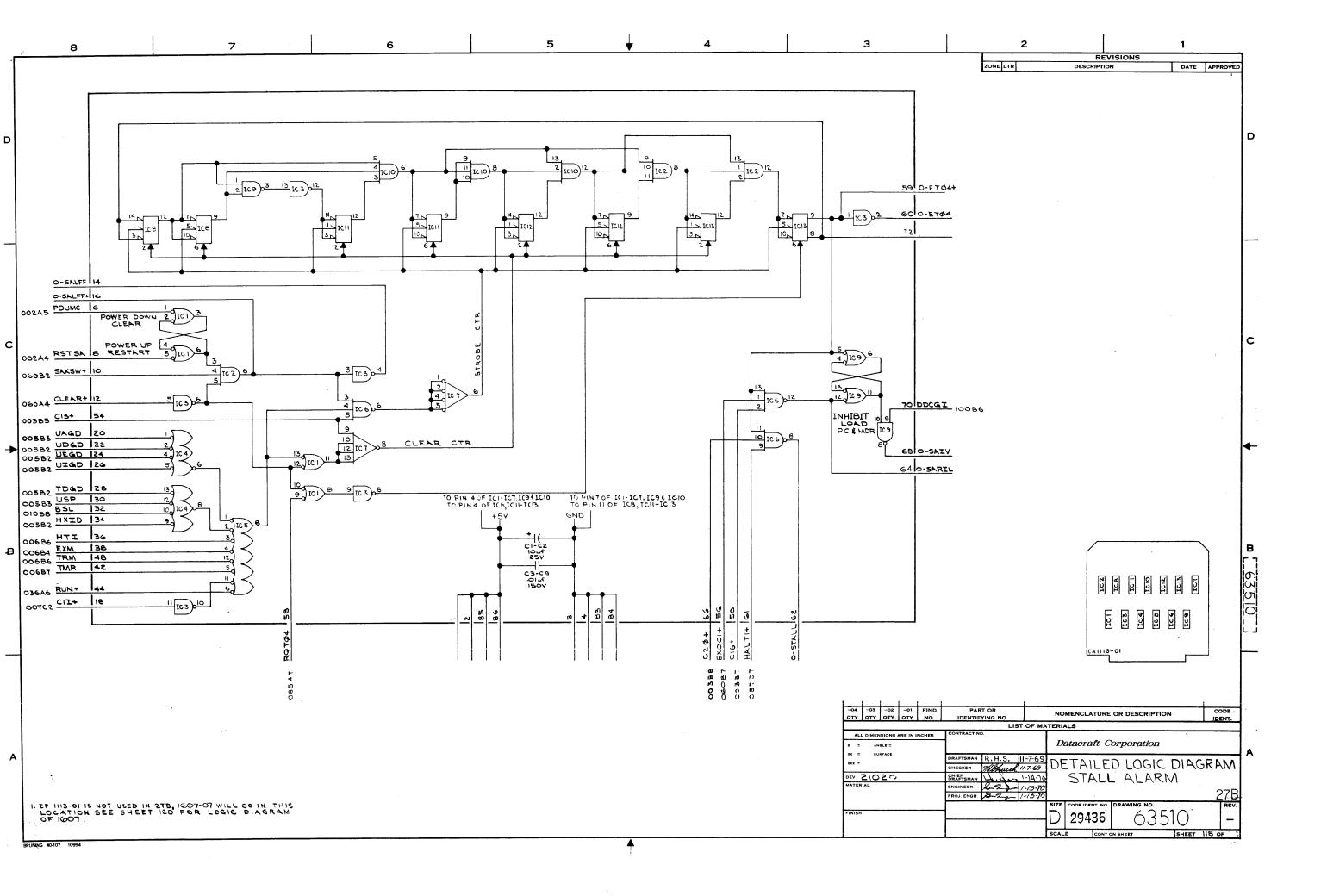


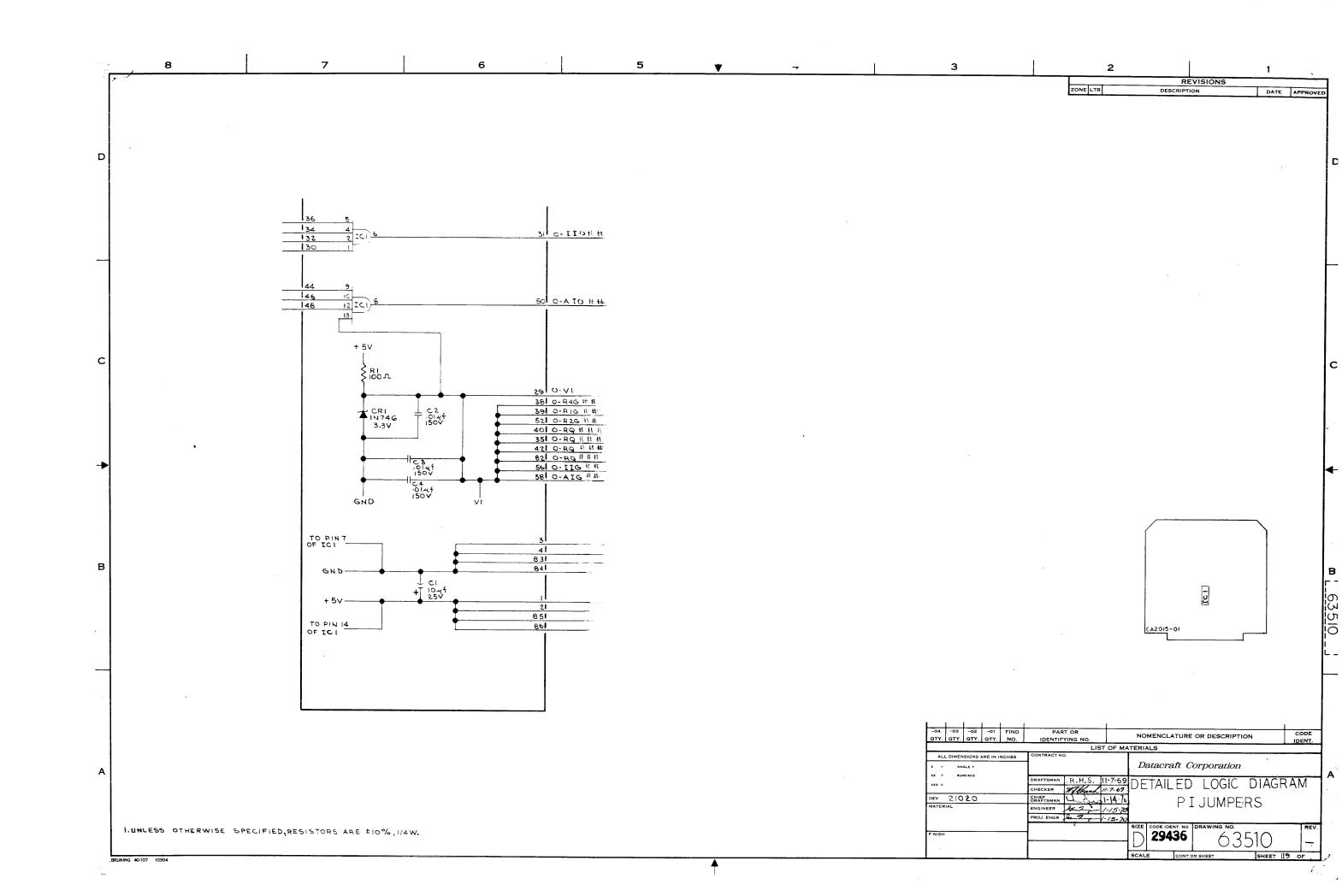


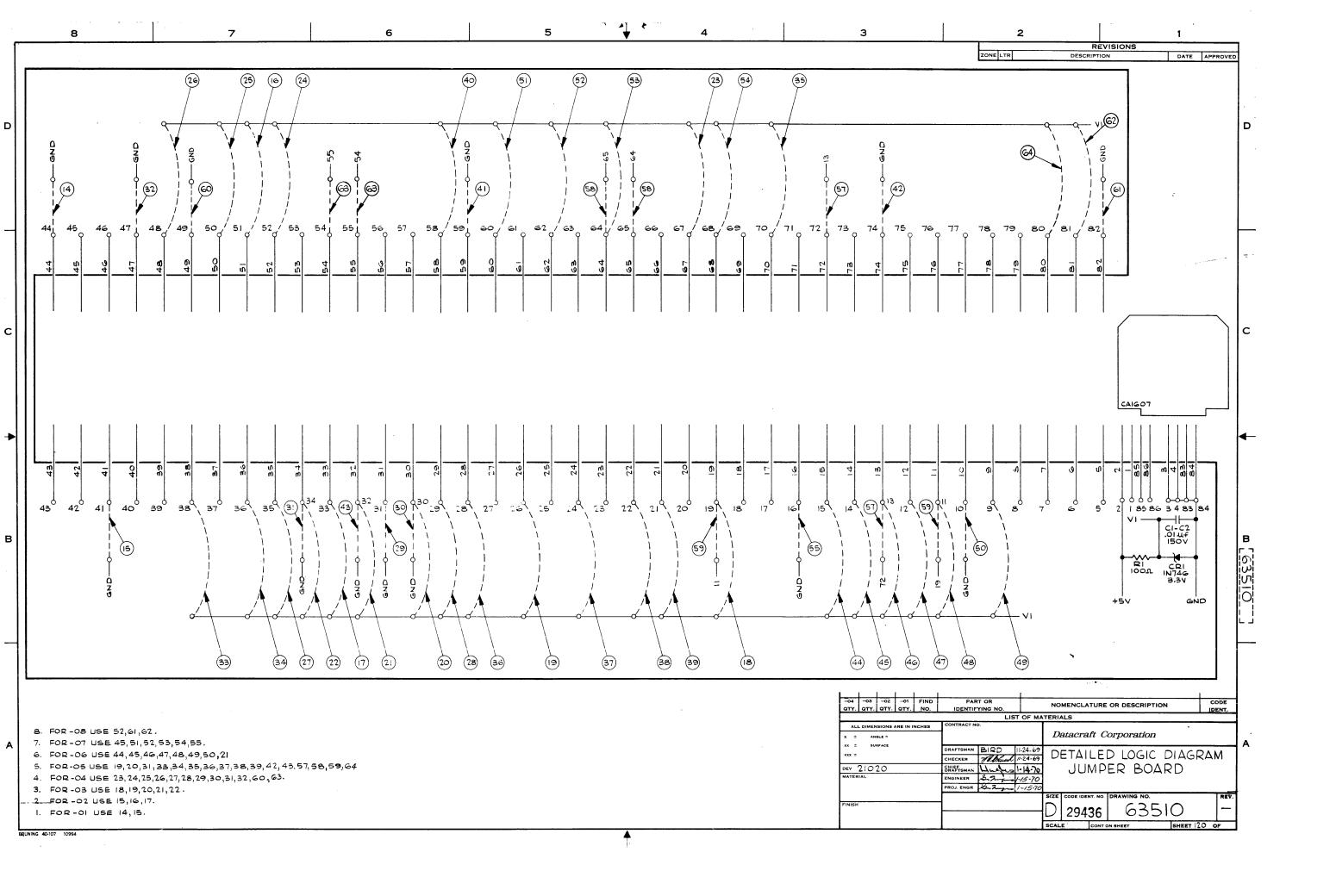


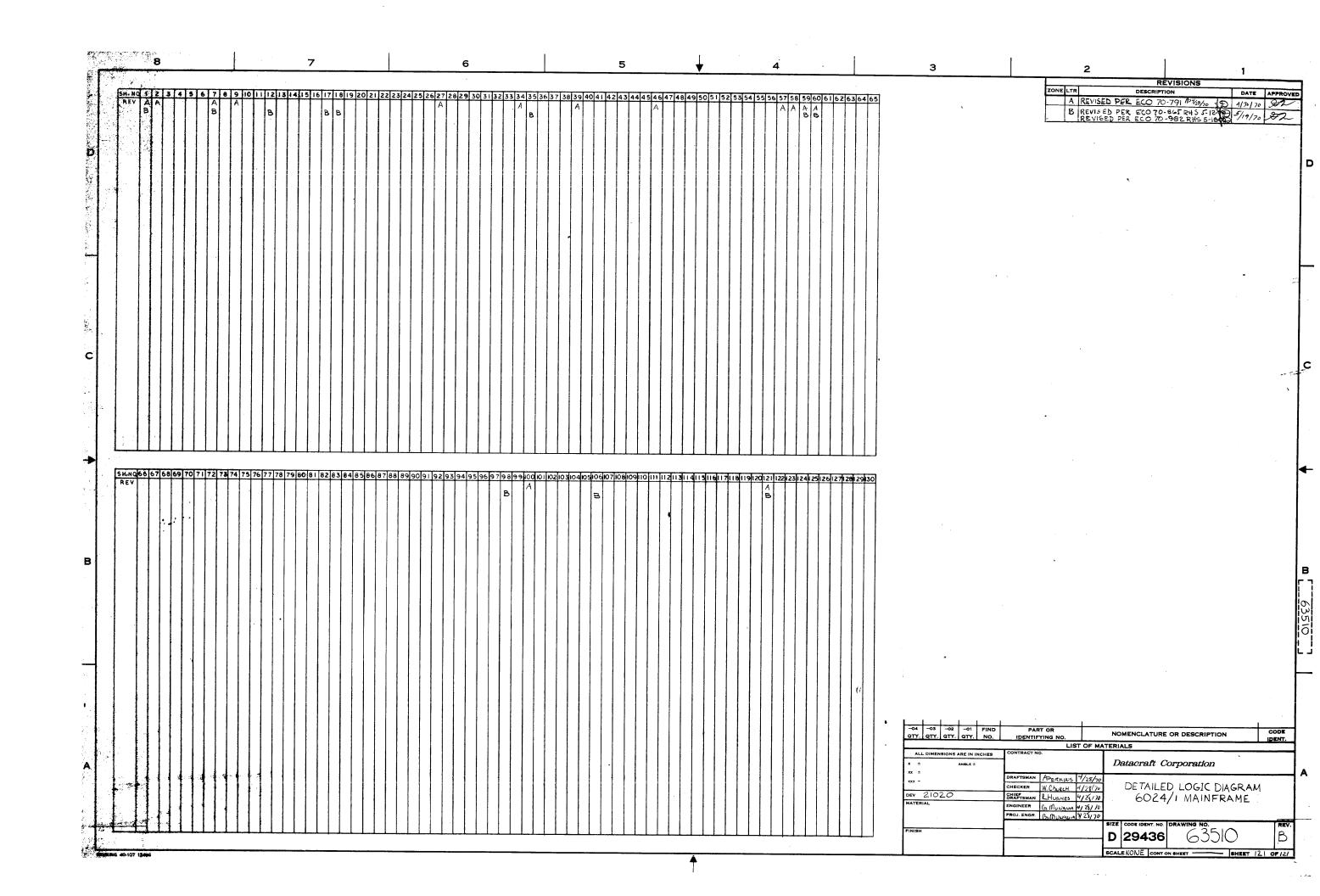












DESCRIPTION DATE APPROVE SEE SHEET 53 FOR REVISION STATUS OF SHEETS CARD LOC. ΙA 2 **A** 3 A 4 A 5 A 6 A 7A 8 A IOA 9 A 11A | 12A | 13A 14A 15A 16A 17A 18A 19A 20A 21A 22A 23A 24A 25A 27A 28A 30A 31A 2005-1 CARD NO. CA 1624-1/ 2005-1 1623-1/ 2006-1 2047-1 1624-1/ 2005-1 2048-1 1623-1, 2006-1 2047-1 2047-2 1623-1, 2006-1 2047-1 1624-1/ 2005-1 2048-1 1624-1/ 2005-1 2048-1 1623-1/ 1623-1/ 2006-1 2047-1 1624-1/ 2005-1 2048-1 2048-2 1623-1/ 2006-1 2047-1 2047-2 1623-17 2006-1 2047-1 1624-1/ 2005-1 2048-1 1624-1/ 2005-1 2048-1 2048-2 1623-1, 2006-1 2047-1 1624-1 1623-1 1623-1/ 2006-1 2017-1 1624-1/ 2005-1 2048-1 1623-1/ 2006-1 2047-1 1624-1/ 1623-1/ 2005-1 2006-1 2048-1 2047-1 1624-1 1624-1/ 1623-1/ 1624-1/ 1623-1 2005-I 2048-I 2048-2 2005-1 2048-1 2048-2 2006-1 2004-1 1626-1 2048-1 2041-1 2048-1 2048-1 2048-2 2047-2048-2 2057-1 2048-Z 2057-1 2048-2 2047-2 2048-2 2048-2 2047-2 2041-2 204/-2 2047-2 2048-2 2047-2 2048-2 2057-1 2047-2 20**48-2** 20**57-1** 2047-2 2057-CARD 2057-1 2057-1 2057-1 2046-2 2057-1 2046-1 2057-1 2057-16 BITS 8 BITS 16 BITS 8 BITS 10C/ABC 10C/ABC 16 BLTS 8 BLTS 16 BLTS 8 BLTS 10C/ABC 10C/ABC 10C/ABC 10C/ABC CH. \$\psi_8\$ CH. \$\psi_8\$ CH. \$\psi_8\$ CH. \$\psi_8\$ TYPE 8 BITS | 16 BITS | 8 BITS | 16 BITS | 8 BITS | 10 C/ABC 8 BITS | 16 BITS | 8 BITS CH. \$ 4 CH. \$18 CH. \$18 CKTS AVAIL PAGE 02 03 04 05 06 07 80 09 10 1.1 12 13 14 15 16 17 18 19 2.0 21 22 23 24 25 26 27 28 CARD LOC. ΙB 5 B 6B 7 B 8 B 9 B IO B HB 12B 13B 14B 15B 16B 17B 18B 19B 20 B 21B 22B 23B 24B 25B 26B 27B 28B 29B 30B 31B B CONN | B C CARD TYPE CKTS AVAII PAGE \triangle

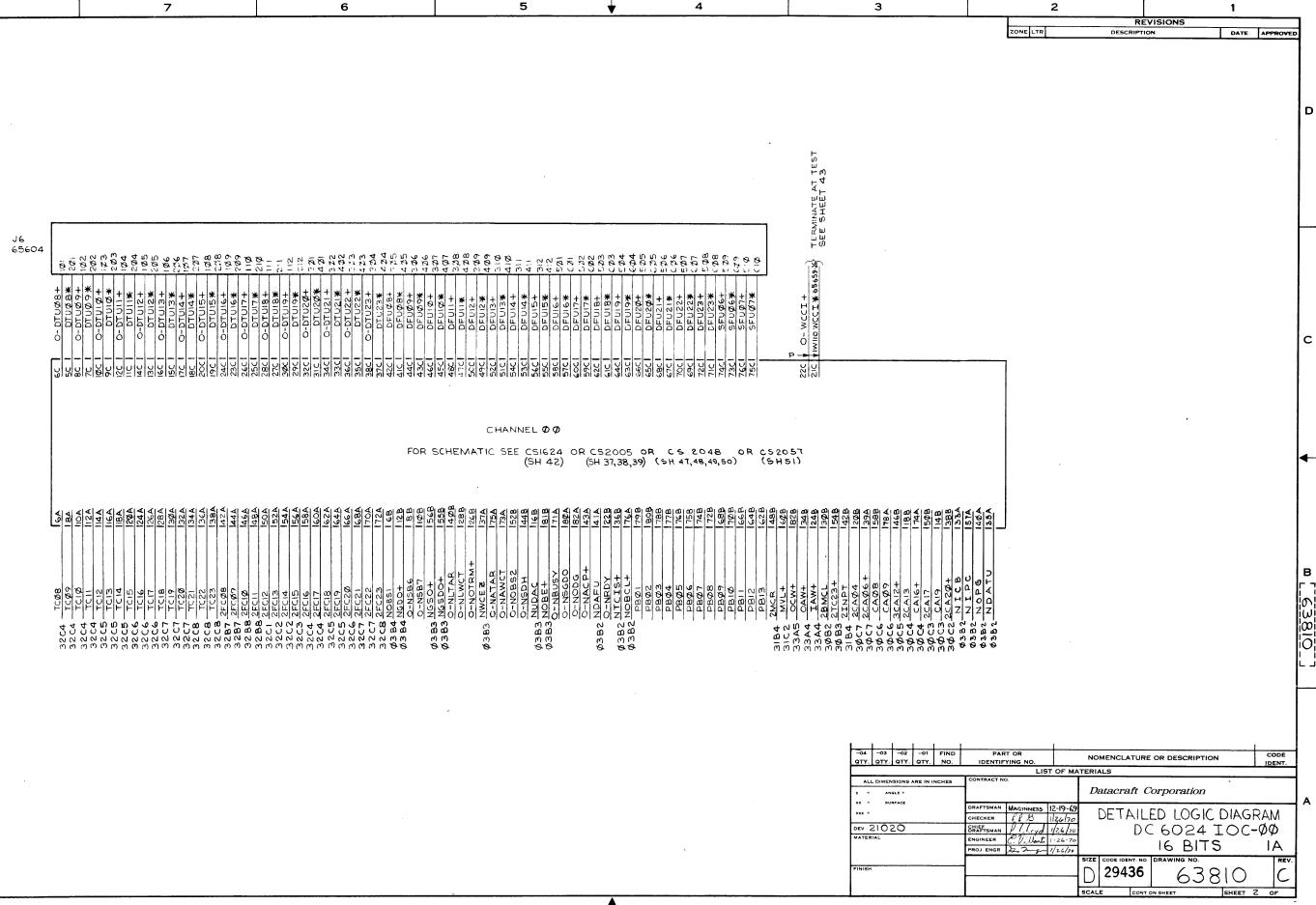
2046 BOARDS TO BE PLACED IN CHAUNEL 188 LOCATION IF LESS
THAN 9 CHAUNELS ARE USED, AND ARE TO BE MOVED TO THE NEXT
HIGHER CHANNEL AS THE CHASSIS FILLS UP. IF ALL CHANNELS ARE
USED THE 2046 BOARDS ARE DELETED

В

Α

NOMENCLATURE OR DESCRIPTION IDENTIFYING NO. IDENT. LIST OF MATERIALS ALL DIMENSIONS ARE IN INCHES Datacraft Corporation x ± ANGLE ± XX = SURFACE DETAILED LOGIC DIAGRAM CHECKER GALL 126/70
CHIEF DRAFTSMAN CLOY 1/26/70 DEV 21020 I/O ASSY CPU ENGINEER C. Villanti 1-26-70
PROJ. ENGR. 22-20-1/2 6/70 63810 D 29436 SHEET | OF 53

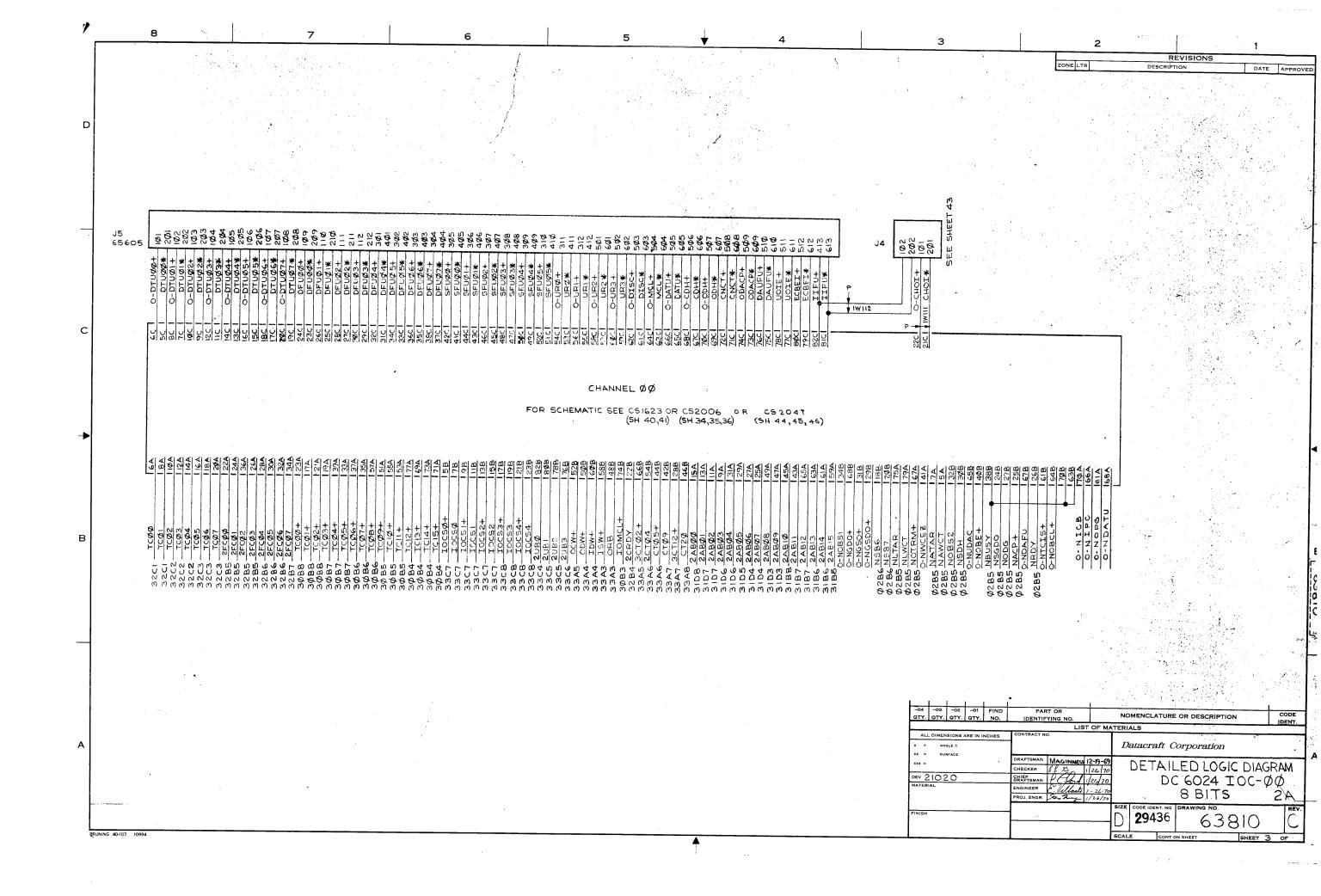
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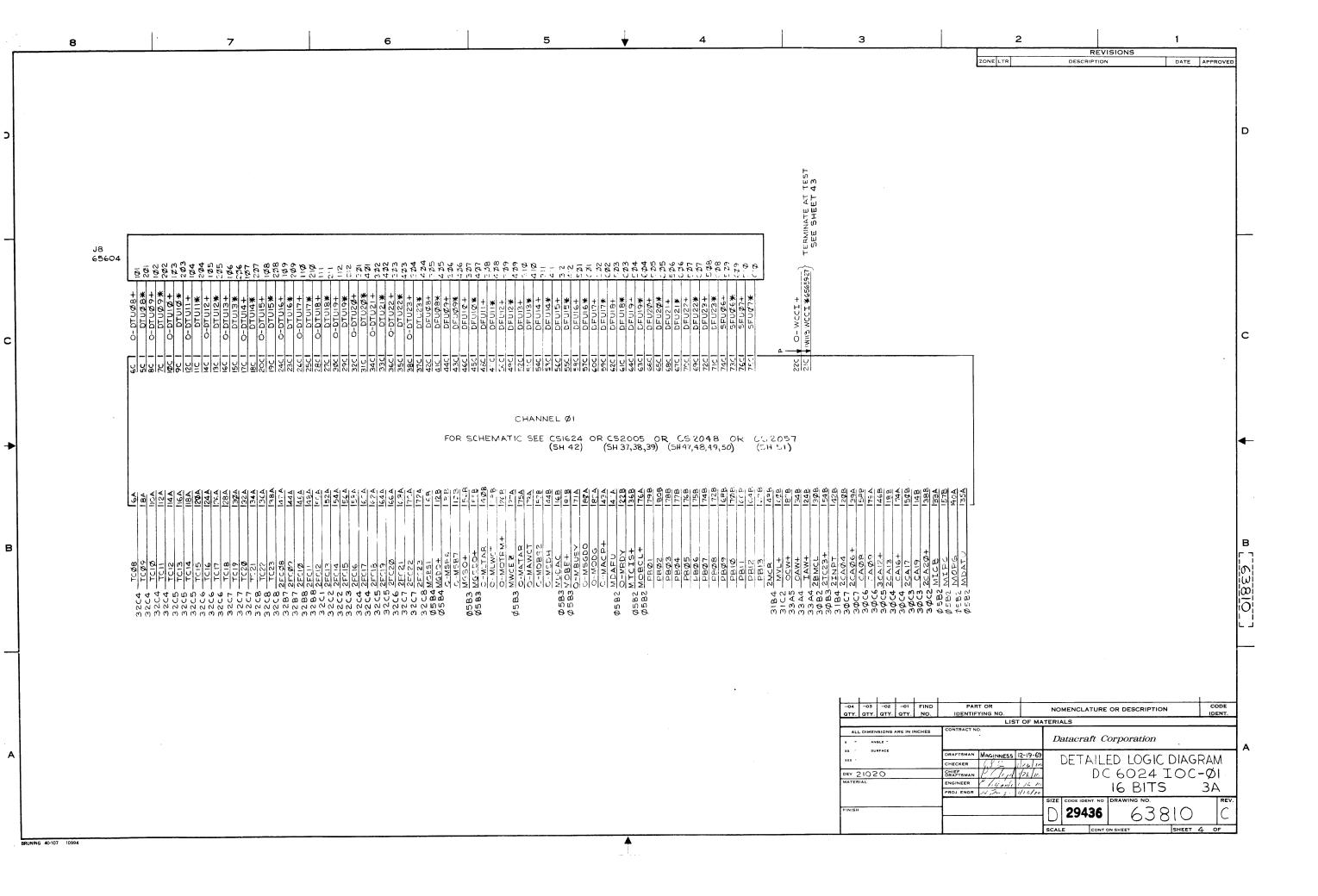


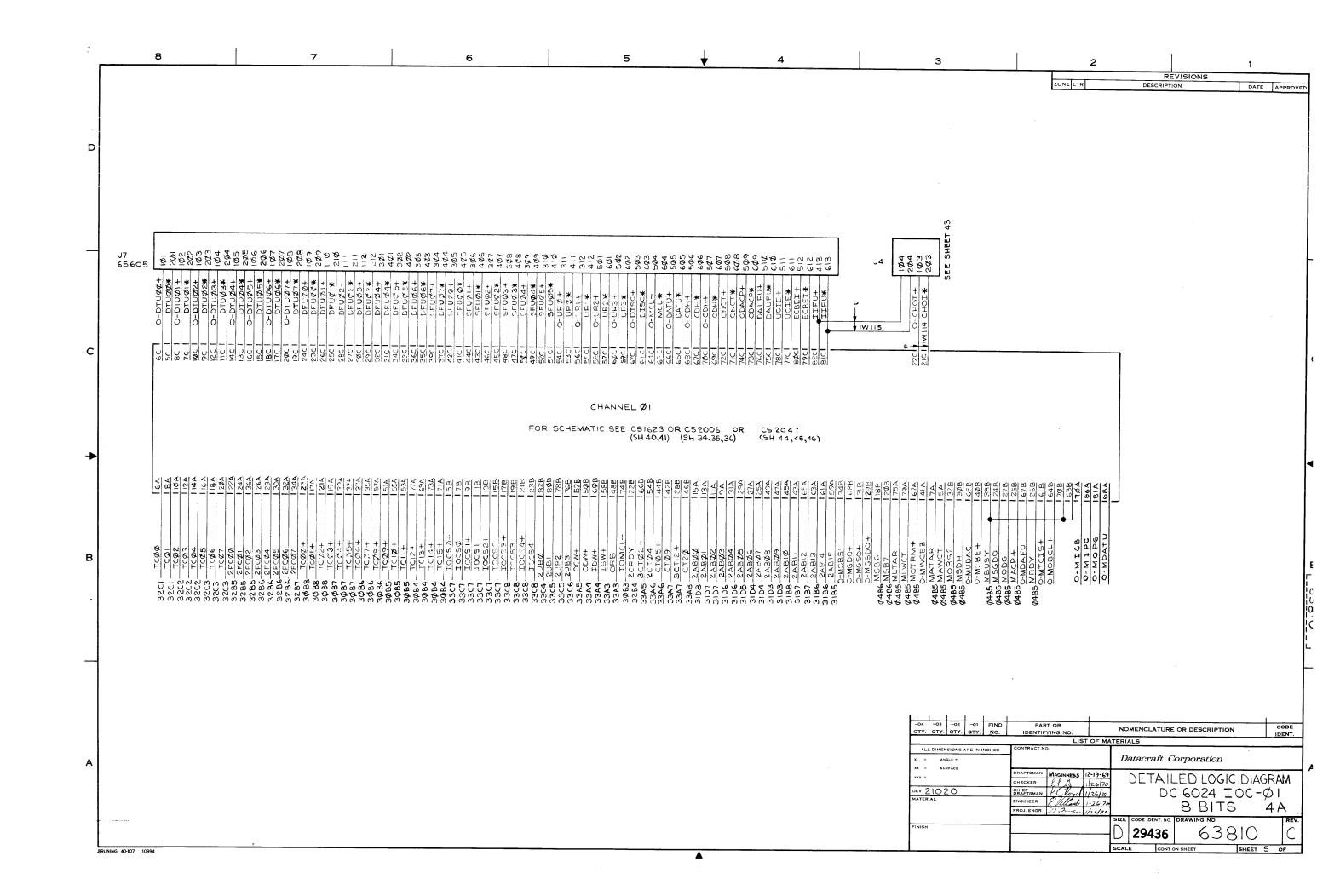
NING 40-107 1099

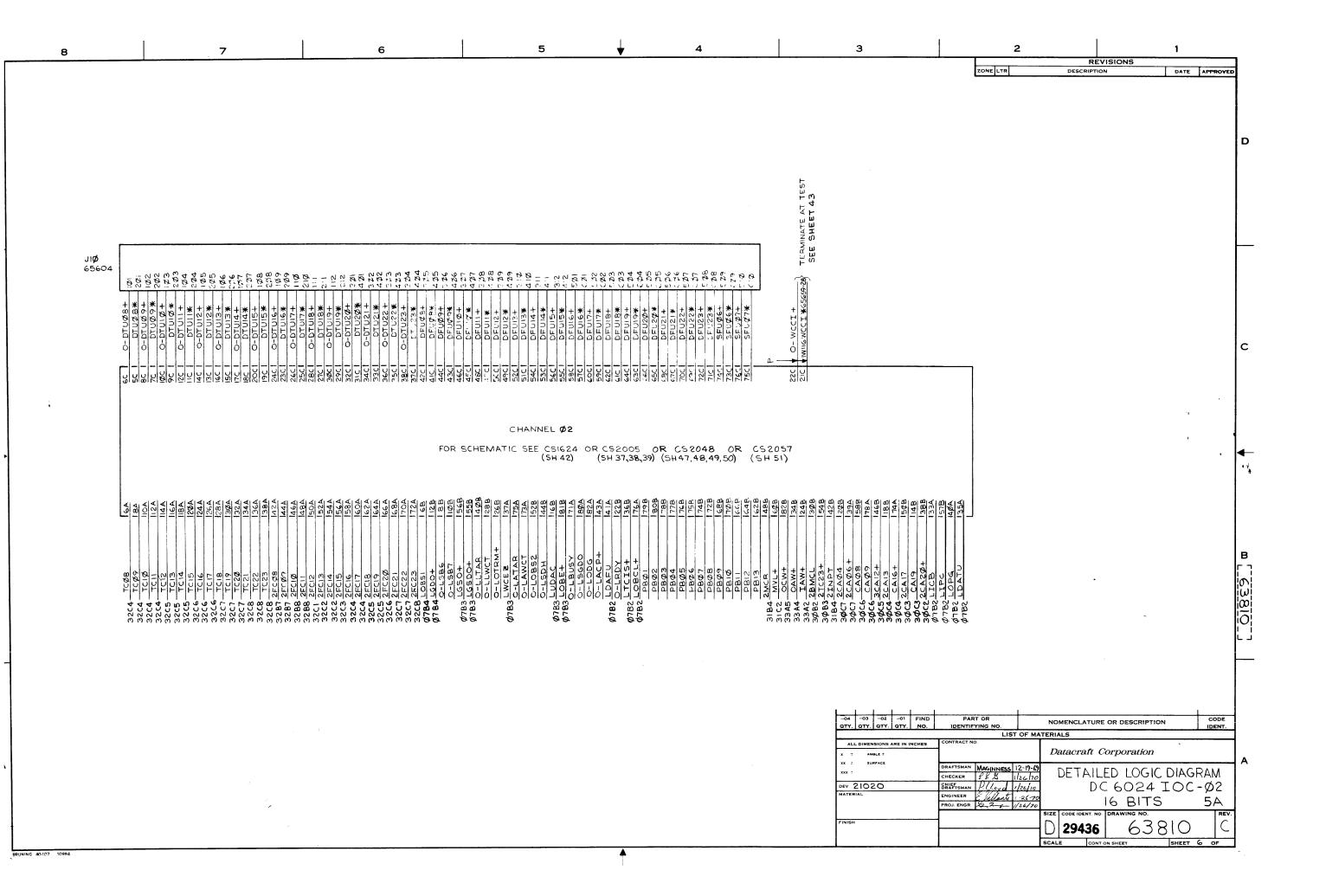
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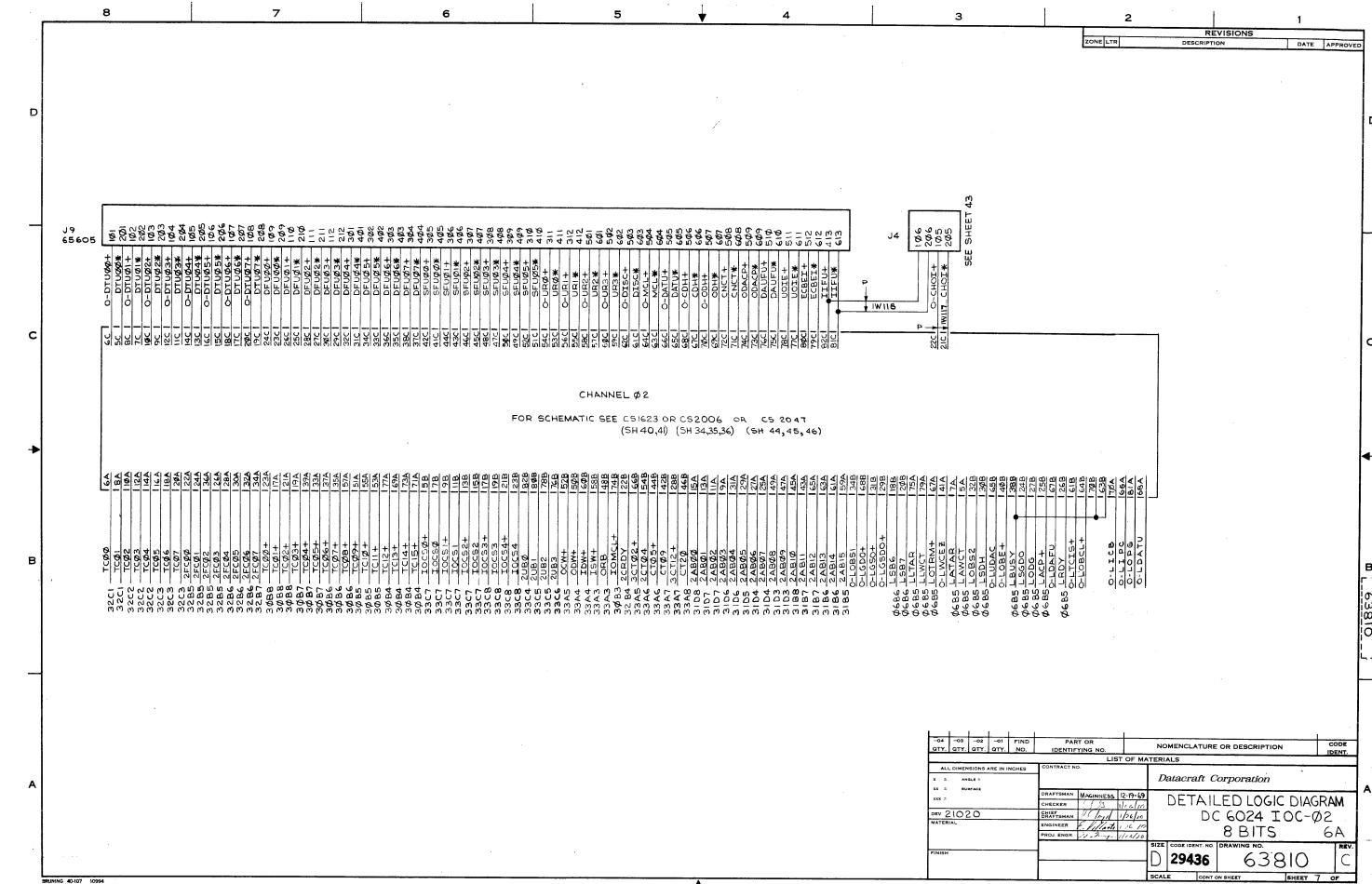
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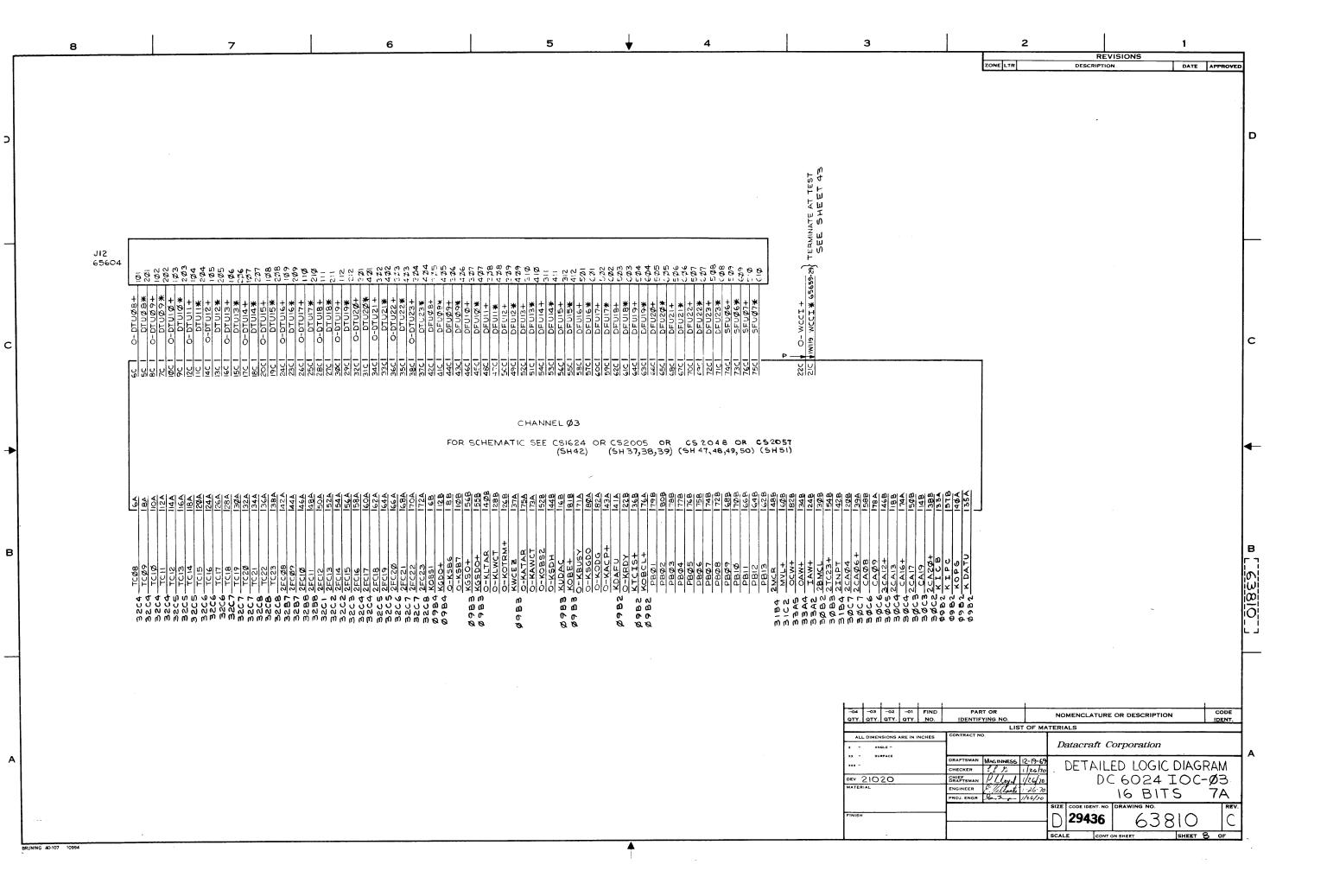


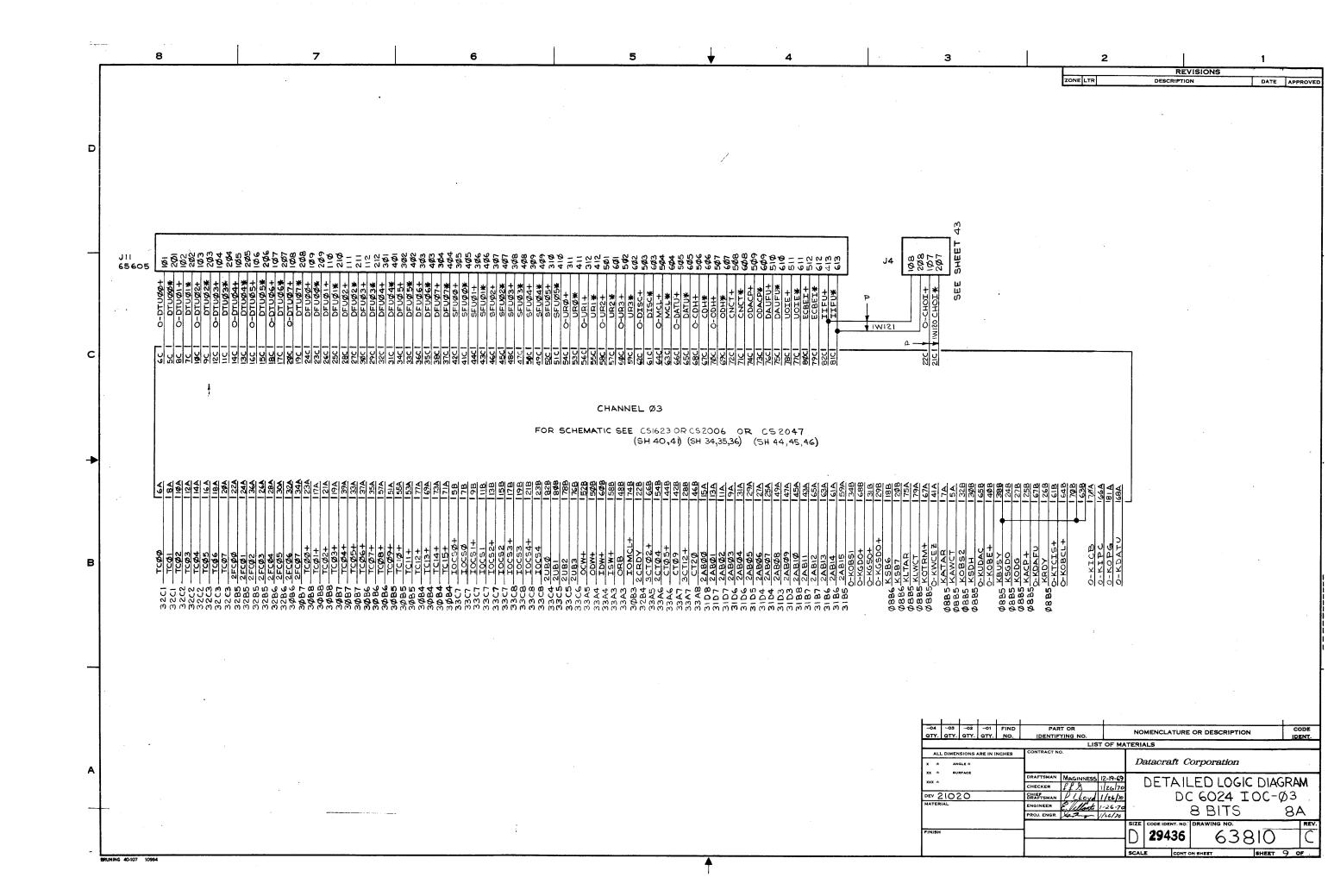


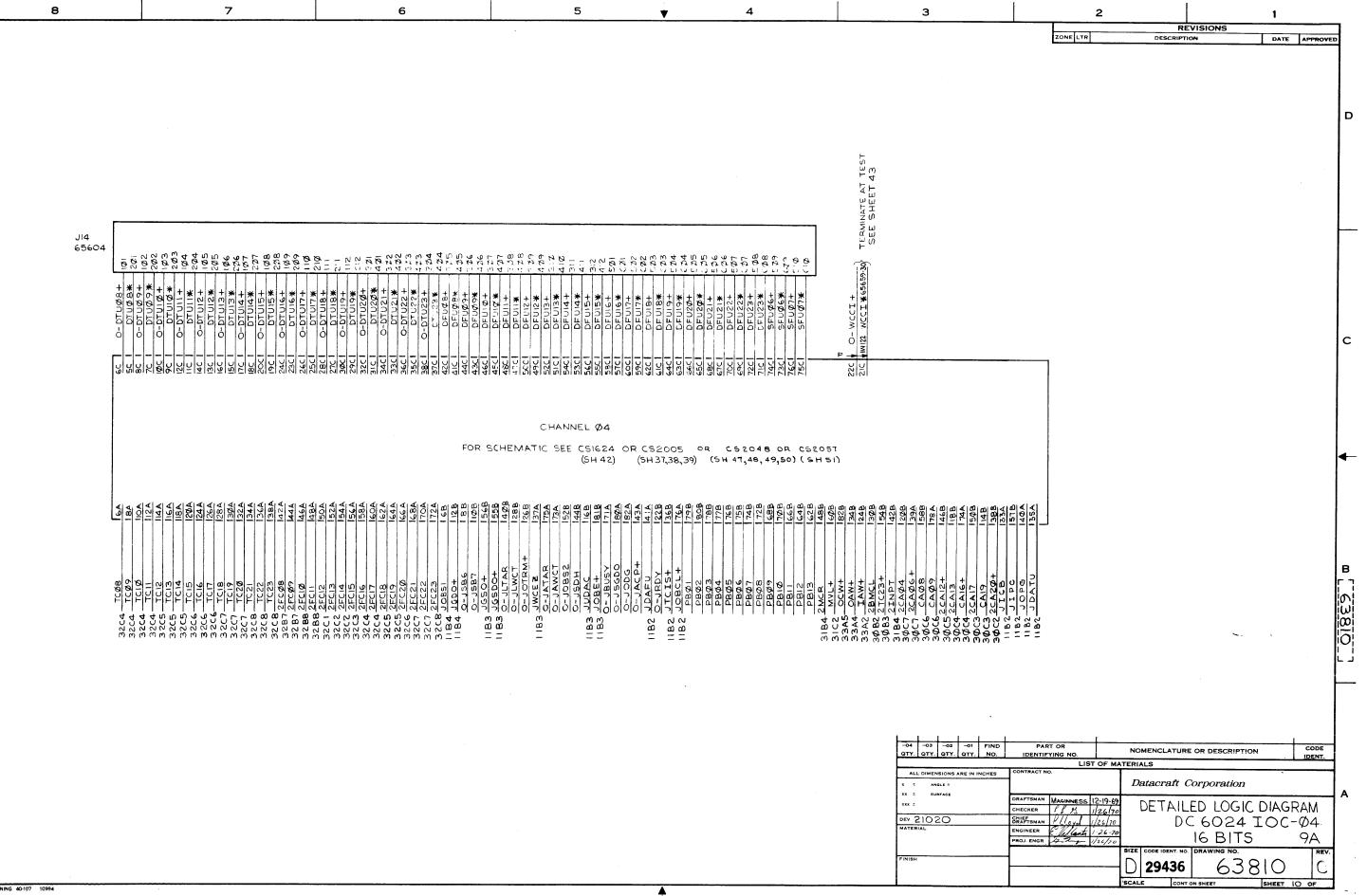


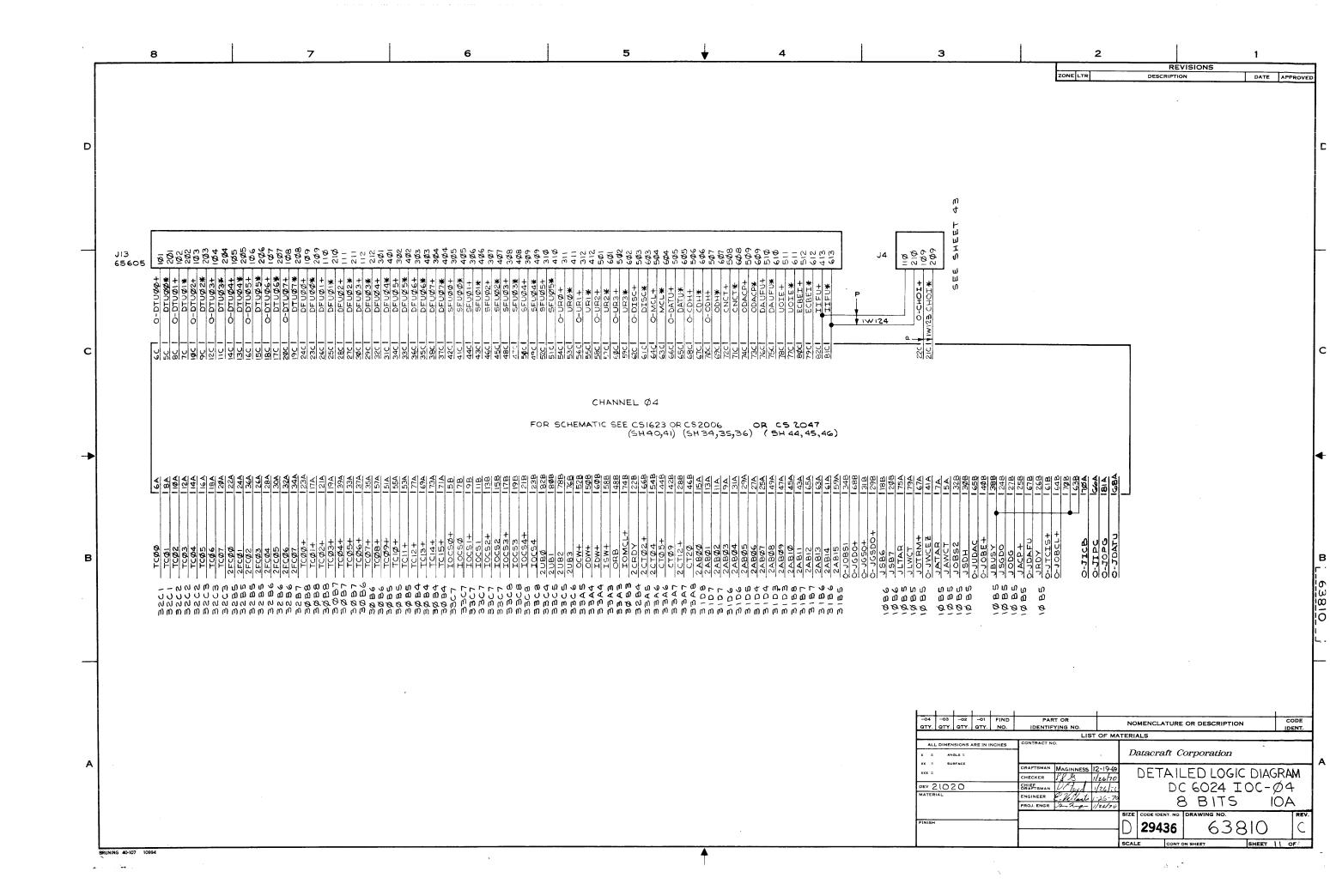


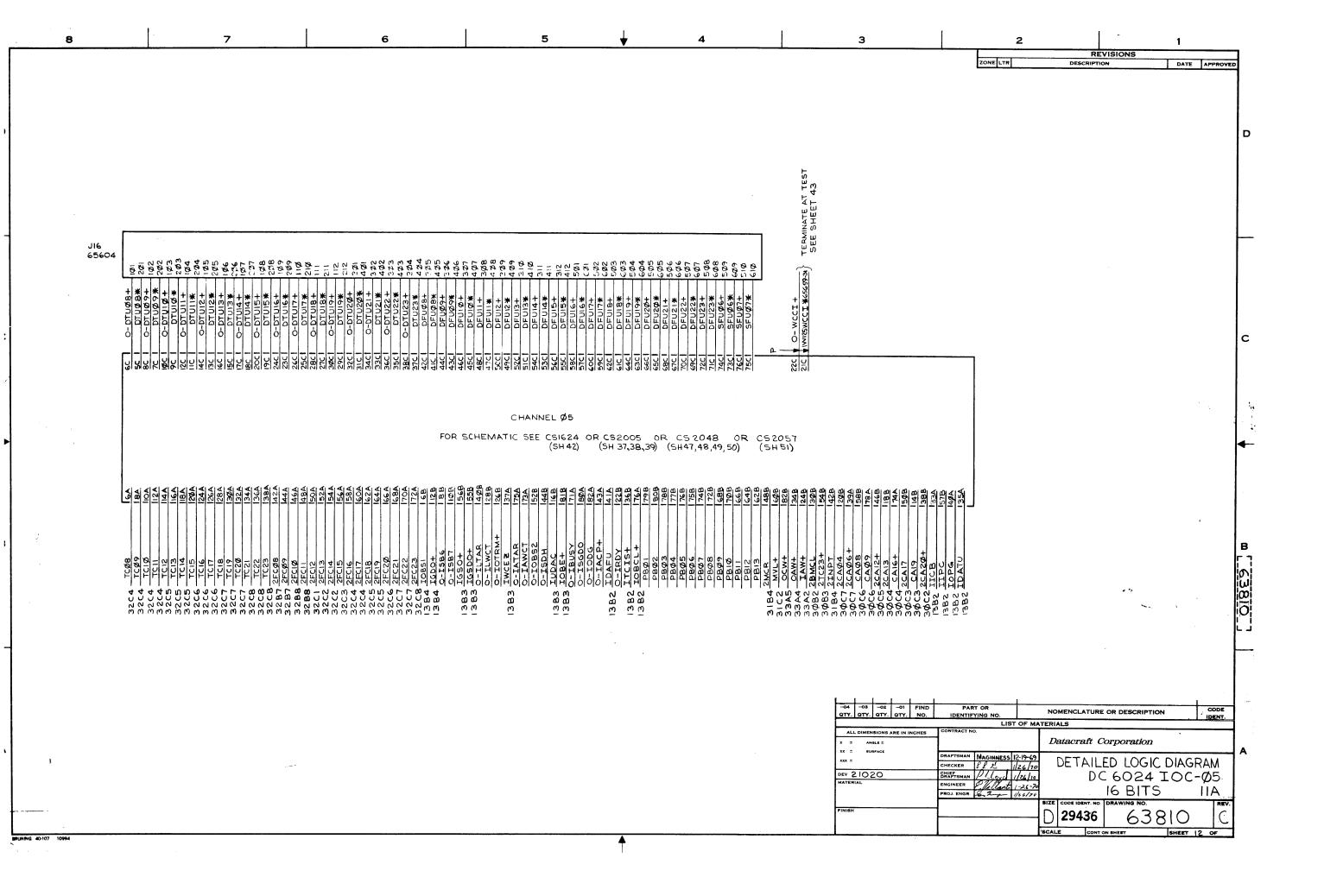


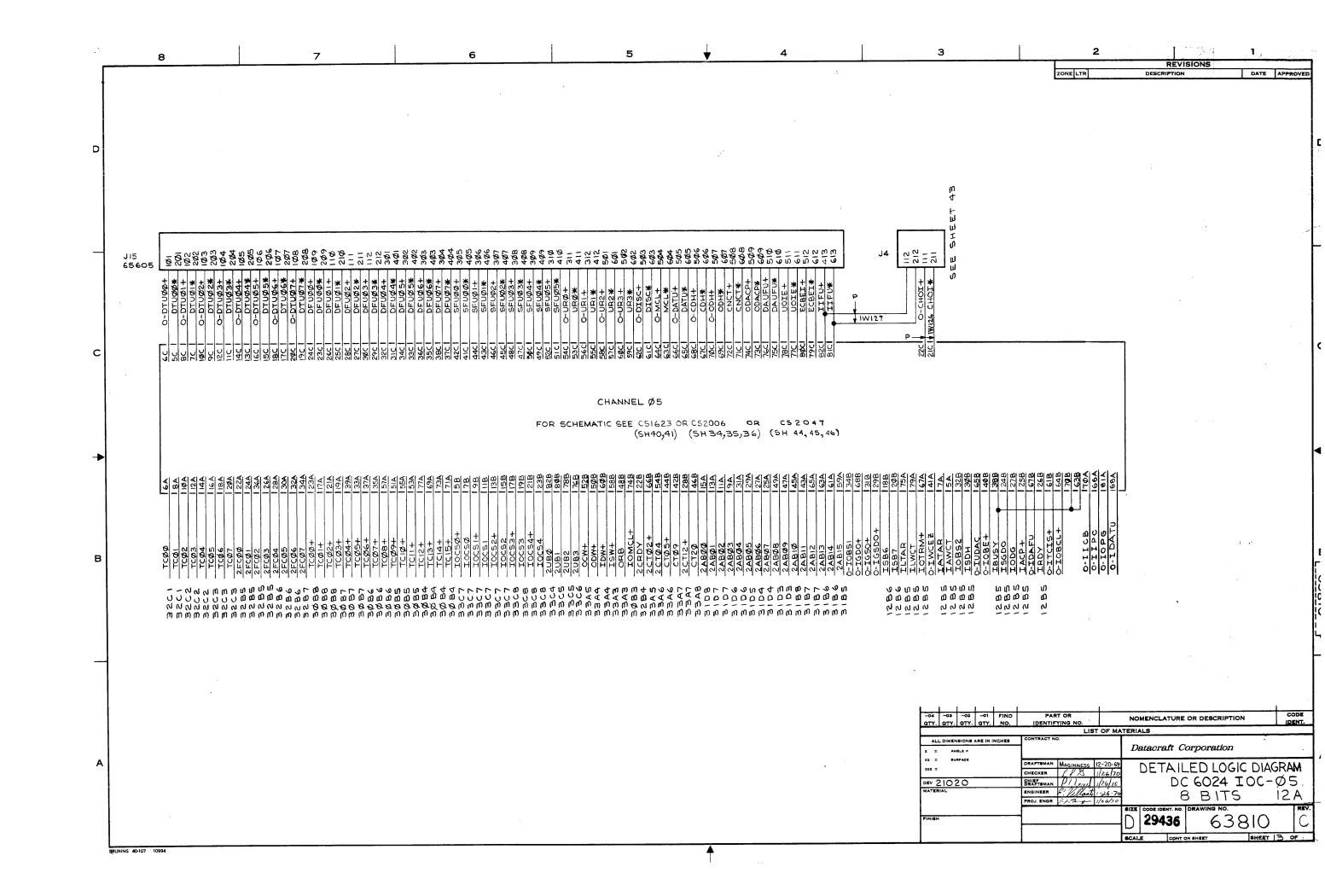


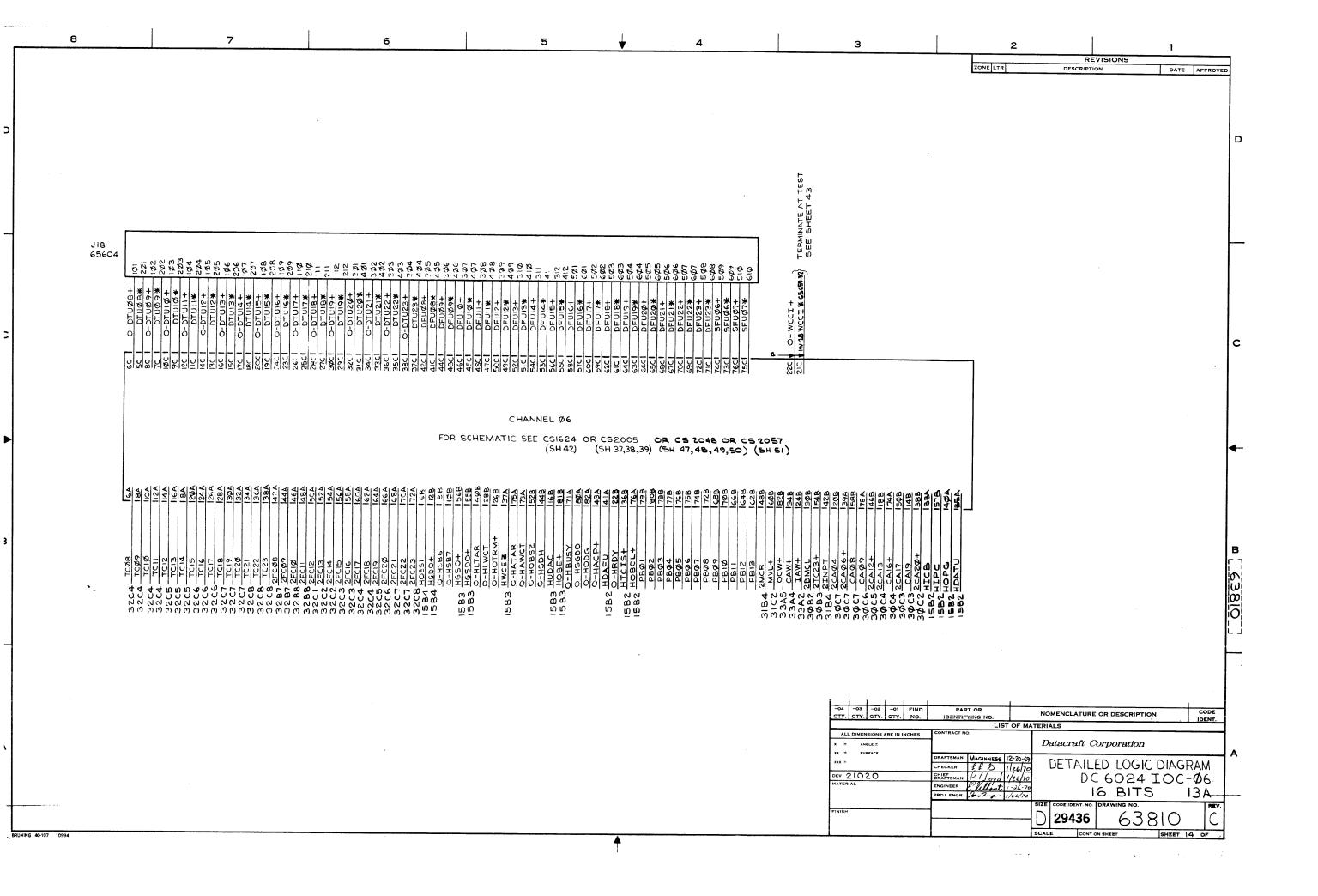


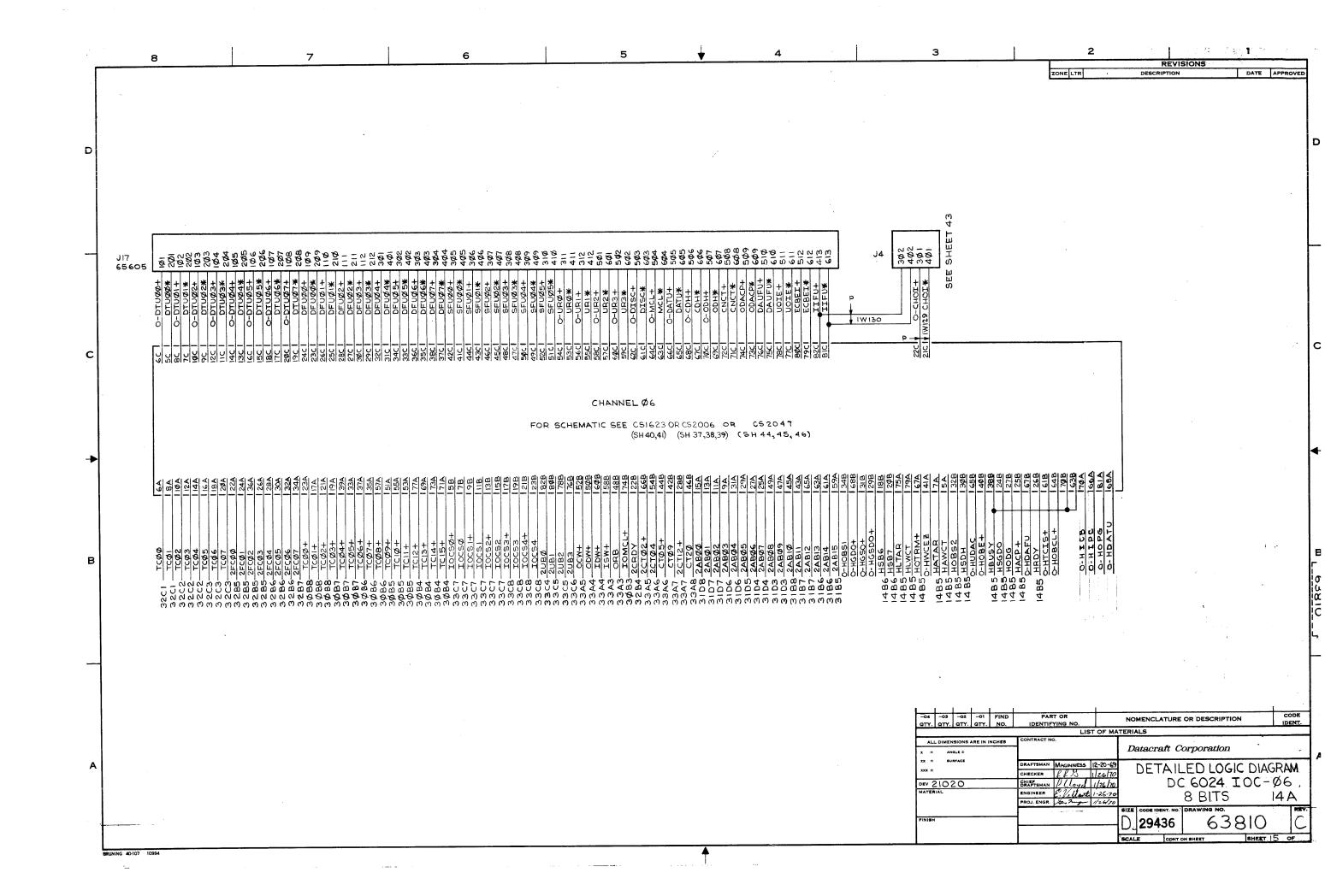


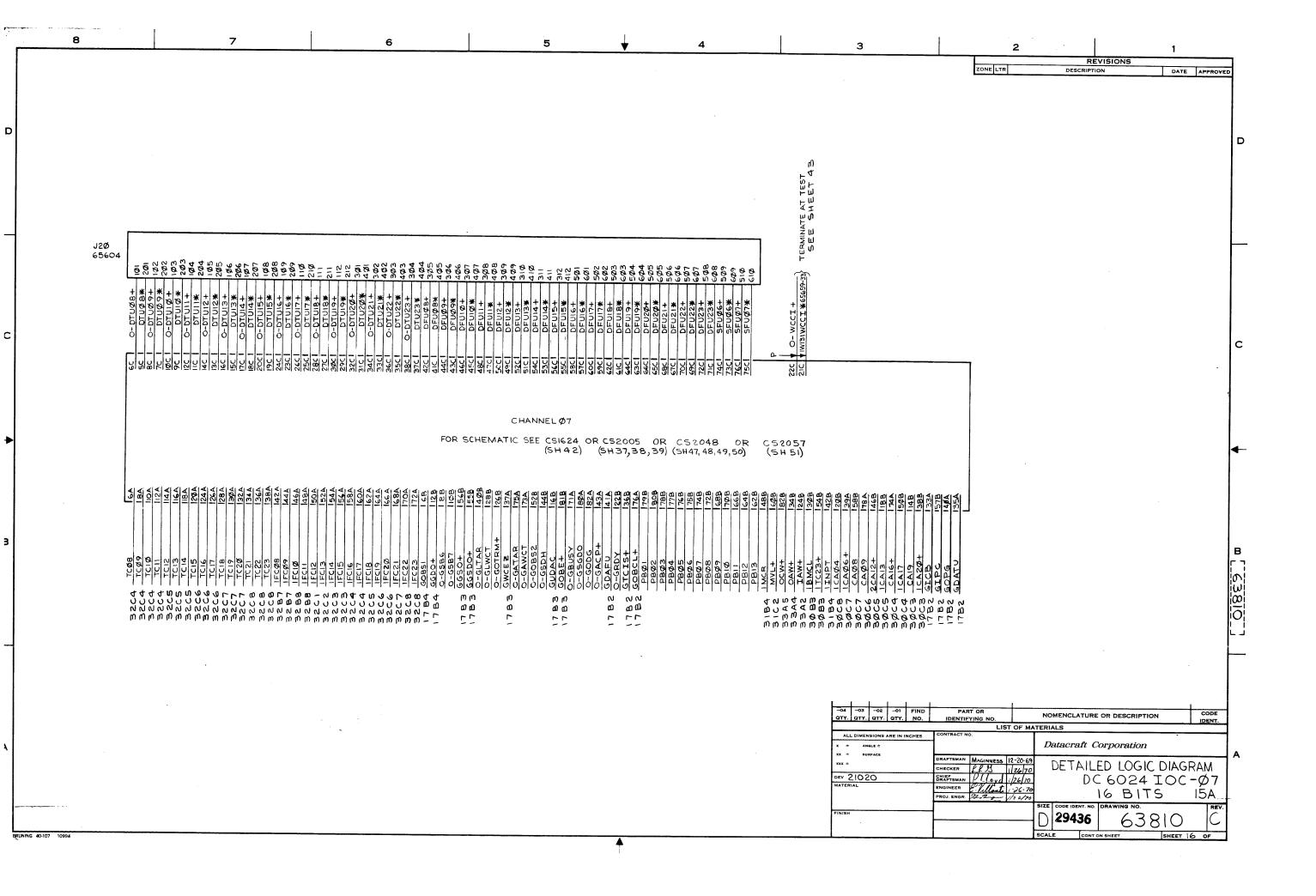


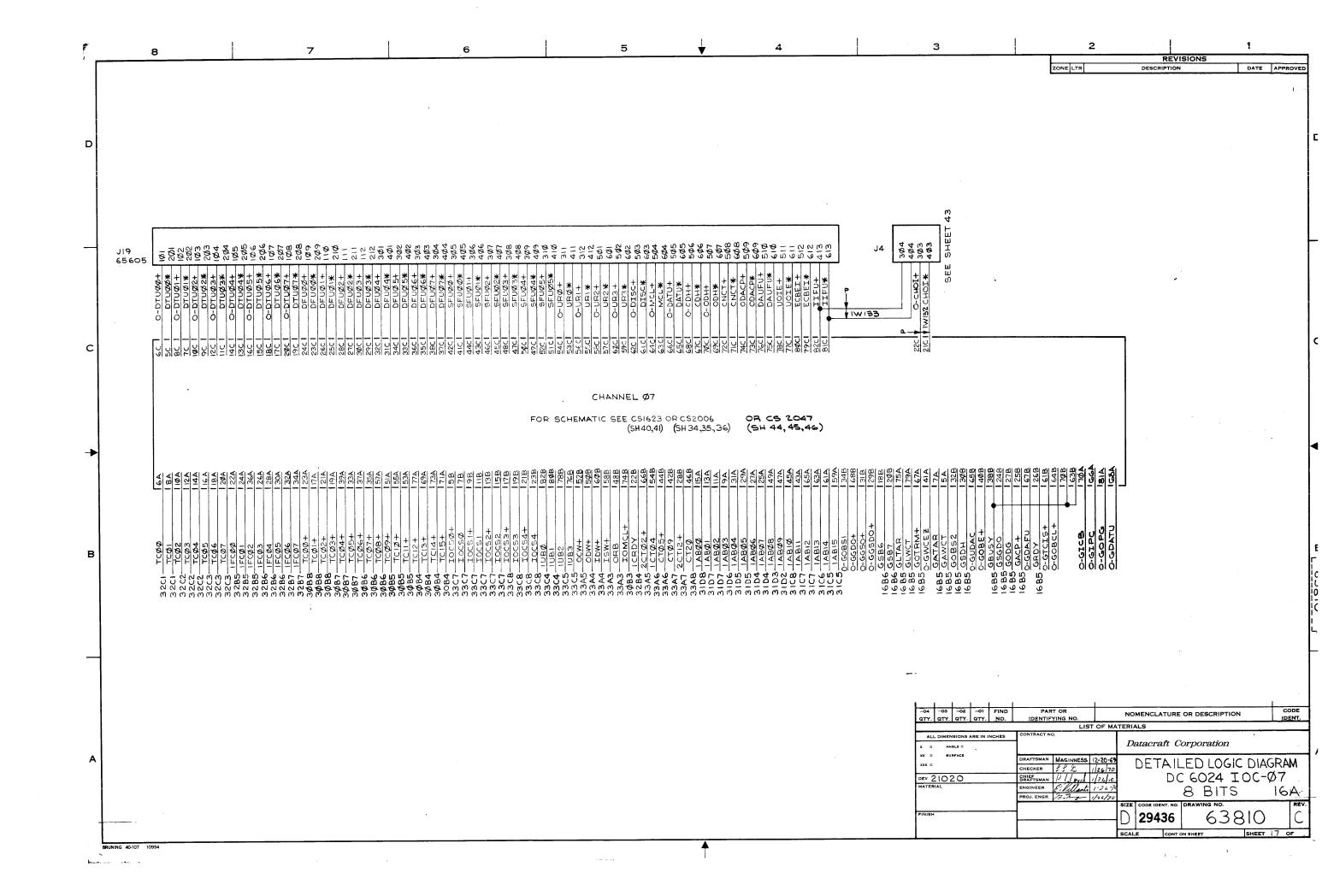


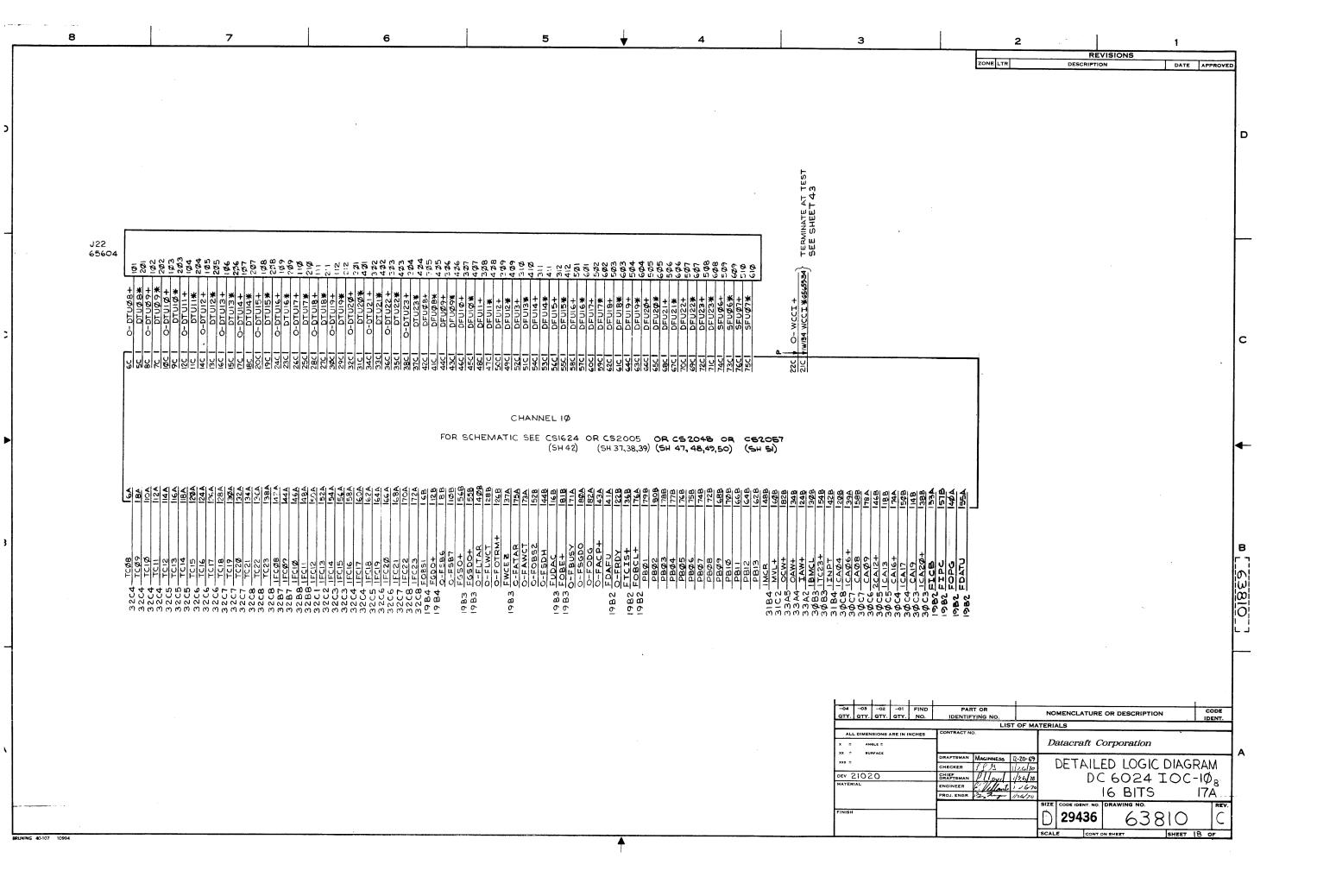


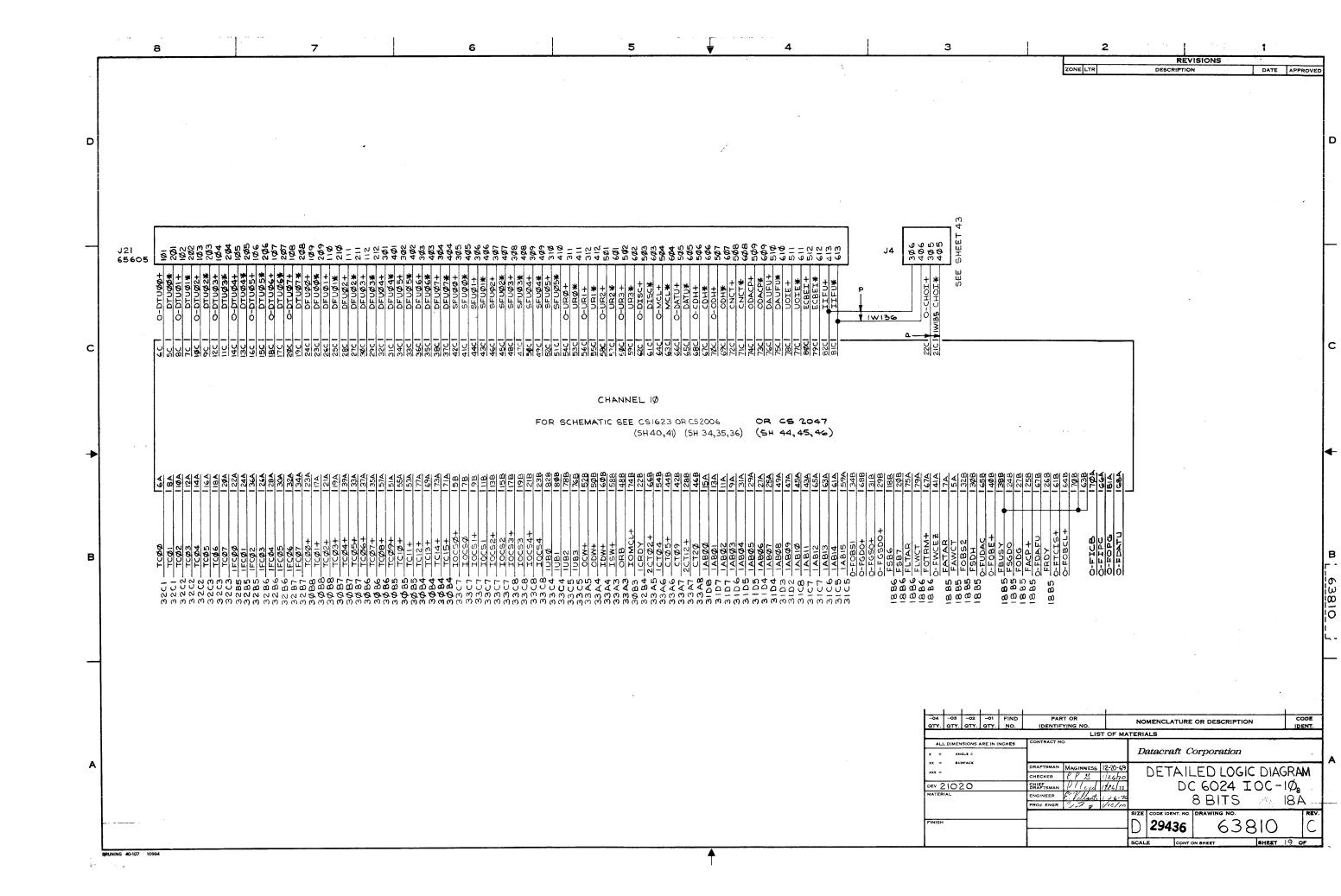


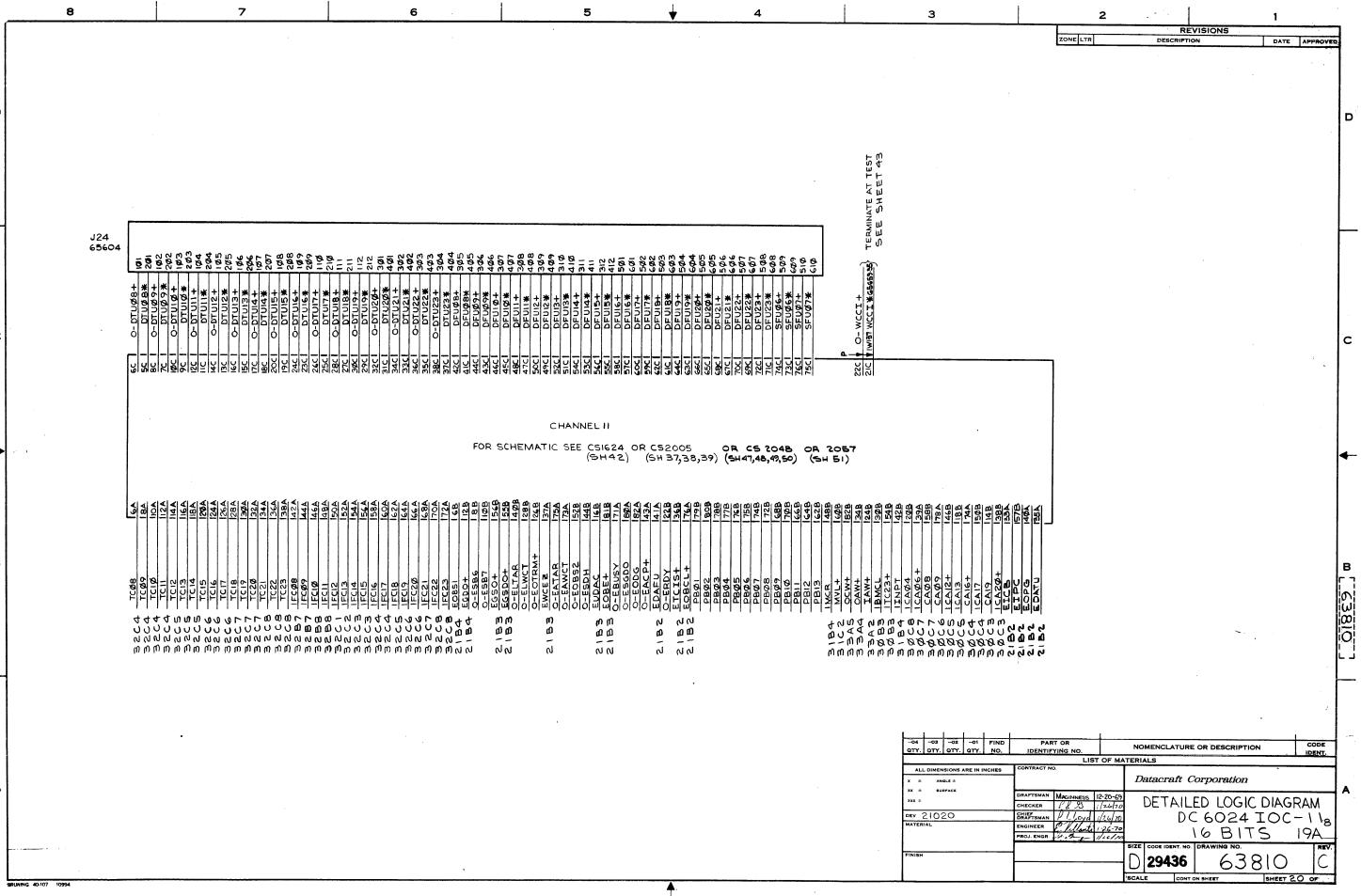


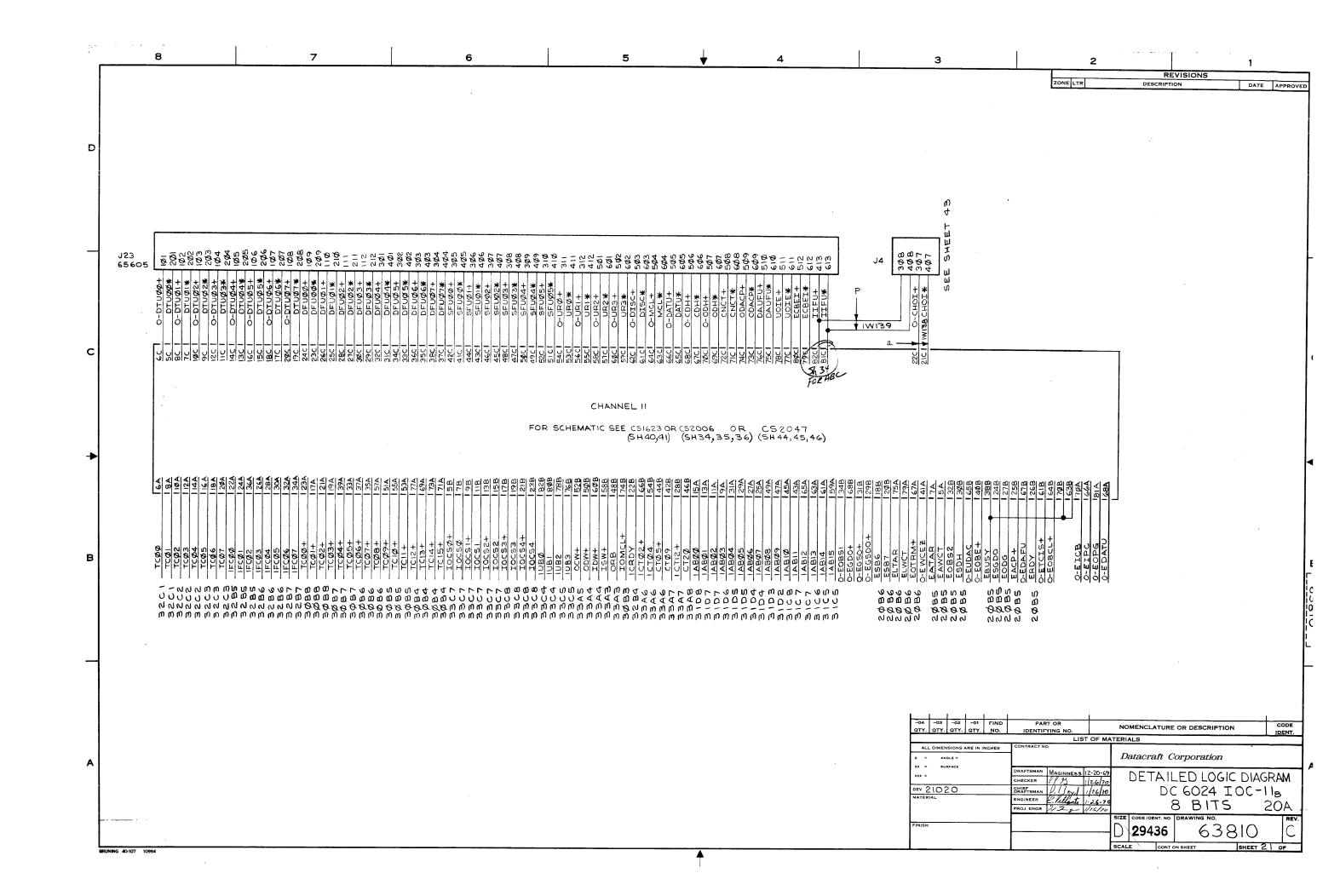


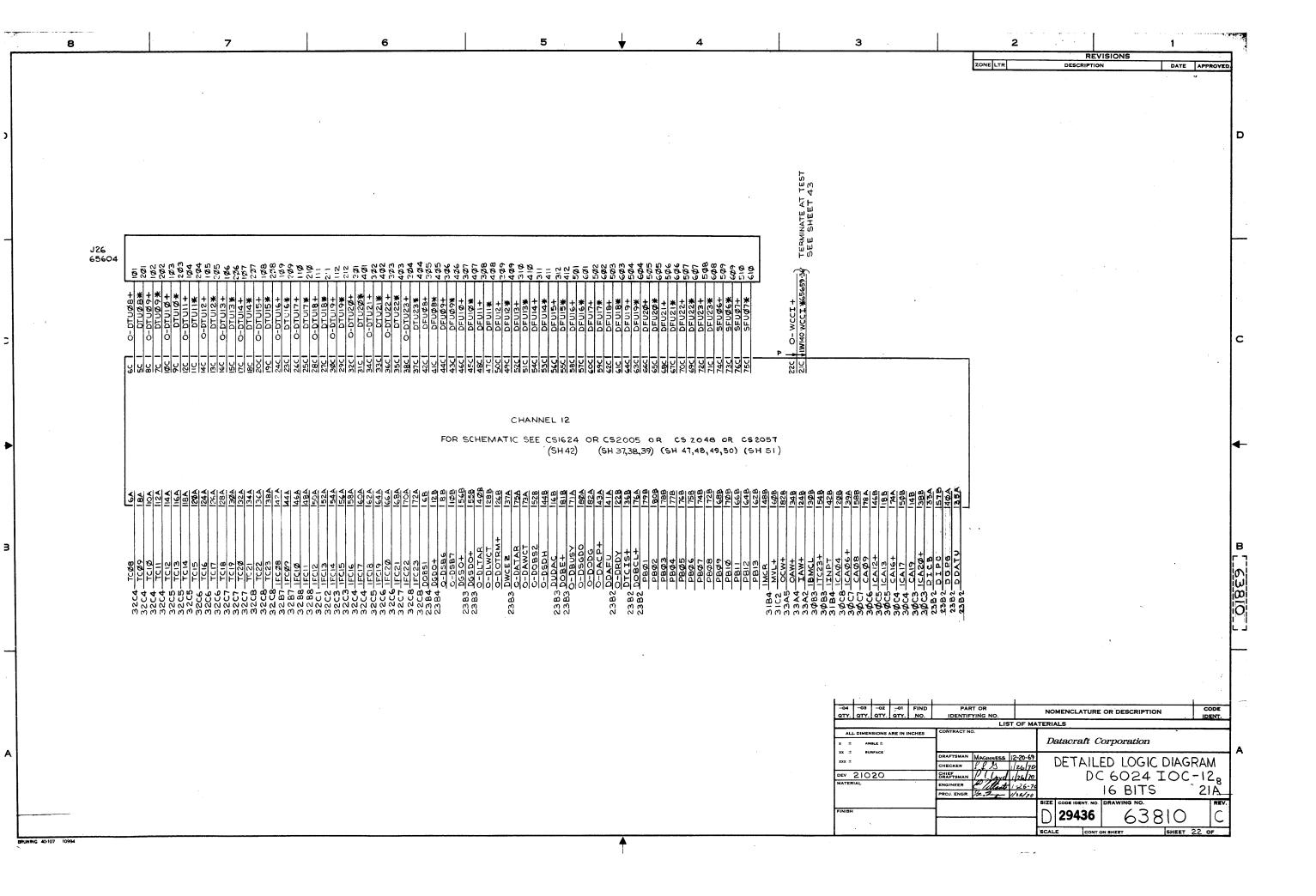


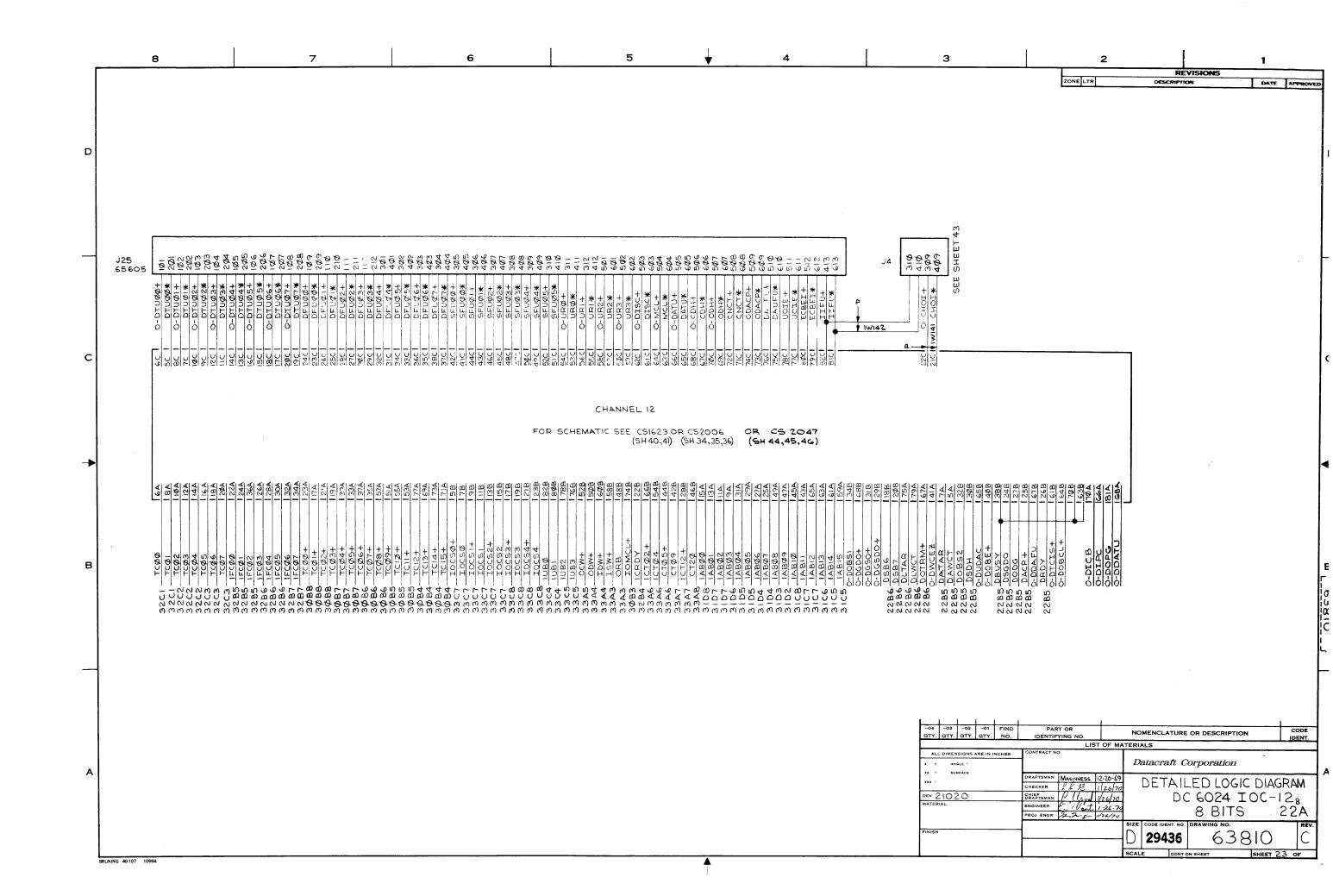


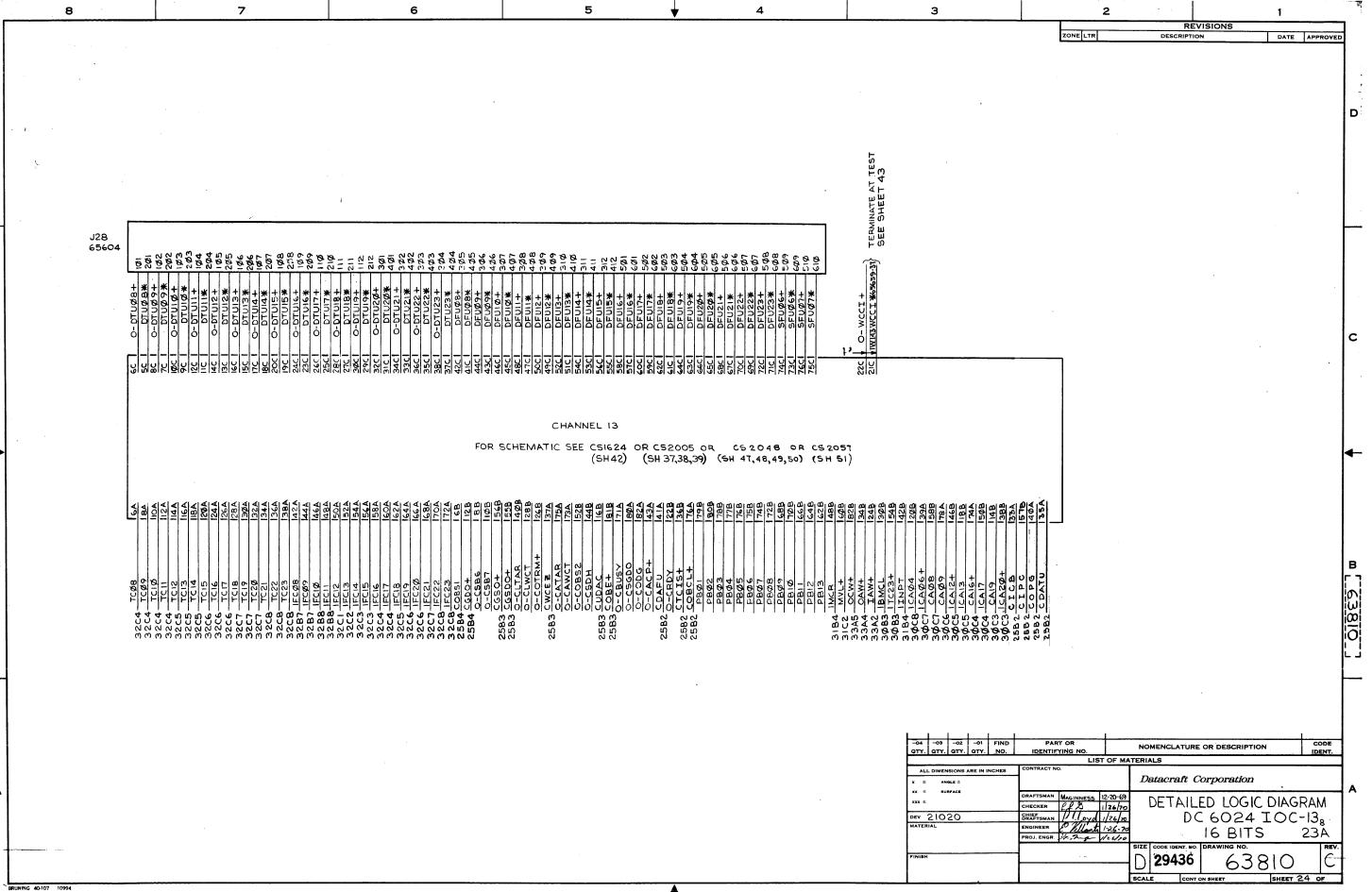


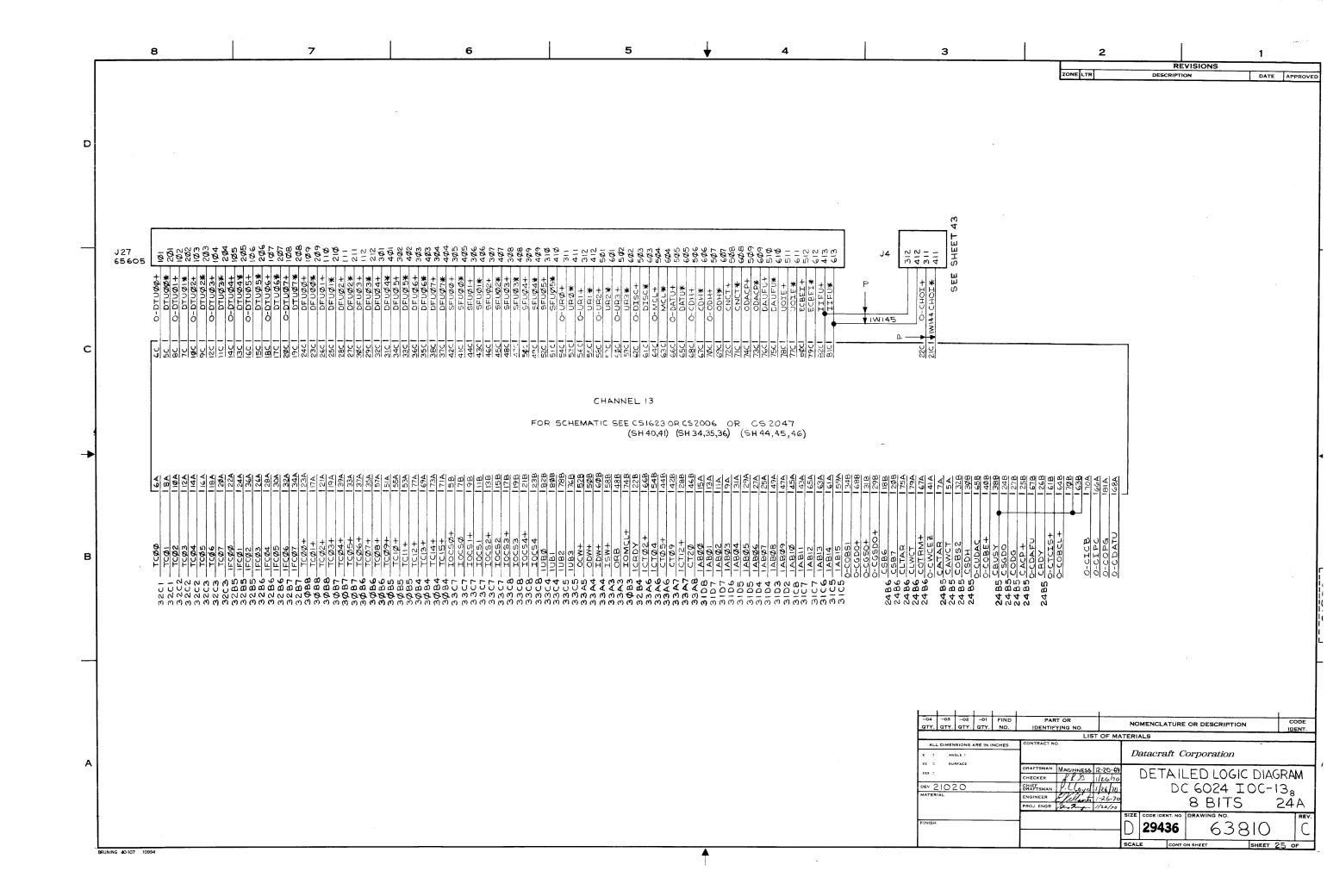


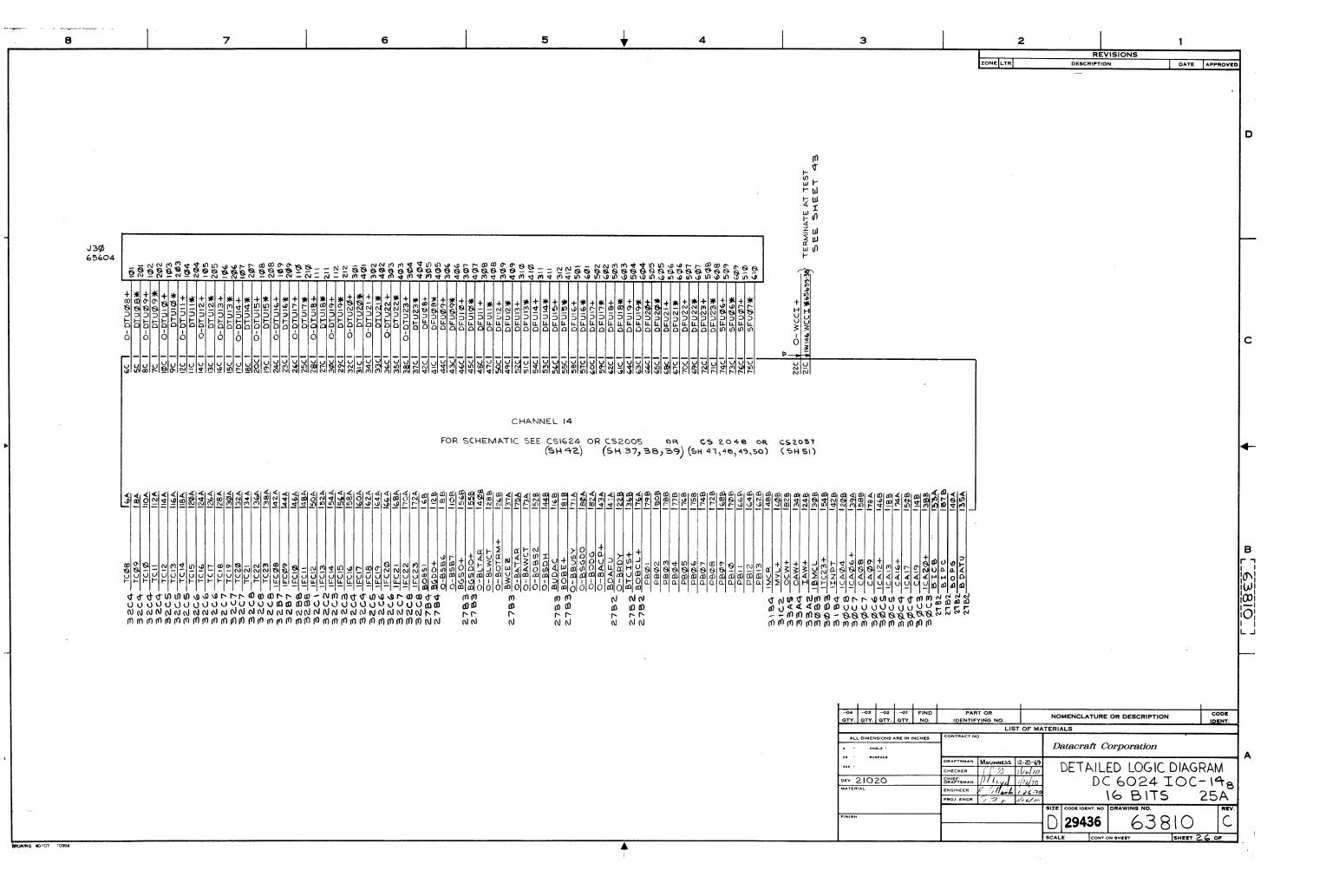


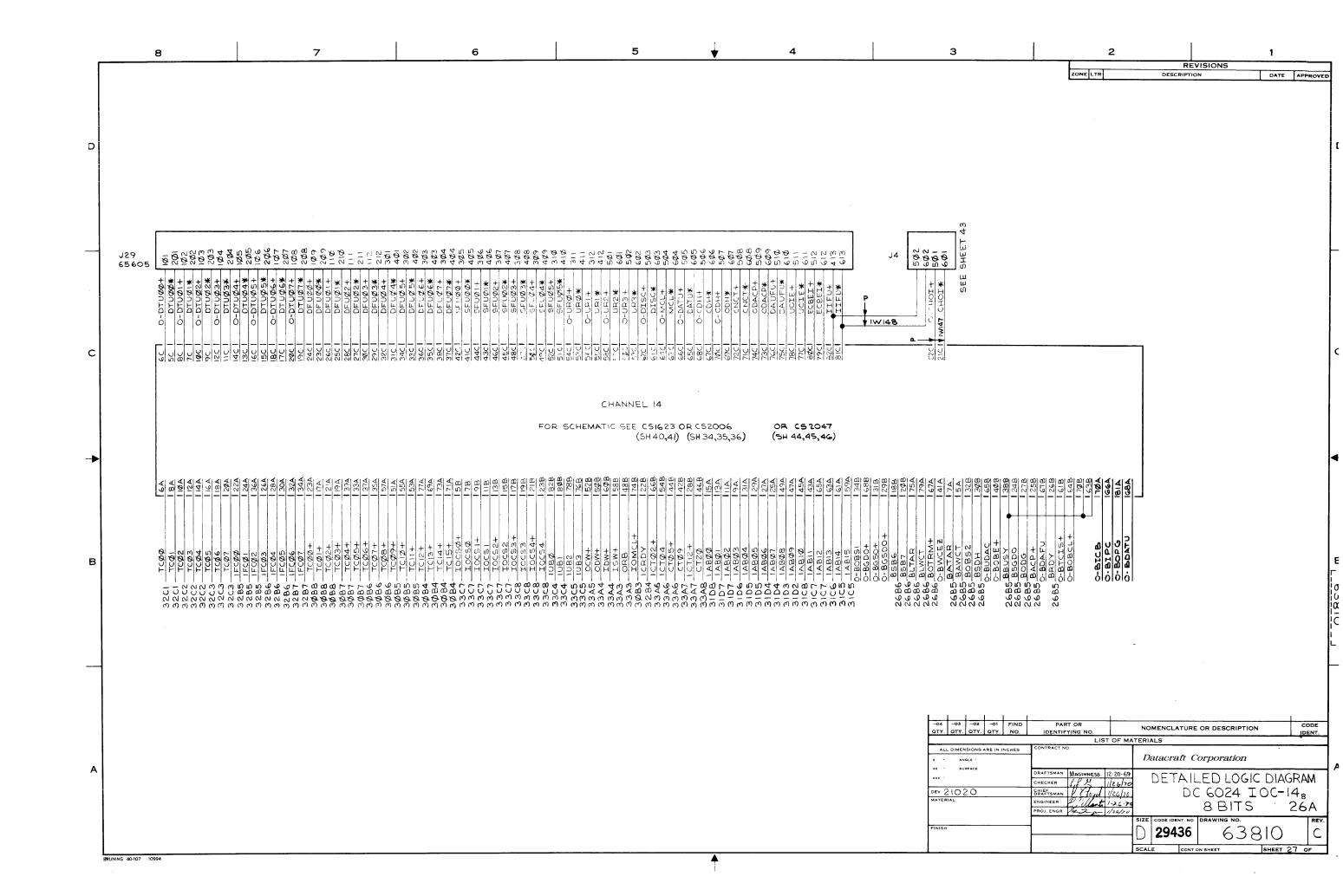


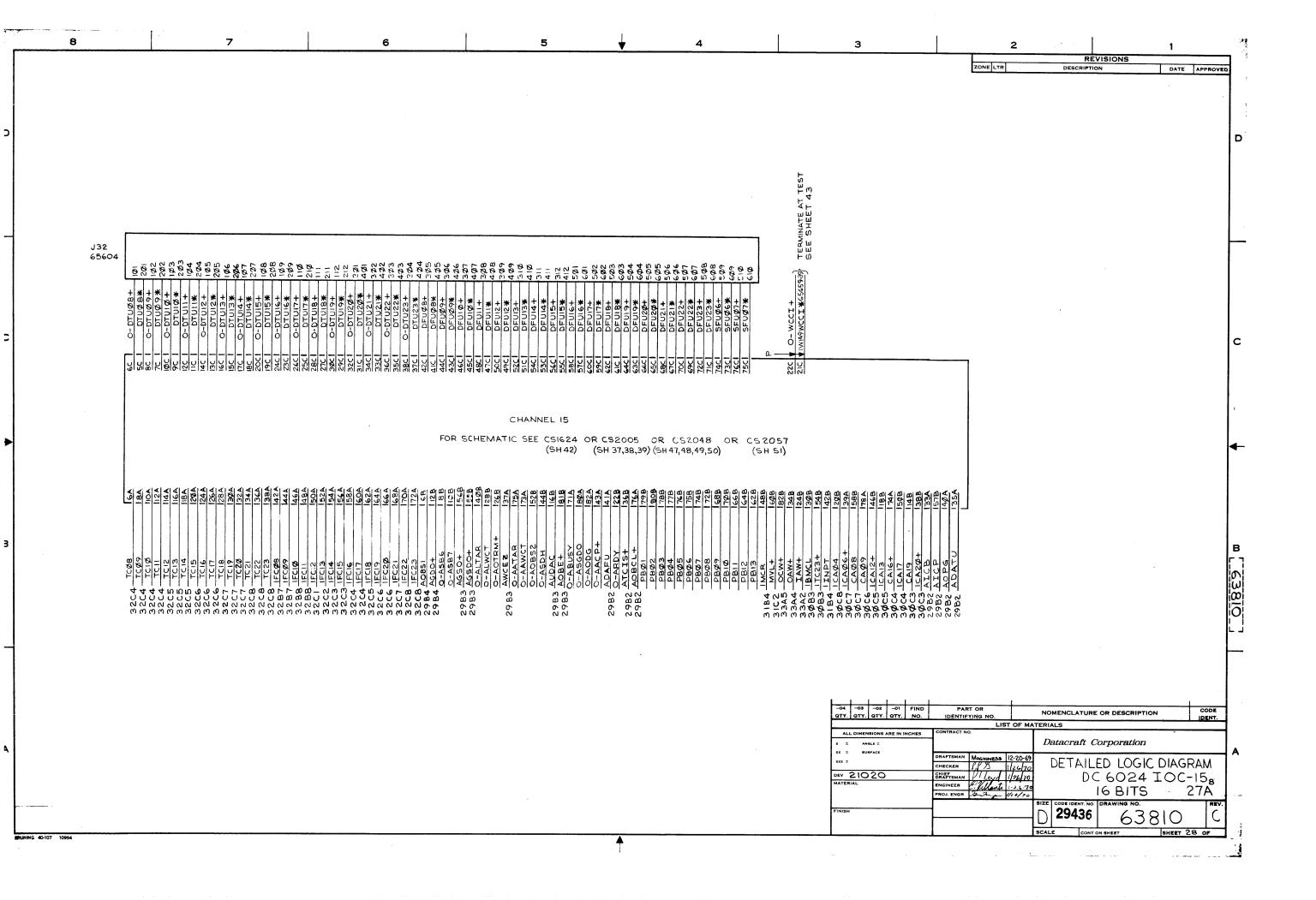


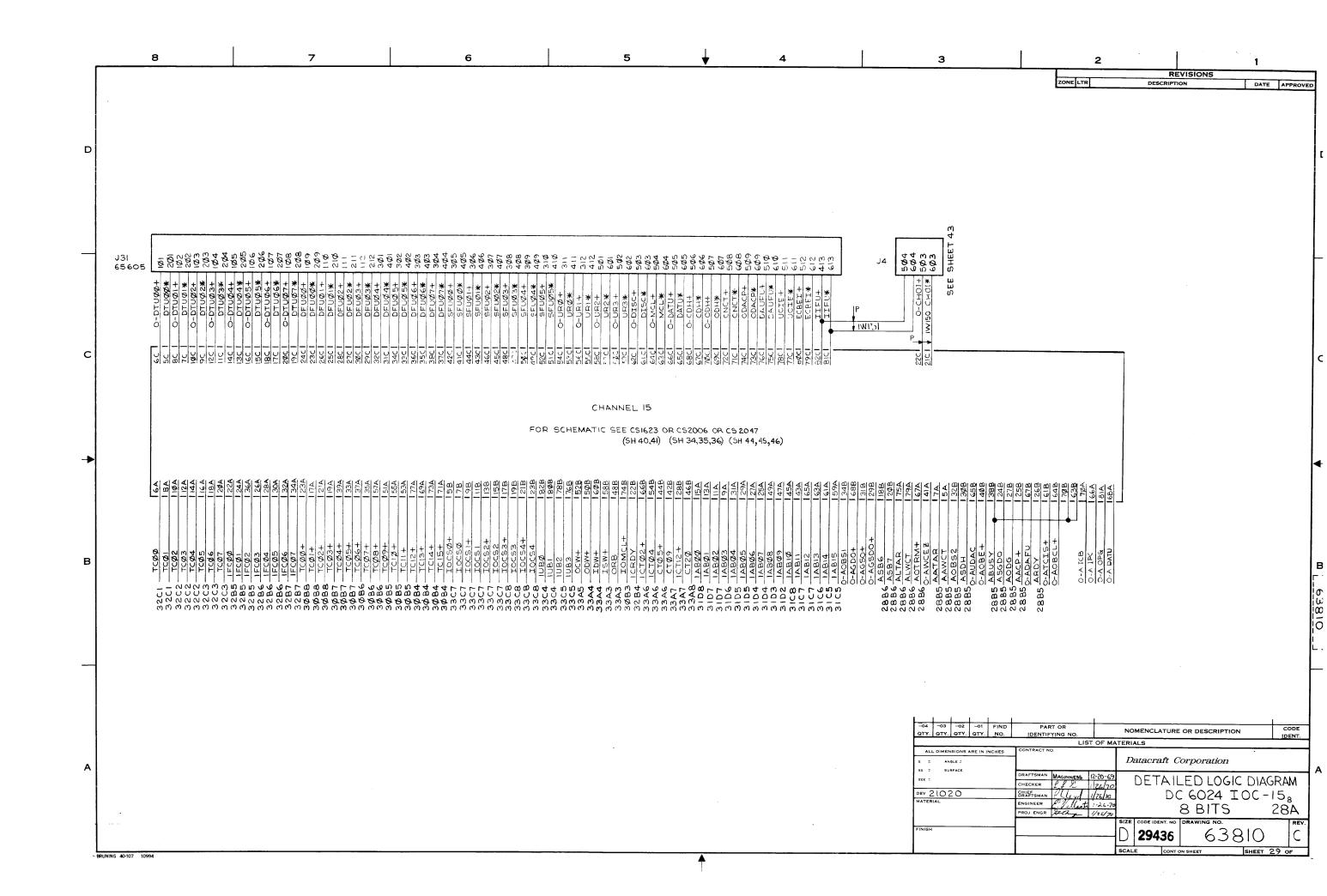


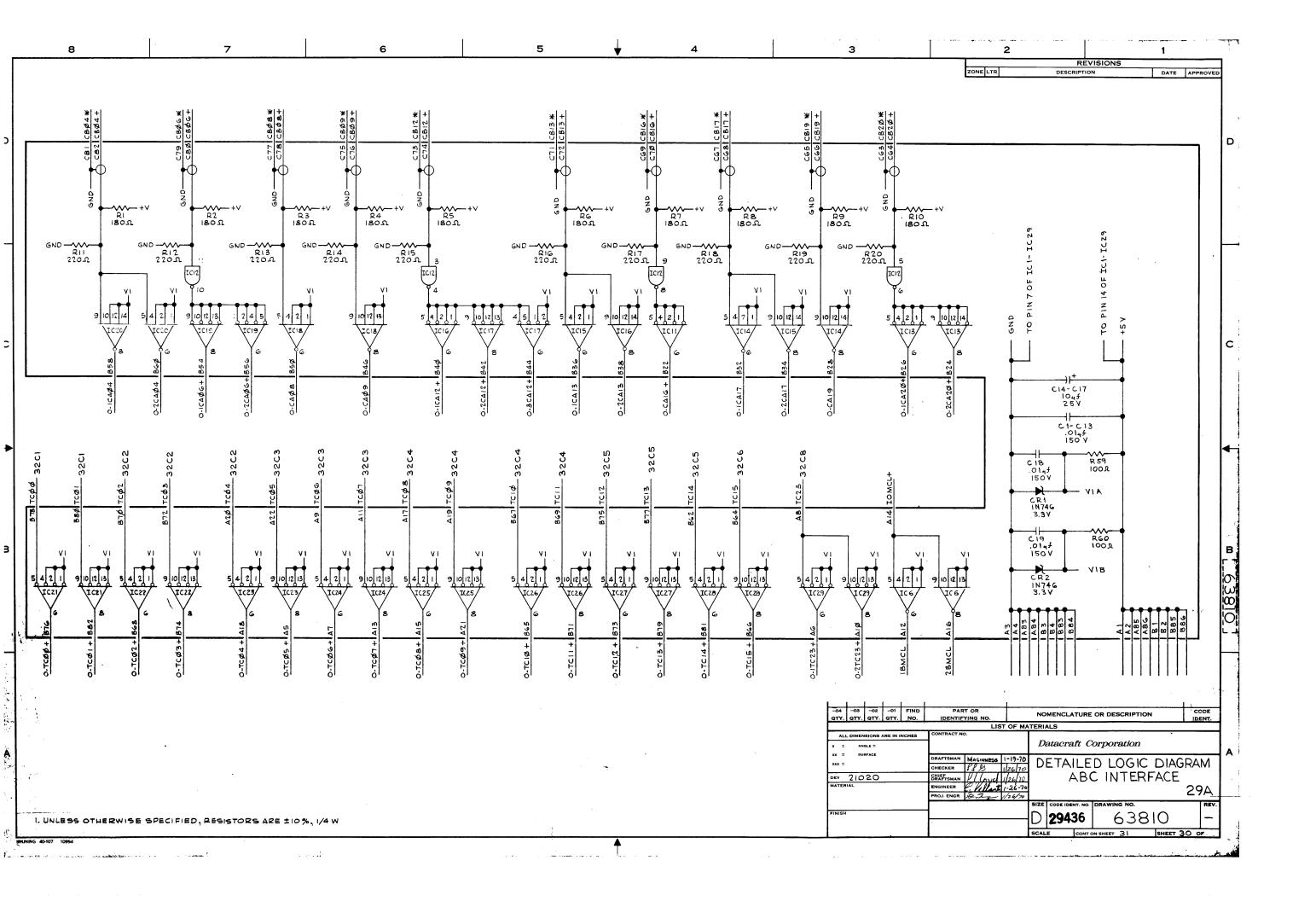


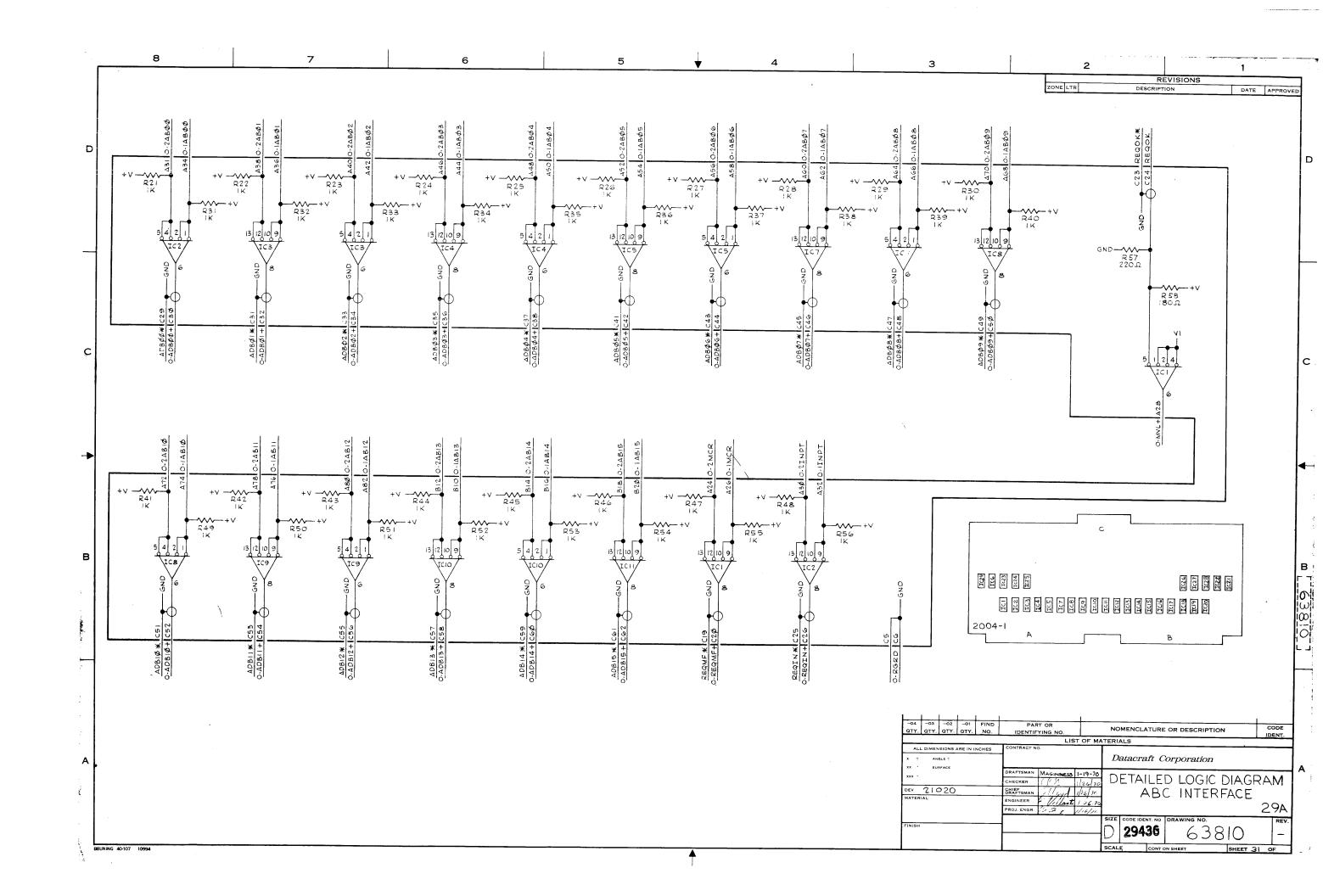


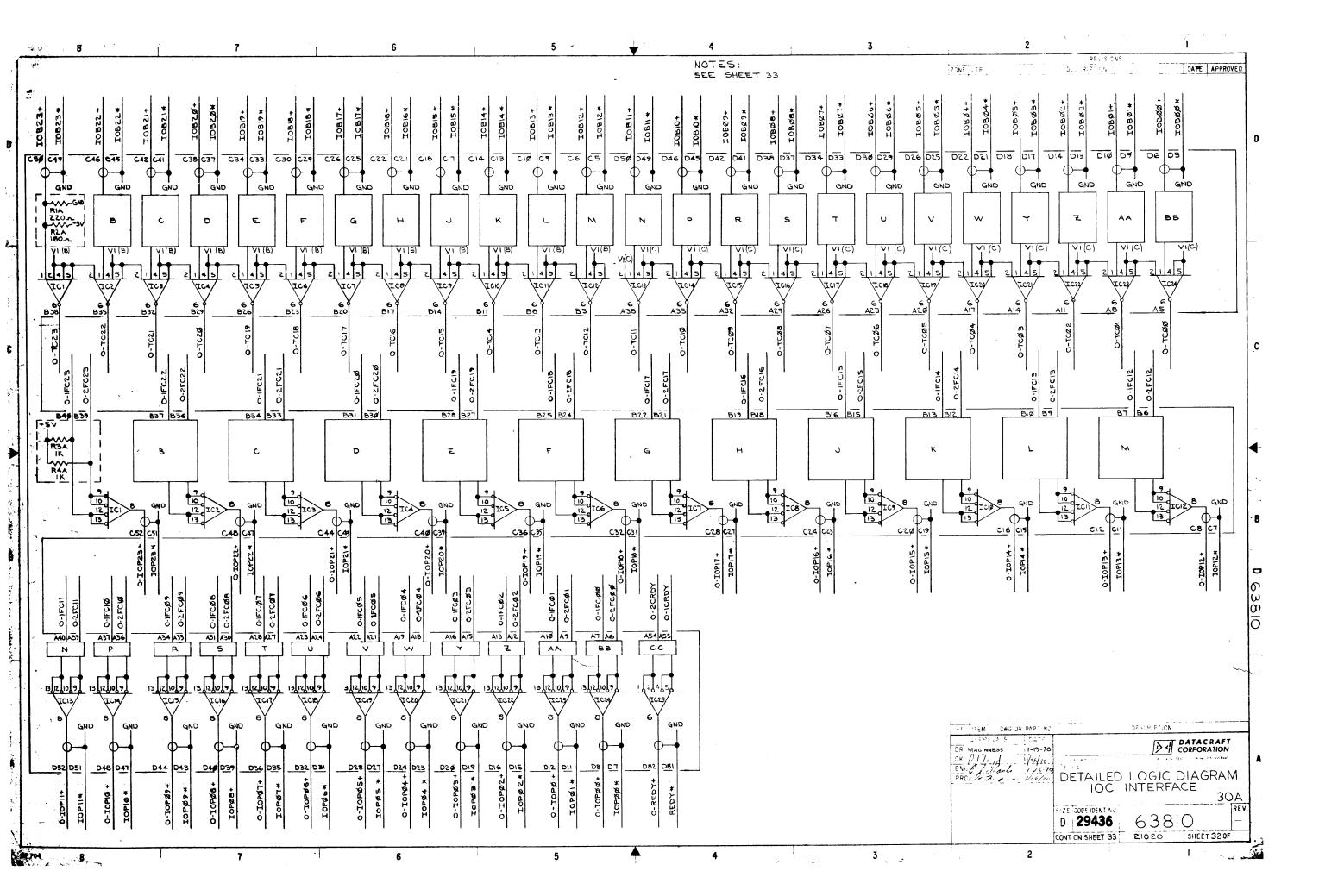


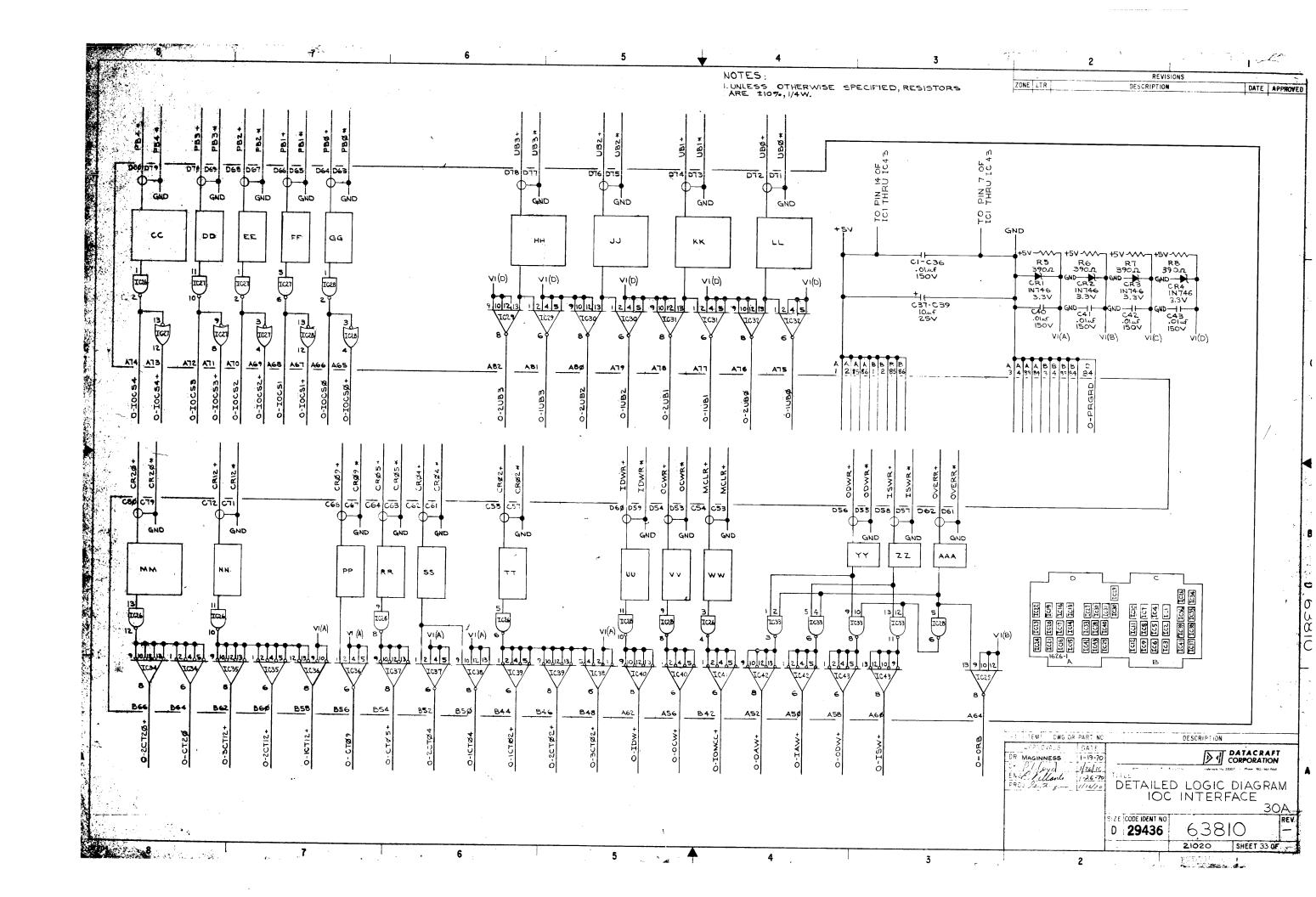


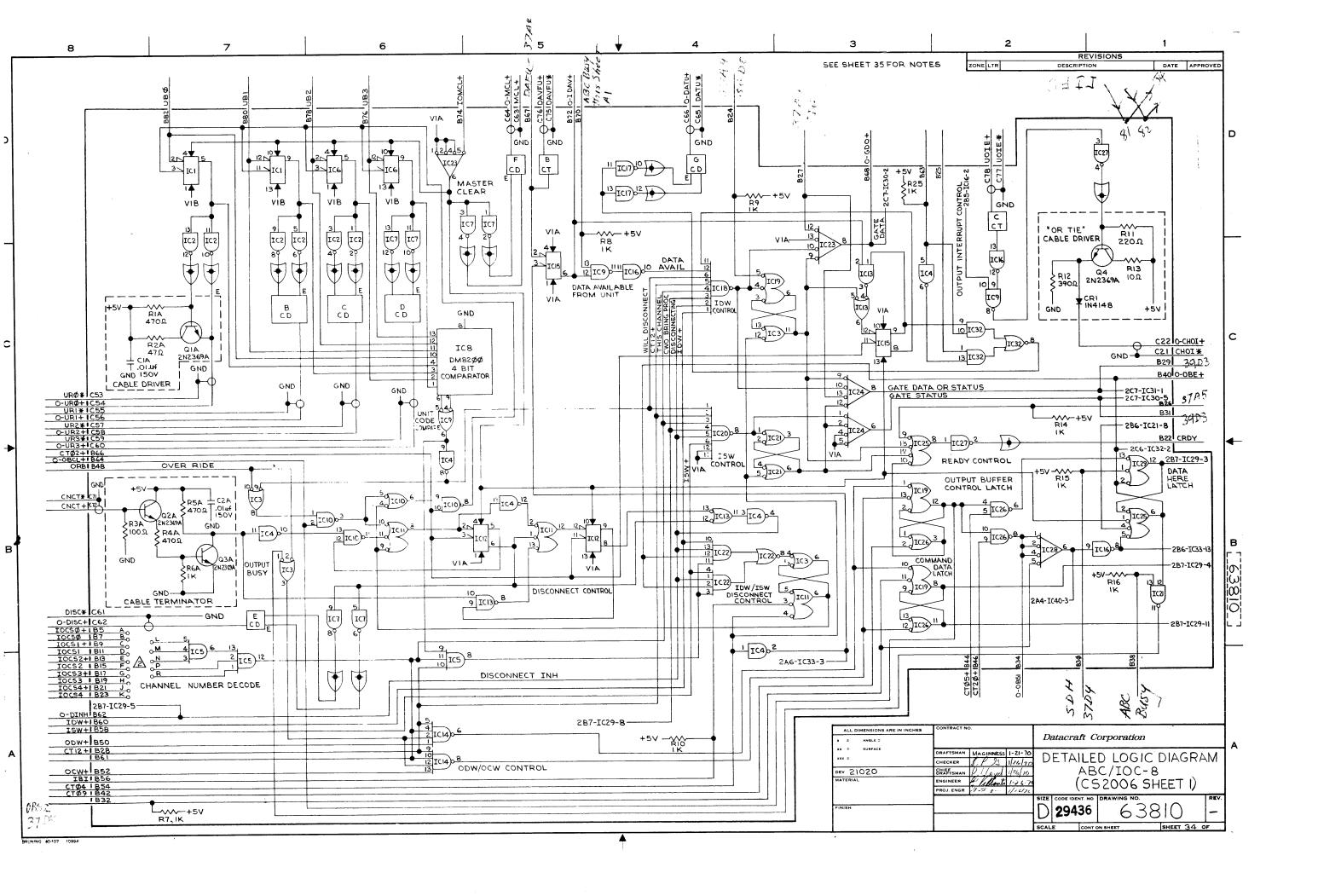


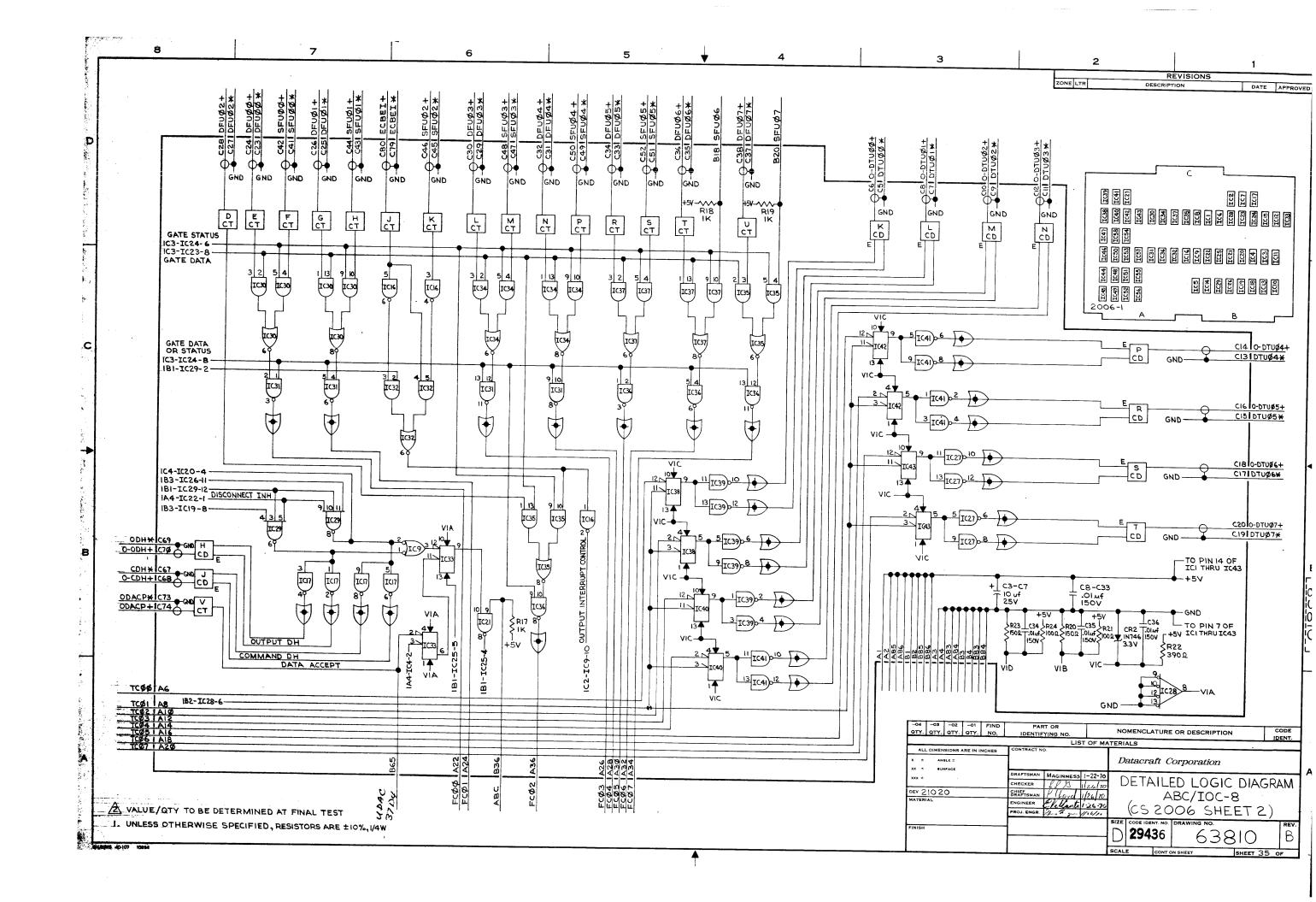


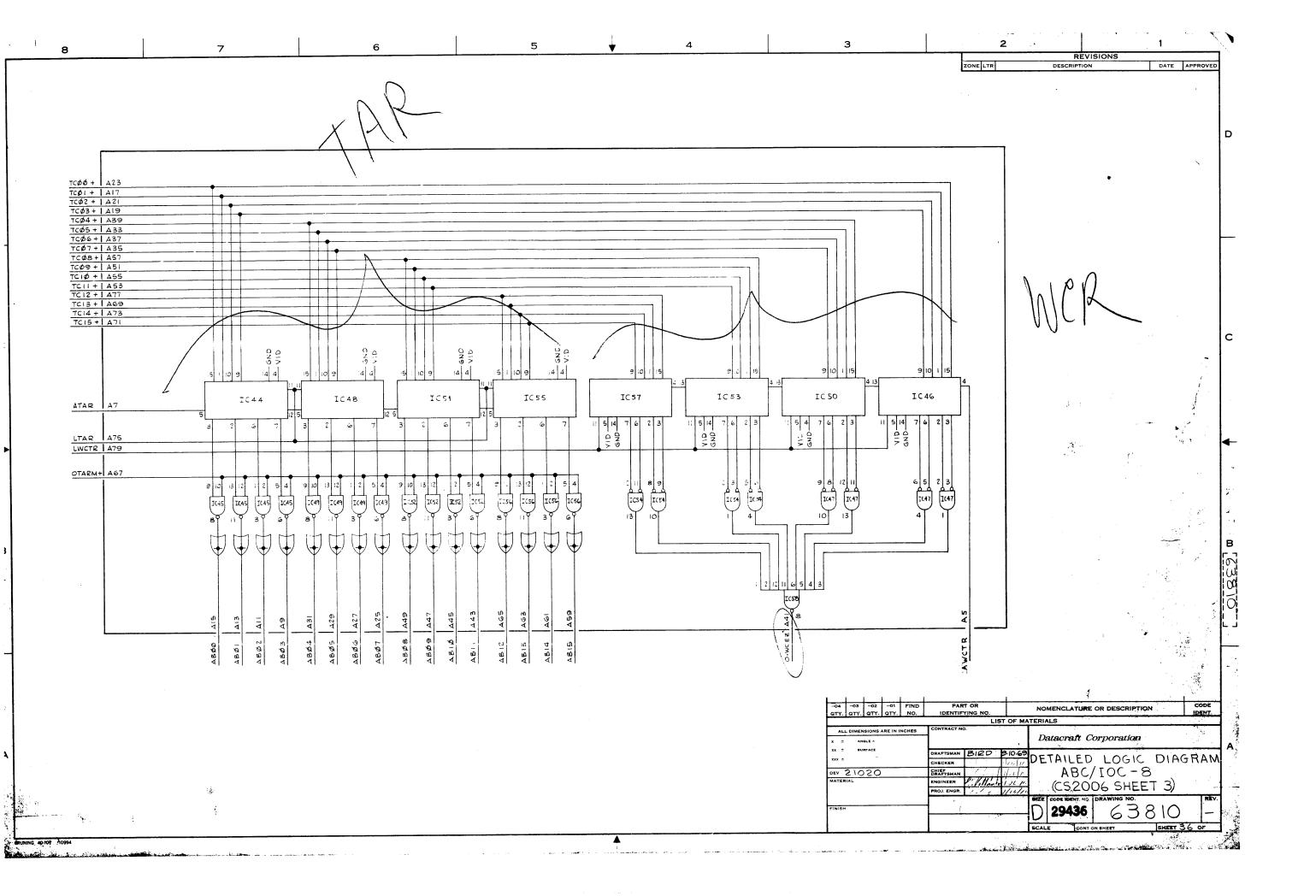


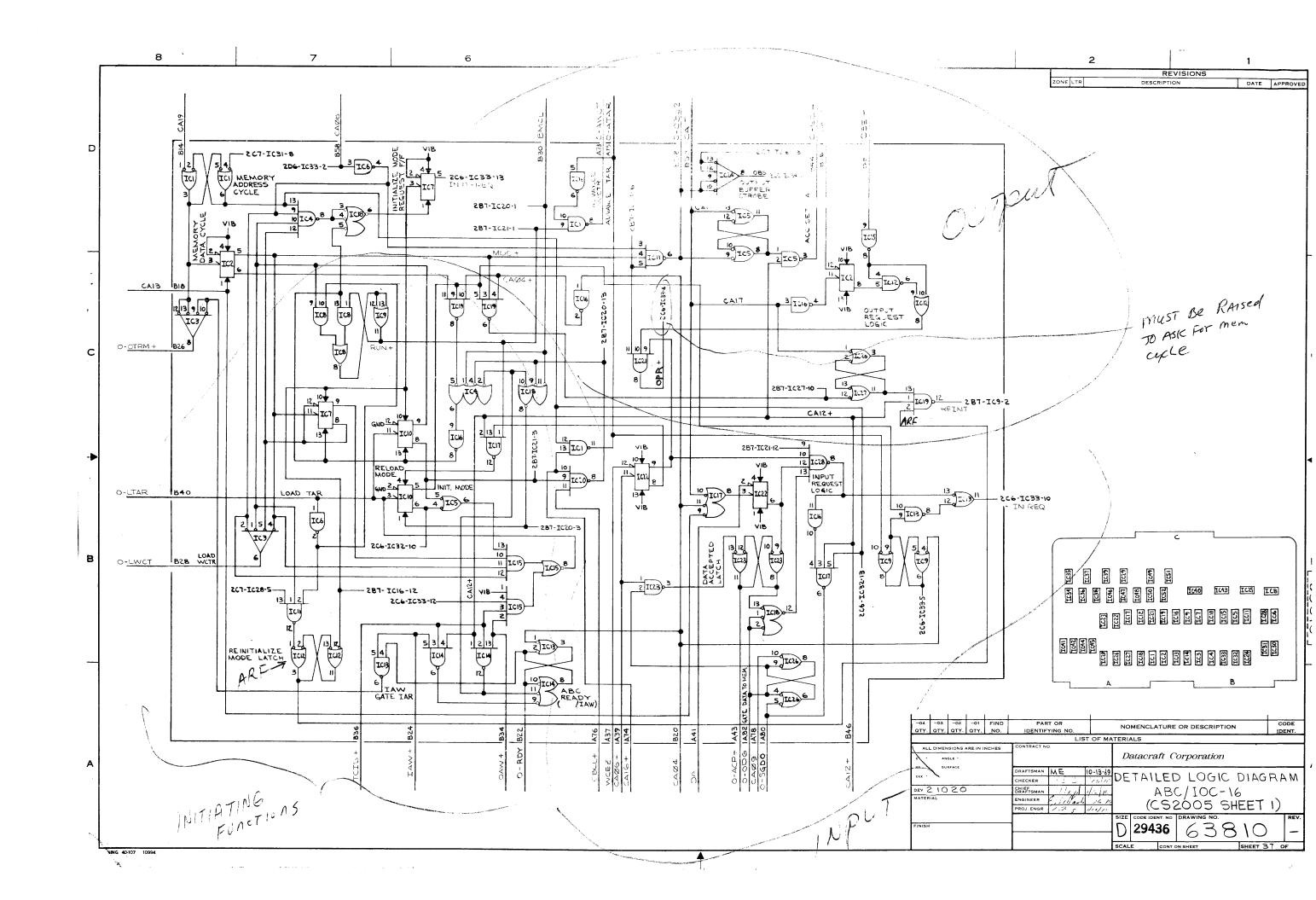


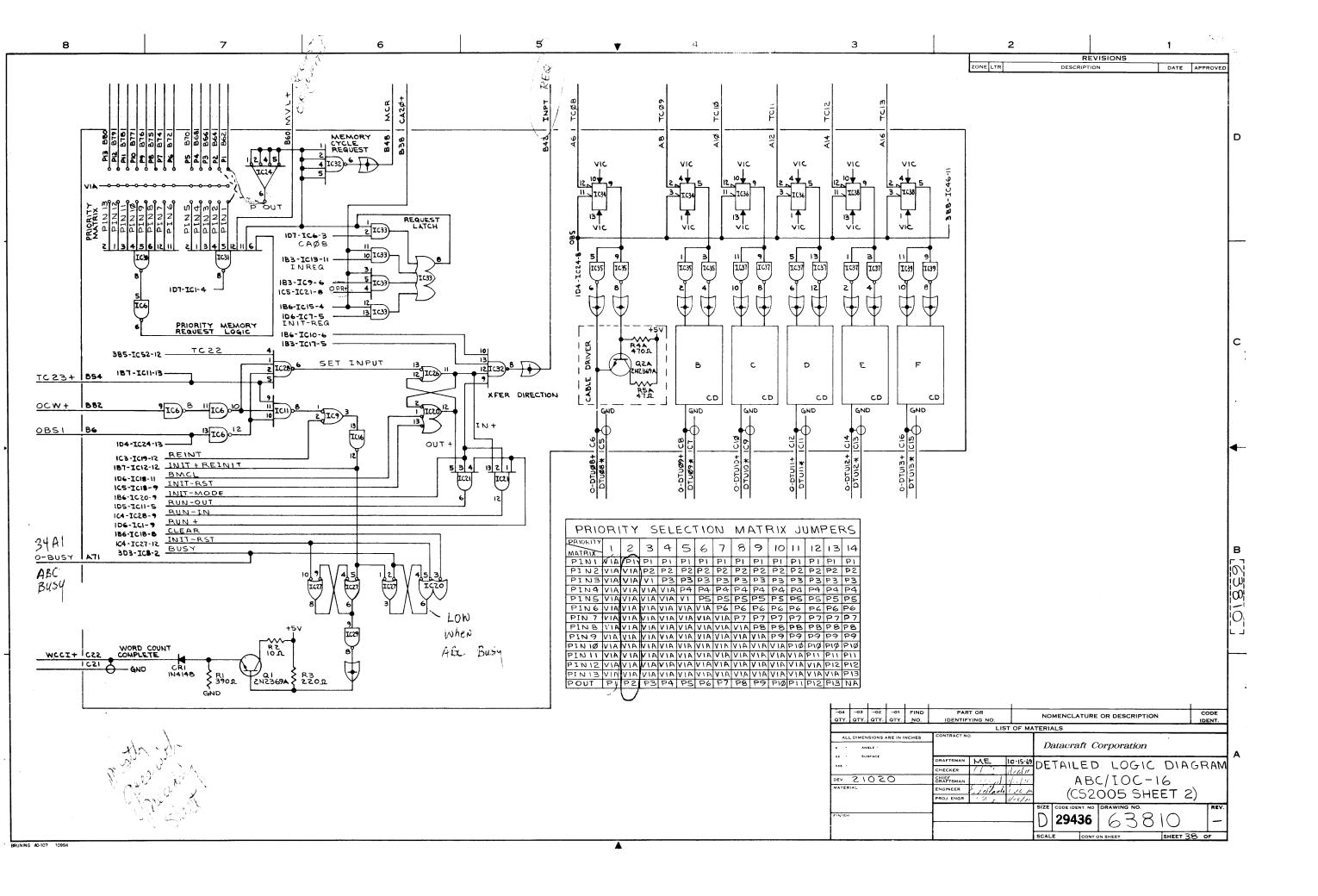


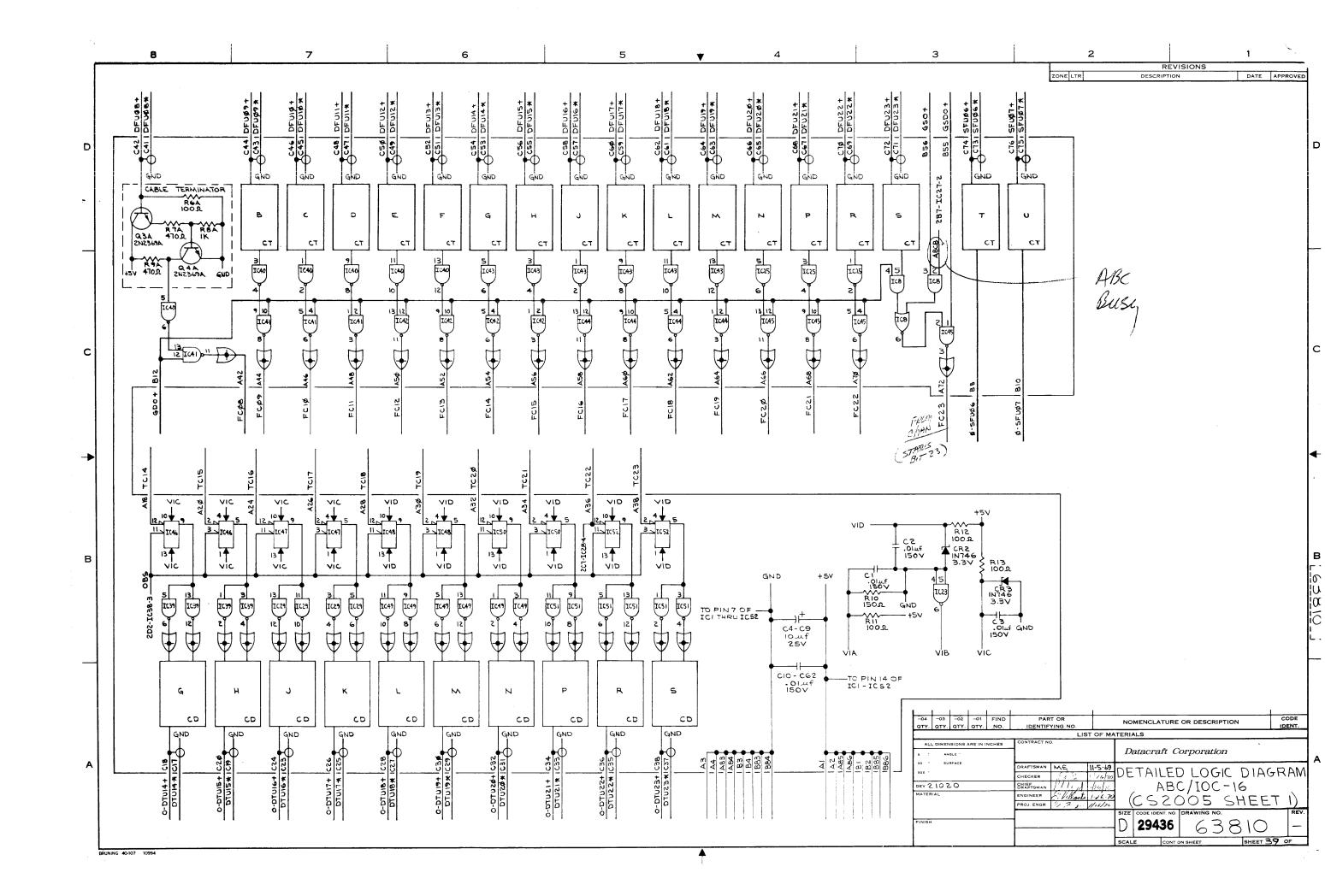


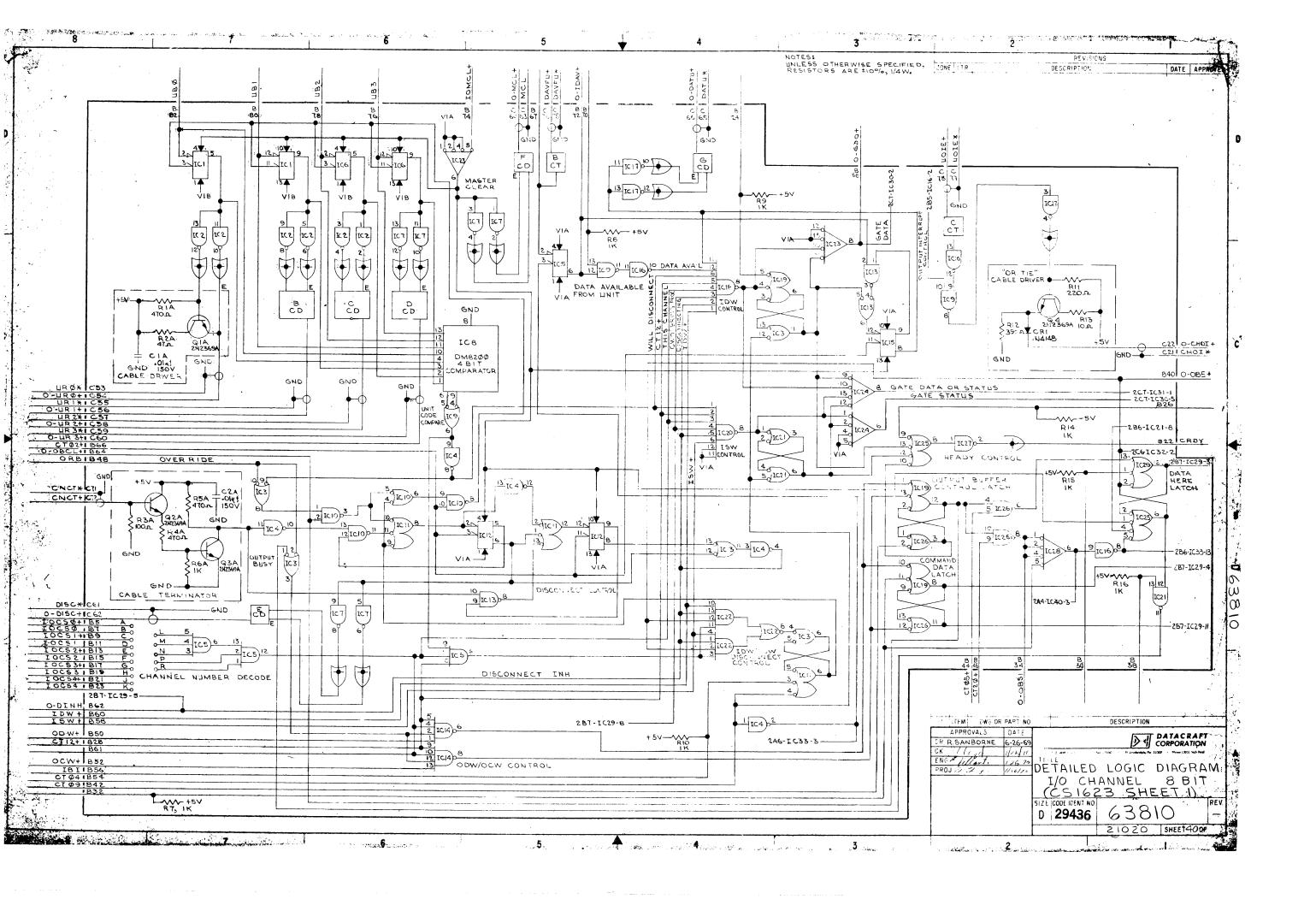


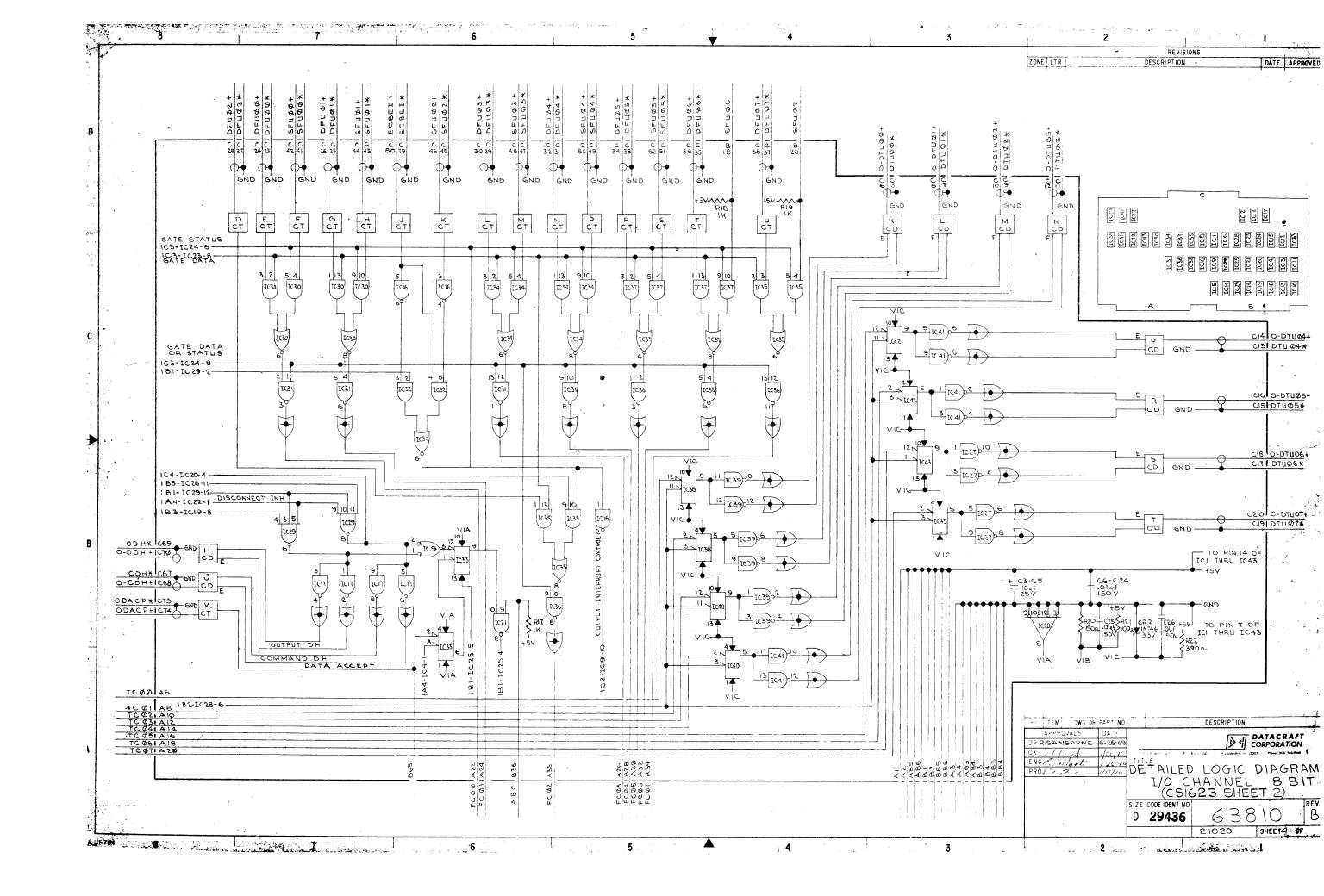


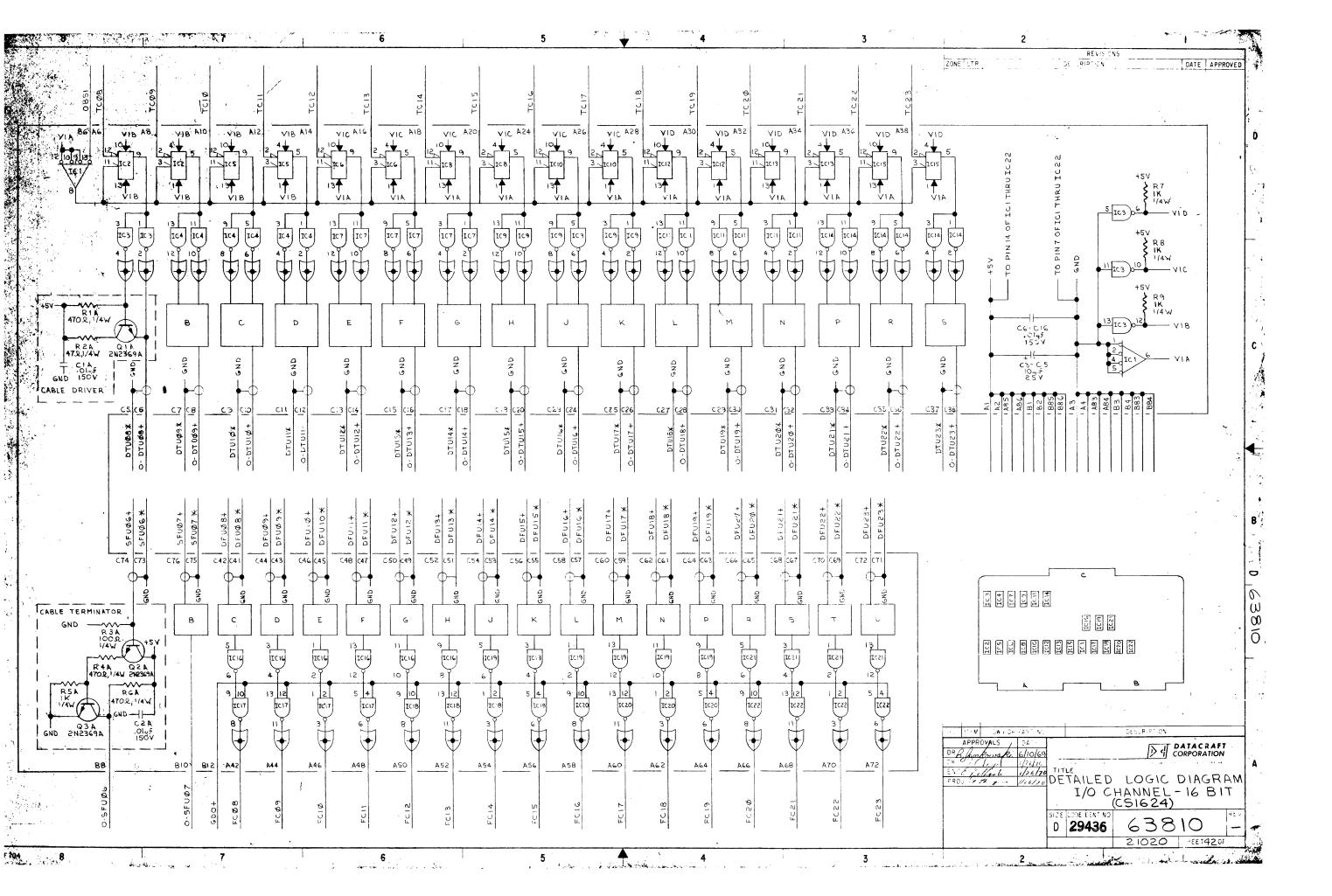


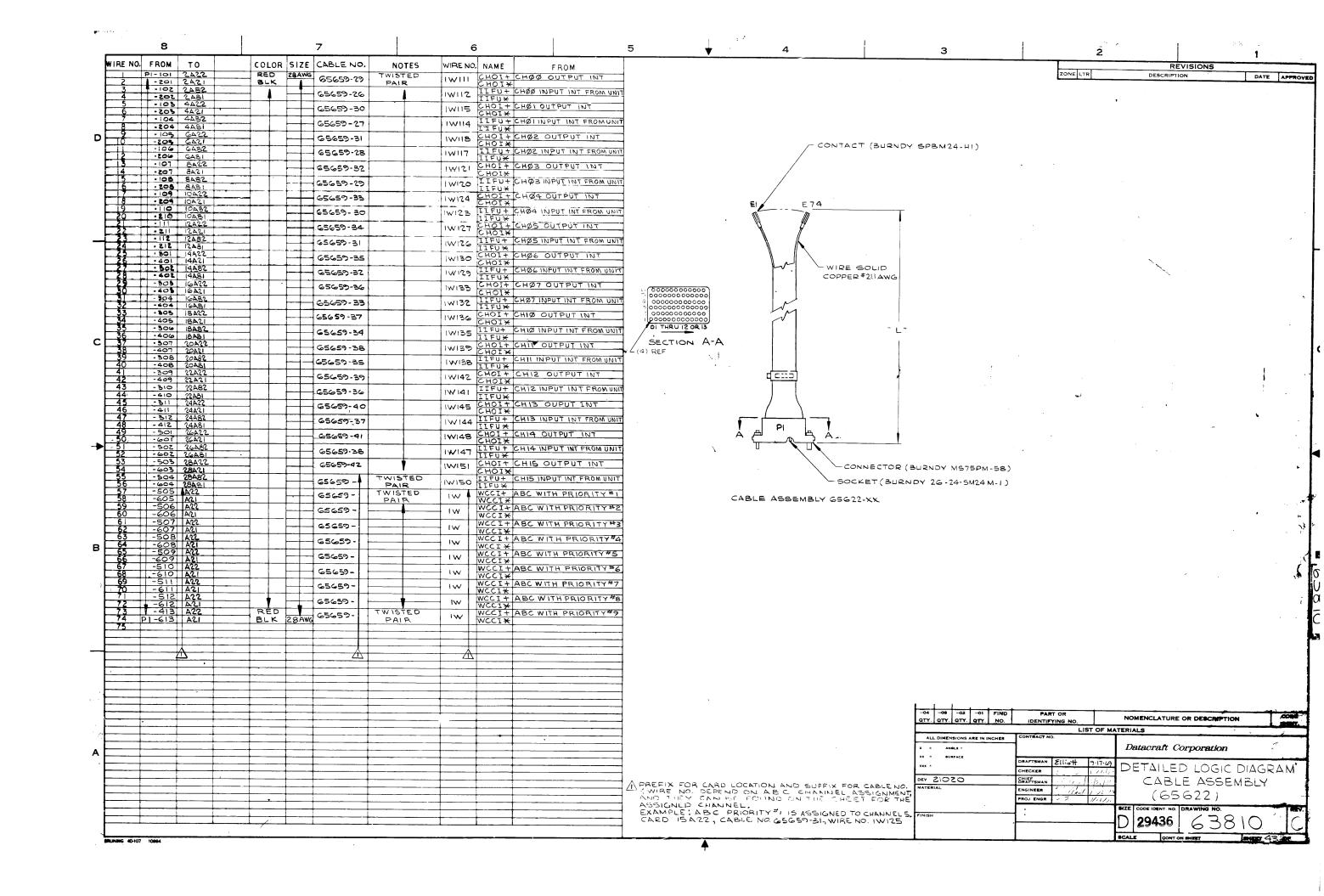


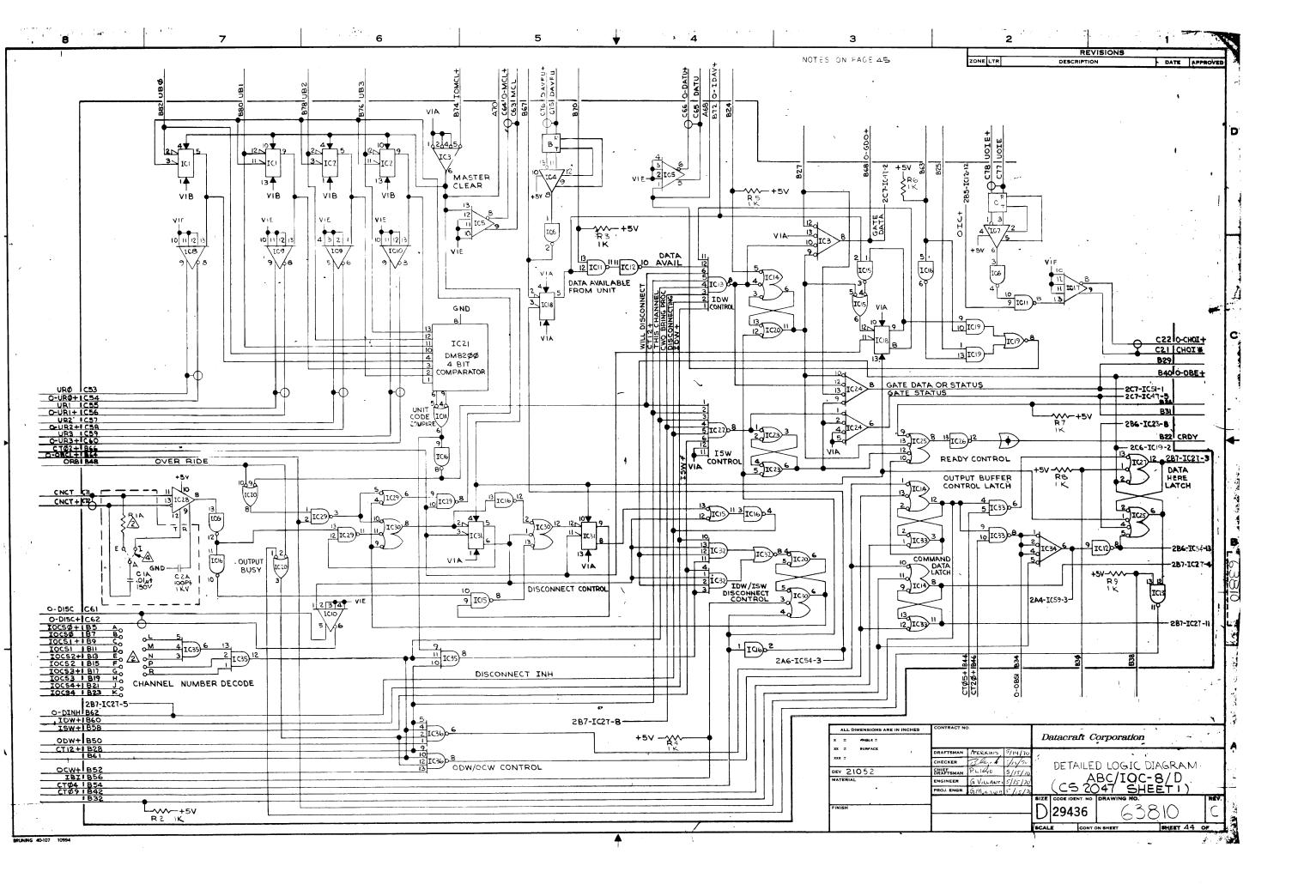


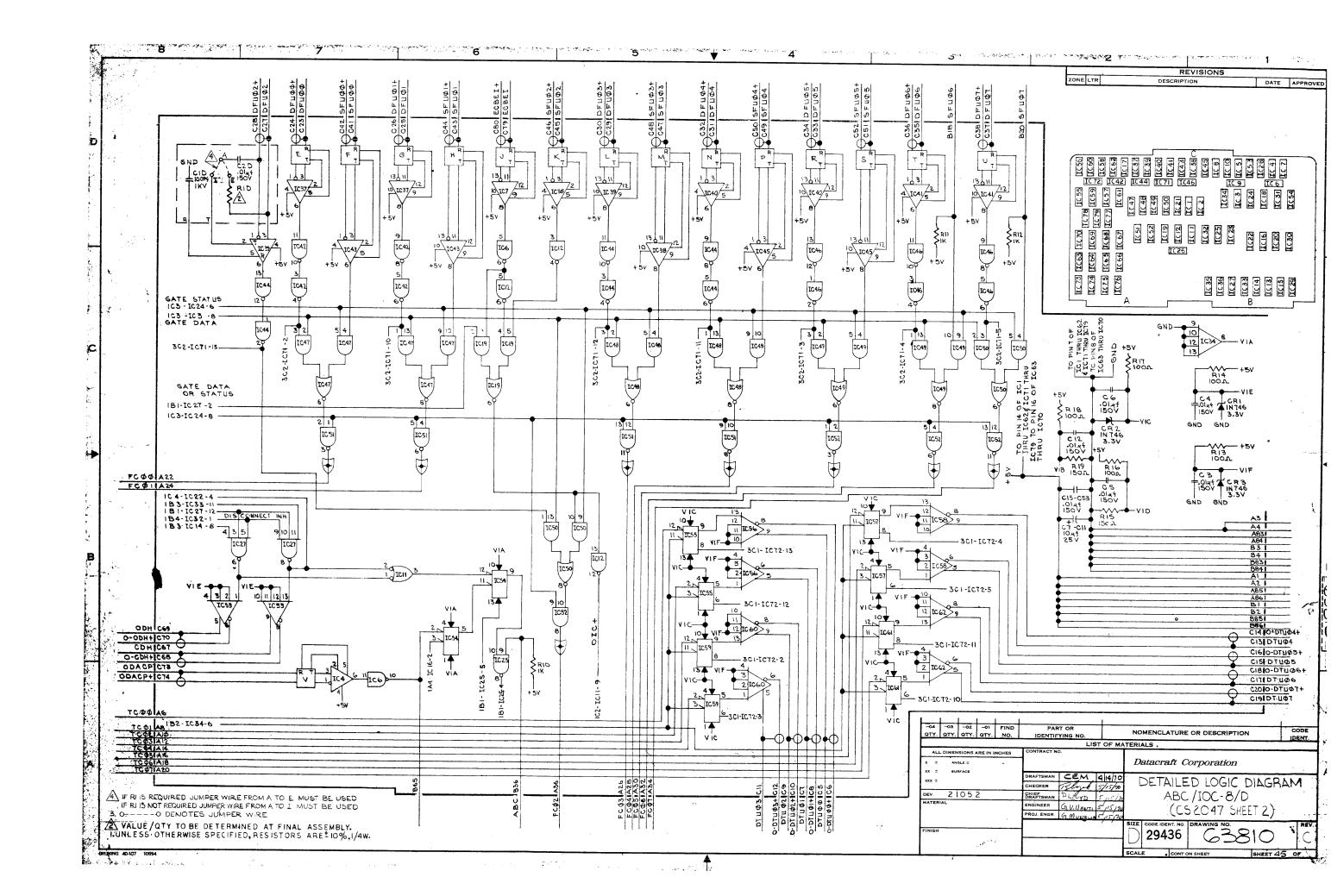


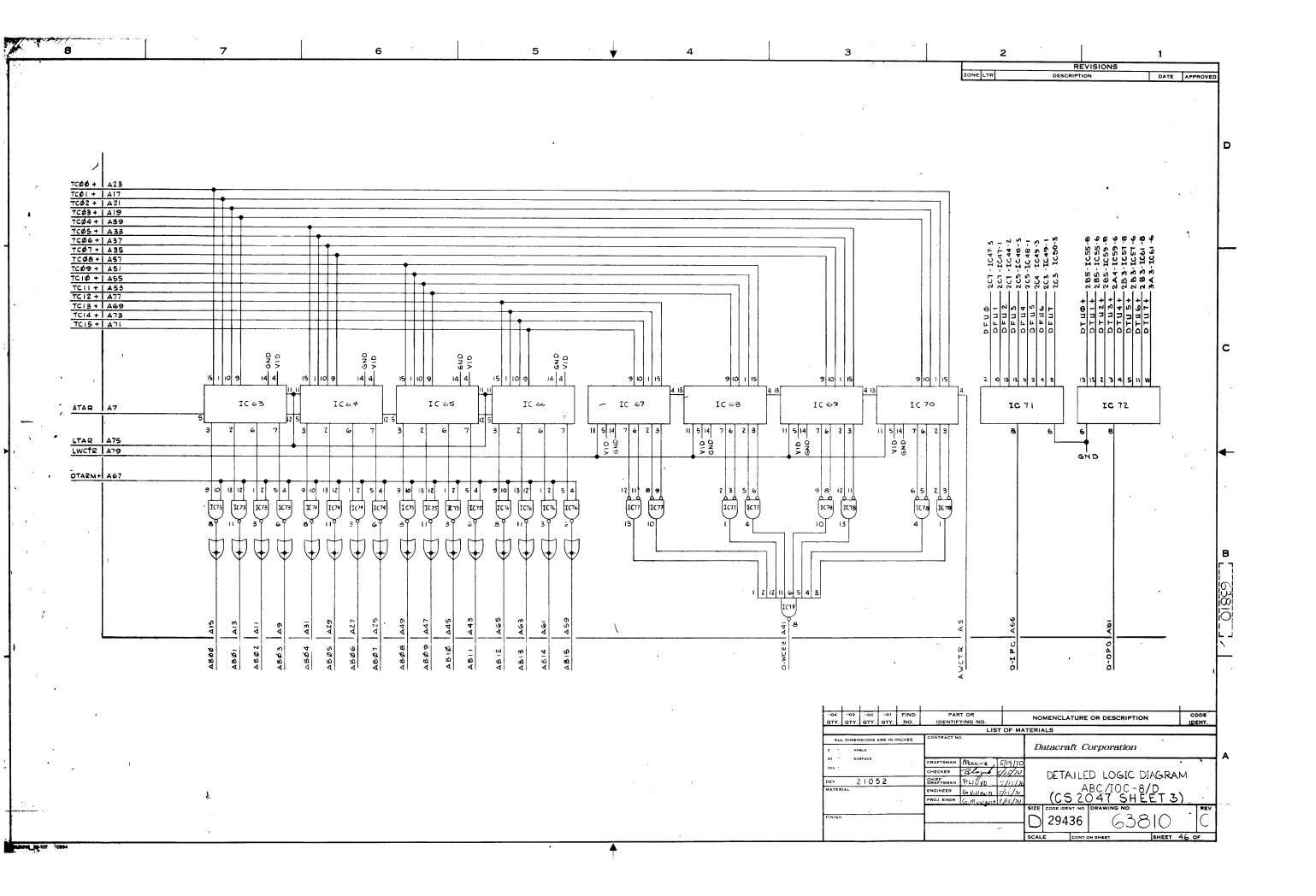


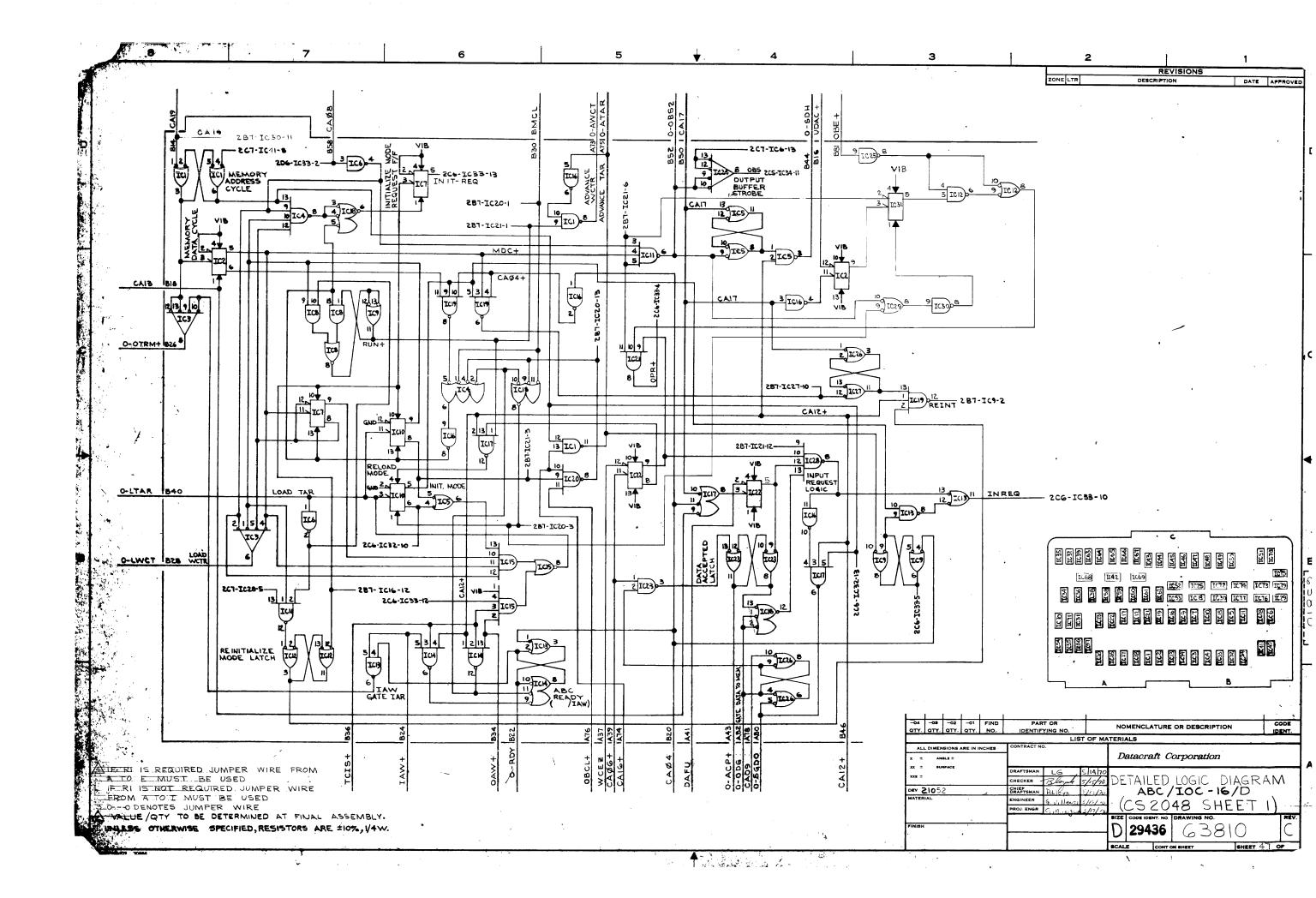


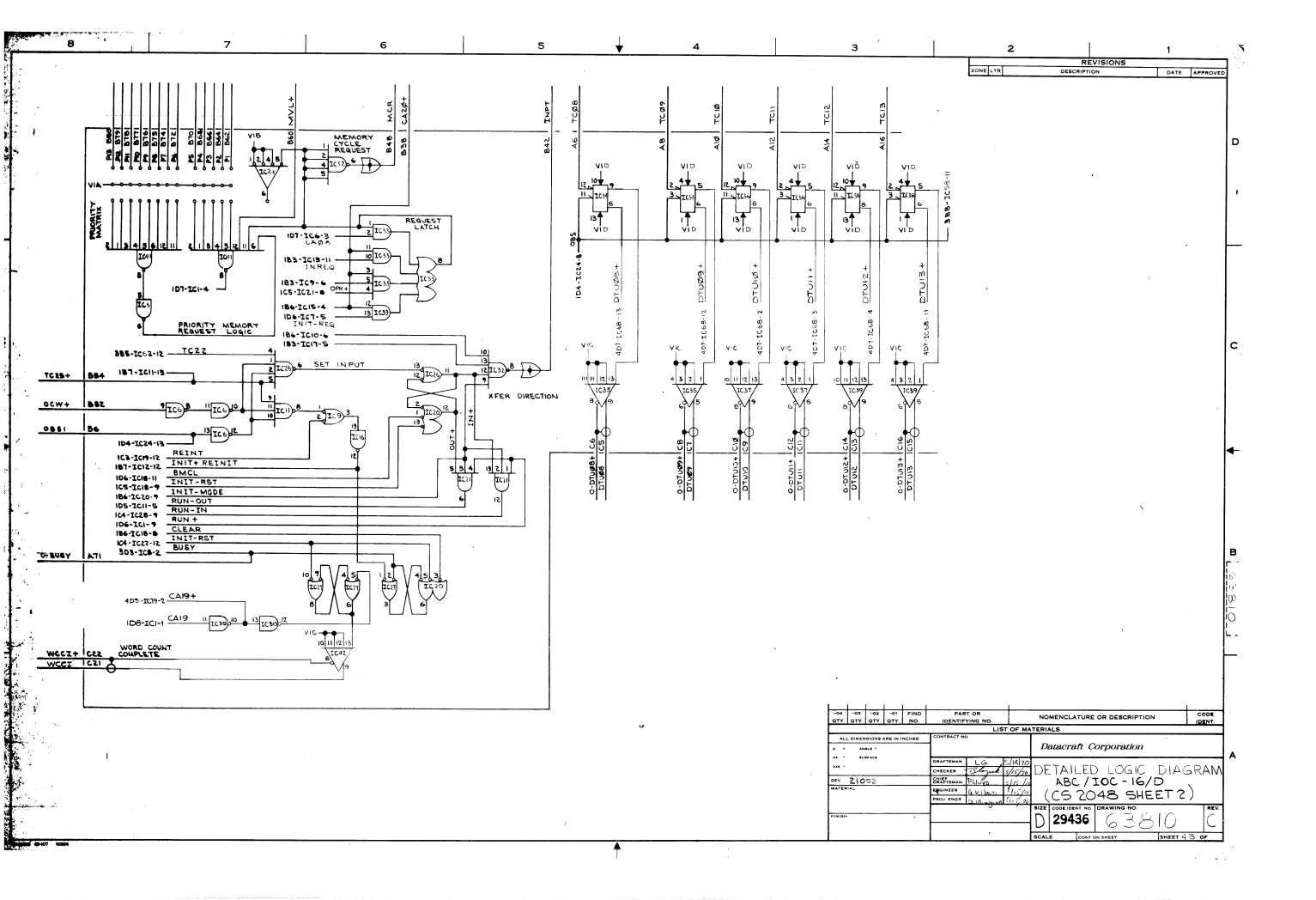


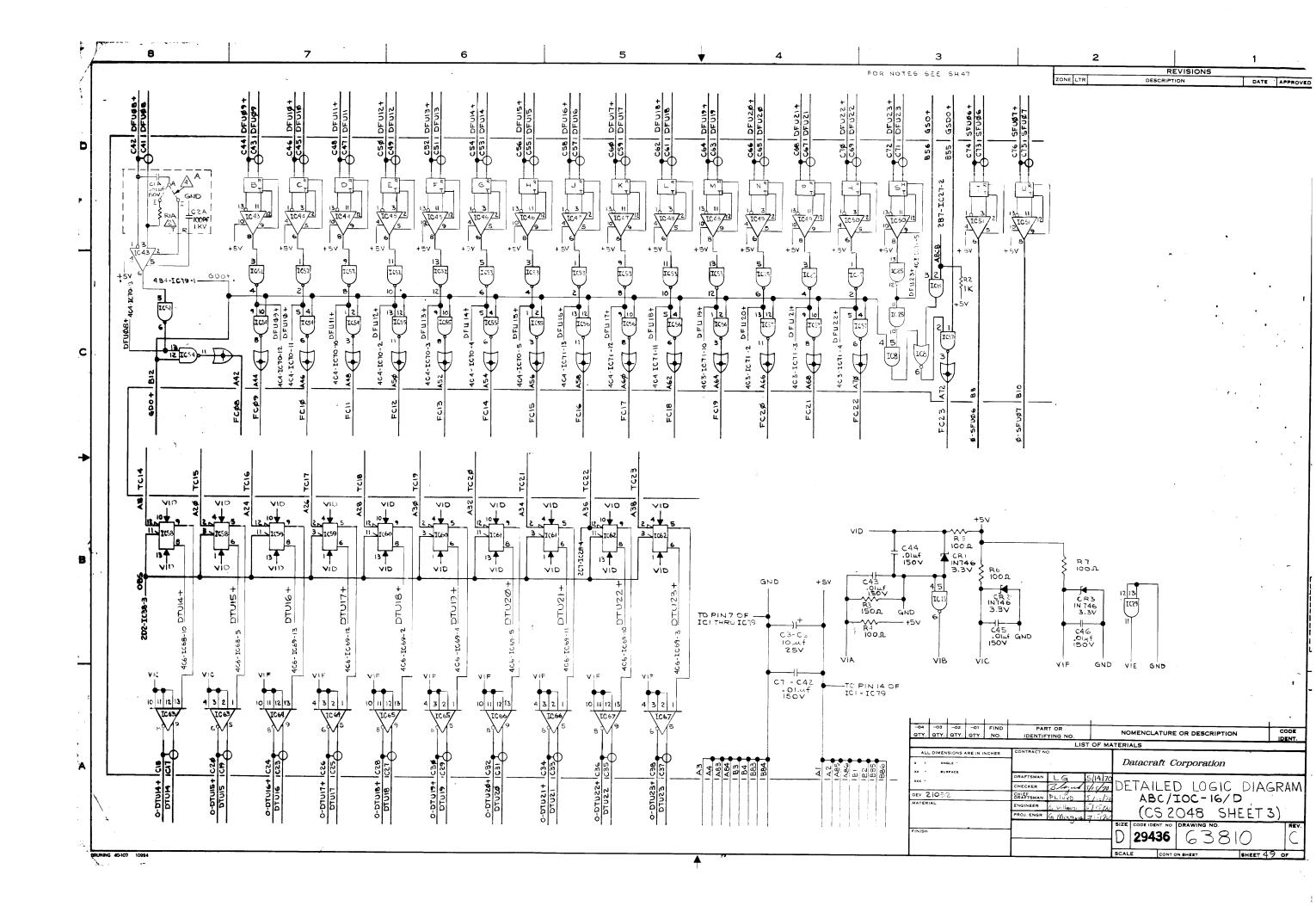


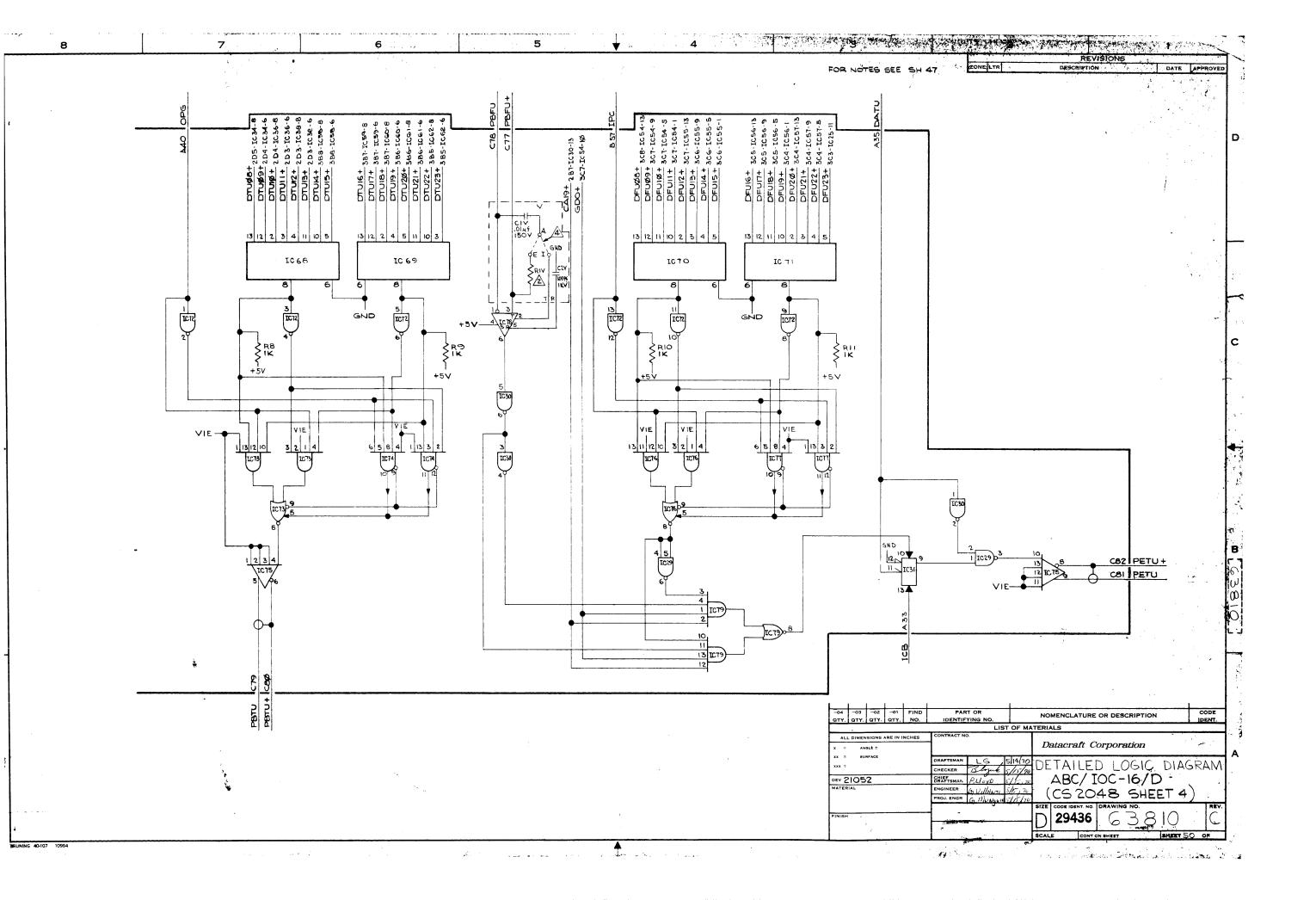


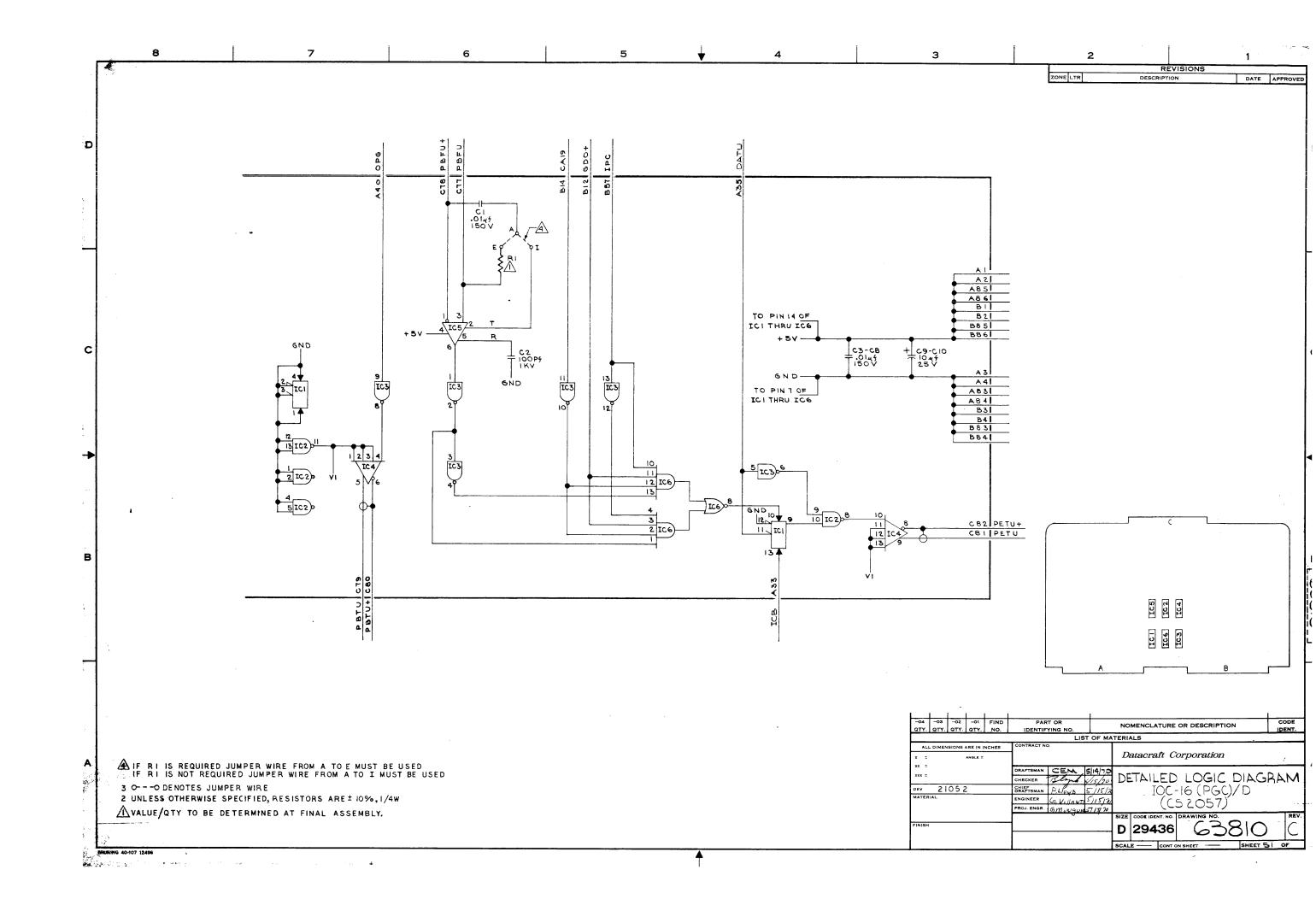


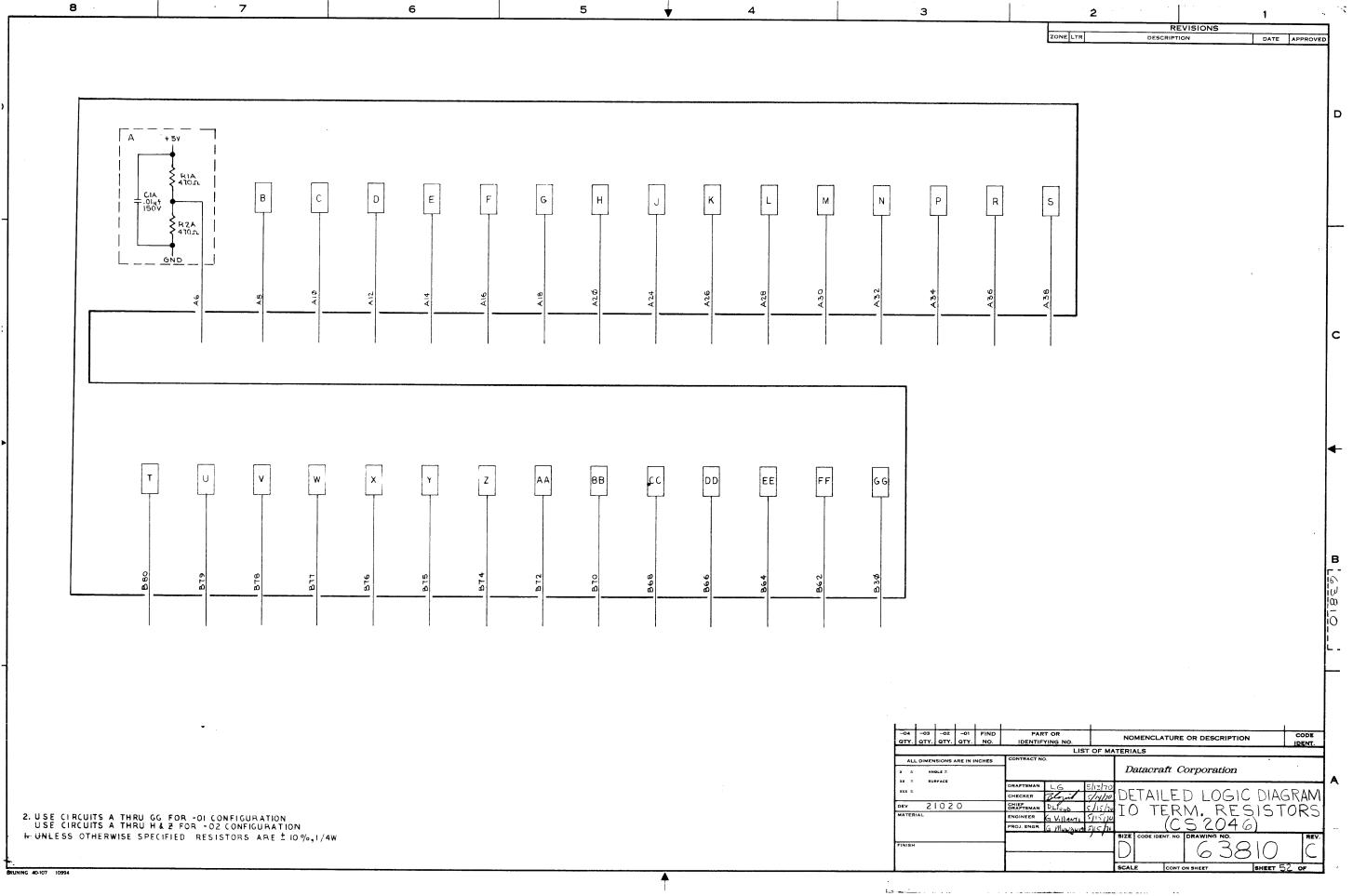


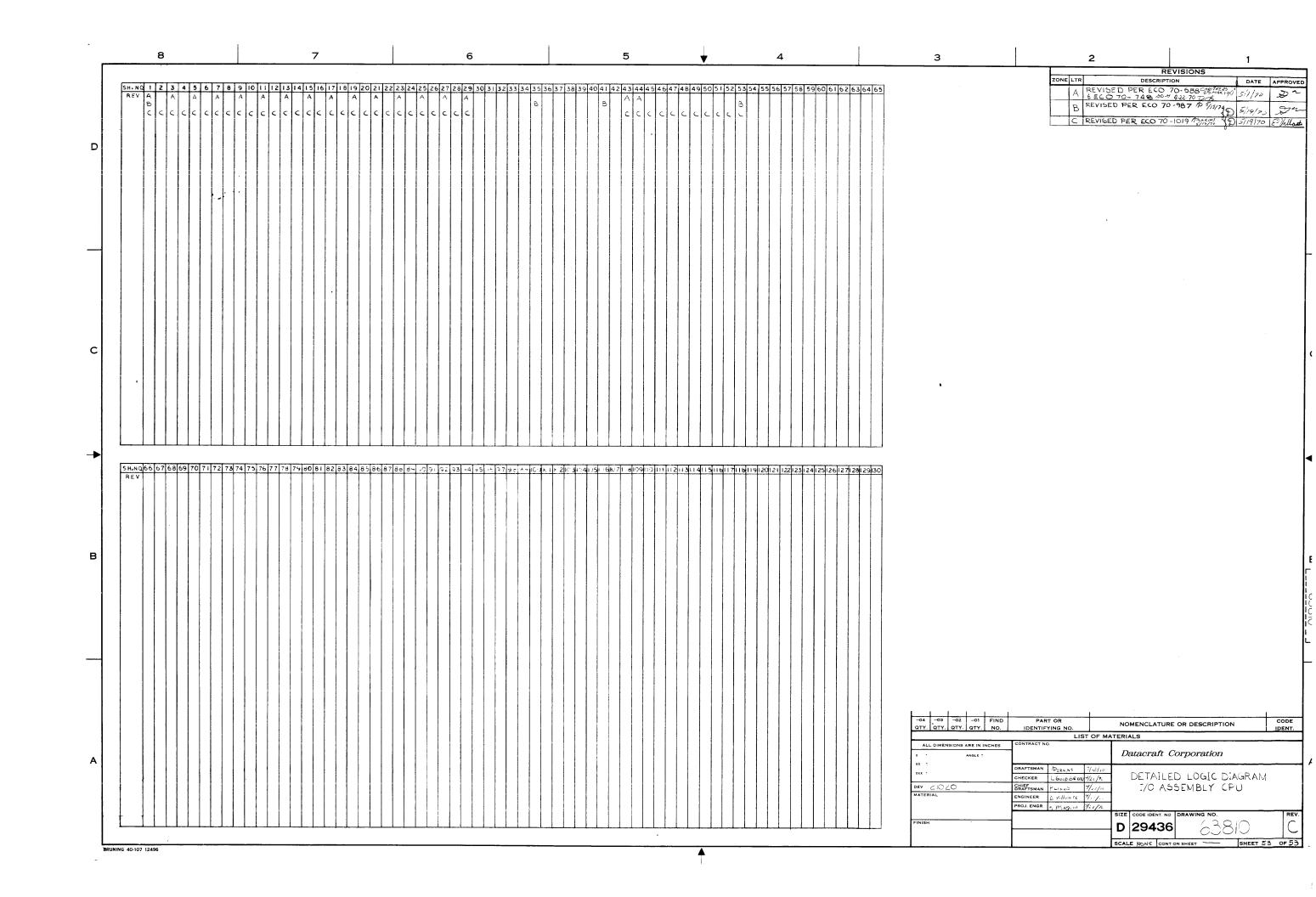


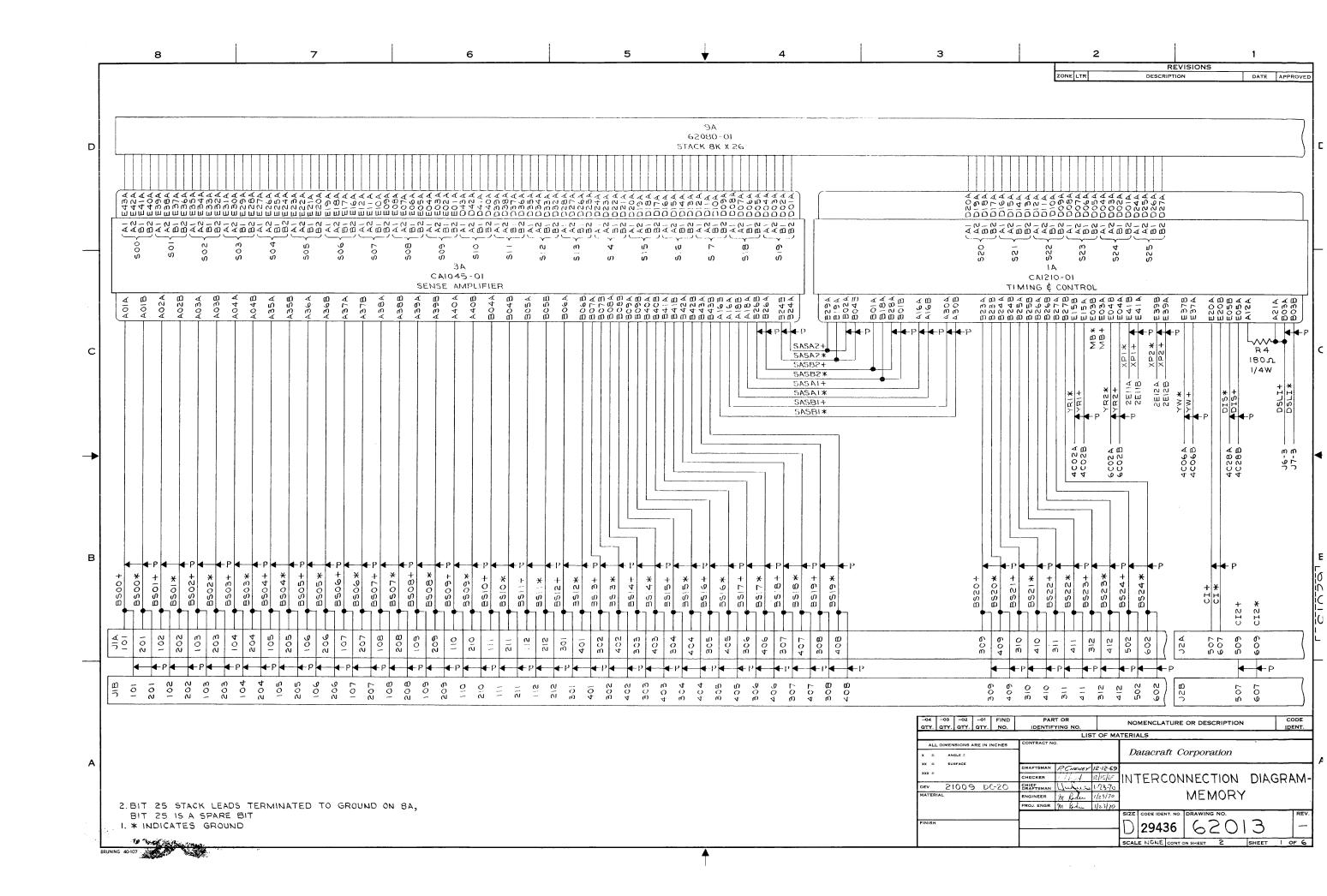


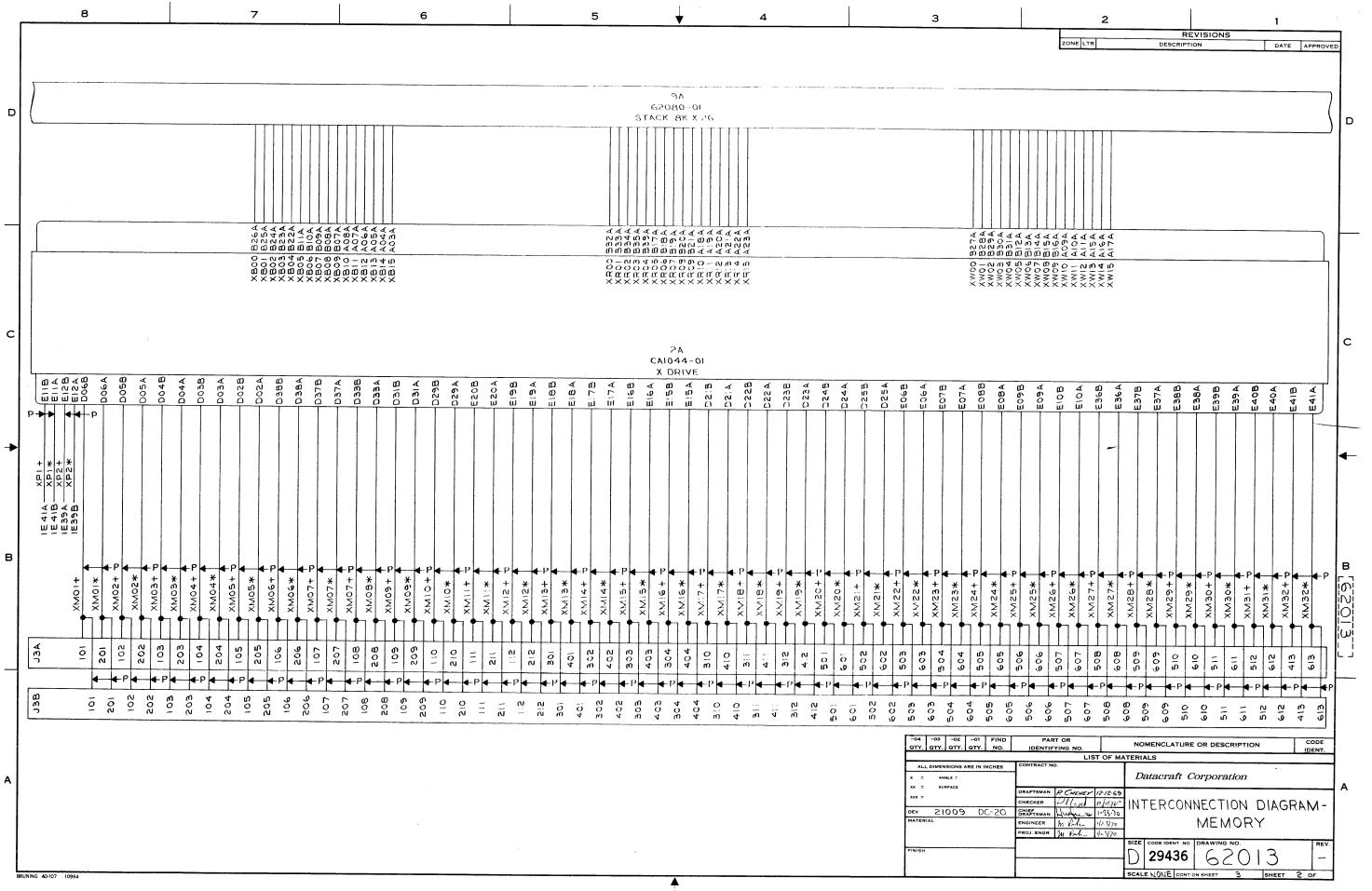


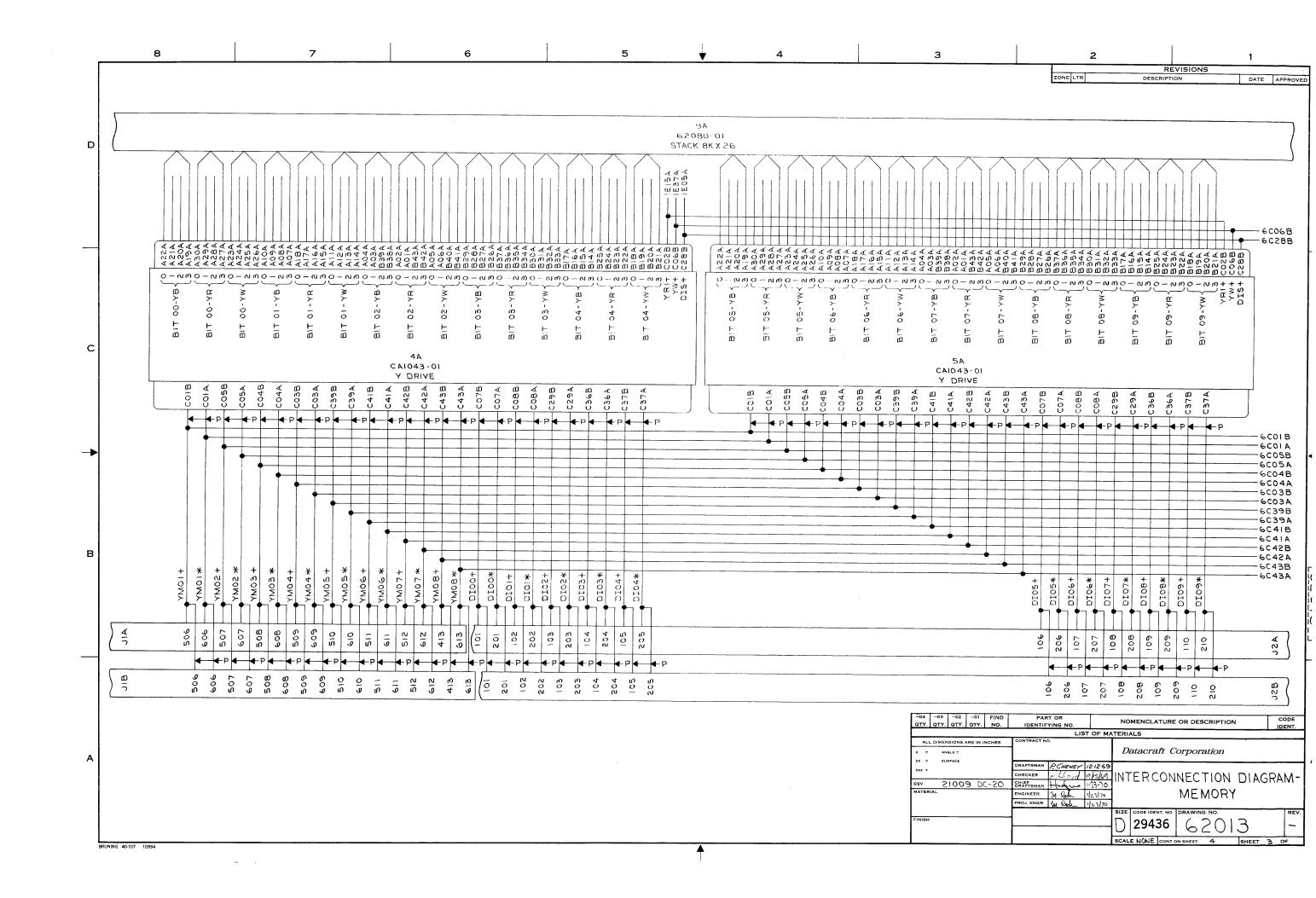


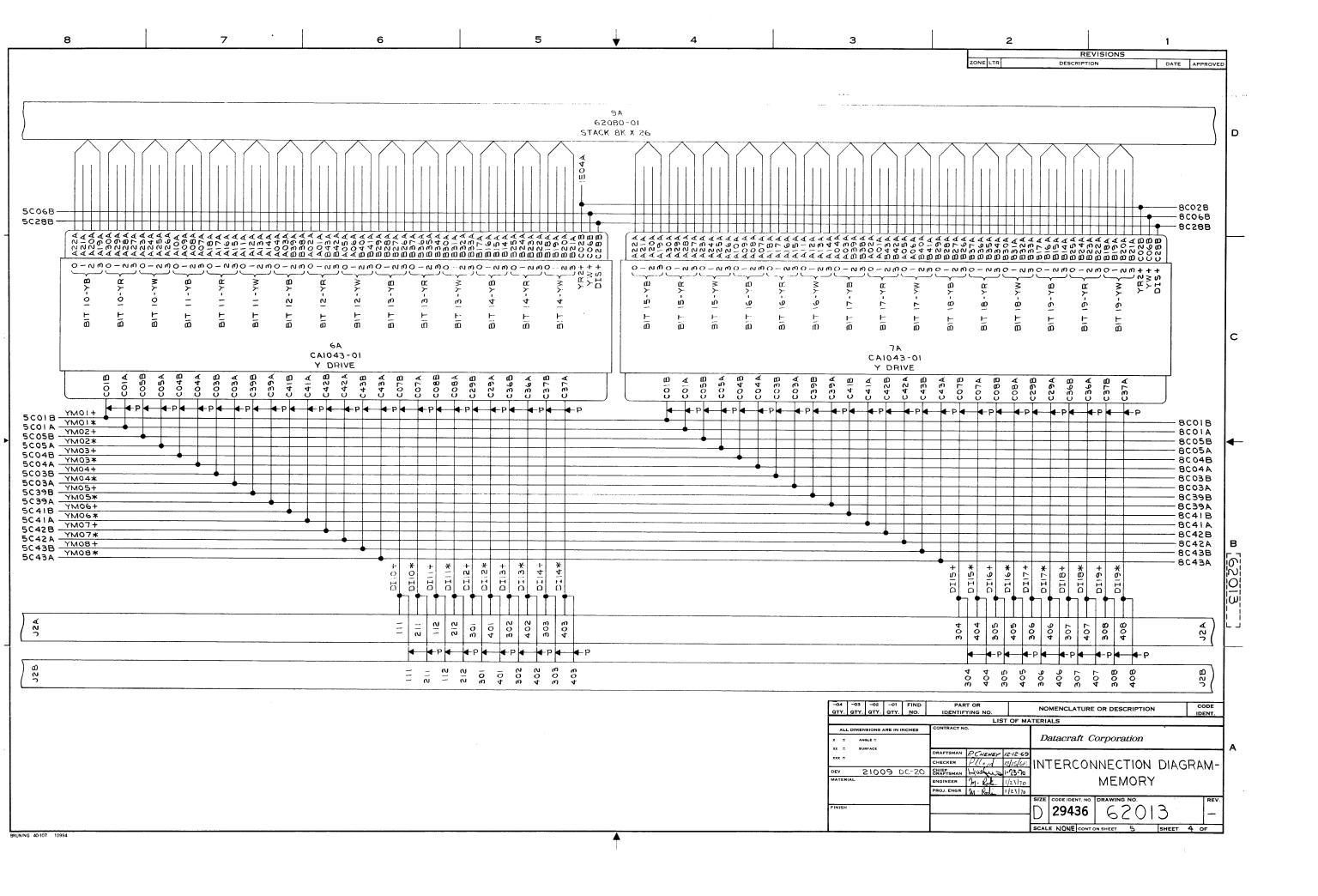


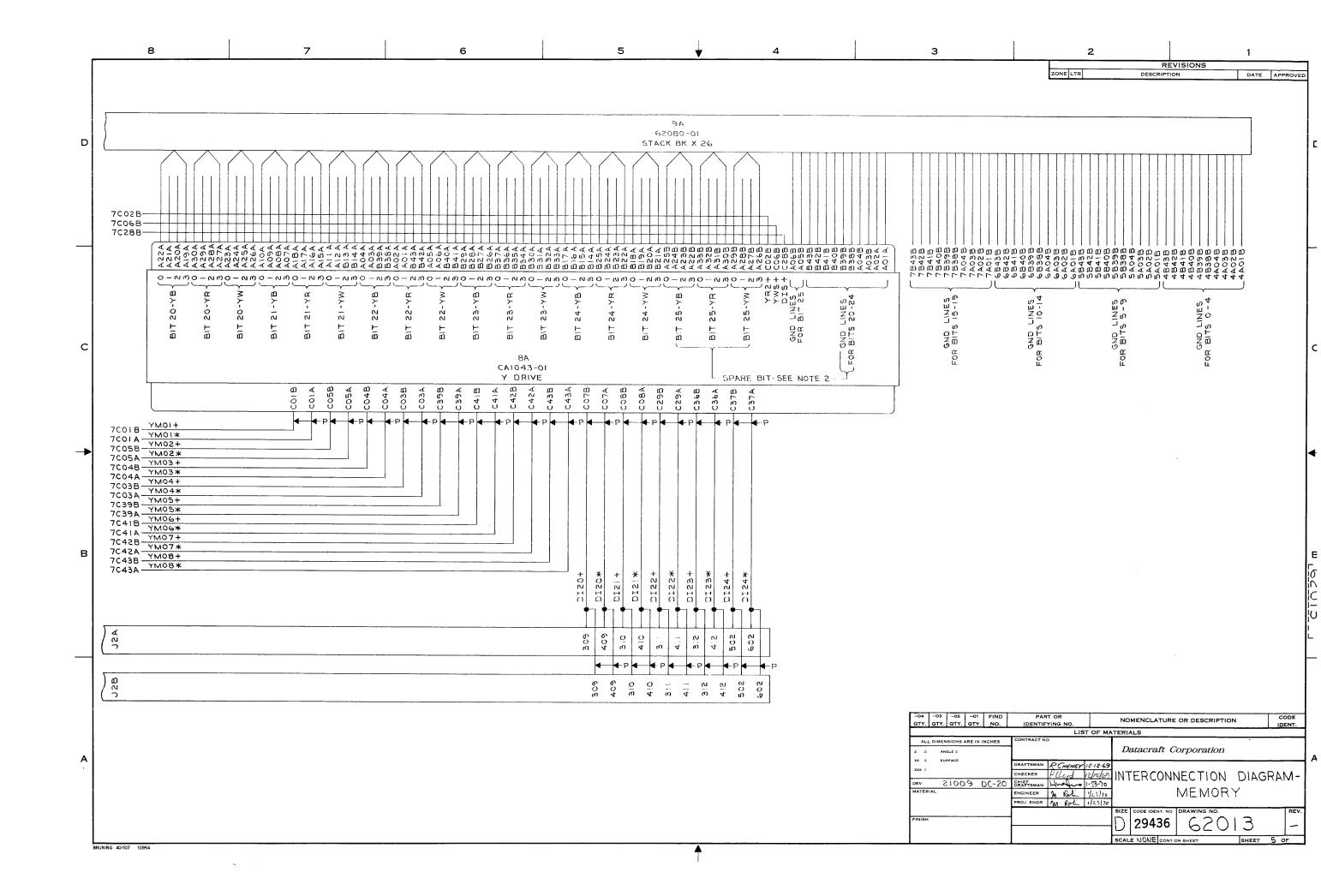


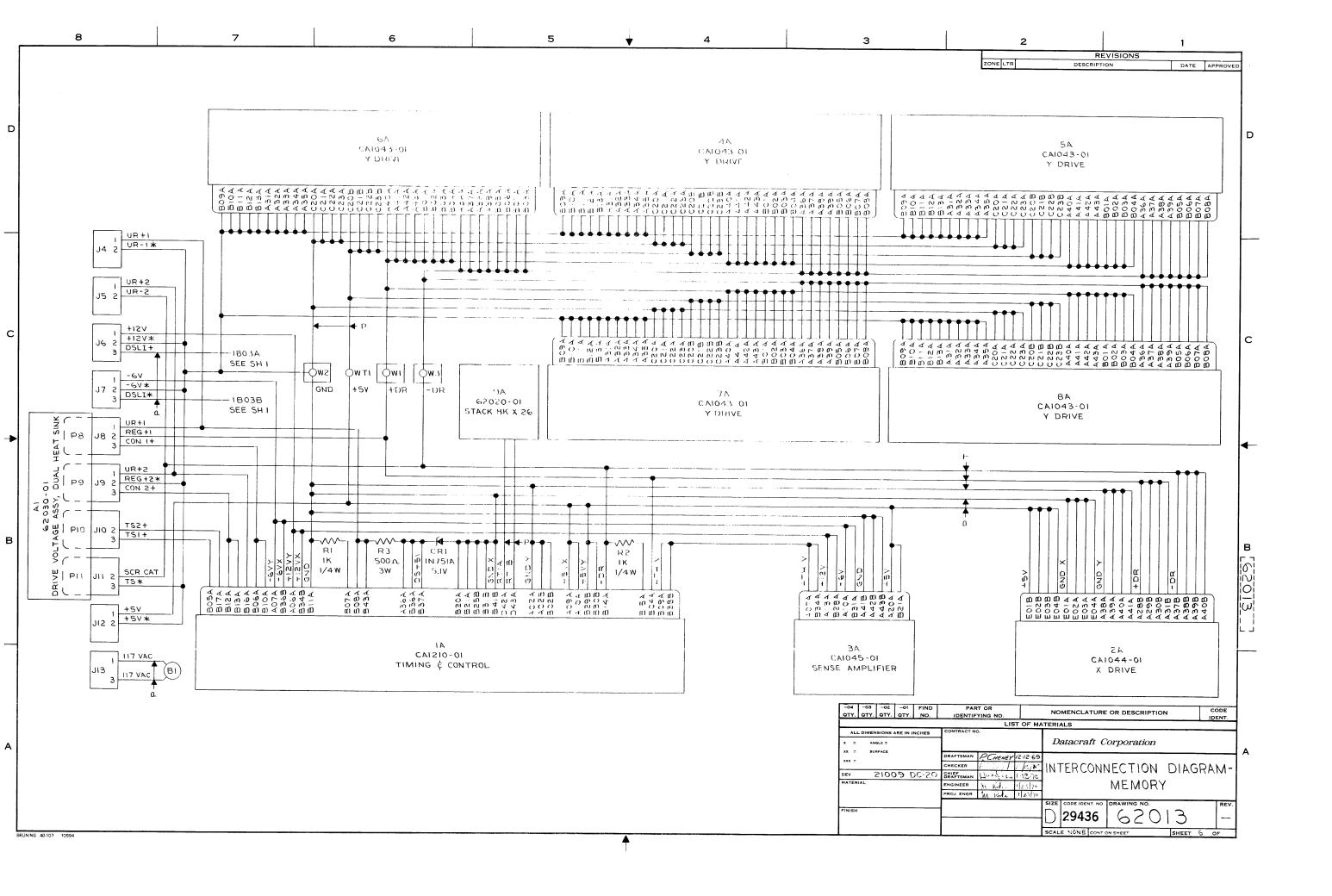


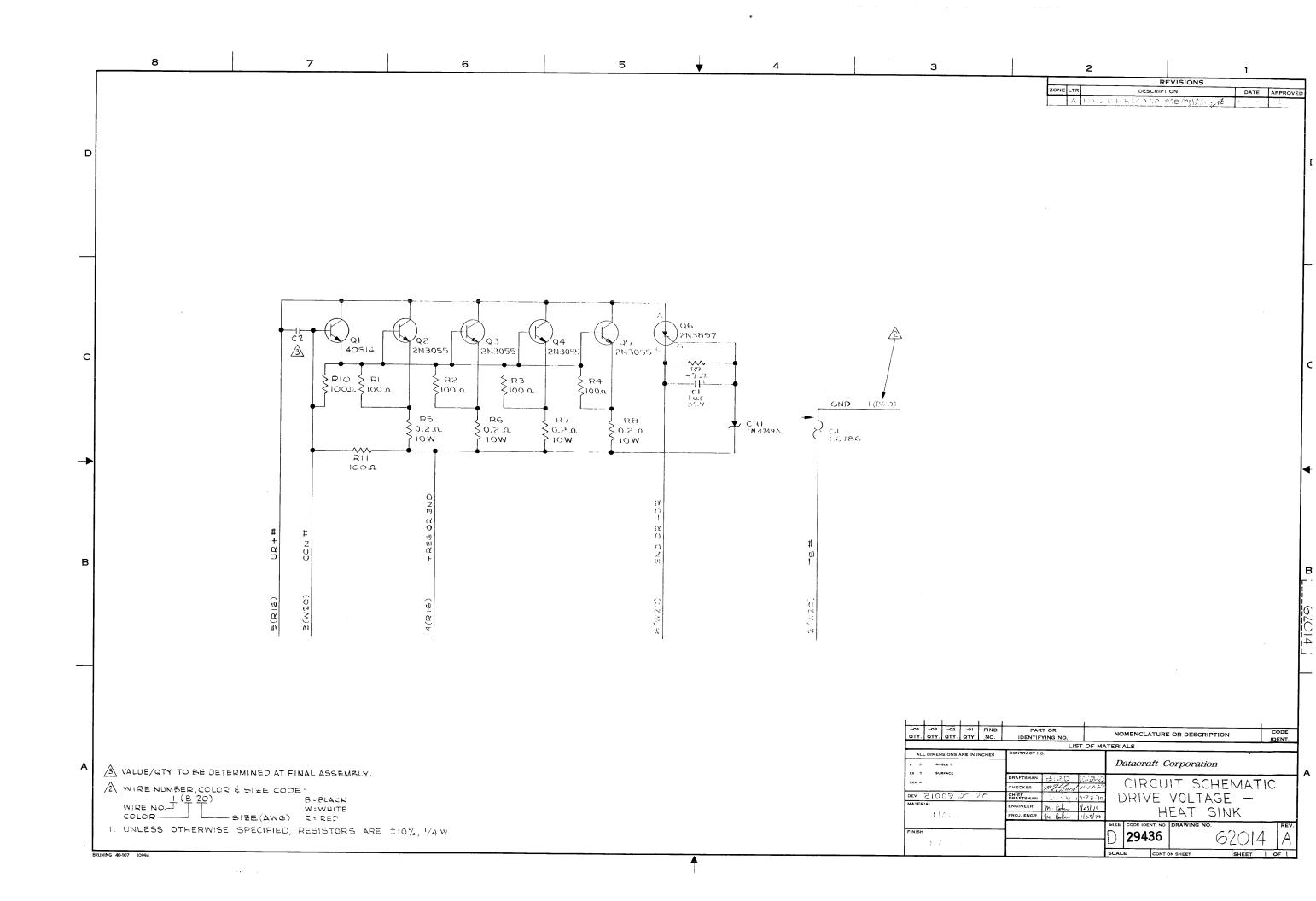


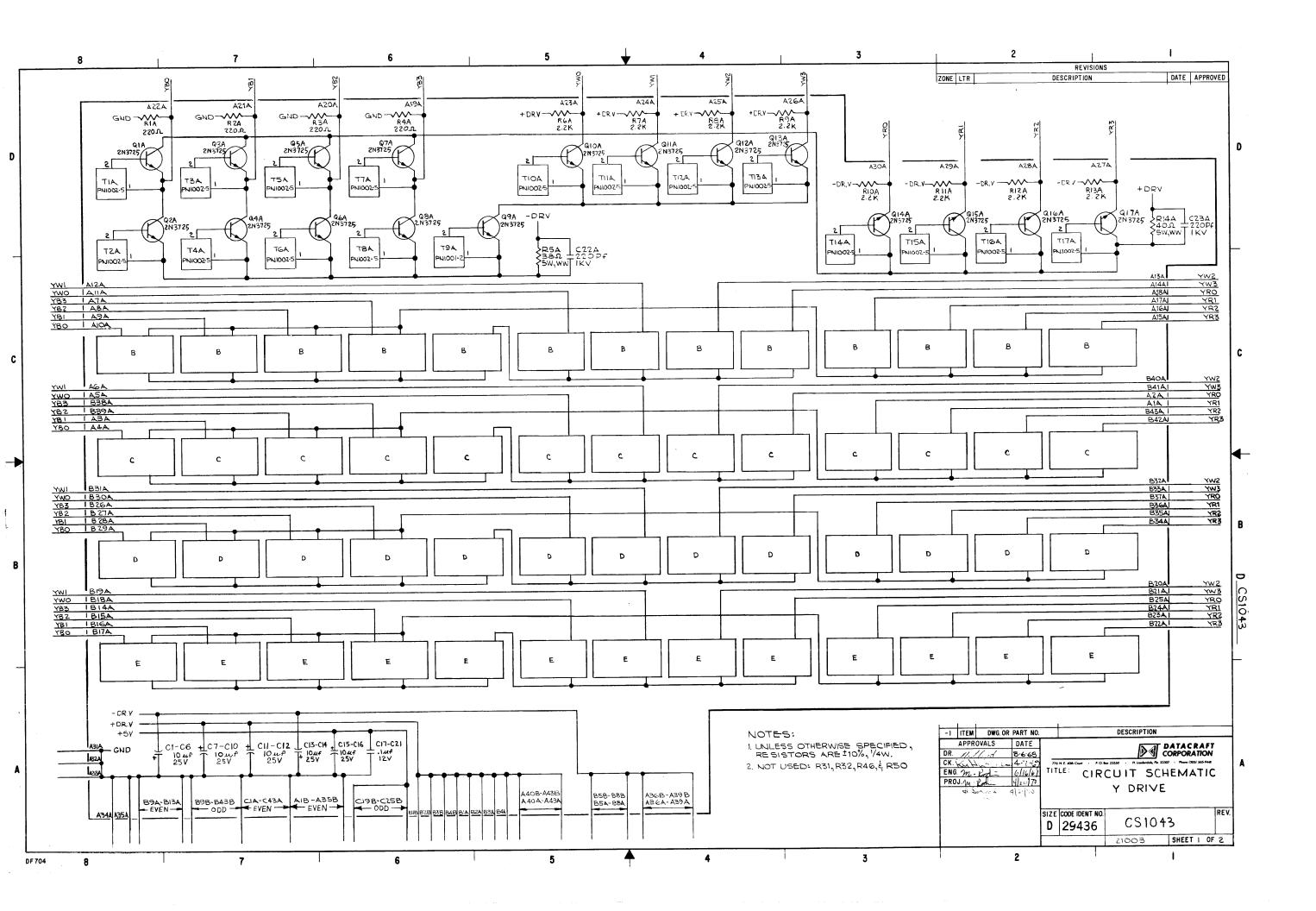


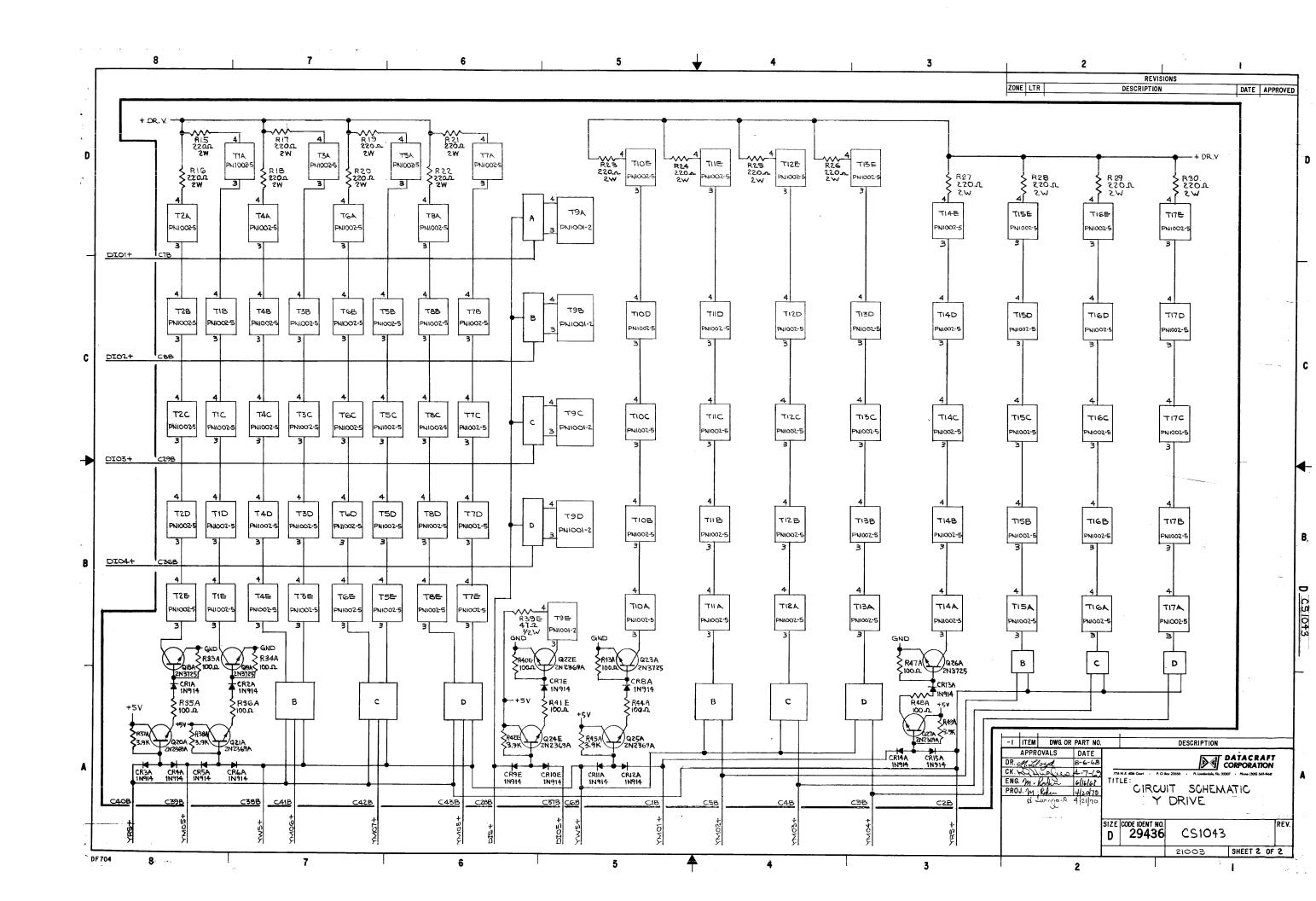


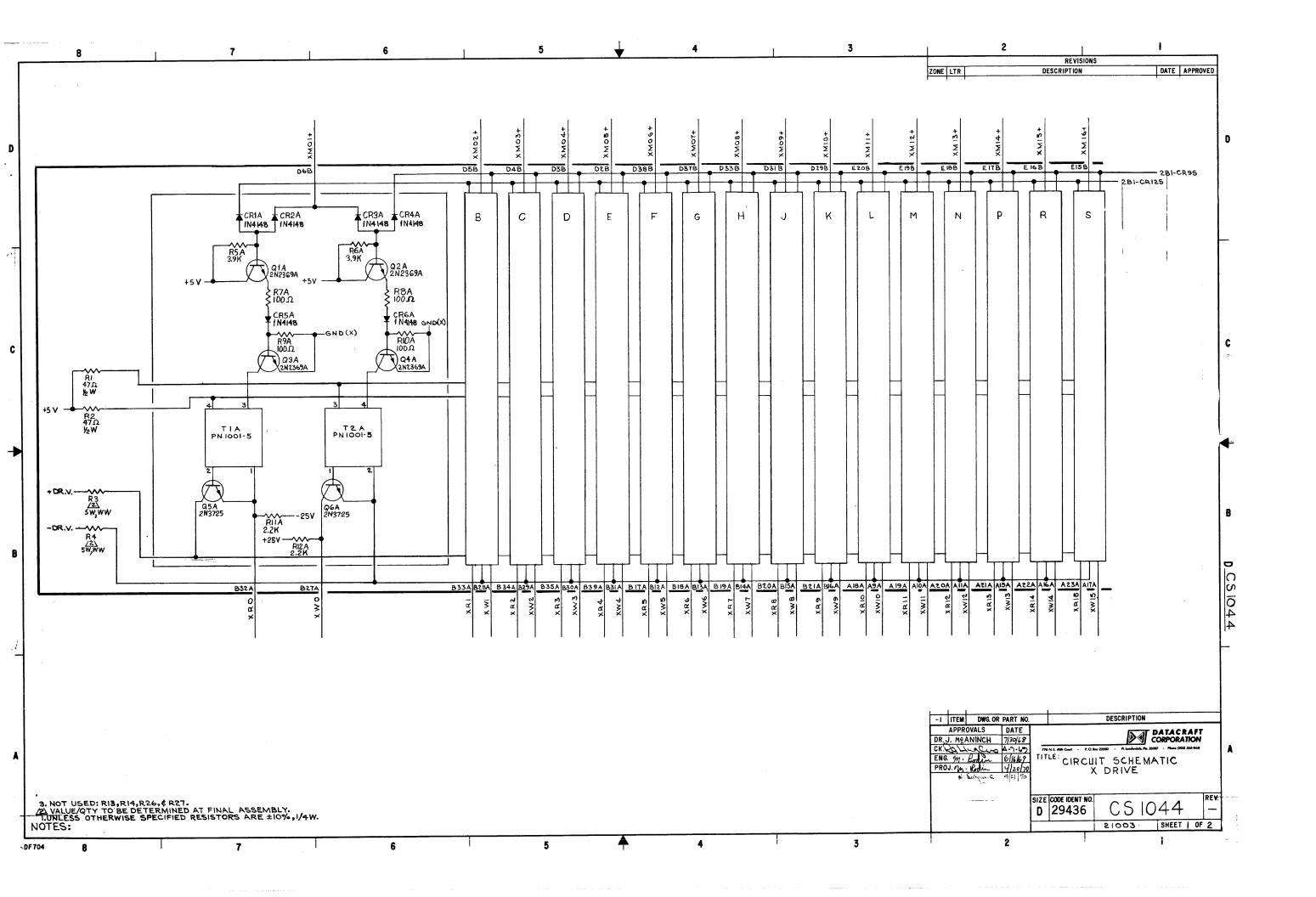


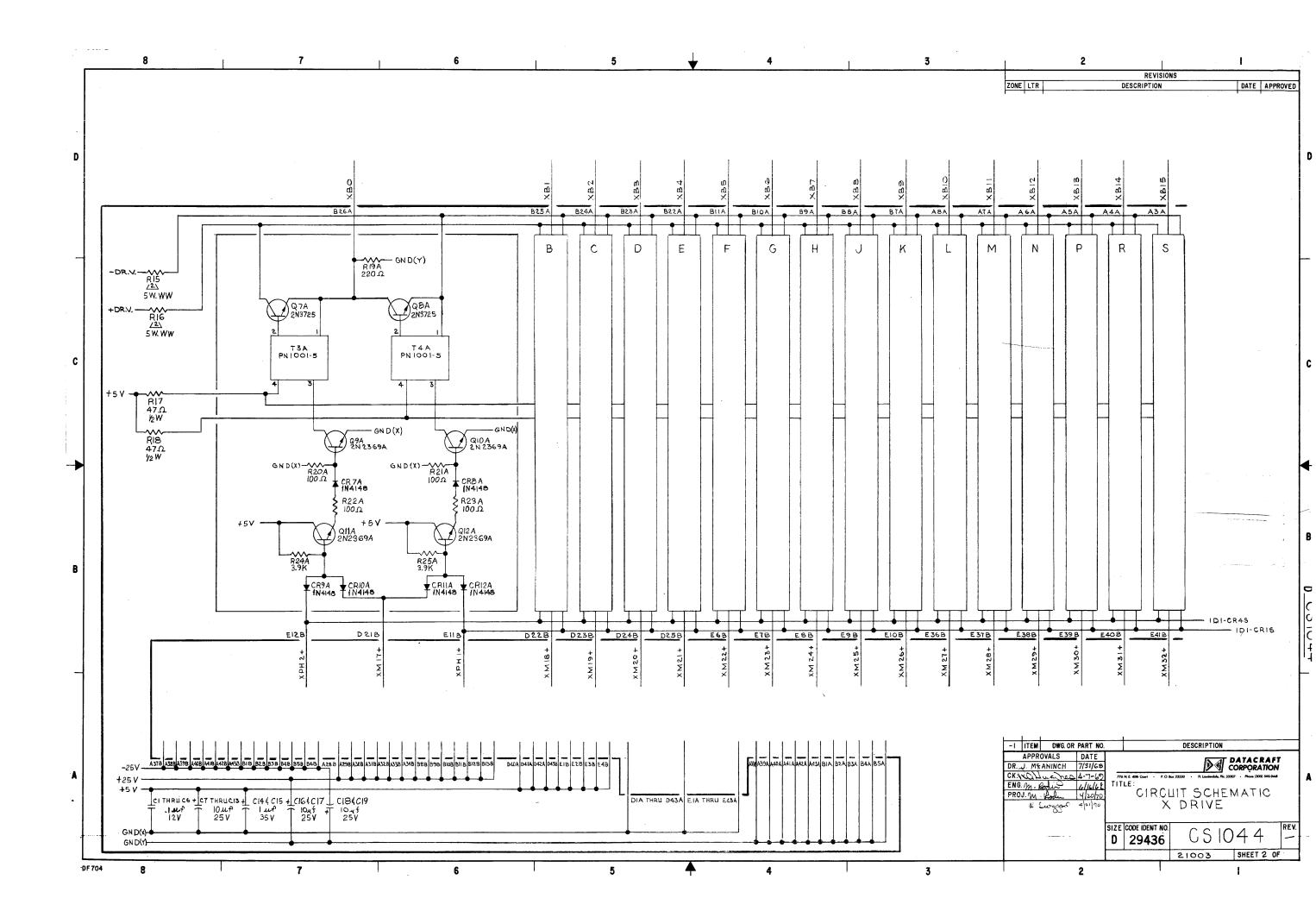


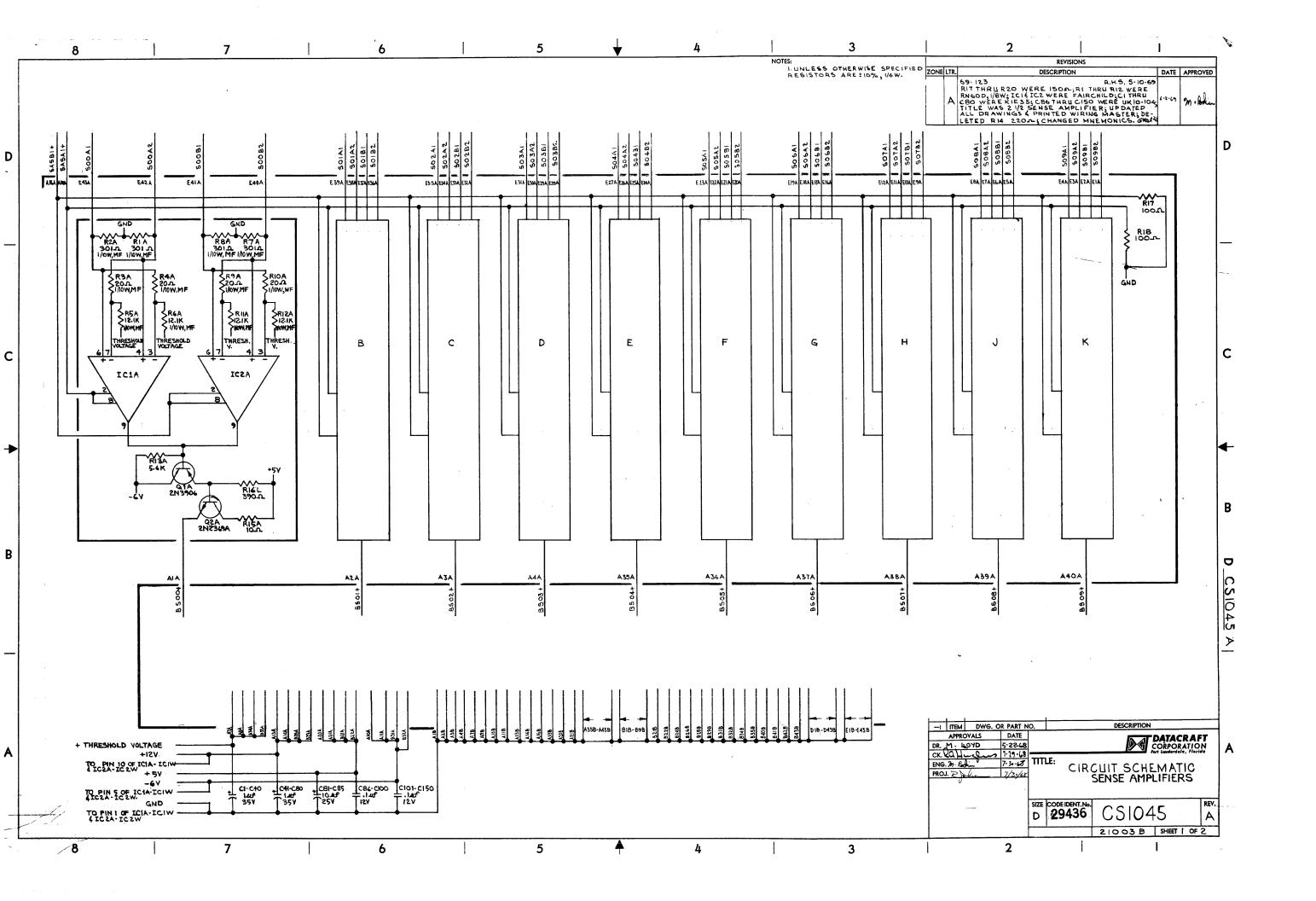


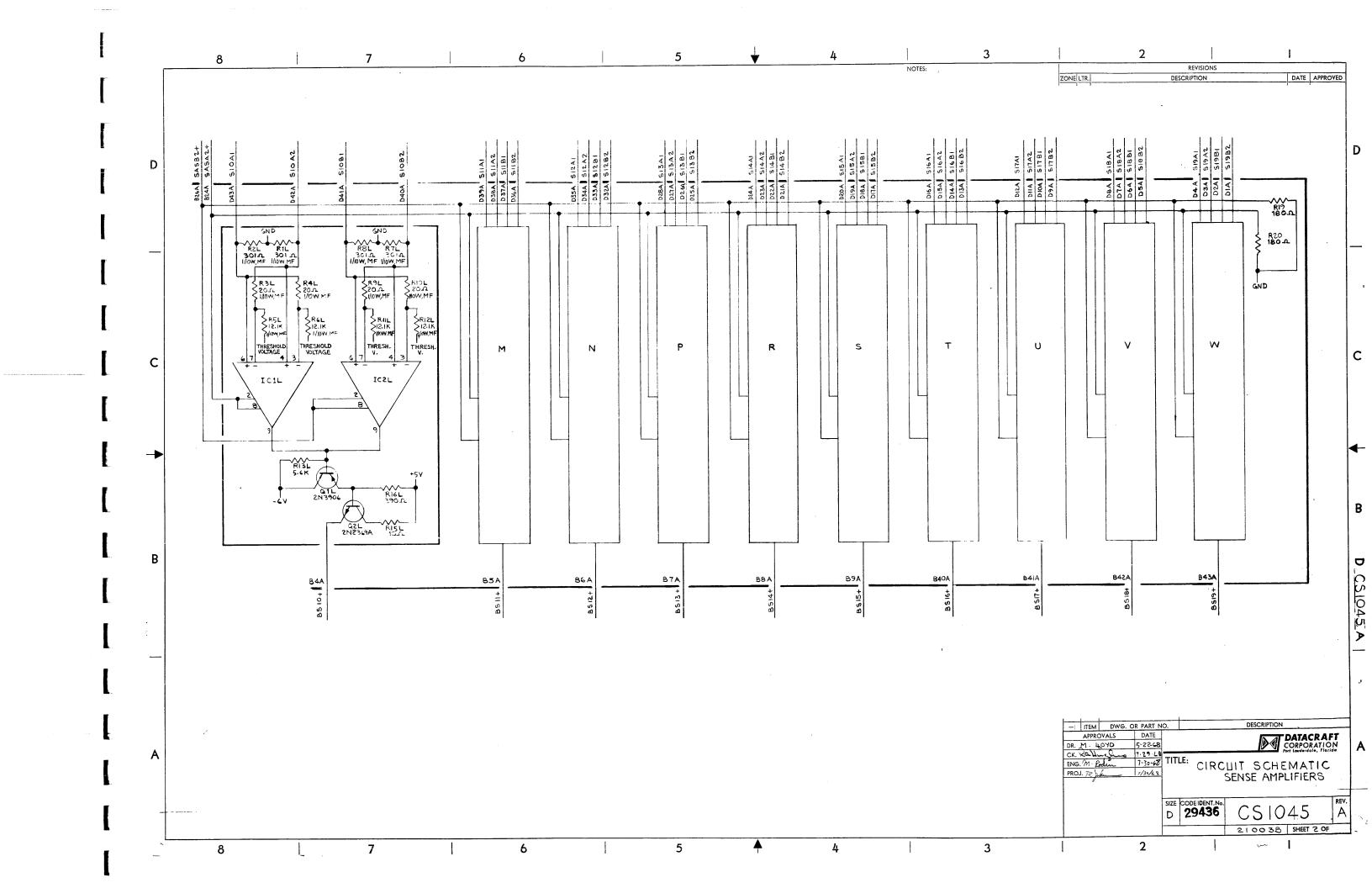


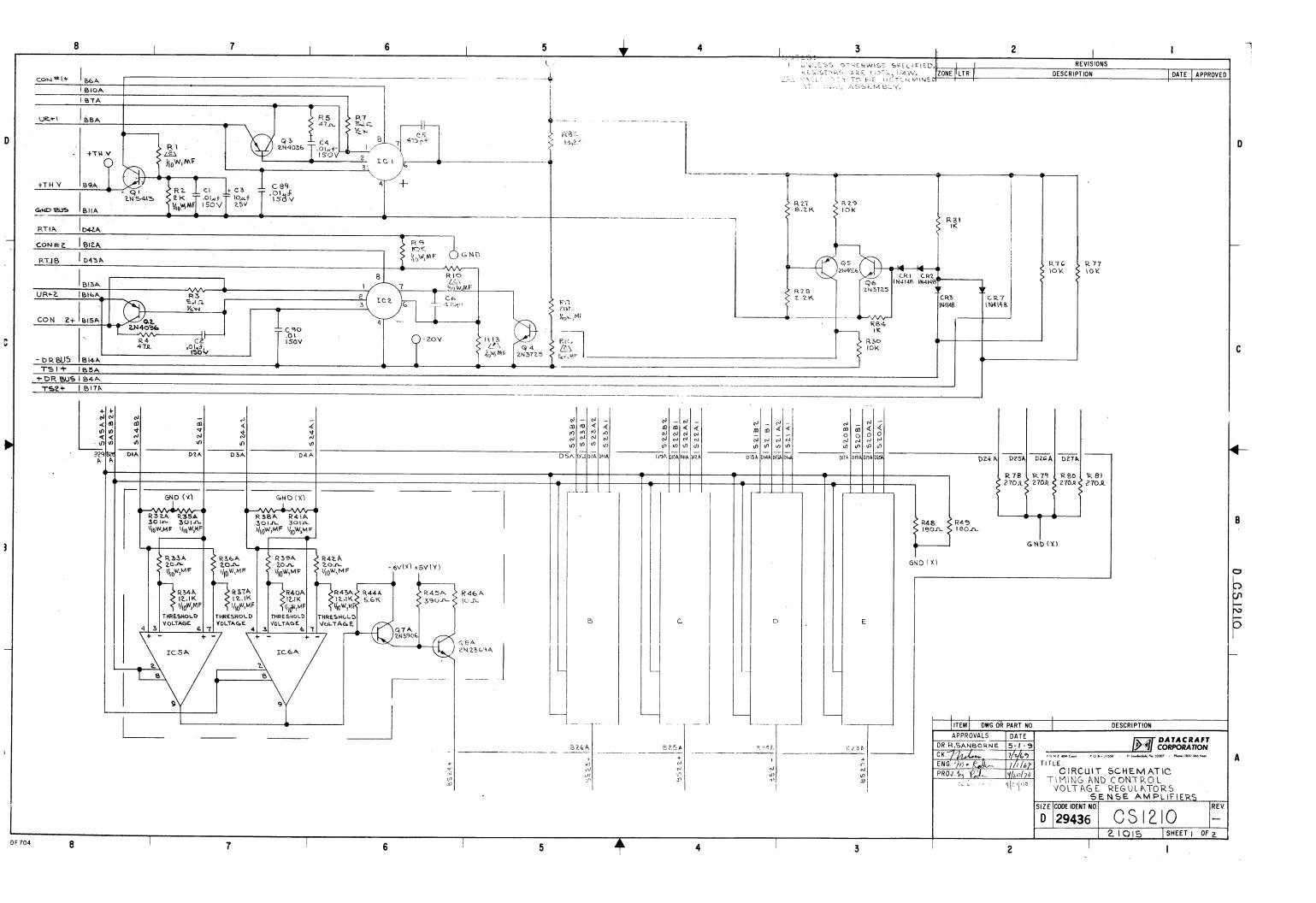


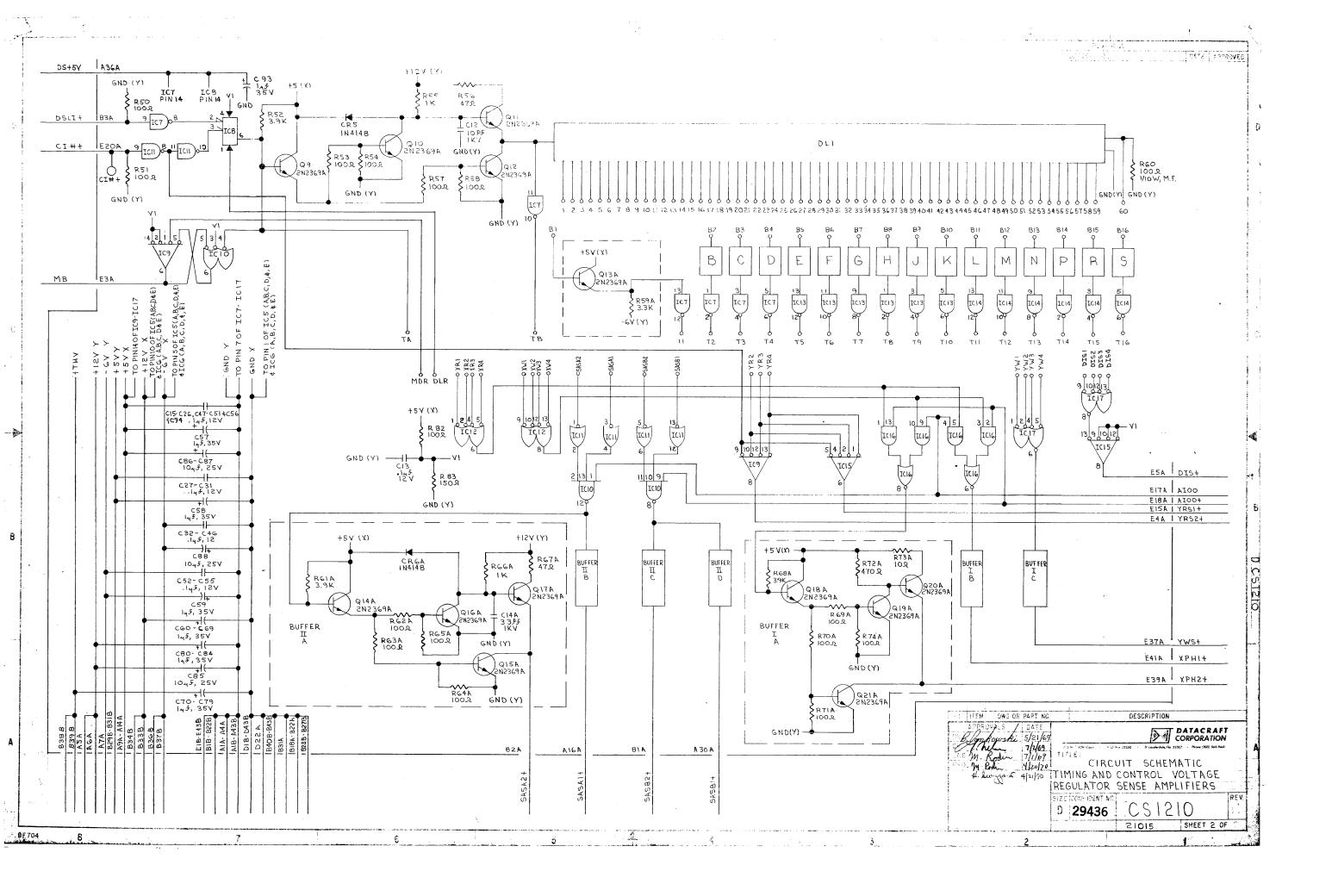


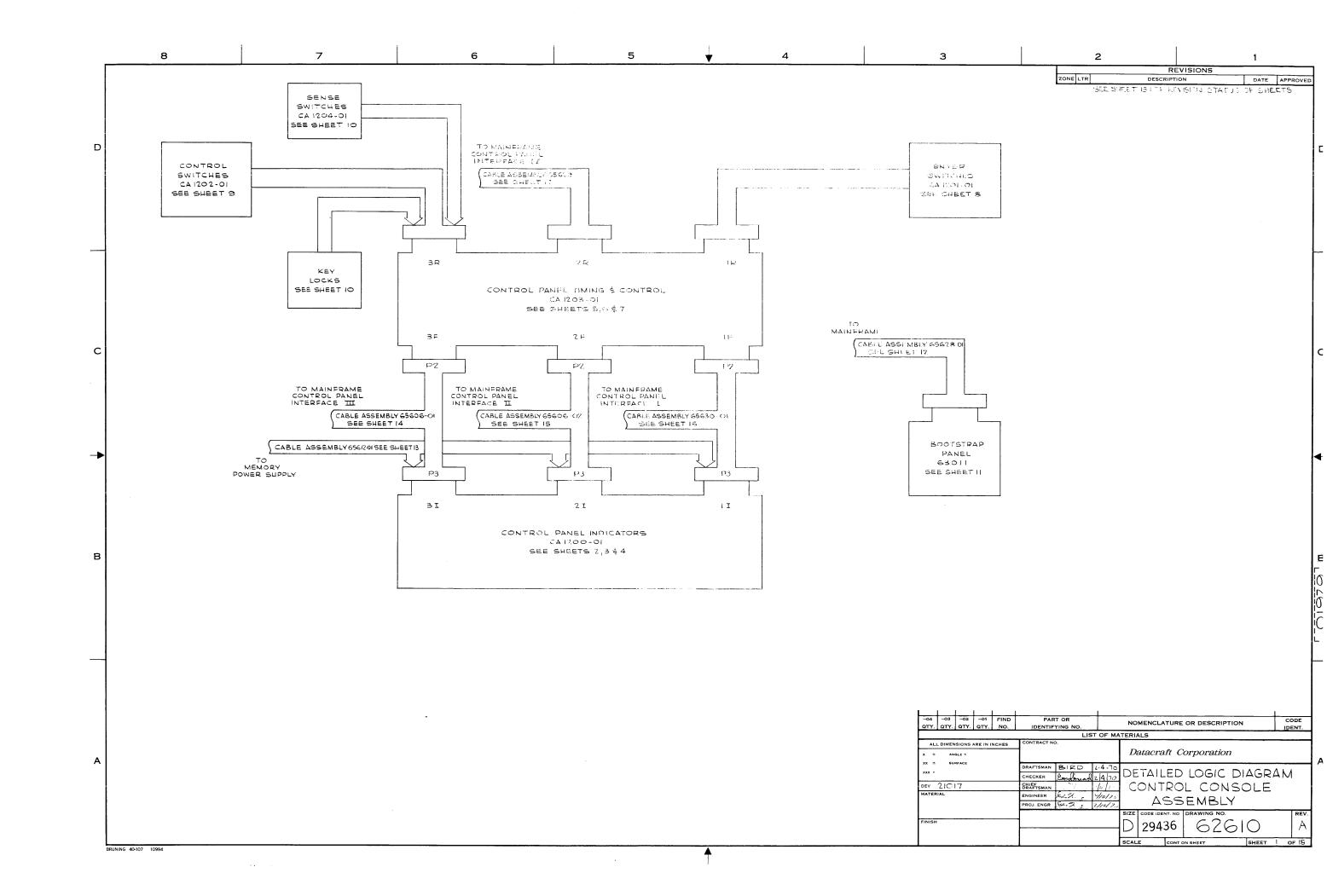


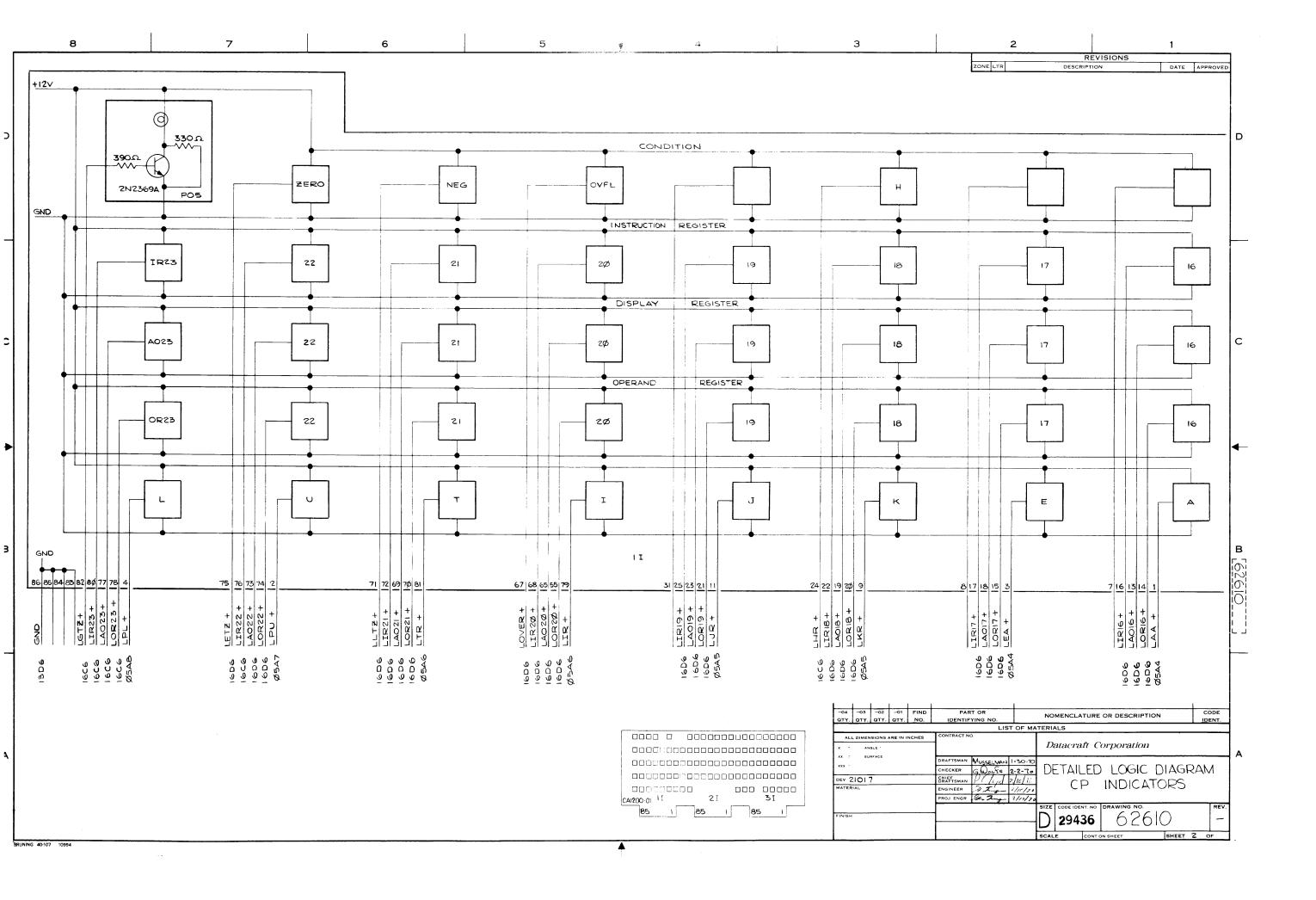


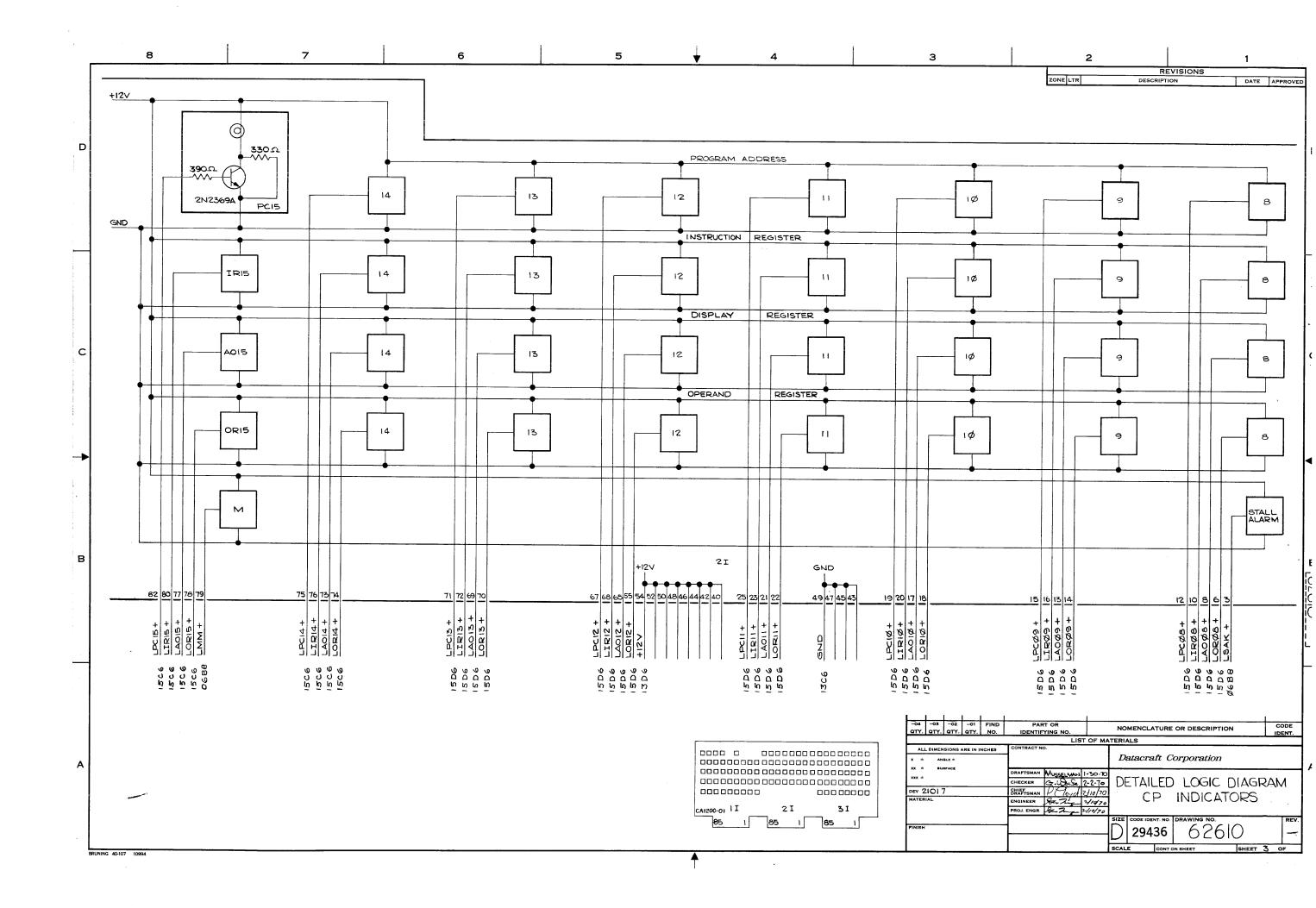


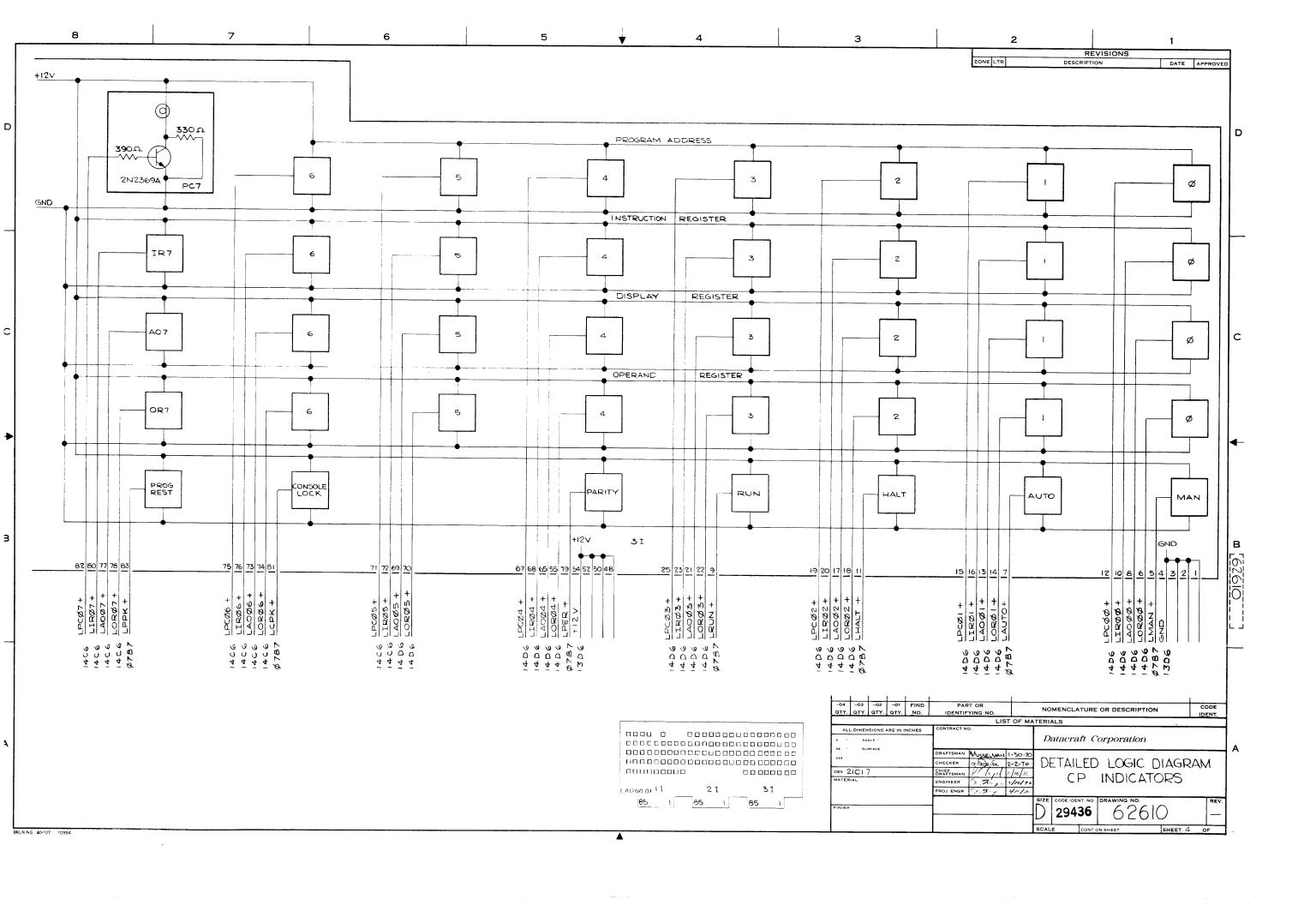


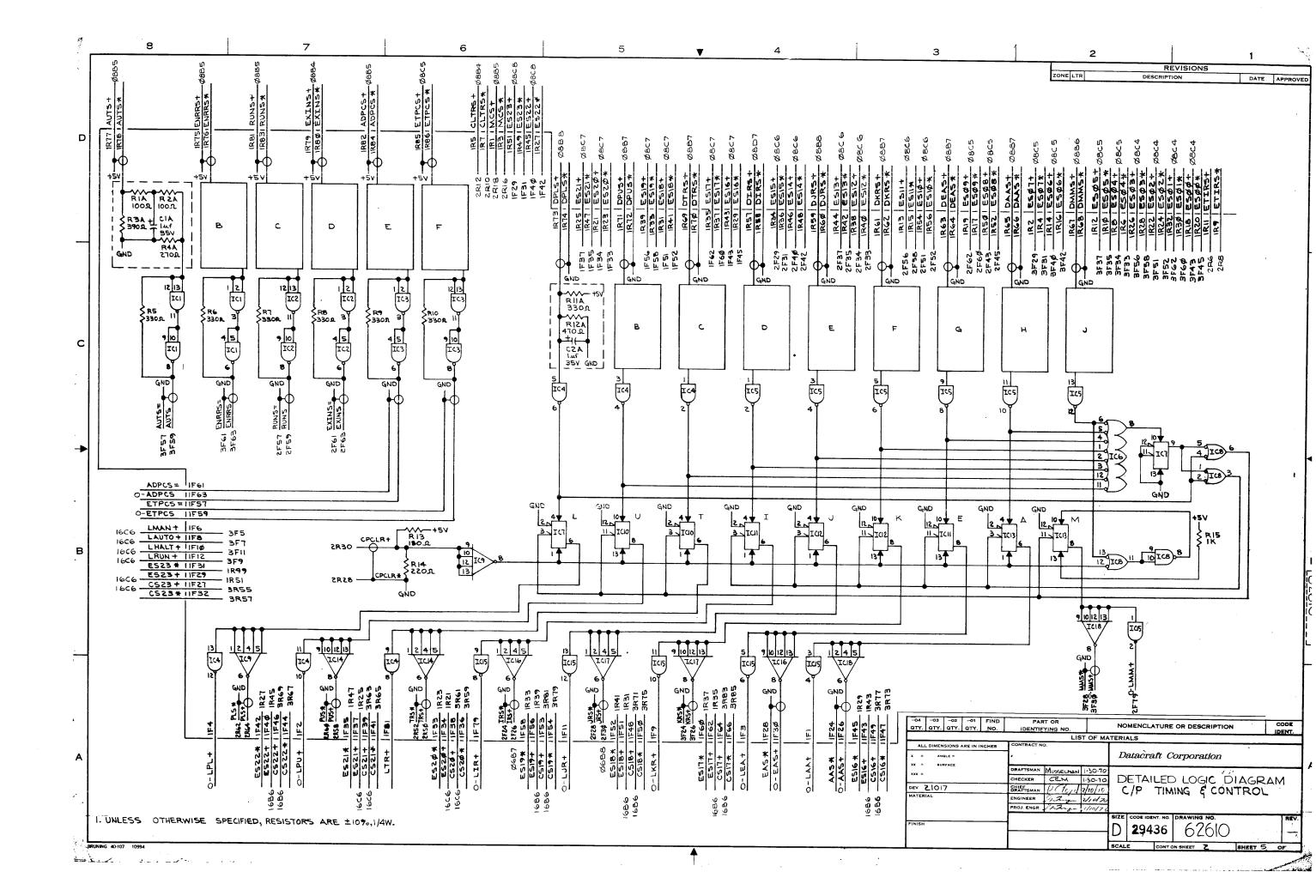


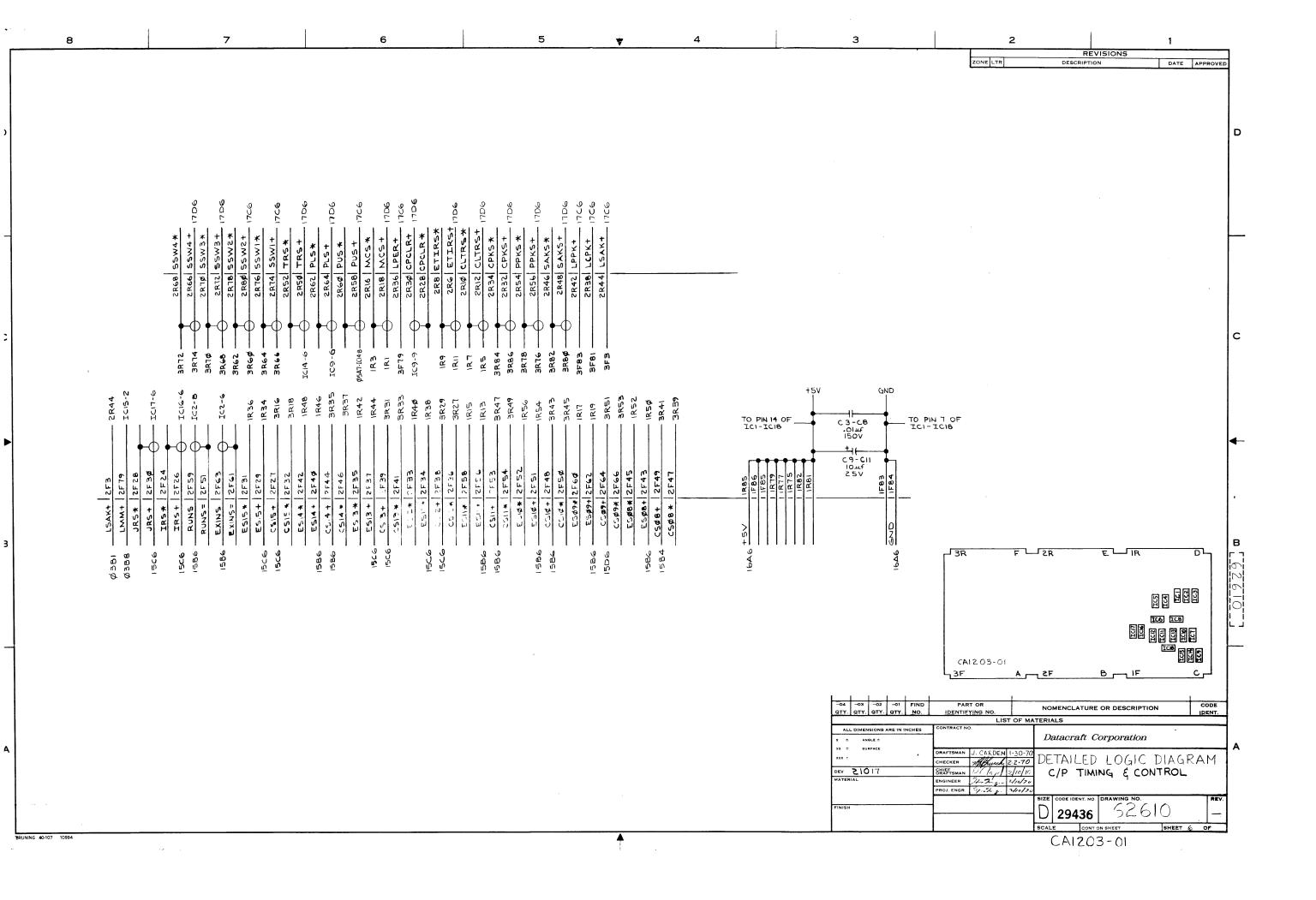


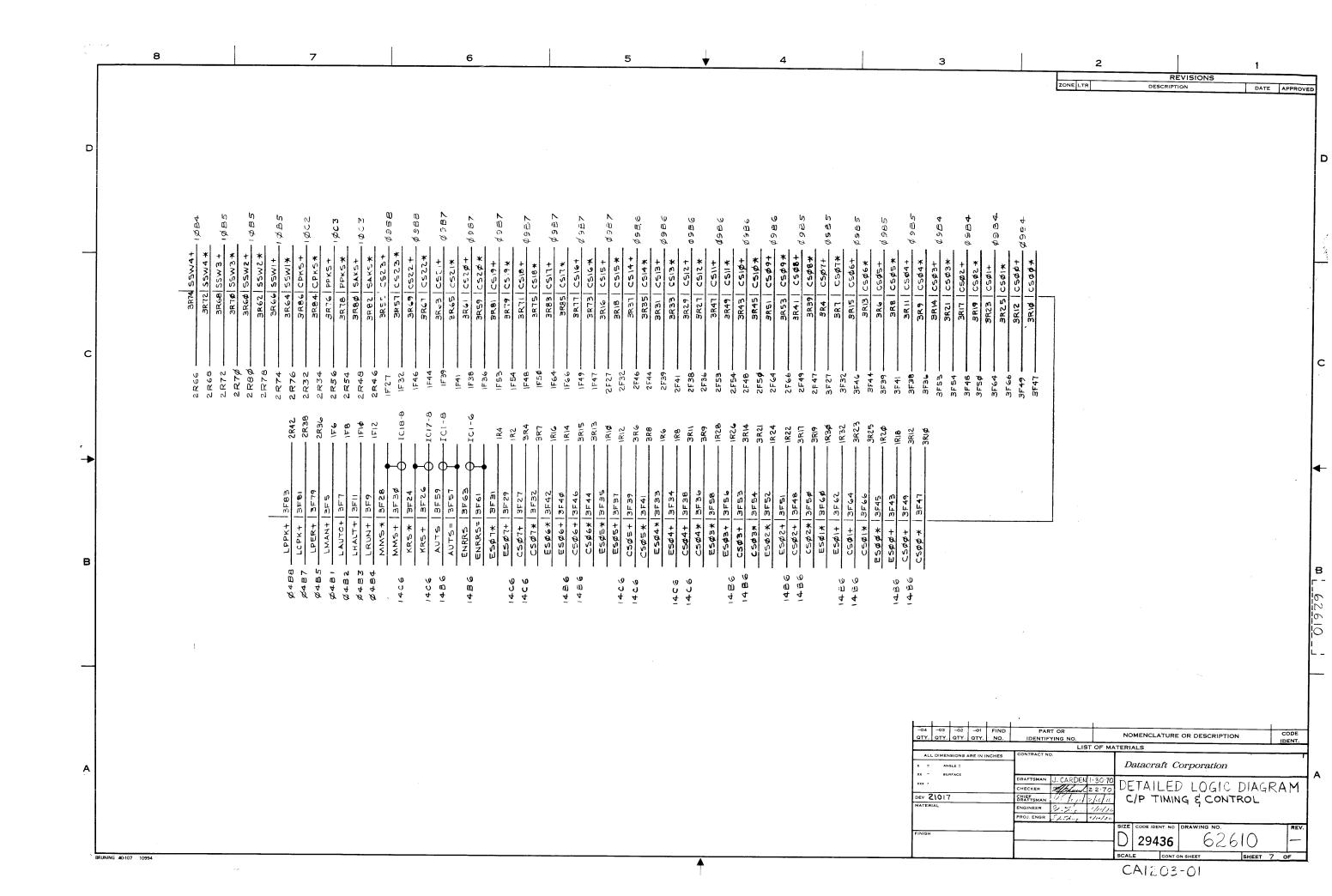


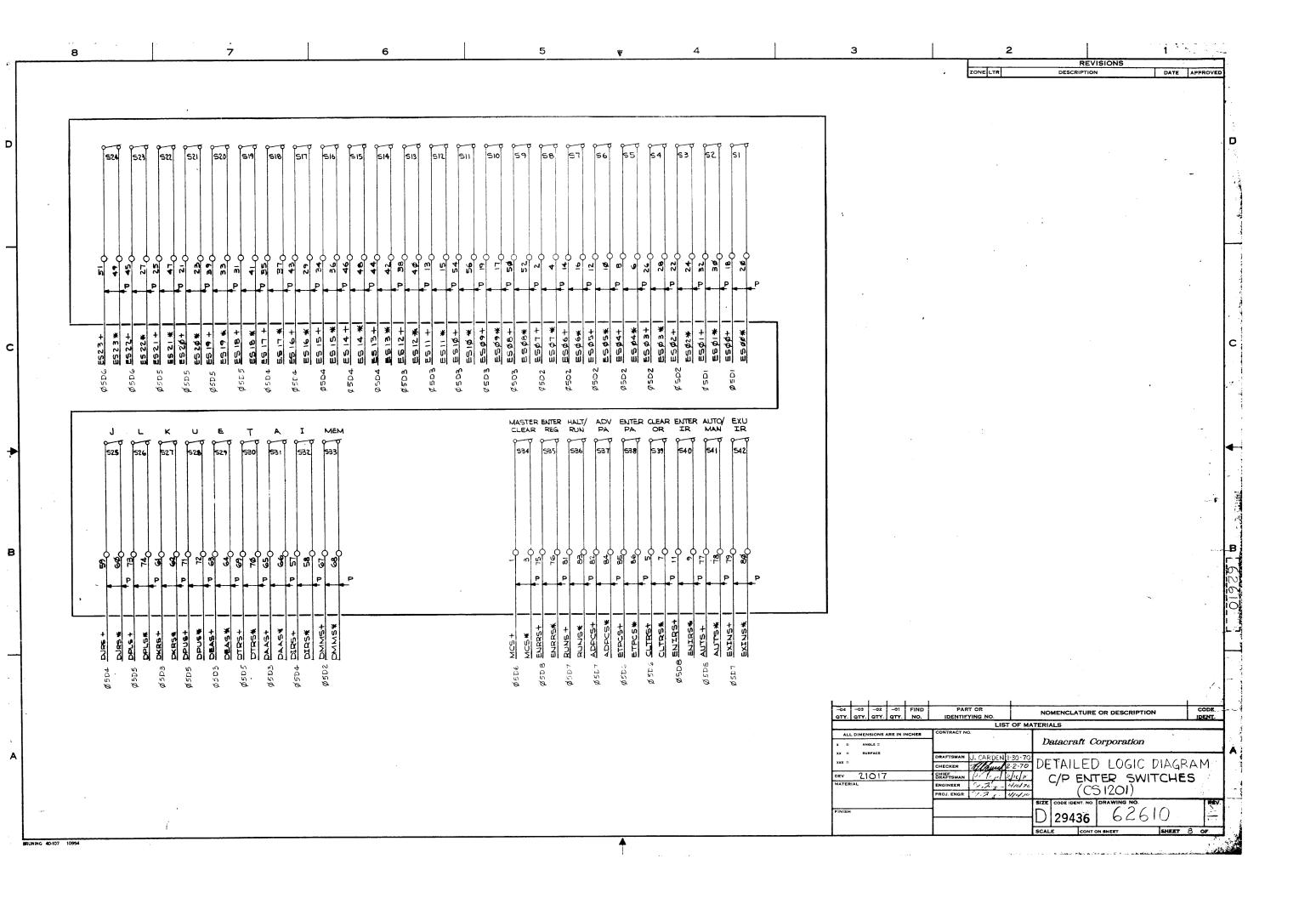


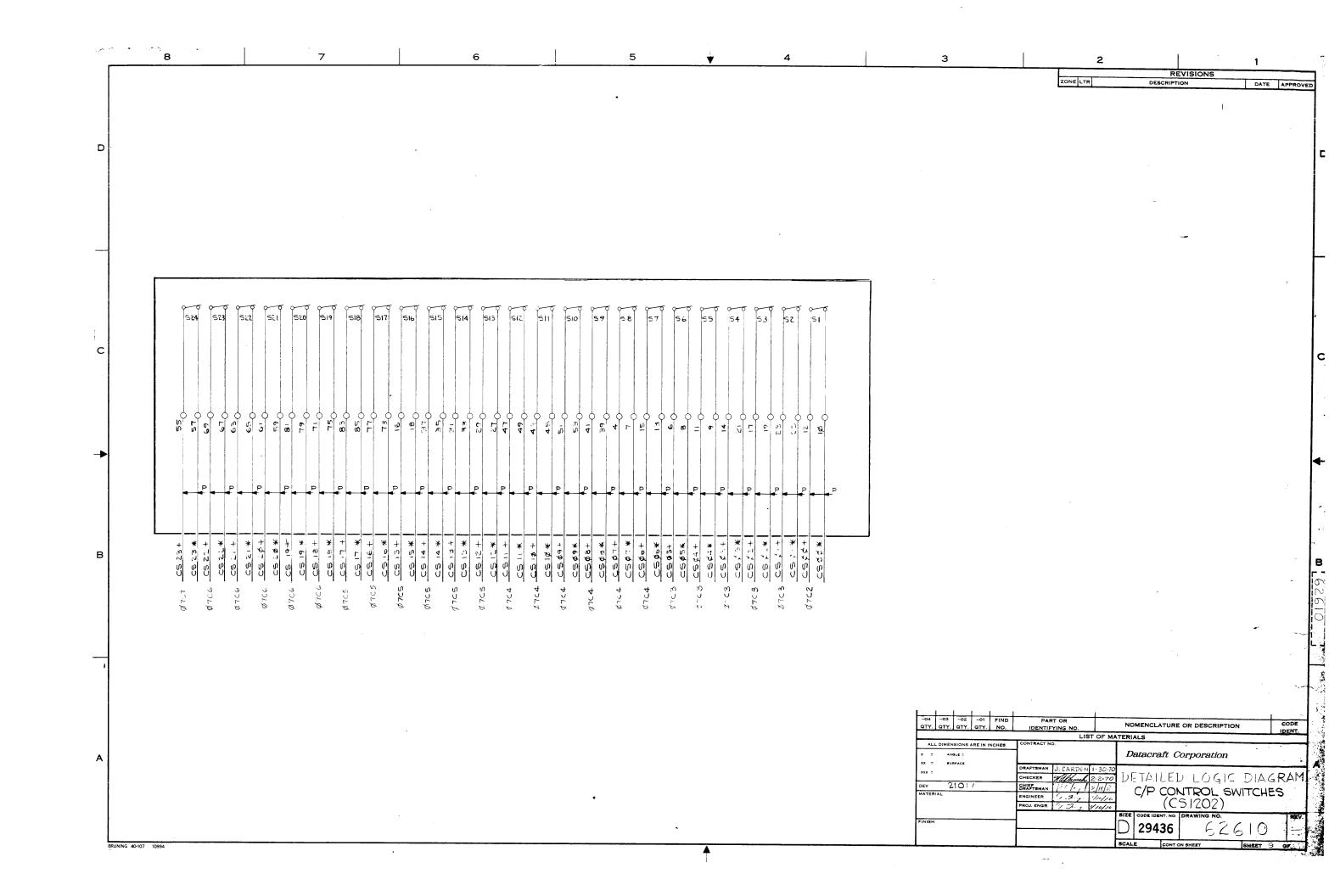


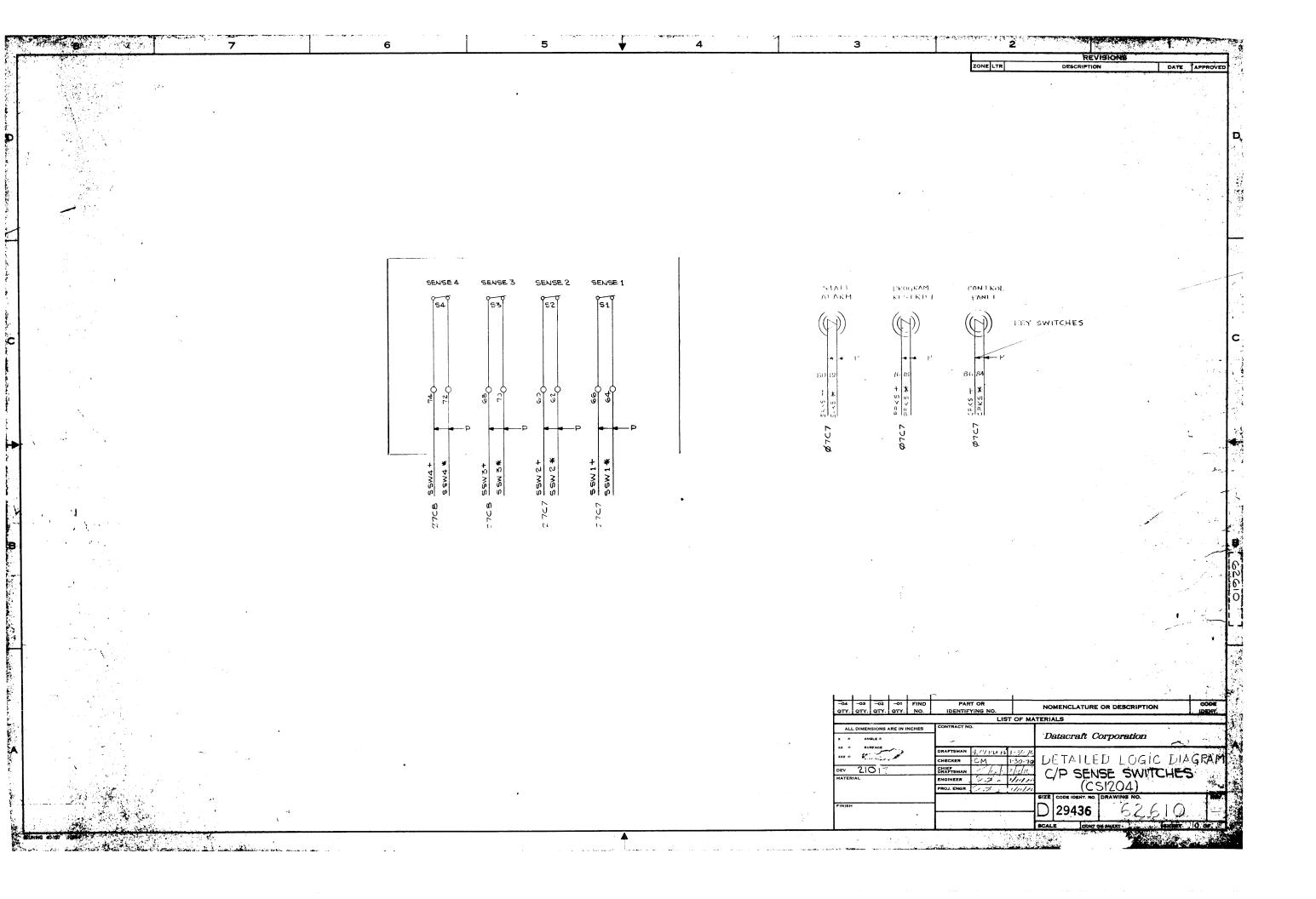


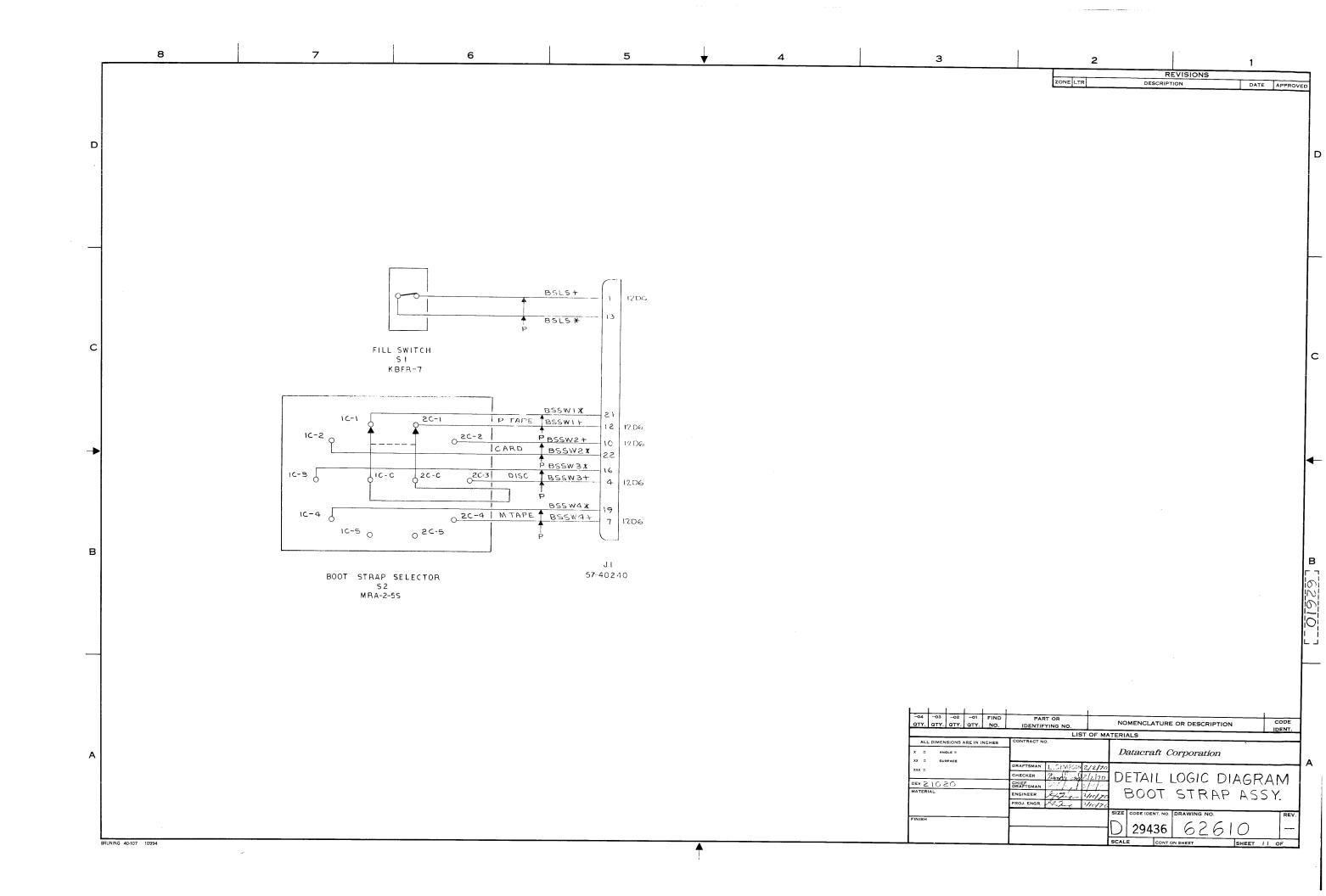


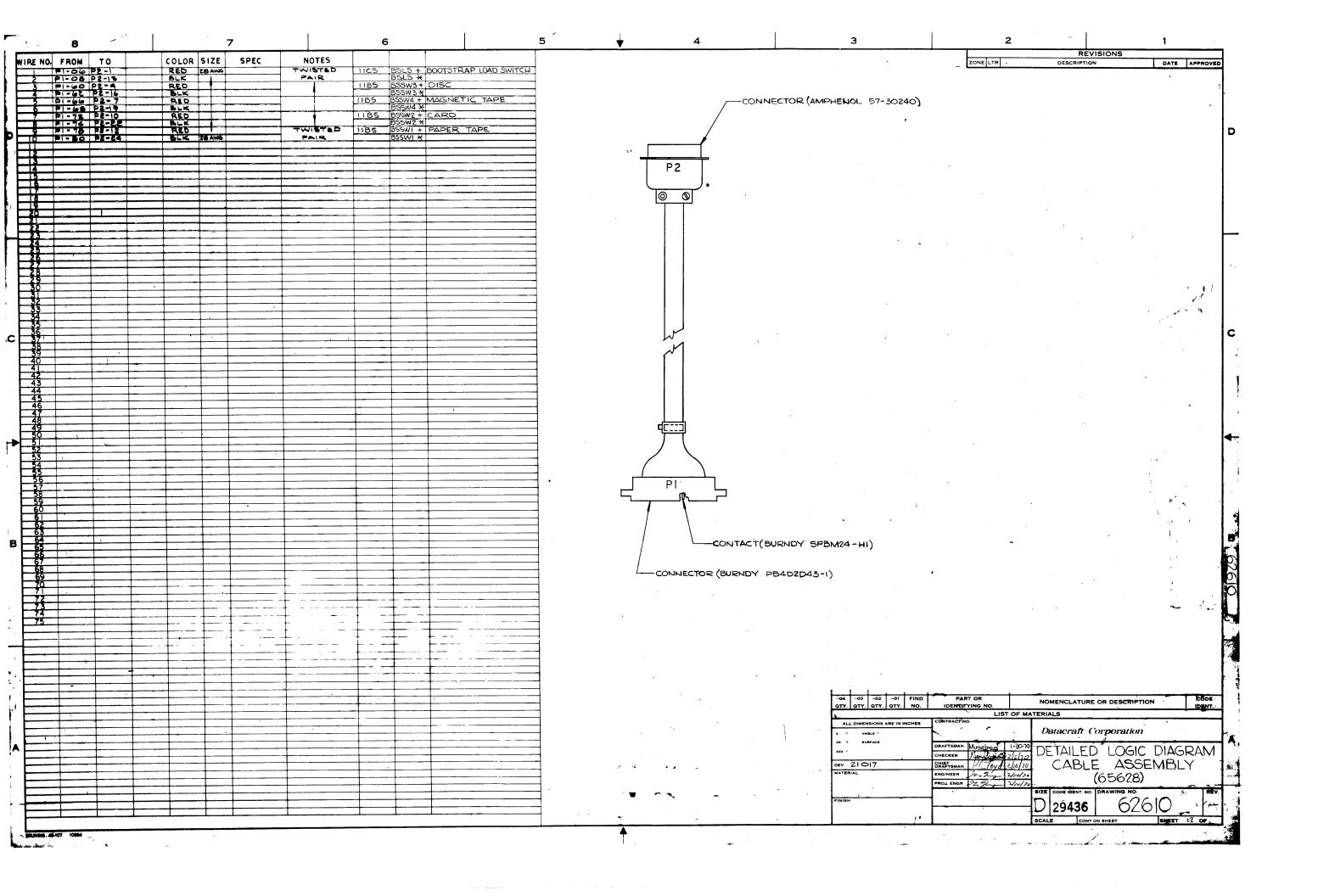


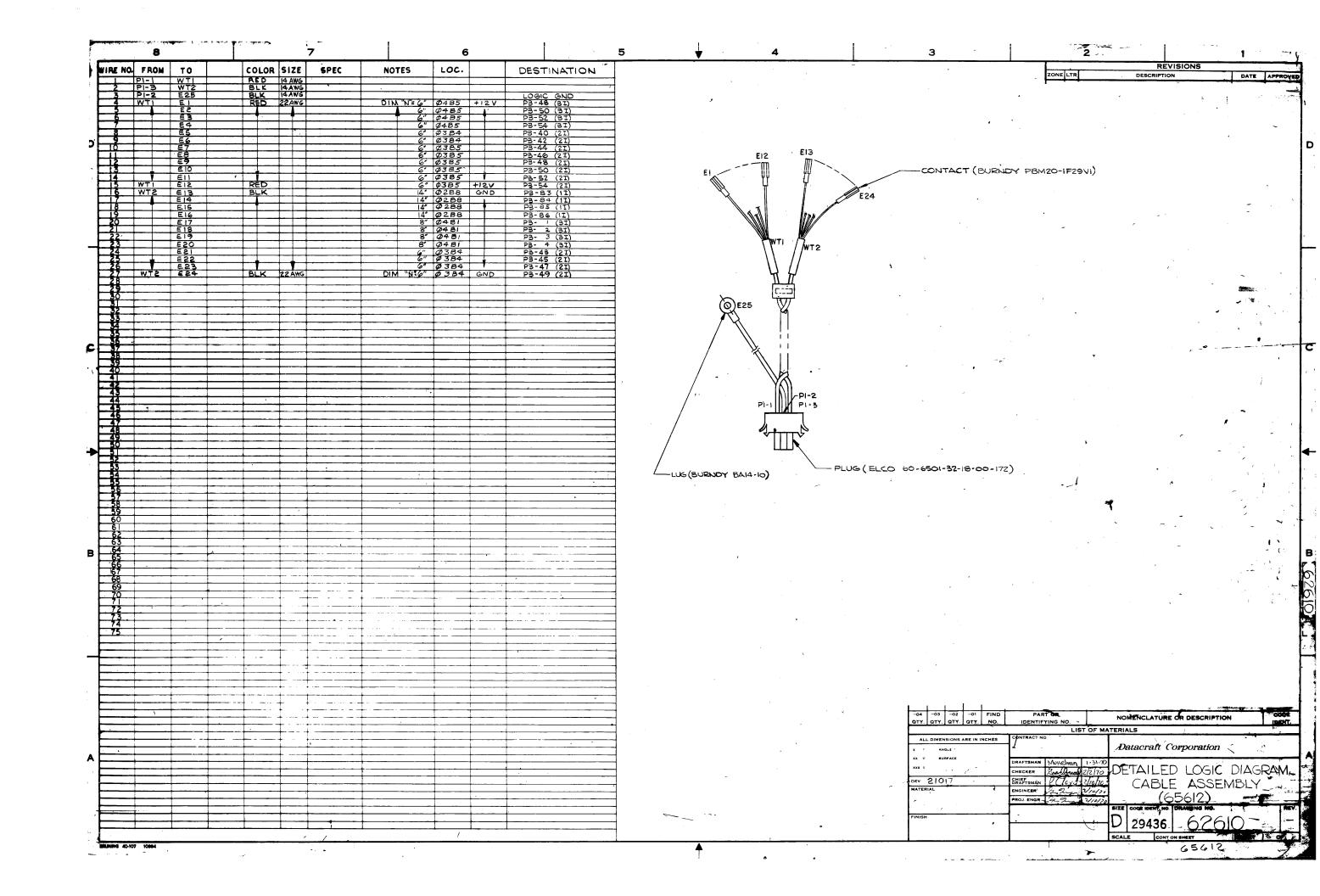


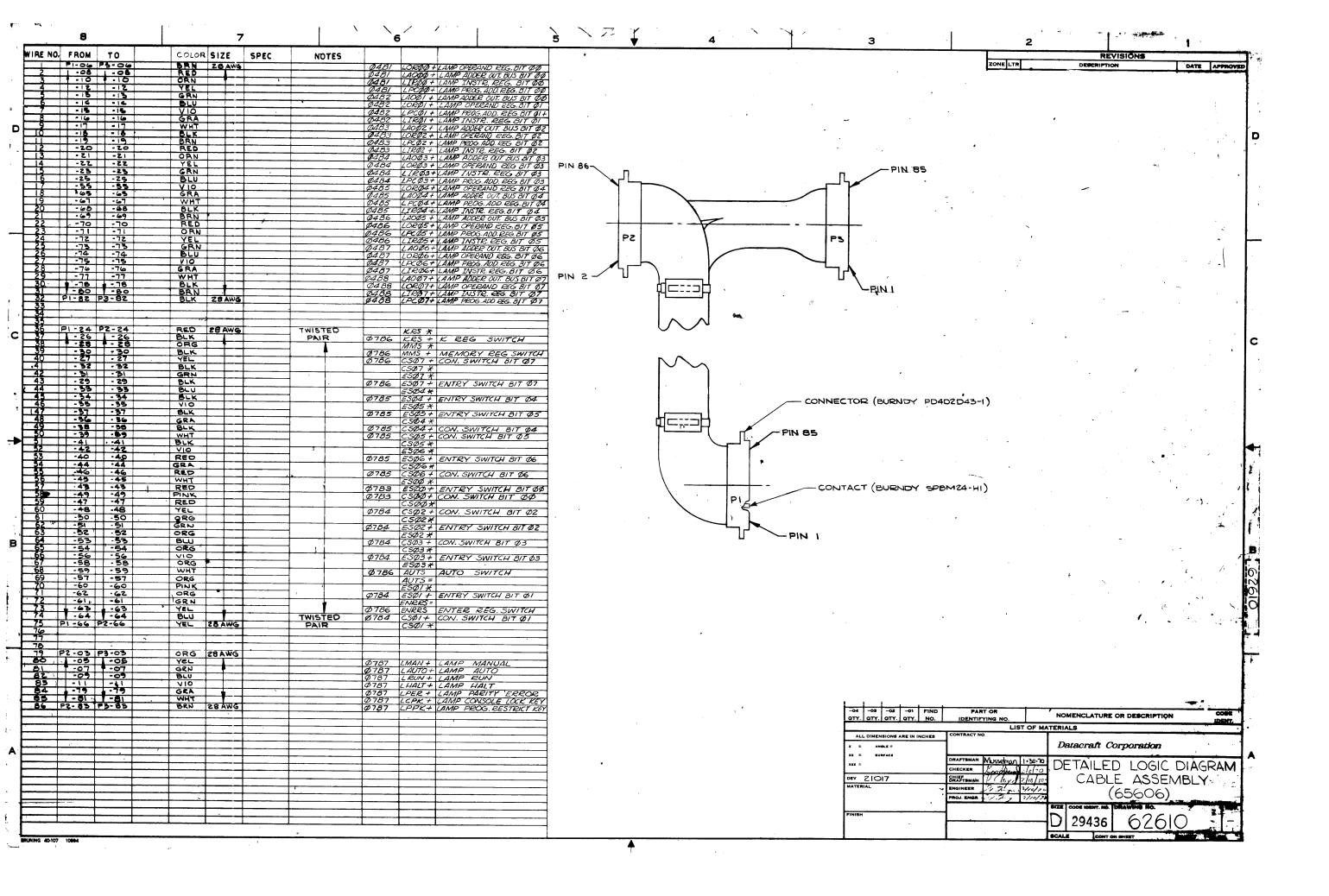


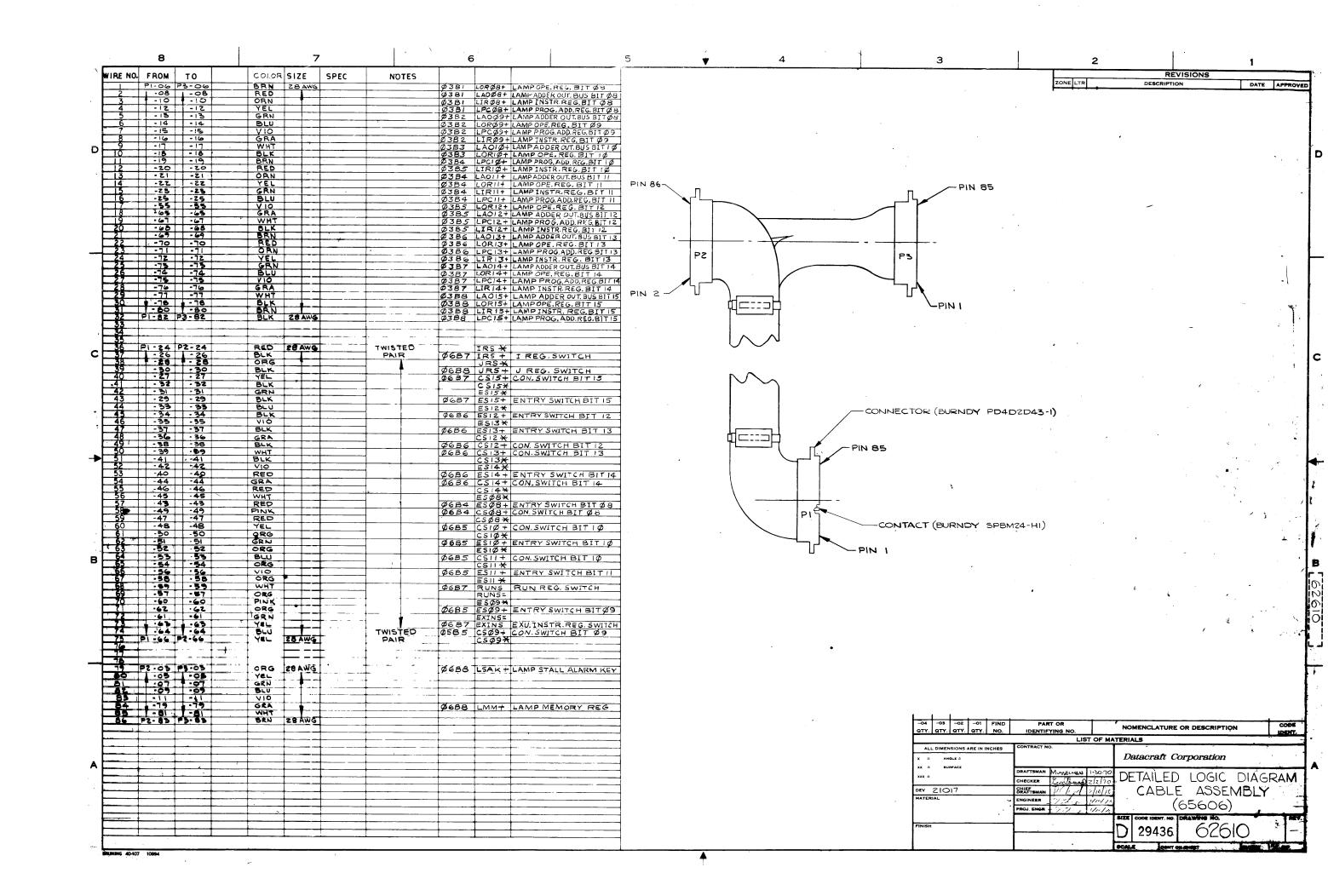


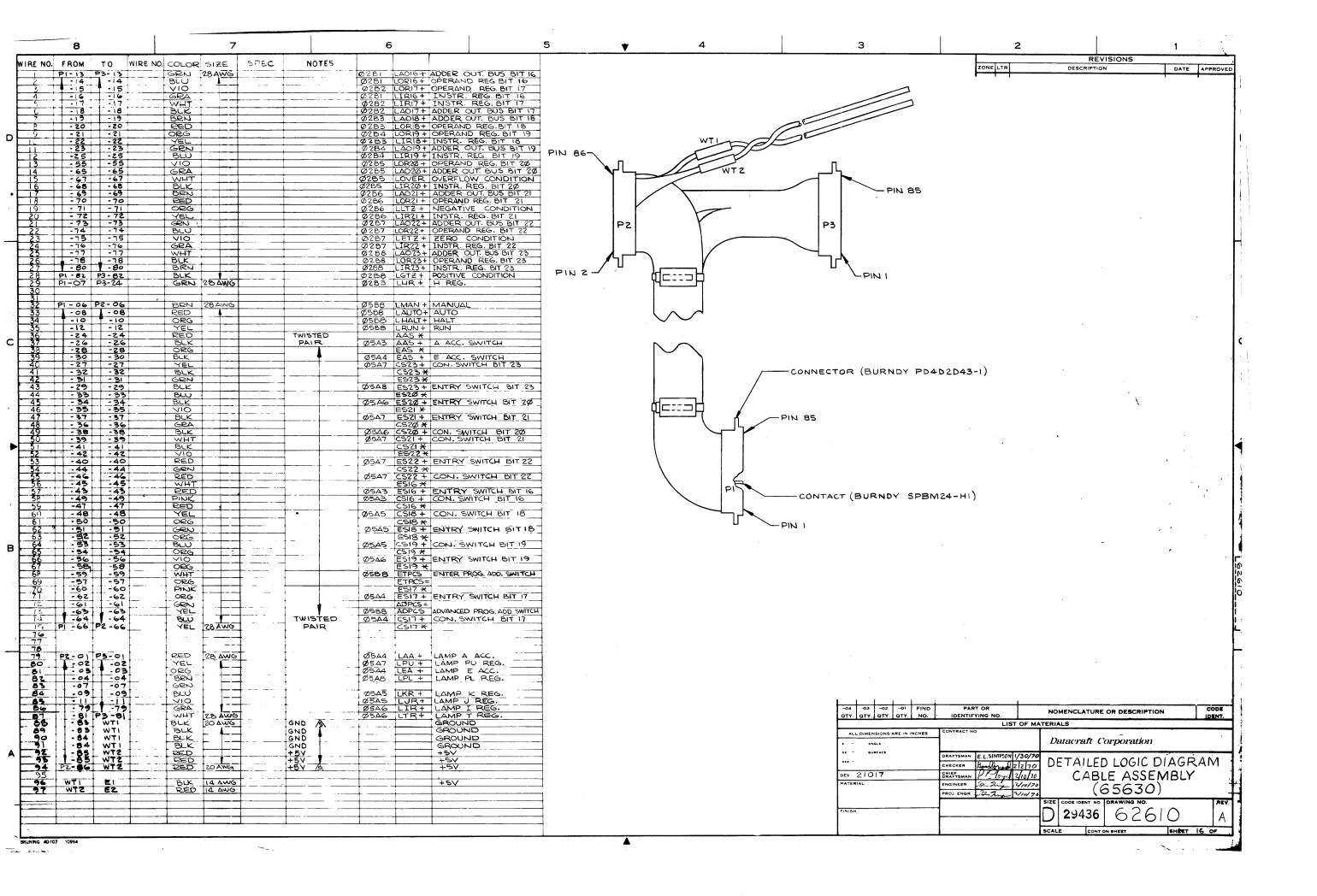


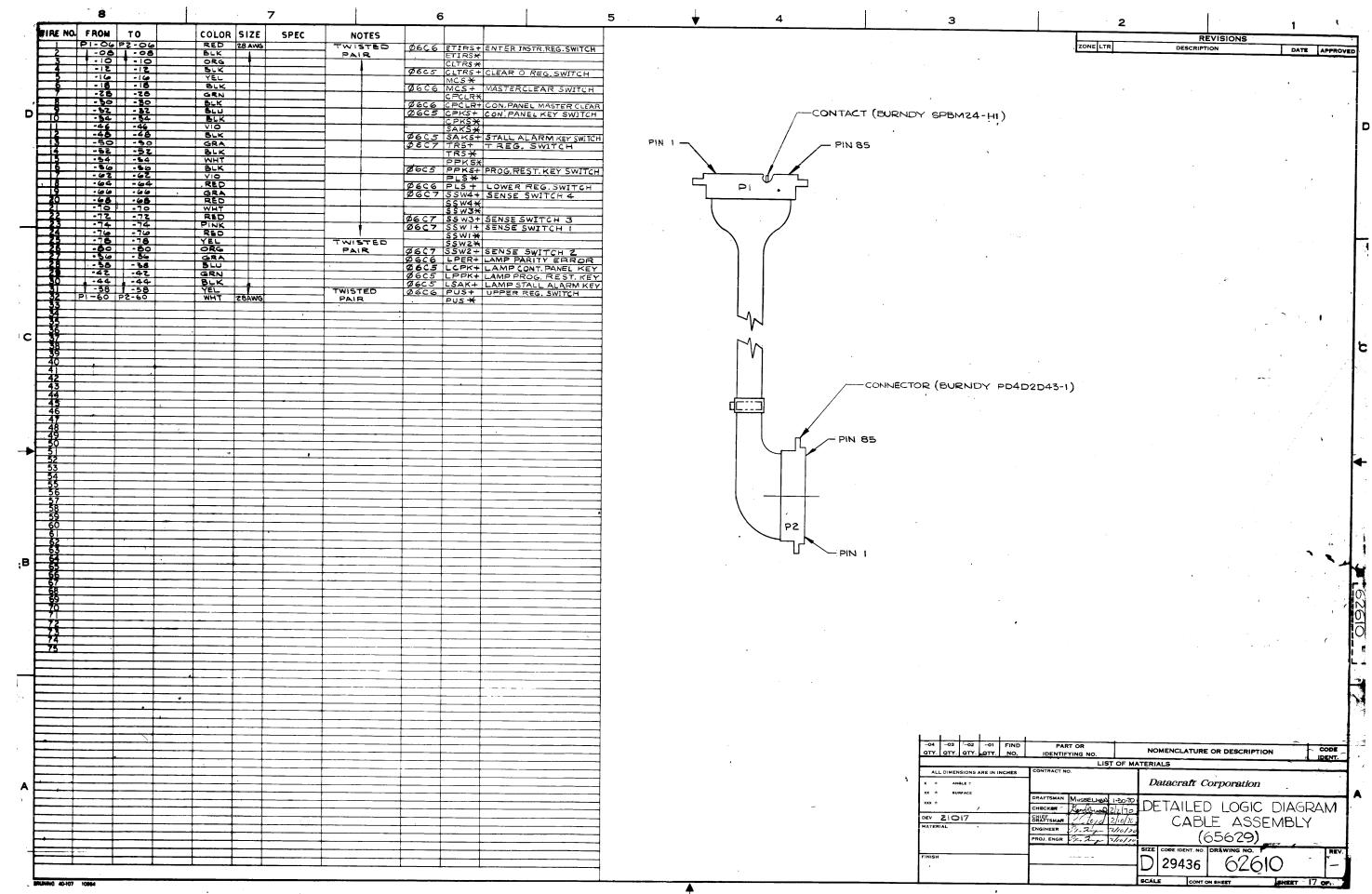


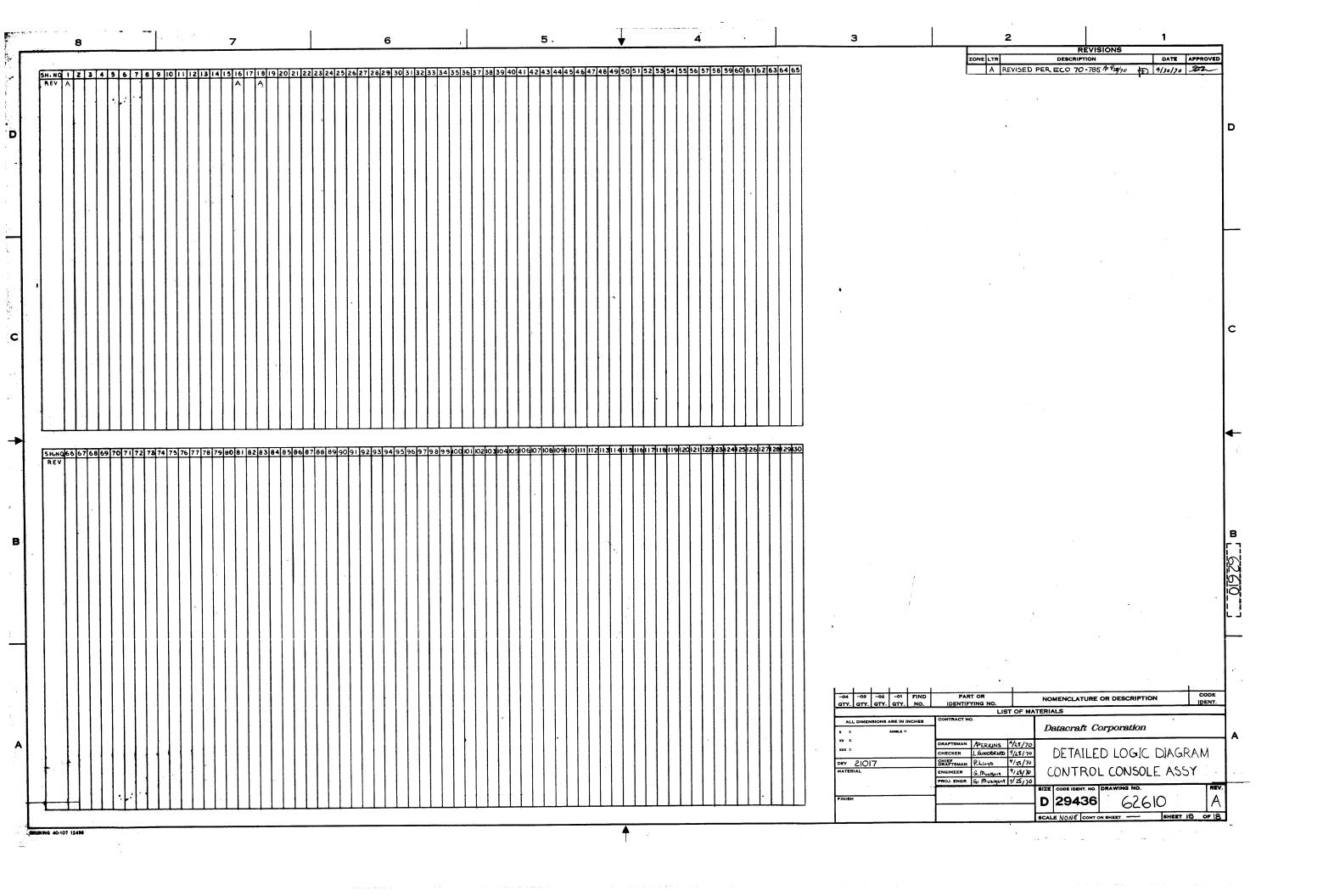


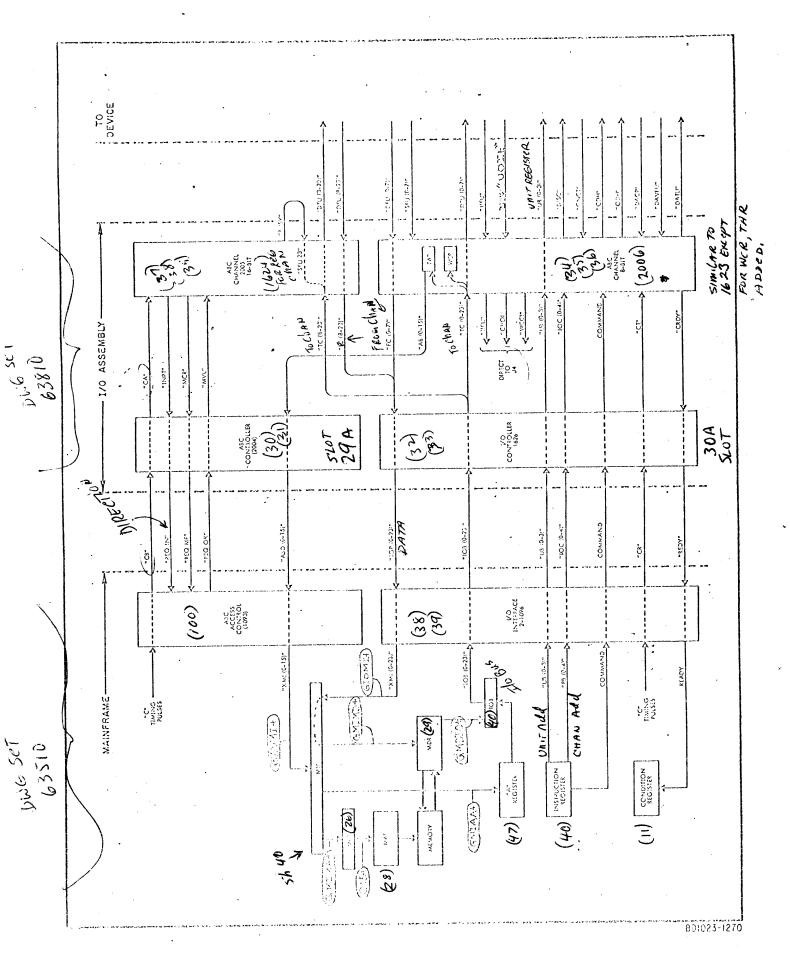




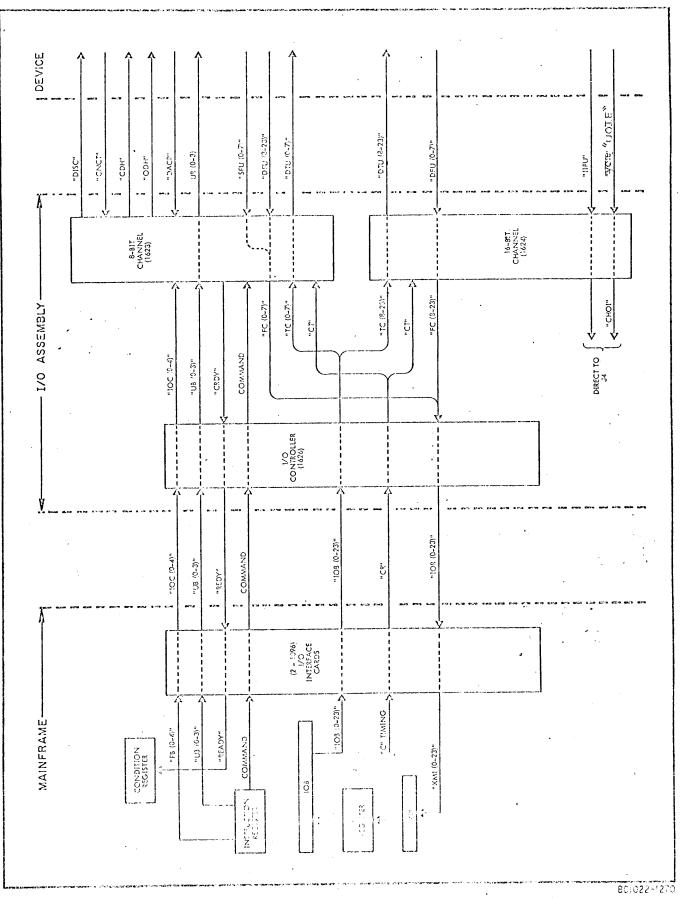








DC 6024. I/O Signal Flow Diagram for ABC Operation



DC 6024. VO Signal Flow Diagram for Single Word Operation