
ERRATA

TO THE TSB12LV21A PCILynx DATA SHEET

(TEXAS INSTRUMENTS LITERATURE NO. SLLS273, APRIL 1997)

This document contains corrections and additions to information in the TSB12LV21A data sheet (TI Literature Number SLLS273, April 1997), also included in *IEEE 1394 Circuits Data Book*, 1997 (TI Literature Number SLLD004). The following items apply to TSB12LV21A *production devices* only, PCI rev. ID = 2&3:

- a. Electrical isolation as described in Appendix J of IEEE 1394–1995 is not supported by the TSB12LV21APGF. TI has an improved isolation technique, and that is the recommended isolation solution.

– Please see *Galvanic Isolation of the IEEE 1394–1995 Serial Bus* (TI Literature Number SLLA011).

- b. If posted writes are enabled, a slave access to PCILynx can be continuously retried on the PCI bus under certain unusual conditions.

Suggested Work Around:

– Set the ENA_POST_WR bit in the miscellaneous control register to 0.

- c. No error indication is given when one of the following types of illegal packet is received:

- A packet containing a header with a tcode for no-data
- A quadlet data packet that also contains a data block payload

In these cases, the tcode specified that there should be no data block payload. If a data block payload is attached to such a packet, no error is indicated. In normal (other than snoop) mode, the PCILynx DMA transfers only the data specified in the header to the data buffer and the remaining data is flushed. In snoop mode, all data is transferred.

- d. In the case of a packet with multiple errors including an ATF underflow, the underflow will not be counted if the ack status reports another error. The ATF underflow counter is incremented only when an ack status of 10010 (type 1, code 2) is returned.
- e. Incidents of transmit ack error code 11110 (type 1, code E) have been reported. This error code indicates that the link reported a corrupted header before the packet was transmitted. The exact nature of this error is not known, however it is believed to be related to retries and/or underflow errors.



(This page has been left blank intentionally.)



IMPORTANT NOTICE

Texas Instruments (TI) reserves the right to make changes to its products or to discontinue any semiconductor product or service without notice, and advises its customers to obtain the latest version of relevant information to verify, before placing orders, that the information being relied on is current and complete.

TI warrants performance of its semiconductor products and related software to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

Certain applications using semiconductor products may involve potential risks of death, personal injury, or severe property or environmental damage ("Critical Applications").

TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, INTENDED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT APPLICATIONS, DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS.

Inclusion of TI products in such applications is understood to be fully at the risk of the customer. Use of TI products in such applications requires the written approval of an appropriate TI officer. Questions concerning potential risk applications should be directed to TI through a local SC sales office.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards should be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance, customer product design, software performance, or infringement of patents or services described herein. Nor does TI warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used.