

PRESS RELEASE

CYPRESS EXTENDS PLD OFFERING, INTRODUCES 20V8

Adds to 16V8, 22V10, CPLD and FPGA Products

SAN JOSE, Calif., February 19, 1996 -- Cypress Semiconductor today expanded its broad programmable logic device (PLD) offering with the introduction of the PALCE20V8. The PALCE20V8, available in volume production today, is a Flash erasable and reprogrammable device which is pin- and functionally compatible with Generic Array Logic (GAL()) products.

Cypress entered the GAL-compatible market in 1994, with the introduction of the PALCE16V8. The PALCE20V8 adds another important element to Cypress's broad-line coverage in the programmable logic market. The company now offers the complete spectrum of programmable logic, including GAL compatible 16V8, 20V8, 22V10 devices, and the UltraLogic() family of high-density programmable logic. UltraLogic includes the FLASH370() family of CPLDs, the recently introduced Ultra38000() family of FPGAs, and the pASIC380 (FPGAs, all supported by Cypress's VHDL-based Warp() design tools.

The PALCE20V8 is offered in QSOP (Quarter-size Small Outline Packages) packaging, a feature that is unique to Cypress's line of standard programmable logic devices. The QSOP package, which measures only 150-mil wide with a 25-mil pitch, reduces board space by 65% when compared to PLCC packaging. The new product also reduces ground bounce significantly due to the lower ground lead inductance- approximately 20% of the ground bounce for PDIP packages.

"The SPLD market continues to be driven by customer demand for low-cost, multiple source products with inexpensive, widely available software support," said Naushik Desai, product marketing manager for SPLDs. "Cypress's entry into the 20V8 market adds a new, strong supplier with a complete line of programmable logic solutions to meet all customer requirements."

The PALCE20V8 is manufactured using Cypress's 0.65 micron Flash CMOS technology (used for Cypress's PAL22V10D and FLASH370 products). The reprogrammability provided by this process allows for increased flexibility to modify the designs and reduce costs. The product has a user-programmable macrocell which includes output polarity control and enables the device to function as a superset to familiar 24-pin PLDs.

The PALCE20V8 is supported by Cypress's Warp2() and Warp3() software. Programming support is provided by Cypress's Impulse3() programmer, as well as a variety of third-party programmers.

Price and Availability

The PALCE20V8 is available in QSOP, (plastic and ceramic) DIP, LCC, PLCC, and SOIC packaging. It is offered in low-power versions and can be used in commercial, industrial and military applications. The PALCE20V8 25-ns version is priced at \$0.65 in 10K-unit quantities.

Cypress Semiconductor is a leader in the design, development, and manufacture of a broad line of high-performance digital integrated circuits, fabricated using its proprietary technologies. Cypress offers a range of products, including static RAM memories, programmable logic devices, data communications, and personal computer products for leading computer, networking, and telecommunications companies worldwide. Cypress stock is traded on the New York Stock Exchange under the symbol CY. Corporate headquarters are located in San Jose, California. The company has a site on the worldwide web at <http://www.cypress.com>, and offers Fax-on-Demand service at (800) 213-5120 or (408) 943-2798 outside of North America.