## SECTION 1

INTRODUCTION

## 1-1. GENERAL

This book contains information for installing the SPERRY UNIVAC Universal Terminal System 400 (UTS 400). The system consists of three major components: a Master Terminal Type 3544, Slave Terminal Type 3548, and a Controller Type 8594. Installation information for each of the system components is contained in the following sections.
Section 2 - Installation
Section 3 - Strapping
Section 4 - De-Installation
Appendix A - Feature Installation
Appendix B - Cable Data
Appendix C - ASCII Conversion Chart
Appendix D - Memory Location
Appendix E - Foldout Illustrations

## 1-2. REFERGNCE MATERIAL

The following books should be available as aids for installing the terminal system:

| Book | Title |
| :---: | :---: |
| MR6085 | SPERRX UNIVAC Universal Terminal System 400 Preinstallation Planning Specifications |
| M 46087 | SPERRY UNIVAC Universal Terminal System Functional Analysis and Servicing |
| MRS088 | SPERRY UNIVAC Universal Terminal System 400 Master Terminal Type 3544 Illustrated Parts Breakdown |
| MR6089 | SPERRY UNIVAC Universal Terminal System 400 Slave Terminal Type 3548 Illustrated Parts Breakdown |
| MR6090 | SPERRY UNIVAC Universal Terminal System 400 Controller Terminal Type 8594 Illustrated Parts Breakdown |

## 1-3. EQUIPMENT DESCRIPTION

The SPERRY UNIVAC Universal Terminal System 400 (UTS 400) is a microprocessor-based, intelligent, general purpose display terminal for use in interactive data commnications with a central processor. The basic areas of application are:

Data entry and accessing
Control and monitoring operations
Offline operations
The UTS 400 consists of a master terminal, slave terminal, and a controller (figure 1-1). The system may be configured as a single master terminal, a master with slaves (up to 3), or as a controller with up to 6 slaves. The system is designed to support existing 7-bit interface devices such as the SPERRY UNIVAC Tape Cassette System (TCS), and 8-bit peripheral devices such as a freestanding diskette subsystem. The system is also compatible with existing UNISCOPE display terminal software.


SLAVE TERMINAL WITH KEYBOARD


CONTROLLER

Figure 1-1. Universal Terminal System 400 - System Components

## 1-4. SPECIAL TOOLS AND SUPPLIES

Common hand tools and the following items are required to install and service the terminal system.

| Equipment | Part Number |
| :--- | :--- |
| Volt-ohmmeter, triplett | $3001444-00$ |
| No. 310 or equivalent* |  |
| Test leads for triplett No. $310^{*}$ | 920508 |
| Printed circuit board (module) extractor* | $2820407-00$ |
| Module extender | $2818455-00$ |
| Extension cable (50 conductor) | $2820659-01$ |
| LED test module | $2812637-01$ |
| $\quad$ Mask, CPU | $2812738-021$ |
| Mask, ROM | $2812738-022$ |
| Mask, I/0 | $2812738-023$ |
| Mask, auxiliary | $2812738-024$ |
| Alignment tool (Hex.) | $2050181-00$ |
| Alignment tool (double ended hex.) | $2050182-00$ |
| Crimping tool | $2050282-00$ |
| Auxiliary interface turnaround | $2820654-00$ |
| I/O test connector | $2818449-00$ |
| Internal auxiliary interface cable | $2816523-00$ |
| Internal communications I/O cable | $2816522-00$ |

## 1-5. PLACEMGMT CONSIDERATIONS

The master and slave terminals are designed to be placed on any flat surface (such as a desk or table top) which affords the operator comfortable access to the console and keyboard and provides good visibility of the display screen. Cable length between the master and slave terminals must also be considered. A minimum of 4 inches ( 10.2 cm ) mast be allowed to the right, left, and rear of the unit for operation.

At lease 2 feet ( 61 cm ) of clearance on all sides of the master and slave terminals must be provided for maintenance; however, if the minimum clearance cannot be provided, leave sufficient cable loop to allow the terminal to be moved to an area with adequate work space.

Since an operator is not required for the controller, the controller may be located in an out-of-the-way place and moved (with cables attached) for maintenance access if sufficient cable loop is provided.

At least 15 inches clearance above the controller must be provided for removal of the top cover. A minimum of $5 \frac{1}{2}$ inches clearance must be provided on all sides of the controller.
*These tools are required for installation.

## 1-6. SIGNAL CABLING

The master and controller are connected to slave terminals, peripheral devices, and comunications devices by means of the cables listed in table 1-1, and illustrated in part in figure 1-2. (Figure 1-2 is a sample system configuration; additional configuration samples are provided in MR6085 SPERRY UNIVAC Universal Terminal System 400 Preinstallation Planning Specifications.), The circled reference numbers in figure 1-2 correspond to the item number listed in table 1-1. Table 1-1 lists all of the standard cables used for interconnecting a master or controller terminal to the system components and communications devices. Table 1-2 provides general information about cable length data, and appendix B provides detailed part number suffixes (dash numbers) relating to the cables and available lengths.


Figure 1-2. Sample System Configuration

Table 1-1. System Signal Cables

| Item | Part Number | Description | $\begin{gathered} \text { Length } \\ \text { (Table 1-2) } \end{gathered}$ |
| :---: | :---: | :---: | :---: |
| 1 | 2805096-Xx* | Unshielded, connects master or controller terminal to multiplexer or Direct Connection Module (DCM). Male or female ends. Maximum length is 300 feet ( 91.4 meters) | D |
| 2 | 2808043-XX | Unshielded, connects master or controller terminal to multiplexer or modem. Maximum length is 50 feet ( 15.2 meters). | B |
| 3 | 2807723-XX | Unshielded, connects master or controller or multiplexer ** to junction box (item 4). One end open and one female end. | D |
| 4 | 2807819-00 | Junction box assembly, used for onsite connections. | - |
| 5 | 2807724-XX | Unshielded, connects junction box to moden, multiplexer or DCM. One end open and one male end. | D |
| 6 | 2807725-XX | Shielded, for long distance runs between junction boxes. For serial or duct installation. Both ends open. | A |
| 7 | 2807765-xx | Shielded, for direct burial long distance runs between junction boxes. (Junction boxes. are not buried.) Both ends open. | A |
| 8 | 2807748-xx | Unshielded, connects multiplexer** directly to a Communication Terminal Module (CTMC). Male and female ends. | D |
| 9 | 2807774-XX | Unshielded, connects junction box to CTMC. One open end and one male end. | D |
| 10 | 2807867-2x | Unshielded, connects multiplexer** directly to Line Terminal Controller (LTC). Male and female ends. | B |
| 11 | 2807868-XX | Unshielded, connects junction box to LTG. One open end and one male end. | B |
| 12 | 2814419-x | Shielded, connects master or controller to Commanications Output Printer, or connects Communications Output Printer to a Tape Cassette System (TCS). Male and female ends. | C |
| 13 | 2814880-XX | Shielded, connects master or controller to TGS, or model 800 terminal printer, or type 0786 printer. Male and female ends. | C |
| 14 | 2816519-xX | Unshielded, connects keyboard to master terminal. | E |

[^0]Table 1-1. System Signal Cables (Cont)

| Item | Part Number | Description | $\begin{aligned} & \text { Length } \\ & \text { (Table 1-2) } \end{aligned}$ |
| :---: | :---: | :---: | :---: |
| 15 | 2820374-50 | Shielded, with female BNC comnectors for connecting slave terminals to master or controller. Maximum length is 2000 feet. | $F$ |
| 16 | 2816515-00 | Shielded, connects display driver to additional slave terminals. | (Internalexpansion) |
| 17 | 2820659-00 | Unshielded, connects display driver to display control; required for additional slave terminals. | (Internalexpansion) |
| 18 | 2816509-00 | Unshielded, connects display driver to DC power supply. | $\begin{aligned} & \text { (Existing } \\ & \text { cable) } \end{aligned}$ |
| 19 | 6432694 | Unsbielded, connects General Communications System (GGS) to multiplexer. | G |
| 20 | 6432697 | Unshielded, connects GCS to item 4. | G |
| 21 | 2871358 | Shielded, with male and female connectors. Intercomects DCMs. | H |
| 22 | 2814880-xX | Shielded, with male lockscrew (3011816-01) on connector P2. Female lockscrew on connector P1 is used for 8 -bit peripheral connections to master or controller. | C |

Table 1-2. System Signal Cables - Available Lengths*

| Designation (Table 1-1) | Available Lengths - Feet |  | Available Lengths - Meters |  |
| :---: | :---: | :---: | :---: | :---: |
|  | Length | Increments** | Length | Increments |
| A | 300 to 500 | 50 | 91.4 to 152.4 | 15.2 |
| B | 5 to 50 | 5 | 1.5 to 15.2 | T. 5 |
| c | 5 and 10 to 200 | 10 | 1.5 and 3 to 61 | 3 |
| D | 3 and 5 to 100 100 to 200 and 200 to 300 | $\begin{aligned} & 5 \\ & 10 \\ & 20 \end{aligned}$ | 9.9 and 1.5 to 30.5 <br> 30.5 to 61 <br> 61 to 91.4 | $\begin{aligned} & 1.5 \\ & 3 \\ & 6.1 \end{aligned}$ |
| E | 4 to 10 | 4, 6, 10 | 1.2 to 3 | 1.2, 1.8, 3 |
| $F$ | $\begin{aligned} & 10,25,50,75, \\ & 100,250,500,750, \\ & 1,000 \text { and } 2000 \end{aligned}$ | as indicated (00 thru 09), respectively | $\begin{aligned} & 3,7: 6,15.2,22.8, \\ & 30.5,76.2,152.4, \\ & 228.6,304.8,610 \end{aligned}$ | as indicated (00 thru 09), respectively |
| G | 16.4 to 98.4 | 16.4 | 5 to 30 | 5 |
| H | 5 to 100 and 100 to 200 and 200 to 300 | $\begin{aligned} & 5 \\ & 10 \\ & 20 \end{aligned}$ | 1.5 to 30.5 <br> 30.5 to 61 <br> 61 to 91.4 | $\begin{aligned} & 1.5 \\ & 3 \\ & 6.1 \end{aligned}$ |

*Except for item G, all cable lengths specified are provided in foot lengths (conversion to metric is shown). Item $G$ is provided in metric lengths (conversion to feet is shown).
**See appendix $B$ for cable part number suffixes (dash numbers) corresponding to all increments.

SECTION 2
INSTALLATION

2-1. INTRODUCTION
This section contains procedures for unpacking, installing and checking the SPERRY UNIVAC Universal Terminal System 400 (UTS 400 ). The procedures are designed to allow a Customer Engineer to install and set-up the terminal system in a minimum amount of time.

## 2-2. EQUIPMENT PLACEMENT

The carrier is responsible for moving the equipment to its approximate location in the prescribed area. The customer should be present while the carrier is unloading the equipment to ensure that it is not handled roughly and that improper lifting devices are not used.

## CAUTION

Do not use a forklift to move equipment containers. The terminal is packed in a polystrene container which should be handled with care. The container can be easily penetrated by forklift prongs, and the terminal severely damaged.

The customer should instruct the carrier as to initial unit placement at the operating location. Correct placement at this time will avoid problems in unpacking later. After the system is loaded and placed in the approximate operating location, inspect the container or containers for signs of damage that may have occurred during shipment.

If damage is found, or a portion of the shipment is missing, this must be noted on the bill of lading. Also any equipment which was handled roughly or dropped during unloading or placement should be so noted on the bill of lading, even though no damage may be apparent. This aids in filing a claim if damage is discovered during unpacking.

## 2-3. UNPACKING

It is recomended that two customer engineers be available for lifting the terminal sysiem during unpacking to avoid possible damage to the units. The procedures for unpacking the master, slave, and the controller are contained in tables 2-1, 2-2, and $2-3$, respectively.

Information about the major electronic components of the terminal system can be found in MR6087, SPERRY UNIVAC Universal Terminal System 400 Functional Analysis and Servicing.

## NOTE

Be sure to save packing material, shipping bags, and the two packing container halves in the event that the system should require shipment to a new location. The number of sets of shipping material is to be determined by consultation with the customer. The procedure for repacking the components is provided in Section 4.

Table 2-1. Master Terminal Unpacking Procedure



Figure 2-1. Master Terminal Module Locations

Table 2-1. Master Terminal Unpacking Procedure (Cont)

| Step | Procedure | Reference |
| :---: | :--- | :---: |
| 10 | If installation is to be completed at this time <br> proceed to table 2-4, if not continue with step <br> 11. | Replace exterior casework and place unit in a <br> suitable place. |

Table 2-2. Slave Terminal Unpacking Procedure

| Step | Procedure | Reference |
| :---: | :---: | :---: |
| 1 | Remove slave terminal container from carton and place on floor. |  |
| 2 | Cut bands holding container sections together and remove container top. |  |
| 3 | Remove bagged communication cable (if present) from top of slave terminal. |  |
| 4 | Remove and unpack slave terminal and keyboard. Check units for visible damage. |  |
| 5 | Remove tape securing power cord and front panel to slave terminal. |  |
| 6 | Remove exterior casework by rotating the two turnlock fastener studs at the rear of the unit. Tilt faceplate forward by loosening screws at top of unit. |  |
| 7 | Ensure that all sockets ere securely in place on the L.E.D. indicator. |  |
| 8 | Remove any internal packing material from module cage A5. |  |
| 9 | Inventory the units and fill in the appropriate sections of the inventory and inspection report shipped with each unit. Notify branch office of any damage or shortage. | Figure 2-2 |
| 10 | If installation is to be completed at this time proceed to table 2-5, if not continue with step 11. |  |
| 11 | Replace exterior casework and place unit in a suitable place. |  |



## NOTE:

Orientation of A1 Character Generator module
is shown for latest build requirements. Earlier units were produced with the module turned
180 degrees (speaker at the rear of the terminal).

Figure 2-2. Slave Terminal - Module Location

Table 2-3. Controller Unpacking Procedure

| Step | Procedure | Reference |
| :---: | :---: | :---: |
| 1 | Remove controller from carton and place on floor. |  |
| 2 | Cut bands holding container sections together and remove container top. |  |
| 3 | Remove bagged commanication cable (if present) from the package. |  |
| 4 | Remove controller top cover as follows: |  |
|  | (a) Loosen two retaining screws on the rear of the cabinet and back the screws out 8 to 10 turns (about 1/4 inch). <br> (b) Tip the cover and front panel toward the front and lift off. |  |
| 5 | With the cover removed, ensure that all L.E.D. indicators are securely in place in their sockets. |  |
| 6 | Remove any internal packing material from module cages A1 and A5. | Figure 2-3 |
|  | NOTE <br> Detailed inspection of printed circuit modules, to include broken or missing ROMs, is accomplished later in the installation procedure. |  |
| 7 | Inventory the units and fill in the appropriate sections of the inventory and inspection report shipped with each unit. (Use the referenced figures to assist in inventory and inspection of the unit.) Notify branch office of any damage or shortage. | Figure 2-3 and Appendix A |
| 8 | If installation is to be completed at this time proceed to table $2-6$, if not continue with step 9. |  |
| 9 | Repiace extericr casework and place unit in a suitable plece. |  |

## 2-4. INSTALLLATION PROCEDURES

The procedures for installing the master terminal are described in table 2-4, the slave terminal in table $2-5$, and the controller in table $2-6$. Table 2-7 provides the final procedures for checking the system and connecting the system to a modem or multiplexer.


Figure 2-3. Controller 'ferminal Module Locations

Table 2-4. Master Terminal Installation

| Step | Procedure | Reference |
| :---: | :---: | :---: |
| 1 | Remove exterior casework by rotating two turnlock fastener studs at rear of unit, then sliding casework forward to clear the faceplate, and lifting off. |  |
| 2 | Roll master terminal on side with power supply chassis supporting the unit. |  |
| 3 | Loosen cable clamp on bottom of unit and route keyboard cable ( $2816519-\mathrm{xX}$ ) thru opening in bottom of master terminal. Leave sufficient cable in unit for a cable loop. Secure cable with cable clamp. | Figure 2-4 |
| 4 | Roll master terminal upright and connect cable connector A5A3P5 to A5A1J5 on character generator module in location A5A1. | Figure 2-5 |
| 5 | If keyboard cable 2816519-XX is not connected to keyboard, connect remaining end of cable to A2J1 on keyboard. | Figure 2-6, <br> Figure 2-7 |



Figure 2-4. Master Terminal - Bottom View


Figure 2-5. Keyboard Cable Connection - Character Generator Module


Figure 2-6. Keyboard Cabling


Figure 2-7. Keyboard Disassembly

UTS 400
Table 2-4. Master Terminal Installation (Cont)


Table 2-4. Master Terminal Installation (Cont)

| Step | Procedure | Reference |
| :---: | :---: | :---: |
| 17 | NOTE <br> The power-on confidence test is initiated by turning power on to the unit. The READY indicator lights when the test is successfully completed; if not successfully completed, the error condition is displayed on the screen. Refer to SPERRX UNIVAC Universal Terminal System 400 Functional Analysis and Servicing; MR6087 for troubleshooting if an error condition is displayed. <br> Set POWER ON/OFF switch to ON position. POWER ON indicator should light and READY indicator should light at successful completion of power-on confindenct test. | Figure 2-10 |



Figure 2-10. Master Terininal Controls and Indicators

Table 2-4. Master Terminal Installation (Cont)



At least one module must be plugged into the backplane to check the $-5 \mathrm{~V},-12 \mathrm{~V}$, and +12 V power supply voltages on A1 backplane.

Figure 2-11. Power Supply Voltage Checkpoints

Table 2-4. Master Terminal Installation (Cont)

| Step | Procedure | Reference |
| :---: | :---: | :---: |
| 21 | If slave terminals are to be installed at this time, proceed to the next step; if not, proceed to step 25. | . |
| 22 | Install feature 2280-00 according to procedures in field instructions for feature installation (FIFI) 2812095. (Feature components are illustrated in appendix A, ROM locations in appendix D, and a wiring diagram in appendix E.) | Appendix $A$ Appendix D Appendix E |
| 23 | If a second slave terminal is to be installed, install features 2278-00 and 2280-00 according to field instructions for feature installation (FIFI) 2812094 and 2812095, respectively. | Appendix A Appendix D Appendix E |
| 24 | If a third slave is to be installed, install feature 2280-00 according to field instructions for feature installation 2812095. Continue with steps 25 and 26 before installing slave terminals (table 2-5). | Appendix A Appendix D Appendix E |
| 25 | If applicable, install 8 K RAM or screen bypass features by inserting universal 8 K RAM module or modules, as required. Refer to section 3 for all strapping information, and to FIFI 2812142 ( 8 K RAM) or FIFI 2812143 (screen bypass). The module is the feature component; ROM locations are shown in appendix $D$, and a wiring diagram in appendix $E$. | Figure 2-1 <br> Appendix D <br> Appendix E |
| 26 | If applicable, install commonications $I / 0$ module (feature 2286-00) according to FIFI 2812096. Install 7-bit auxiliary interface module (F2285-00) according to FIFI 2812093, and 8-bit peripheral interface module ( $\mathrm{F} 2285-00$ ) according to FIFI 2812144. Refer to section 3 for all applicable strapping information, including additional features obtained by strapping. Feature components are shown in appendix $A$, $R O M$ locations in appendix $D$, | Figure 2-1 <br> Appendix A <br> Appendix D <br> Appendix E |
|  | NOTE <br> Connectors J12, J14, J15, or J16 on the rear of the terminal may be wired for 7- or 8-bit peripheral connections. It is recommended that J 12 be wired for 7-bit peripheral connections, and 514 for 8 -bit peripheral connections. |  |
| 27 | If slave terminals are to be connected to the master, refer to table 2-5 for instructions; if no slave terminals are to be installed, refer to table 2-7 for final installation procedures, including comection of 7 - or 8 -bit peripheral devices. | Table 2-5 <br> Table 2-7 |
| 28 | Replace top cover on the master terminal when installation of modules is complete. |  |

Table 2-5. Slave Terminal Installation

| Step | Procedure | Reference |
| :---: | :--- | :--- |
| 1 | Remove exterior casework of a slave terminal by <br> rotating two turnlock fastener studs at rear of <br> unit, then sliding casework to the rear of the unit <br> and lifting off. | Position unit on its side and remove screen on <br> bottom of unit. |
| 4 | Route keyboard cable (2816519-xX, W5) through opening <br> in bottom of unit and connect cable end marked <br> A5A3P5 to A5A1J5 on character generator module A5A1. <br> Leave sufficient cable loop and install screen on <br> bottom of unit. Insure that cable is positioned in <br> slot under cable clamping device. | Figure 2-12 |



Figure 2-12. Slave Terminal - Keyboard Cable Routing

Table 2-5. Slave Terminal Installation (Cont)

| Step | Procedure | Reference |
| :---: | :---: | :---: |
| 5 | If keyboard cable 2816519-XX is not connected <br> to keyboard, connect remaining end of cable <br> to A2J1 on keyboard. <br> CAUTION | Figure 2-13 |
|  | Ensure that slave terminal power cord is <br> not plugged in before setting voltage <br> selection switch on back of monitor power <br> supply. |  |
|  |  |  |

Locate the voltage selection switch on the back of the monitor power supply (see referenced figure). Ensure that voltage selection is correct. If neccessary, use a standard blade screw driver, set the voltage selection switch for the required voltage (115 or 230 rolts AC).

7
Check circuit breaker 1 (CB-1) power cord wiring according to the detail shown in the referenced figure. Rewire circuit breaker as required for voltage selection.

8
Fill out Configuration Descriptions Record shipped with each unit. Follow instructions on form. Keep a record on reverse side of form for all future FCOs installed.

Figure 2-14


Figure 2-13. Slave Terminal - Keyboard Cabling


Figure 2-14. Slave Terminal Voltage Selection

Table 2-5. Slave Terminsl Installation Procedure (Cont)

| Step | Procedure | Reference |
| :---: | :---: | :---: |
| 9 | Check for broken or cracked connectors or terminel boards, and bent or shorted connector pins. |  |
| 10 | Ensure that all push-on terminals are securely attached to their respective switches, indicators and potentiometers, and so forth. | Figure 2-15 <br> (Foldout) |
| 11 | Inspect for metal chips, wire cuttings, solder drippings or other loose particles of foreign material. |  |
| 12 | Ensure that all cables are properly routed and securely plugged into the appropriate connectors. | Figure 2-15 <br> (Foldout) |
| 13 | Set power ON/OFF awitch to OFF position. NOTE |  |
|  | Units are shipped from the factory strapped for 115 VAC 60 Hz with an H . Hubbell 5251, 5252, or equivalent power plug. On units to be used with other power sources, an appropriate primary plug must first be installed. | $\cdots$ |
| 34 | Use ohmeter to ensure that AC power input leads are not shorted. |  |
| 15 | Connect power cord to primary AC power source. |  |
| 16 | Strap unit as described in Section 3; ensure that 50 or 60 Hz strap selection (character generator module) is properly selected. |  |
| 17 | Set power ON/OFT Ewitch to ON position. POWER ON indicator (front panel) should light. |  |

Table 2-5. Slave Terminal. Installation (Cont)

| Step | Procedure |  |  | Reference |
| :---: | :---: | :---: | :---: | :---: |
| 18 | Using volt-ohmmeter, check power supply voltage at following pins on A2P2. Set power ON/OFF switch to ON after meter is connected and to OFF after each voltage check. |  |  | Figure 2-16 |
|  | Term |  |  |  |
|  | Return | Output |  |  |
|  | A2P2-2 | A2P2-1 | -11.4 to -12.6 |  |
|  | A2P2-2 | A2P2-5 | 4 VAC to 12VAC |  |
|  | A2P2-2 | A2P2-8 | +11.4 to +12.6 |  |
|  | A2P2-2 | A2P2-9 |  |  |



FIgure 2-16. Slave Terminal Voltage Checkpoints

Table 2-5. Slave Terminal Installation (Cont)

| Step | Procedure | Reference |
| :---: | :---: | :---: |
| 19 | If voltages are not within tolerance, refer to SPERRY UNIVAC Universal Terminal System 400 Functional Analysis and Servicing, MR6087 for adjustment procedures. | MR6087 |
| 20 | Perform CRT module adjustments as required. Refer to SPERRY UNIVAC Universal Terminal System 400 Functional Analysis and Servicing, MR6087 for edjustment procedures. | MR6087 |
| 21 | Replace the slave terminal casework. |  |
| 22 | Connect terminal input cable $2820374-X X$ between $J 1$ on the slave terminal and either $\mathrm{J} 4, \mathrm{~J} 6$, or J 8 on the master terminal, or J3, J5, J7, J9, J11, or J13 on the controller terminal. | Figures 2-17, $2-18,2-19$ |
| 23 | Connect terminal output cable $2820374-\mathrm{XX}$ between J 2 on the slave terminal and either J5, J7, or J9 on the master terminal, or J4, J6, J8, J10, J12, or J14 on the controller terminal. | $\begin{aligned} & \text { Figures 2-17, } \\ & 2-18,2-19 \end{aligned}$ |
| 24 | Refer to table 2-7 for procedures for connecting peripheral devices and communications connections. |  |



Figure 2-17. Slave Termiral I/O Connections


Figure 2-18. Master Terminal I/O Connections


Figure 2-19. Controller Terminal I/O Connections

Table 2-6. Controller Installation

| Step | Procedure | Reference |
| :---: | :---: | :---: |
| 1 | Complete unpacking procedures according to table 2-3. | Table 2-3 |
| 2 | If not already accomplished, remove controller top cover. (Loosen two retaining screws at rear of cabinet and back the screws out about $1 / 4$ inch. Tip the cover and front panel toward the front and lift off. |  |
| 3 | Check for broken or cracked connectors on terminal boards, and bent or shorted connector pins. Inspect for foreign material. |  |
| 4 | Ensure that all push-on terminals are securely attached to their respective switches, indicators and potentiometers, etc. | $\begin{aligned} & \text { Figure 2-20 } \\ & \text { (Foldout) } \end{aligned}$ |
| 5 | Ensure that all internal cables are properly routed and plugged into the appropriate connectors. <br> CAUTION | Figure 2-20 <br> (Foldout) |
|  | Two cables in the UTS 400 system require keying. They are: | $\begin{aligned} & \text { Figure 2-9 } \\ & \text { (Foldout) } \end{aligned}$ |

Deternine the AC voltage of the operating location. If necessary, remove the cover from power supply module A5A3 (figure 2-3) and strap the power supply as described in section 3.

Replace power supply module cover, if removed.
Check all modules for proper strapping. Strap modules, Section 3 and as necessary, according to the information in section 3. While the modules are removed from the unit, check for broken, loose, or missing ROM elements according to the information in appendix D. Replace modules.

9
Fill out Configuration Description Record shipped with each unit. Follow instructions on form. Keep a record on reversa side of form for all future $\mathrm{FCO}_{s}$ installed.

Table 2-6. Controller Installation (Cont)

| Step | Procedure | Reference |
| :---: | :---: | :---: |
| 10 | Set POWER ON/OFF switch on front panel to OFF position. | Figure 2-21 |
|  | NOTE <br> Units are shipped from the factory strapped for 115 VAC 60 Hz with a Hubbell 5251 , 5252, or equivalent power plug. On units to be used with other power sources, an appropriate primary power plug must first be installed. |  |
| 11 | Use ohmmeter to ensure that $A C$ power input leads are not shorted. |  |
| 12 | Connect power cord to primary AC power source. NOTE |  |
|  | The power-on confidence (POC) test is initiated by turning power on to the unit. The READY indicator lights when the test is successfully completed; if not successfully completed, the error condition is displayed on the screen. Refer to SPERRY UNIVAC Universal Terminal System 400 Functional Analysis and Servicing, MR6087 for a detailed description of POC test. |  |
| 13 | Set POWER ON/OFF switch to ON position. POWER ON indicator should light and READY indicator should light at successful completion of $P O C$ test. | Figure 2-21 |



Figure 2-21. Controller Controls and Indicators

Table 2-6. Controller Installation (Cont)

| Step |  |  | dure | Reference |
| :---: | :---: | :---: | :---: | :---: |
| 14 | Using volt-ohmmeter, ch following locations on OFF switch to ON after OFF after each voltage <br> Terminal E1 is <br> VAC source. <br> Termin <br> Return <br> E6 (GND) <br> E6 <br> E6 <br> E6 | Using volt-ohmmeter, check power supply voltage at following locations on A1 backplane. Set POWER ON/ OFF switch to ON after meter is connected and to OFF after each voltage check. <br> NOTE <br> Terminal E1 is not connected to the 6.3 VAC source. | upply voltage at <br> Set POWER ON/ nnected and to E <br> cted to the 6.3 <br> Voltages $\begin{aligned} & -10.8 \text { to }-13.2 \\ & -4.5 \text { to }-5.5 \\ & +5.0 \text { to }+5.4 \\ & +10.8 \text { to }+13.2 \end{aligned}$ | Figure 2-22 |



45757
NOTE
At least one module must be plugged into the backplane to check the $-5 \mathrm{~V},-12 \mathrm{~V},+5 \mathrm{~V}$, and +12 V power supply voltages on the A1 backplane.

Figure 2-22. Power Supply Voltage Checkpoints - Controller

Table 2-6. Controller Installation (Cont)

| Step | Procedure | Reference |
| :---: | :---: | :---: |
| 15 | If voltages are not within tolerance or one or more of the voltages are missing refer to SPERRY UNIVAC Universal Terminal System 400 Functional Analysis and Servicing, MR6087 for power supply data. <br> NOTE <br> Up to 6 slave terminals may be connected to the master terminal. Unless the controller was originally configured for the maximum number of terminals, it will be necessary to install the required features to accommodate the slave terminals used with the system. <br> Feature F2280-01, Display Control, is required for each slave terminal used with the controller. For every two slave terminals, one feature F2278, Display Driver, is required. <br> Feature F2280-01 consists of a display control module, a 50-pin cable assembly, and a BNC wiring harness, which includes two BNC connectors. Feature F2278 consists of a display driver module. |  |
| 16 | If slave terminals are to be installed at this time, proceed to the next step; if not, proceed to step 20. |  |
| 17 | Install feature 2280-00 according to procedures in field instructions for feature installation (FIFI) 2812095. (Feature components are illustrated in appendix A, ROM locations in appendix $D$, and a wiring diagram in appendix E.) | Appendix A Appendix D Appendix E |
| 18 | If a second slave terminal is to be installed, install features 2278-00 and 2280-00 according to field instructions for feature installation (FIFI) 2812094 and 2812095, respectively. | Appendix A Appendix D Appendix E |
| 19 | If more than 2 slave terminals are to be installed, install features F2280 and F2278 as required. After installation of features, but before connecting and installing slave terminals, complete steps 20 and 21, and proceed to step 22. | Appendix A <br> Appendix D <br> Appendix E |

Table 2-6. Controller Installation (Cont)

| Step | Procedure | Reference |
| :---: | :---: | :---: |
| 20 | If applicable, install 8 K RAM or screen bypass features by inserting universal 8 K RAM module or modules, as required. Refer to section 3 for all strapping information, and to FIFI 2812142 ( 8 K RAM) or FIFI 2812143 (screen bypass). The module is the feature component; ROM locations are shown in appendix $D$, and a wiring diagram in appendix $E$. | Figure 2-1 <br> Appendix D <br> Appendix: $E$ |
| 21 | If applicable, install communications I/O module (feature 2286-00) according to FIFI 2812096. Install 7-bit auxiliary interface module (F2285-00) according to FIFI 2812093, and 8-bit peripheral interface module (F2285-00) according to FIFI 2812144. Refer to section 3 for all applicable strapping information, including additional features obtained by strapping. Feature components are shown in appendix $A$, ROM locations in appendix $D$, and a wiring diagram in appendix $E$. <br> NOTE <br> Connectors J15, J16, J17, or J18 on the rear of the terminal may be used for 7- or 8-bit peripheral connections. It is recomended that J15 be used for 7 -bit peripheral connections, and $\$ 16$ for 8-bit peripheral connections. | Figure 2-1 <br> Appendix $A$ <br> Appendix D <br> Appendix E |
| 22 | If slave terminals are to be connected to the controller, refer to table 2-5 for instructions; if no slave terminals are to be installed, refer to table 2-7 for final installation procedures, including connection of 7 - or 8 -bit peripheral devices. | $\begin{aligned} & \text { Table } 2-5 \\ & \text { Table } 2-7 \end{aligned}$ |
| 23 | Replace top cover on the controller when installation of modules is complete. |  |

Table 2-7. Final Checkout and Installation Procedure


Table 2-7. Final Checkout and Installation Procedure (Cont)

| Step | Procedure | Reference |
| :---: | :---: | :---: |
| 4 | Connect peripheral devices requiring an 8 -bit interface to J 14 on the master terminal or to J 16 on the controller terminal. A typical system equipaent arrangement with cabling connections is shown in figure 2-25. | Figure 2-23, <br> Figure 2-24, Figure 2-25, (foldout) and Appendix B |
|  | NOTE |  |
|  | Eight bit devices employ the same signal cable ( $2814880-\mathrm{XX}$ ) as the 7-bit devices; however, the cable ends are reversed, as shown in the figures. |  |
| 5 | Install terminating resistor 2821616 on the last 8 -bit peripheral in the chain. |  |
| 6 | Connect system signal cable (modem or multiplexer) 2806096-XX or 2808043-XX to I/O connector J1 on the master or controller. <br> NOTE |  |
|  | If a UTS 400 master or controller is to operate with a UNISCOPE 100 or UNISCOPE 200 terminal on the same SPERRY TNIVAC multiplexer, the multiplexer used must contain a revised control board which provides strapping provisions for extension of the timeout period. See the following step. |  |
| 7 | For concurrent operation of the UTS 400 and a UNISCOPE $100 / 200$ terminal only, ensure that the following revision board is installed in the applicable multiplexer: <br> Control Multiplexer: Control board 2807758-12 or higher dash number |  |
|  | (The new printed circuit boards will be factory installed on terminal multiplexers with a serial number of 8000 or above.) |  |
| 8 | Check operation of unit by performing POC test and back-to-back tests of peripheral modules according to instructions contained in MR6087, SPERRY UIIVAC Universal Terminal System 400 Functional Analysis and Servicing (sections 14 and 17). |  |



NOTES:
(1) The 800 Terminal Printer ( 800 TP ) can operate as a 7 -bit or 8 -bit device, depending on the control logic board in the unit. For 7-bit operation, use: Control Logic Board 2818422* or
Control Logic Board 2818351*
For 8-bit operation, use:
Control Logic Board 2818574 only
(2) Cable 2814880 is used for both 7 -bit and 8 -bit peripheral operation. However, for 8-bit peripherals the female connector (P1) on the cable connects to a male connector on the terminal; this is opposite of the standard 7-bit connection.
*Katakana characters not available

Figure 2-23. Master Terminal - System Cabling

(1) The 800 Terminal Printer ( 800 TP ) can operate as a 7 -bit or 8 -bit device, depending on the control logic board in the unit. For 7-bit operation, use: Control Logic Board 2818422* or Control Logic Board 2818351* For 8-bit operation, use:

Control Logic Board 2818574 only
(2) Cable 2814880 is used for both 7 -bit and 8 -bit peripheral operation. However, for 8-bit peripherals the female connector (P1) on the cable connects to a male connector on the terminal; this is opposite of the standard 7-bit connection.
*Katakana characters not available

Figure 2-24. Controller Terminal - System Cabling

## SECTION 3

## STRAPPING

## 3-1. GENERAI

This section contains the strapping information and procedures for the Universal Terminal System 400 (UTS 400). Strapping selections in the UTS 400 are used to select voltages, determine remote identifiers (RIDs), stations identifiers (SIDs) to select device addresses and ROM addresses, to index display control modules, to select DIDs, make programability selections, select screen bypass module, and other functions outlined in this selection.

The strapping information is presented at a printed circuit board (module) level. All functions that require strapping on each module should be strapped (or checked for proper strapping) at the same time; then the strapping should be performed for the next module. The modules and their strappable functions are listed in table 3-1. Figure 3-1 shows the module locations in the UTS 400 Master; figure 3-2 shows the module locations in the UTS 400 Controller.

NOTE
Location of plug-in ROM (firmware) elements on the 10 K ROM/Switch, 8 K RAM, Communications $I / O$, and 7-Bit Auxiliary Interface and 8-Bit Peripheral Interface modules should be verified during the strapping procedure by reference to appendix D in this book. The CPU module requires no strapping selections, but does contain ROM elements which should also be verified by reference to Appendix D.

Table 3-1. Strapping

| Function | Module Part No. $/$ <br> Ref. Designator | Paragraph | Figure |
| :---: | :---: | :---: | :---: |
| Power Supply | 2036465 (A5A3) | 3-4 | 3-3 |
| 1OK ROM/Switch Module ROM Address/Device Address Peripheral Device Selection System Mode Selection Memory Partitioning Screen Bypass Control Page and and Screen Format | 2818437-05 (A1A7) | $\begin{aligned} & 3-5 \\ & 3-6 \\ & 3-7 \\ & 3-8 \\ & 3-9 \\ & 3-10 \end{aligned}$ | $\begin{aligned} & 3-4 \\ & 3-4 \\ & 3-5 \\ & 3-5 \\ & 3-6 \\ & 3-6 \end{aligned}$ |
| Character Generator Module | 2818481 (A5A1) | 3-11 | 3-7 |
| Display Control Module | $\begin{aligned} & 2818442 \text { (A1A3) } \\ & 2818483 \text { (A1A3) } \end{aligned}$ | 3-12 | 3-8 |
| 8 K ram Module | 2818418 (A1A8) | 3-13 | 3-9 |
| Screen Bypass Module | 2818418 (A1A8) | 3-14 | 3-10 |
| Communications I/O Module <br> Device Address <br> Multiplexer/Modem Selection <br> ROM Enable <br> ROM Address <br> RID and SID | 2818414 (A1A4) | $\begin{aligned} & 3-15 \\ & 3-16 \\ & 3-17 \\ & 3-18 \\ & 3-19 \\ & 3-20 \end{aligned}$ | $\begin{aligned} & 3-11 \\ & 3-11 \\ & 3-11 \\ & 3-11 \\ & 3-11, \\ & 3-12,3-13, \\ & 3-14, \end{aligned}$ |
| 7-Bit Auxiliary Interface Module <br> Device Adaress <br> ROM Enable <br> ROM Address | 2818398 (A1A5) | $\begin{aligned} & 3-21 \\ & 3-22 \\ & 3-23 \\ & 3-24 \end{aligned}$ | $\begin{aligned} & 3-15 \\ & 3-15 \\ & 3-15 \\ & 3-15 \end{aligned}$ |
| 8-Bit Peripheral Interface | 2818443 (A1A6) | 3-25 | 3-16 |
| Keyboard Asseinbly | $\begin{aligned} & 2892287 \text { or } \\ & 2892288 \end{aligned}$ | 3-26 | $3-17,3-18$ |

## 10- POSITION MODULE ASSEMELY (AI)



> EXPANSION MODULE ASSEMBLY (A5)

|  |
| :---: |
| (A1) CHARACTER GENERATOR |
| (A2) COPTIONAL DISPLAY DRIVER) |
| (A2) DISPLAY DRIVER |
| (A3) POWER SUPPLY |



NOTES:

1. THE AT MODULE POSITION SHOWN IS THE RECOMMENDED FACTORY LOCATION. MODULE POSITIONS ON THE COMMON BUS BACKPLANE ARE VARIABLE.
2. ADDITIONAL A1 MODULES, IN VARYING COMBINATIONS, MAY BE LOCATED IN THE REMAINING MODULE SLOTS. OPTIONAL MODULES ARE:

8K RAM (1 TO 3 MODULES)
SCREEN BYPASS *
7-BIT AUXILIARY INTERFACE
8-BIT PERIPHERAL INTERFACE
DISPLAY CONTROL (1 TO 3 MODULES IN ADDITION TO THE STANDARD DISPLAY CONTROL MODULE)
3. SEE FIGURE 2-1 FOR MODULE ASSEMBLY (A1 AND A5) LOCATIONS

Figure 3-1. Master Terminal Module Locations


Figure 3-2. Controller Terminal Module Locations

## 3-2. STRAPPING DEVICES

Strapping connectors, links, and plugs ("suitcase" straps) used in the UTS 400 have the following part numbers:

14 contact, dual in-line connector - 2899289-00


46979

16 contact, dual in-line connector - 2899373-00


46980

Link, $\frac{1}{4}$-inch length size 24 or 23 - 3007807-00
Plug ("suitease" strap) 2892325-06 (Black) -07 (Red)

## 3-3. STRAPPING PROCEDURES

Strapping is accomplished by inserting links or plugs in the printed circuit boards, or by lifting pins on the dual in-line strapping connectors, or setting a switch according to the information given in the referenced tables and figures. Note that strapping connectors resemble integrated circuit packs; however, each pin on one side of a strapping connector feeds through the body of the connector and is electrically comm with the corresponding pin on the other side of the connector. Thus, the pins on either side of the connector may be lifted for strapping. However, lifting too many pins on one side of the connector may cause the remaining pins on that side to become disengaged from the socket. Lifting more then four pins on one side of a strapping connector is not recommended.

## 3-4. POWER SUPPLY STRAPPING

Power supply 2036465 contains a small printed circuit board (A2) which is used to select power line voltage. The voltage selection accommodates the power supply module and the CRT module for nominal line voltages of 115 or 230 VAC in the frequency range of 48 to 62 Hz . Select the voltage according to the instructions in table 3-2 and figure 3-3.

Table 3-2. Power Supply 2036465 Strapping
(Figure 3-3)

| Voltage Selection | Procedure |
| :--- | :--- |
| 85 to 125 VAC | Plug A2 into J3 with the part number side of A2 toward the <br> bottom edge of the power supply module and with the end of |
|  | A2 marked "85-128 vOLTS AT 48-62 Hz" nearest to connector |
| J3. |  |
| 185 to 255 VAC | Plug A2 into J3 with the part number side of A2 toward the <br> bottom edge of the power suply module and with the end of <br> A2 marked "185-255 VOLTS AT 48-62 Hz" nearest to connector <br> J3. |



Figure 3-3. Power Supply 2036465 (A1A3) Strapping

## 3-5. 10K ROM/SWITCH MODULE STRAPPING

The 10 ROM/switch module 2818437 (A1A7) contains 17 switch blocks for strapping the following functions:

- R.OM address range
- Device address
- Peripheral device selection and DIDs
- System modes of operation
- Memory partitioning (programmability function)
- Screen bypass control page function
- Screen format

Switch blocks are designated SO thru S14 and 62Q5 and 62C5. Switch block S14 is not used.

## 3-6. ROM ADDRESS AND DEVICE ADDRESS

ROM address strapping is accomplished by specified switch settings on switch blocks 6295 and 6205. The device address is determined from switches on switch block 62Q5, as shown in table 3-3.

The address range of ROMS 0 through 7 (figure 3-4) is strapped using switch block 6205 ; the address range of ROMS 8 and 9 is strapped using switch block 6205. Within the address range ( 10 K octal or 1 K hex) of ROMS 8 and 9 , an offset or starting address is also selectable in 2 K octal ( 400 hex ) increments. Starting addresses for the UTS 400 are show in table 3-4.

Select the device address and ROM address ranges by setting the switch blocks as specified in tables 3-3 and 3-4, following.

Table 3-3. 10K ROM/Switch Module - ROM Address Strapping (ROMS 0 thru 7) and Device Address Strapping (Figure 3-4)

| Address |  | Function | Switch Block 6295 |  |
| :---: | :---: | :---: | :---: | :---: |
| Octal | Hex |  | Switch | Position* |
| 75 | 30 | Device Address | 1 | On |
|  |  | Device Address | 2 | Off |
|  |  | - | 3-4 | Not Used |
| $\begin{gathered} 40,000 \\ \text { to } \end{gathered}$ | $\begin{aligned} & 4000 \text { to } \\ & 5 \mathrm{FFF} \end{aligned}$ | ROM Address | 5 |  |
| 57,777 |  | ROM Address | 6 | Off |
|  |  | ROM Address | 7 | On |
|  |  | ROM Address | 8 | Off |

*On = logical 1

Table 3-4. 10 K ROM/Switch Module - ROM Address Strapping ROMS 8 (39L1) and 9 (39G9) (Figure 3-4)

| Starting Address |  | Function | Switch Block 62C5 |  |
| :---: | :---: | :---: | :---: | :---: |
| Hex | Octal |  | Switch | Position* |
| 7400 | 72000 | ROM 39G9 OFFSET | 1 | ON |
|  |  | ROM 3969 OFFSET | 2 | OFF |
| 7800 | 74000 | ROM 39L1 OFFSET | 3 | OFF |
|  |  | ROM 39L1 OFFSET | 4 | ON |
| Address Range |  | ROM ADDRESS | 5 | ON |
| From | From |  |  |  |
| 4000 | 40,000 | ROM ADDRESS | 6 | ON |
| to | to | ROM ADDRESS | 7 | ON |
| 5FFF | 57,777 | ROM ADDRESS | 8 | OFF |
|  |  |  |  |  |

$$
\text { *On = Logieal } 1
$$



Figure 3-4. 10K ROM/Switch Module 2818437 (A1A7) Strapping

## 3-7. PERIPHERAL DEVICE SELECTION

Switch blocks S0 through S11 define peripheral device types and peripheral DIDs. Each switch block (SO through S11) eatablishes the operating parameters for one peripheral device. The first 4 switches on each switch block select the low order bits of the peripherial device DID; the remaining switches define the device.

The switch blocks can be strapped in variable sequences with the lowest number switch block selecting the first peripheral of that type. If switch block SO is strapped for a printer, the printer is automatically designated P1 on the control page. The next switch block strapped for a printer will automatically be designated P2. If switch block S1 is strapped for a tape cassette unit, the cassette unit is automatically designated C1 on the control page. The next switch block strapped for a cassette will automatically be designated C2. All unused switches on the gwitch blocks must be set to the OFF position. The peripheral device switch blocks SO through S11 are strapped as shown in table 3-5.

Table 3-5. 10K ROM/Switch Module - Peripheral. Device Strapping (Figure 3-5)

\begin{tabular}{|c|c|c|c|c|}
\hline \multirow[t]{2}{*}{Switch Block} \& Hex \& Octal \& \multirow[t]{2}{*}{Switch Number} \& \multirow[t]{2}{*}{\begin{tabular}{l}
Function \\
(If \(0 n\) )
\end{tabular}} \\
\hline \& \multicolumn{2}{|l|}{Address} \& \& \\
\hline so through S11 \& 5FFO \& \[
\begin{aligned}
\& 57760 \\
\& \text { thru } \\
\& 57773
\end{aligned}
\] \& 2
3
4
5

4
6

7 \& | Switches 1 thru 4 select the least significant bits of the DID and must match the actual device. If switch 7 is pressed $0 N$ (read/write device selection), the DID selected by switches 1 thru 4 is the write DID for the device. A read DID one number greater than the write DID is selected by system firmware functions. |
| :--- |
| Off: selected DID defines first drive of diskette. |
| On: selected DID defines second drive of diskette. |
| On: device is retryable (TCS and diskette. |
| Off: device is not retrjable (printers) |
| On: read/write device (TCS and diskette) |
| Off: write only device (printers) |
| On: device on 8-bit interface |
| Off: device on 7-bit interface | <br>

\hline
\end{tabular}

## 3-8. SYSTEM MODE SELECTION

Switch block S12 provides strapping provision for selecting the varicus system modes of operation and is strapped as shown in table $3-6$.

| Switch Block | Address |  | Switch: Number | $\begin{aligned} & \text { Function* } \\ & (\text { If On) } \end{aligned}$ |
| :---: | :---: | :---: | :---: | :---: |
|  | Hex | Octal |  |  |
| 512 | 5FFC | 57774 | 1 | Cycle power-on confidence (POC) test. <br> Run Comm Back-to-Back test in POC. <br> Run Peripheral Back-to-Back test in POC. <br> Time selection for Auto Answer/Hangup feature. <br> Enable DLE 6 response on initialize. <br> Enable Auto Answer/Hangup feature. <br> Enable retry of peripheral operations initiated online. |
|  |  |  | 2 |  |
|  |  |  | 3 |  |
|  |  |  | 4-5 |  |
|  |  |  |  |  |
|  |  |  | 6 |  |
|  |  |  | 7 |  |
|  |  |  | 8 |  |



Figure 3-5. 10K ROM/Switch Module 2818437 (A1A7) Strapping

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## 3-9. MEMORY PARTITIONING

The system RAM is shared by both user programs (P) and peripheral output buffering (B) and may be expanded to 24 K in 8 K increments. The RAM can be partitioned into 4 K units allowing a maximum of 6 units to be allocated to either user programs or peripheral output buffering. The RAM partitioning is defined by switch block S13 on the 10 K ROM switch module as shown in table 3-7.

Table 3-7. 10 K ROM/Switch Module - Memory
Partitioning (Figure 3-6)

| Switch Block | Address |  | $\begin{aligned} & \text { Switch } \\ & 321 \end{aligned}$ | $\begin{aligned} & \text { Increments } \\ & 123456 \end{aligned}$ | User RAM <br> $(P)=$ |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | Hex | Octal |  |  |  |
| S13 | 5 FFD | 57775 | 000 | B $\mathrm{BBBB}^{\text {B }}$ | OK |
|  |  |  | $0 \quad 01$ | PBBBBB | 4K |
|  |  |  | 010 | PPBBB | 8 K |
|  |  |  | $\begin{array}{lll}0 & 1\end{array}$ | P P PBBE | 12 K |
|  |  |  | 100 | P P P PB | 16K |
|  |  |  | $1 \begin{array}{lll}1 & 0 & 1\end{array}$ | PPPPPB | 20\% |
|  |  |  | 110 | P P P P P | 24 K |
|  |  |  | $1 \quad 1$ | B $\mathrm{BBBB}^{\text {B }}$ | OK |

3-10. SCREEN bYPASS CONTROL PAGE AND SCREEN FORMAT
Switch block S13 also contains the switch (6) which allows the master station (or primary slave) to enter the control page in the screen bypass memory. (See note.)

Switch block $S 13$ also contains strapping for the UTS 400 screen format. Refer to table 3-8 and select switches 6, 7, and 8 as required. Set switches 4 and 5 to OFF.

## NOTE

The screen bypass memory should be indexed (paragraph 3-26) as the last refresh memory in the system. Switch 6 actually enables the master station or primary slave operator to set the control page in the last indexed memory, which is normally screen bypass. Do not set switch 6 on if the screen bypass feature is not present.

Table 3-8. 10 K ROM/Switch Module - Screen Bypass Control Page and Screen Format. Strapping (Figure 3-6)

| Switch Block S13 |  | Function |
| :---: | :---: | :---: |
| Switch | Position* |  |
| 4, 5 | OFF | Not Used |
| 6 | ON | Enable control page function |
| 6 | OFF | Control page (offline function) disabled |
| 7, 8 | $\begin{array}{ll}7 & 8 \\ \text { OFF } & \text { OFF }\end{array}$ | Screen Format as follows: $24 \times 80$ |
|  | ON OFF | $24 \times 64$ |
|  | OFF ON | $12 \times 80$ |
|  | $\mathrm{ON} \quad \mathrm{ON}$ | $16 \times 64$ |

$$
\text { *ON = logical } 1
$$



Figure 3-6. 10 K ROM/Switch Module 2818437 (A1A7) Strapping

## 3-11. CHARACTER GENERATOR MODULE STRAPPING

The character generator module 2818481 (A5A1) is strapped for:

- AC input
- 64 or 96 characters selection
- Katakana selection

Strap the module as shown in table 3-8.

Table 3-9. Character Generator Module Strapping (Figure 3-7)

| Function | Strap Location <br> (Strap In) |
| :--- | :--- |
| 96 Character Selection | E3-E4 |
| 64 Character Selection | E3-E5 |
| 50 Hz Selection | E1 - E2 and E7 - E8 <br> $(E 2-E 9$ is not a functional location - for <br> strap storage only) |
| 60 Hz Selection | E6-E7 |
| Katakana Selection | E10-E12 |
| All other language | E10-E11 |



Figure 3-7. Character Generator Module 2818481 (A5AT) Strapping

3-12. DISPLAY CONTROL MODULE STRAPPING
The Display Control Module 2818442 or 2818483 (A1A3) contains a switch block for strapping Display Control module identification. Strapping provisions are identical for both part numbered modules.

The Display Control module provides screen refresh for the display CRT. In a basic system configuration, refresh capability is provided for the master terminal only. If slave terminals are part of the system configuration, an additional Display Control module must be installed in the master for each slave terminal in the system. The switches on switch block 0685 provide the selection identification for each of the Display Control modules. (A write only index register on each module reflects the state of the lower six data bits according to the switch settings.) Only one switch should be closed on each module and the switches must be in sequential order from module to module. Switch 6 must be closed for the first Display Control module, switch 5 for the second module, and so on. If a gap is left in the switch sequence, all slave terminals strapped to a higher index number will not be selected. The switch settings on the Display Control modules are made as shown in table 3-10.

Table 3-10. Display Control Module Identification Strapping (Figure 3-8)

| Index | Switch Block 06Q5 | Description |
| :---: | :---: | :---: |
|  | Switch ON |  |
| 0 | 06Q5-6 | 1 - Master |
| 1 | 06Q5-5 | 2 - First slave |
| 2 | 0685-4 | 3 - Second slave |
| 3 | 06Q5-3 | 4 - Third slave |



Figure 3-8. Display Control Module 2818442 or 2818483 (A1A3) Strapping

## 3-13. 8K RAM MODIIE STRAPPING

By means of strapping selections, the universal 8 K RAM module 2818418 (A1A8) can be configured to function as a general purpose RAM, or as the screen bypass module for the UTS 400. This paragraph defines the strapping for the general purpose RAM, designated as the $8 K$ RAM module. (Refer to paragraph 3-26 for screen bypass module strapping.)

Both the master terminal (type 3544) and the controller (type 8594) may accommodate up to three universal 8 K RAM modules used as general purpose RAM ( 8 K RAM modules). In the master unit, the recommended location for the modules is in slots 3 through 8 (figure 3-1); in the controller, the modules should be placed in slots 7 through 12 or 15 through 17. (See figure 3-2).

Strap the 8K RAM modules according to the information in tables 3-11 and 3-12. The modules are strapped according to the number of 8 K RAM modules used in the system, as indicated by the tables. Strap the first module according to the column labeled "1st module" in each table. If there are two modules, strap one module according to the colum labeled "1st module," and the second module according to the column labeled "2nd module." If all three modules are used, strap each module according to the information in the tables.

NOTE

Switch 5 on each module must be in the OFF position for the 8 K RAM selection. The ON position is for screen bypass selection.


Figure 3-7. 8K RAM Module 2818418 (A1A8) Strapping

Table 3-11. 8K RAM Module Strapping - Switch Block 62G5 (Figure 3-9)

| Function | Block 62G5 | Switch Position |  |  |
| :---: | :---: | :---: | :---: | :---: |
|  | Switch | 1st Module | 2nd Module | 3rd Module |
| Address range bit 15 | 1* | OFF | OFF | OFF |
| Adaress range bit 14 | 2* | ON | OFF | OFF |
| Address range bit 13 | 3* | OFF | ON | OFF |
| Address range (not used for 8K RAM) | 4* | OFF | OFF | OFF |
| Screen bypass enable/8K RAM | 5 | OFF | OFF | OFF |
| 8 K PAM/4K RAM enable | 6 | OFF | OFF | OFF |
| Enable device read (ON) | 7 | ON | OFF | OFF |
| Force selection (ON); Enable device address function to select (OFF) | 8 | ON | ON | ON |

*OFF = $\operatorname{logical~1;~bit~} 0$ is least significant bit.

Table 3-12. 8K RAM Module Strapping - Switch Block 14E5 (Figure 3-9)

| Function | Block 14E5 | Switch Position |  |  |
| :--- | :---: | :---: | :---: | :---: |
|  | Switch | 1st Module | 2nd Module | 3rd Module |
|  | 1 | OFF | OFF | OFF |
|  | 2 | OFF | OFF | OFF |
|  | 3 | OFF | OFF | OFF |
| Maint. Ind. Bit (3rd module) | 6 | OFF | OFF | OFF |
| Maint. Ind. Bit (2nd module) | 7 | OFF | OFF | OFF |
| Maint. Ind. Bit (1st module) | 8 | OFF | OFF | ON |

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## 3-14. SCREEN BYPASS MODULE STRAPPING

By means of strapping selections, the universal 8K RAM module 2818418 (A1A8) can be configured to function as the screen bypass module for the UTS 400. This paragraph defines the strapping required to configure the module for the screen bypass function.

The recomended location for installing the screen bypass module in the master unit (type 3544) is in slots 3 through 8 in the A1 module assembly (figure 3-1); in the controller, the recommended slots are 7 through 12 or 15 through 17 (figure 3-2).

NOTE
To enable the screen bypass control page function, switch 6 on switch block 13 (ROM/switch module) must be set to ON. This function allows the operator at the master station (or primary slave) to change the control page in the screen bypass memory. The switch should not be set to $O N$ if the screen bypass feature is not present.

Set the switches on switch blocks 13, 62G5, and $14 E 5$ according to the information in tables $3-8,3-13$, and $3-14$.


Figure 3-10. Screen Bypass Module 2818418 (A1AB) Strapping

Table 3-13. Screen Bypass Module Strapping (Figure 3-10)

| Function | Switch Block 62G5 |  |
| :---: | :---: | :---: |
|  | Switch | Position |
| Address range bit 15 | 1* | ON |
| Address range tit 14 | 2* | ON |
| Address range bit 13 | 3* | OFF |
| Address range bit 12 | 4* | OFF |
| SK RAM enable/screen bypass | 5 | ON |
| 8 K RAM/4 RAM enable | 6 | ON |
| Enable device read (ON) | 7 | ON |
| Force selection (ON); |  |  |
| Enable device address 1 |  |  |
| function to select (OFF) | 8 | OFF |

* OFF = logical 1; bit 0 is least significant bit

Set the switches on switch block 1LE5 by placing the switches ON or OFF according to the information given in table 3-14.

Table 3-14. Screen Bypass Module Strapping (Figure 3-10)

| Display Control Modules | Switch Block 14E5 |  |
| :---: | :---: | :---: |
|  | Switch | Position |
| 1 Module installed | 7 | ON - all other switches off |
| 2 Modules installed | 6 | ON - all other switches off |
| 3 Modules installed | 5 | ON - all other switches off |
| 4. Modules installed | 4 | ON - all other switches off |
| 5 Modules installed | 3 | ON - all other switches off |
| 6 Modules installed | 2 | ON - all other switches off |

3-15. COMMONTCATIONS I/O MODULE (SYNGHRONOUS) STRAPPING
The Communications I/O Module (Synchronous) 2818414 (A1A4) contains strapping connectors or switches for strapping the following functions:

- Device address
- Multiplexer/Modem selection
- ROM enable (strapping plug)
- ROM address range
- RID and SID selections

The strapping is described in the following paragraphs and illustrated in figure 3-11.

## NOTE

Except for the ROM enable strap, functions specified in the following paragraphs may be strapped using either strapping connectors or switches, depending on the module dash number. Communications I/O Modules 2818414-00 through -03 contains strapping connectors; modules with dash numbers 04 and higher contain switch blocks.

## 3-16. DEVICE ADDRESS

Strapping for the Communications $I / O$ module address is accomplished by either lifting pins on a strapping connector or setting switches on the specified switch block at module coordinates 14Q. Both methods are shown in table 3-10.

As indicated by the following tabular information, only the initial device address in the address range is deteremined by strapping. Selection of the device addresses within the established address range is accomplished by the system firmware. Note that the modules containing strapping connectors are designed to provide the device address with all of the applicable connector pins lifted (open); however, the modules containing switches require a specified switch setting.

Table 3-15. Communications I/O Device Address Strapping (Figure 3-8)

| Device <br> Address | Description | Block 14Q |  | Connector Pins ( 14 Q ) |
| :---: | :---: | :---: | :---: | :---: |
|  |  | Switch | Position |  |
| 10-15 | ADR3, (Bit 3) | 14Q-3 | Off | 3 or 12 lifted |
|  | ADR4, (Bit 4) | 14Q-4 | On | 4 or 11 lifted |
|  | ADR5, (Bit 5) | 14Q-5 | Off | 5 or 10 lifted |

## 3-17. MULTIPLEXER/MODEM SELECTION

The multiplexer/moder selection is strapped as shown in table 3-11. The strapping cornector or switch block is located at coordinates 14 Q .

Table 3-16. Comunications I/O Multiplexer/Modem Strapping (Figure 3-8)

| Description | Block 140 |  | Connector Pins (14Q) |
| :---: | :---: | :---: | :---: |
|  | Swi.tch | Position |  |
| Multiplexer | $\begin{aligned} & 14 Q-6 \\ & 140-7 \end{aligned}$ | $\begin{aligned} & \mathrm{On} \\ & \mathrm{Off} \end{aligned}$ | $\begin{aligned} & 6 \text { and } 9 \text { in } \\ & 7 \text { or } 8 \text { lifted } \end{aligned}$ |
| Modem | $\begin{aligned} & 14 Q-6 \\ & 14 Q-7 \end{aligned}$ | $\begin{aligned} & \text { Cff } \\ & \text { On } \end{aligned}$ | $\begin{aligned} & 6 \text { or } 9 \text { lifted } \\ & 7 \text { and } 8 \text { in } \end{aligned}$ |

3-18. ROM ENABLE
Connect the ROM enable strap between E1 and E2 at module coordinates 40 E (figure 3-1).

## 3-19. ROM ADDRESS

The Communications I/O module ROM address strapping selects the ROM starting address. Selection of the addresses in the address range is accomplished by firmware functions. This means that all ROM elements must be inserted sequentially on the module. For example, if only two ROMs are used, they must be installed in positions 1 and 2. Refer to table 3-17 for strapping information.

Table 3-17. Communications I/O ROM Address Strapping (Figure 3-11)

| Address Range | Description | Switch Block 62E |  | Connector Pins |
| :---: | :---: | :---: | :---: | :---: |
|  |  | Switch | Position |  |
| $\begin{aligned} & 1000 \text { to } \\ & 1 \mathrm{FFF} \end{aligned}$ | ADR12 (Bit 12) | 62E-7 | On | 7 or 8 lifted |
|  | ADR13 (Bit 13) | 62E-6 | Off | 6 or 9 lifted |
|  | ADR14 (Bit 14). | 62E-5 | Off | 5 or 10 lifted |
|  | ADR15 (Rit 15) | 62E-4 | Off | 4 or 11 lifted |



Figure 3-11. Communications I/O Module (Synchronous) 2818414 (A1A4)

## 3-20. RID AND SID

The RID and SID functions are strapped in an identical manner, either through switches or strapping connectors. Module locations and coordinate designations for swtich blocks or connectors are shown in figure 3-12.

For the module containing strapping connectors (2818414-00, to -03 ), the lifted pins are interpreted as binary ones; pins 7 or 8 of the connector correspond to bit 0 (least significant bit) and pins 1 or 14 correspond to bit 6 (most significant bit).

For modules containing switch blocks (2818414-04 and up), the off position of the switches is interpreted as a binary one. Since the switch blocks contain eight usable switches, it is possible to strap the device for a Remote identifier (RID) between 00 and 7F ( 000 to 177 octal), and to strap the device between 00 and FF ( 000 to 377 octal) for a station identifier (SID). Switch 8 corresponds to bit 0 (least significant bit) and switch 1 corresponds to bit 7 (most significant bit). The address may be decoded by reading the binary bit positions on the switch or strapping connector. (Refer to Appendix C, ASCII Conversion Chart, for RID and SID codes).

Sample RID and SID strapping configurations are shown for swtich blocks 06N (RID) and 22L (SID) in figure 3-13.

## NOTE

The RID selection for communications modules using switches for strapping must be prefixed by a bit 7 (switch 1) selection. Setting switch 1 ON indicates that the module is synchronous; setting switch 1 OFF indicates that the module is asynchronous. The switch must be set for each comrunications module used in the system.

Ensure that switch 1 (bit 7) on RID switch block 06 N is set to ON for the synchronous communications module.


Figure 3-12. Communications I/O Module (Synchronous) 2818414 (A1A4) Strapping

SAMPLE RID $=3 \mathrm{~B}_{16}=00111011$


NOTE:
VALID RIDS:
O40-1178
$20-4_{16}$

Figure 3-13. Communications I/O Module - Sample RID

SAMPLE SID $=5 E=01011110$


NOTE:
VALIO SIDS:
120-1578
$50-6 F_{16}$

Figure 3-14. Communications I/O Module - Sample SID

## 3-21. 7-BIT AUXILIARY INTERFACE MODULE STRAPPING

The 7-bit auxiliary interface module 2818398 (A1A5) contains strapping connectors or switches for strapping:

- Device address
- ROM enable
- Peripheral ROM address


## 3-22. DEVICE ADDRESS

Strapping for the device address is accomplished by lifting pins on a connector, or setting switches on the specified switch block.

As indicated by the following tabular information (table 3-13), the initial device address in the range is determined by strapping; firmware functions select device addresses. The strapping device is located at coordinates 46J.

Table 3-18. 7-Bit Peripheral Interface Device Address Strapping (Figure 3-15)

| Device Address | Description | Switch Block 46 J |  | Connector Pins |
| :---: | :---: | :---: | :---: | :---: |
|  |  | Switch | Position* |  |
| 20-21 | ADR1 (Bit 1) | 1 | Off | 1, 14 lifted |
|  | ADR2 (Bit 2) | 2 | Off | 2, 13 lifted |
|  | ADR3 (Bit 3) | 3 | Off | 3, 12 lifted |
|  | ADR4 (Bi.t 4) | 4 | Off | 4, 11 Iifted |
|  | ADRS (Bit 5) | 5 | On | 5, 10 lifted |

$$
* O_{n}=\text { Logical } 1
$$

## 3-23. ROM ENABLE

Connect the ROM enable strap at coordinate 42 E (from terminals E1 to E2) to erable ROM operation.

## 3-24. ROM ADDRESS

The 7 -bit peripheral interface module address strapping selects the ROM starting address. Selection of the addresses in the addresses range is accomplished by firmare functions. Strap the ROM address as shown in table 3-14.

Table 3-19. 7-Bit Peripheral Interface ROM Address Strapping (Figare 3-15)

| Address Range | Description | Switch Block 62S |  | Connector Pins |
| :---: | :---: | :---: | :---: | :---: |
|  |  | Switch | Position* |  |
| $\begin{aligned} & 2000 \text { to } \\ & 2 \mathrm{FFFF} \end{aligned}$ | ADR12 (Bit 12) | 7 | Off | 7, 8 lifted |
|  | ADR13 (Bit 13) | 6 | On | 6, 9 lifted |
|  | ADR14 (Bit 14) | 5 | Off | 5, 10 lifted |
|  | ADR15 (Bit 15) | 4 | Off | 4, 11 lifted |

*ON = logical 1


Figure 3-15. 7-Bit Peripheral Interface Module 2818398 (A1A5) Strapping

## 3-25. 8-BIT PERTPHERAL INTERFACE STRAPPING

The eight-bit peripheral interface module 2818443 (A1A6) contains a ROM enable strap ( $E 1$ to E2) and a memory address selection strap ( $E 3$ to E4). Insert the ROM enable strap between E1 and E2. For the UTS 400, the memory selection strap is removed to select a memory address range and device (port) addresses as show in table 3-20.

Table 3-20. 8-Bit Peripheral Address Strapping (Figure 3-16)
E3-E4 Strap Removed

| Function | Hex. | Octal |
| :--- | :--- | :---: |
| Memory or Register Name | Memory Address Selection |  |
| LK ROM Firmware for | 6000 to | 60,000 to |
| 8-Bit Peripheral IF Module | 6 FFF | 67,777 |
| 1K RAM Read/Write Buffer | 7000 to | 70,000 |
| and DMA area | 73 FF | 71,777 |


|  | Port Address Selection |  |
| :--- | :---: | :---: |
| Status Register (Read) | 28 | 50 |
| Control Register 1 (Write) | 28 | 50 |
| Control Register 2 (Read) | 29 | 51 |
| Command Register (Write) | 29 | 51 |
| Address Pointer Register - <br> Lower Bits (Read/Write) | 2 A | 52 |
| Address Pointer Register - <br> Upper Bits (Read/Write) | $2 B$ | 53 |



Figure 3-16. 8-Bit Peripheral Interface 20 万人18443 (A1A6) Strapping

## 3-26. KEYBOARD STRAPPING

The UTS 400 keyboard assembly (2892287 or 2892288) contains strapping for the following functions:

64 or 96 character selection
Spare bar option

- Nationality (language) options (except Katakana)

NOTE
The Katakana keyboard is a separate option (selection). Part numbers are 2892411 (or 2892412). No strapping options are required, except the spare bar selection as noted in table 3-22.

The two keyboard assemblies, which are functionally identical, are manufactured by different vendors. Strapping fumctions may, therefore, contain different circuit board locations, but all nomenclature or reference designators used for strapping are printed identically on each assembly (see figure 3-17 and 3-18).

The following strapping information (tables 3-21 and 3-22) includes required strapping for optional language selections. Note that 64 or 96 character selections for language options may differ from the standard selections characterized as "domestic".

Specific language options also require installation of translation ROMs A and $B$ (table 3-23). (Appendix D provides ROM A and B selection part numbers.) In addition, a resistor pull-up pack (RES 1) and an OPTION switch must be in place on the printed circuit board when the translation ROMs are installed.

Table 3-21. Domestic Keyboard Strapping (Figures 3-17 and 3-18)

| Function | Strap |
| :--- | :--- |
| 64 character selection | E1 to E2 in |
| 96 character selection | E2 to E3 in |
| Space bar output | E4 to E5 in |
| 150 (68 hex.) | E4 to E5 in |
| 40 (20 hex.) | E5 to E6 in |
| Nationality strap | E7 to E8 in |
| OPTION switch block | All switches ON |
| (if installed) |  |

Table 3-22. Space Bar Strapping (Figures 3-17 and 3-18)

| Function | Language | Strap |
| ---: | ---: | ---: |
| Space Bar Output |  |  |
| 150 (68 hex.) | All except Katakana | E4 to E5 in |
| 40 (20 hex.) | All except Katakana | E5 to E6 in |
| 150 (68 hex.) | Katakana* | E1 to E2 |
| $40(20$ hex.) | Katakana* | E2 to E3 |

*The space bar output is the only strappable option for a Katakana keyboard which is a separate selection.

Table 3-23. Keyboard Strapping - Nationality Options (Figures 3-17 and 3-18)

| Nationality | Character <br> Selection | Character <br> Strap in | Nationality <br> Strap in | Translation RCM Required* | OPTION <br> Switch | RES 1 <br> Installed |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Domestic | 64 | E1 to E2 | E7 to E8 | No | All ON | No |
| Domestic | 96 | E2 to E3 | E7 to E8 | No | All ON | No. |
| France | 64 | E1 to E2 | E7 to E8 | No | All ON | No |
| France | 96 | E2 to E3 | E7 to E8 | No | AII ON | No |
| U. K. | 64 | E1 to E2 | E7 to E8 | No | All ON | No |
| U. K. | 96 | E2 to E3 | E7 to E8 | No | All ON | No |
| Sweden | 64 | E2 to E3 | E8 to E9 | ROM $A$ and $B$ | All OFF | Yes |
| Sweden | 96 | E2 to E3 | E8 to E9 | ROM $A$ and $B$ | All OFF | Yes |
| Germany | 64 | E2 to E3 | E8 to E9 | ROM $A$ and $B$ | All OFF | Yes |
| Germany | 96 | E2 to E3 | E8 to E9 | ROM $A$ and $B$ | All OFF | Yes |
| Spain | 64 | E2 to E3 | E7 to E8 | ROM $A$ and $B$ | All OFF | Yes |
| Spain | 96 | E2 to E3 | E7 to E8 | ROM $A$ and $B$ | All OFF | Yes |
| Denmark/ <br> Nor"way | 64 | E2 to E3 | E8 to E9 | ROM A and B | All OFF | Yes |
| Denmark/ <br> Norway | 96 | E2 to E3 | E8 to E9 | ROM A and B | All OFF | Yes |

*See appendix D for translation ROM part numbers.


Figure 3-17. Keyboard 2892287 - ROM Locations and Strapping



[^0]:    *Refer to Appendix B table B-4 for instructions for converting cable 2805096-XX for use with modems.
    **This cable is also used to provide direct connection to UNISCOPE Display Terminals. Cannot be used for direct connection to the UTS 400.

