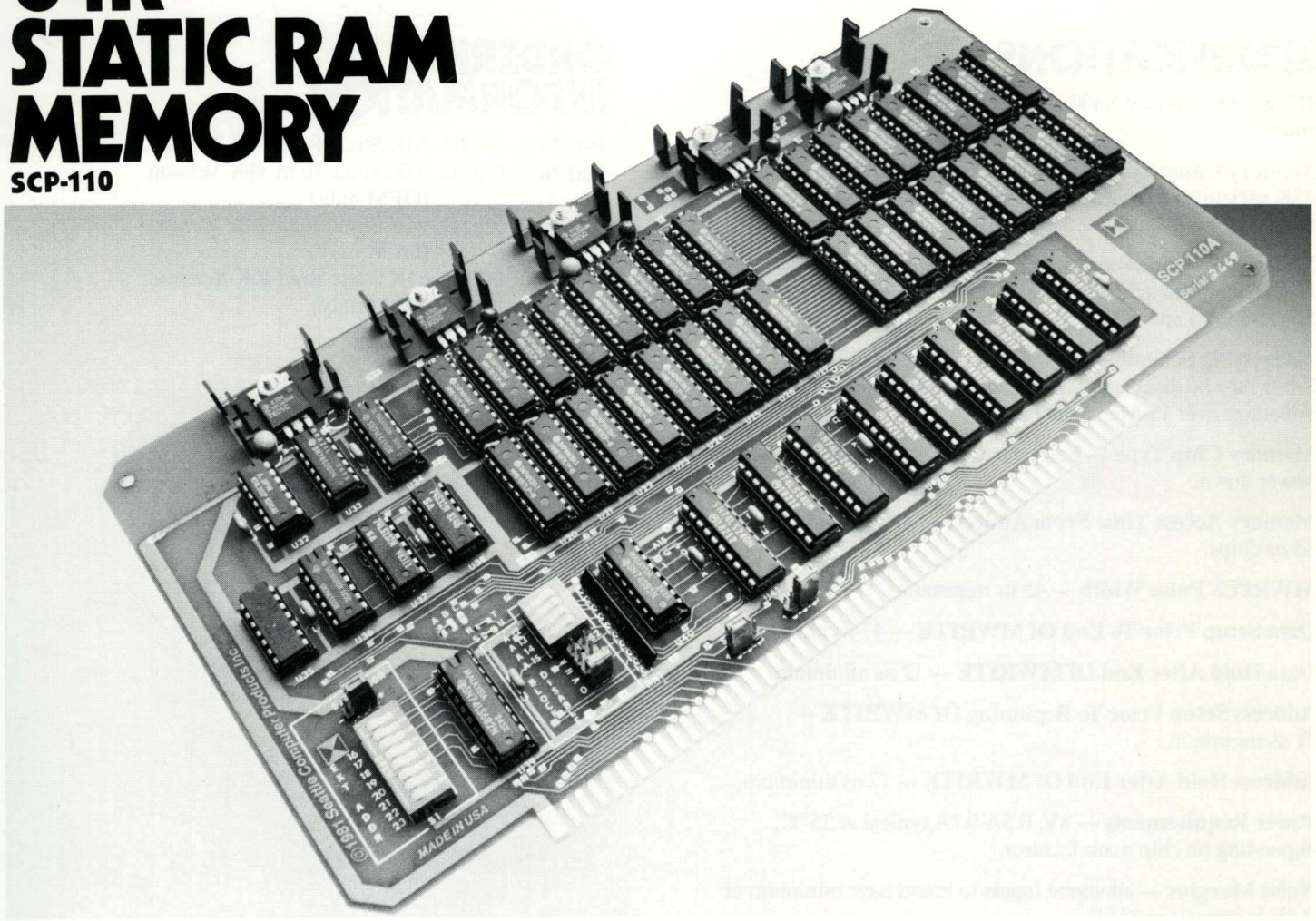
64K STATIC RAM MEMORY

SCP-110



The Seattle Computer 64K Static RAM was designed for system engineers who need fast, highly reliable S-100 memory. To speed system design and integration, the static memory board was developed to be easy to interface with a variety of microprocessors and DMA devices.

The 64K static board provides system designers an off-the-shelf alternative to sensitive dynamic RAM designs with their critical timing requirements. The use of a fully static design provides a significant advantage to designers working with high performance processors at clock speeds of 10 MHz or above. This is enhanced by Schmitt trigger buffers on all signal inputs which provide superior performance even under "noisy" conditions.

FEATURES

- Can act as either an 8-bit or 16-bit wide memory. Dynamic data bus switching per IEEE-696 Standard (S-100).
- Fully static design eliminates system timing problems. Promotes reliable operation with a wider range of CPU cards and DMA devices.
- High Speed — Uses 16K x 1 fully static 85 ns chips. Fast enough to allow memory management without wait states.
- Low power — 64K board typically uses 0.6 A when active, less when powered-down.
- Can be set to ignore address lines A16-A23 for non-extended address (64K) systems.
- Block disabling feature allows disabling any 4K block, all of the board above a 4K boundary; or all of the board below a 4K boundary.
- Low noise design — All signal inputs to board go through Schmitt trigger buffers.
- Available to OEMs in 16K increments.

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SPECIFICATIONS

IEEE-696 Standard S-100 — Fully compatible in all functional specifications.

Memory Capacity — 64K bytes. Available in 16K, 32K and 48K variations for OEMs only. 16K and 48K versions are 8-bit wide only.

Memory Organization — 64K by 8-bits or 32K by 16-bits. Dynamically switched by state of "Sixteen request" signal. See above for special OEM variations.

Addressing Scheme — Extended 24-bit addressing. Upper 8-bits may be disabled. Can be addressed on any 64K boundary over 16-Megabyte range.

Memory Chip Type — Fully static 6167 type, 16K by 1 with power down.

Memory Access Time From Address — 175 ns with 85 ns chips.

MWRITE Pulse Width — 45 ns minimum.

Data Setup Prior To End Of MWRITE — 47 ns minimum.

Data Hold After End Of MWRITE — 12 ns minimum.

Address Setup Prior To Beginning Of MWRITE — 71 ns minimum.

Address Hold After End Of MWRITE — 17 ns minimum.

Power Requirements — 8V, 0.5A-0.7A typical at 25°C, depending on chip manufacturer.

Noise Margins — all signal inputs to board have minimum of 0.4V hysteresis at 25°C.

Operating Environment — 0°C to 70°C.

Reliability — Seattle Computer 64K Static RAM boards have been in production for over two years. The typical first year reliability rate exceeds 98%.

Limited Warranty Summary

When sold by Seattle Computer or through an authorized Seattle Computer dealer, this product is warranted to the end-user for a period of 90-days for both parts and labor. When sold to the end-user by an OEM, the warranty terms vary. Consult your OEM for specific warranty coverage. Seattle Computer offers repair service for its manufactured products beyond warranty coverage. This is a summary of the warranty. A complete warranty statement is printed in the product manual and is also available from Seattle Computer upon request.

ORDERING INFORMATION

Part No.:	011004	64K Static Ram-64K Version
Part No.:	011003	64K Static Ram-48K Version (OEM only)
Part No.:	011002	64K Static Ram-32K Version (OEM only)
Part No.:	011001	64K Static Ram-16K Version (OEM only)

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