

.REM C

IDENTIFICATION

PRODUCT CODE: AC-E884B-MC  
PRODUCT NAME: CXBEAR0 M7855 BUS TESTER MODULE  
PRODUCT DATE: SEPTEMBER 1978  
MAINTAINER: DEC/X11 SUPPORT GROUP

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1.0 ABSTRACT

BEA IS AN IOMOD THAT CAN HANDLE FROM 1 TO 12 UNIBUS EXERCISERS. THE MODULE WILL HAVE THE UBE(S) DOING DATI(S), DATOR(S), DATIP(S), AND DATO(S), THEN CHECKS FOR CORRECT DATA TRANSFERS. THESE TRANSFERS ARE DONE FIRST ON AN NPR LEVEL AND INTERRUPTS WITH A BR7, THEN THE REQUEST IS SEQUENTIALLY LOWERED TO A BR4.

2.0 REQUIREMENTS

HARDWARE:

1 TO 12 UBE(S); WITH MORE THAN ONE UBE, ALL SHOULD HAVE W1 JUMPER EXCEPT THE UBE THAT IS THE FURTHEST FROM THE PROCESSOR ELECTRICALLY.

SOFTWARE:

STORAGE:: BEA REQUIRES:  
1. DECIMAL WORDS: 1371  
2. OCTAL WORDS: 02533  
3. OCTAL BYTES: 5266

3.0 PASS DEFINITION

1 ITERATION CONSISTS OF SETTING UP ONE OR MORE UBE(S) TO DO DATA TRANSFERS, THEN CHECKING THOSE TRANSFERS. 1 PASS WILL EQUAL 12,000 ITERATIONS.

4.0 EXECUTION TIME

ONE PASS WILL RUN IN APPROX. 1 MINUTE, RUNNING ALONE ON AN 11/45.

5.0 CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS: DEVADR=170000; VECTOR=510; BR1=7, BR2=6; DEVCNT=1

REQUIRED PARAMENTERS: NONE

6.0 DEVICE/OPTION SETUP

1 TO 12 UBE(S); WITH MORE THAN ONE UBE, ALL SHOULD HAVE W1 JUMPER EXCEPT THE UBE THAT IS THE FURTHEST FROM THE PROCESSOR ELECTRICALLY.

7.0 TEST SEQUENCE

- A. LOAD DEVICE INTR VECTORS AND GET READ AND WRITE BUFFER SIZES.
- B. INITIALIZE REGISTERS.
- C. GET PHYSICAL ADDRESSES FOR WRITE AND READ BUFFERS.
- D. CLEAR DEVICE REGISTERS AND WRITE BUFFER AREA; AND SET UP READ BUFFER AREA.
- E. CHECK FOR DEVICES AND IF NONE LEFT, GO TO H.
- F. LOAD DEVICE REGISTERS FOR DESIRED TRANSFER.
- G. GO BACK TO E.
- H. SET OFF ALL DEVICES SIMULTANEOUSLY.
- I. WAIT FOR THEIR INTERRUPTS.
- J. IF NOT ALL DEVICES INTERRUPTED, GO BACK TO I.
- K. CHECK THE BUFFER AREAS FOR CORRECT TRANSFERS.
- L. ROTATE REQUEST LEVELS AND DATA PATTERNS.
- M. IF THIS IS NOT THE FINAL ITERATION, GO BACK TO D.
- N. INDICATE END OF PASS AND GO BACK TO B.

8.0 OPERATION OPTIONS

THE FIRST REGISTER ADDRESS OF EACH UBE PROCEEDS IN INCREMENTS OF 20. EVERY BIT OF DVID1 CORRESPONDS TO A PARTICULAR UBE ADDRESS BEING PRESENT; I.E., BIT 0 SET = UBE ADDRESS OF 170000, BIT 1 SET = UBE ADDRESS OF 170020, BIT 2 SET = UBE ADDRESS OF 170040, ETC.

THE UBE(S) CAN BE ON THE BUS IN ANY ADDRESSING SEQUENCE. THE ONLY REQUIREMENT IS THAT DVID1 REFLECTS THE ADDRESSES OF EXISTING UBE(S).

9.0 NON-STANDARD PRINTOUTS

ALL PRINTOUT HAVE THE STANDARD FORMATS DESCRIBED IN THE DEC/X11 DOCUMENT WITH THE FOLLOWING ADDITIONS PRINTED BELOW IT:

1. FOR DATA TRANSFER ERRORS (DATI/P OF DATO/B) THE CONTENTS OF THE FOLLOWING REGISTERS PRE-PRINTED AS WELL AS THE MEMORY LOCATION AND CONTENTS. THERE SHOULD BE A DISCREPANCY BETWEEN REDB AND CONTENTS OF MEMORY ON ERRORS.

BEDB BECC BERA BECR1 BECR2 MEM-ADDR MEM-CONTENTS

2. FOR "INTERRUPTS ON ERROR - NOT ON DONE" THE ABOVE REGISTERS WILL ALSO BE PRINTED OUT EXCEPT DISREGARD THE MEMORY ADDRESS AND CONTENTS SINCE IT IS NOT PREVIOUSLY SET UP.
3. FOR "NOT ALL DEVICES INTERRUPTING" ERROR, ONLY TWO ADDITIONAL REGISTERS ARE PRINTED - DV AND MASK. DV IS A TEMPORARY STORAGE LOCATION FOR DVID1 AND MASK IS THE LOCATION INDICATING WHICH DEVICES INTERRUPTED. BIT COMPARISON OF THESE TWO REGISTERS WILL INDICATE WHICH DEVICES DID OR DID NOT INTERRUPT.

DV MASK  
[

```
203 000000- IOMOD <REAB >,170000,510,7,6,12000,73
204 000000- MODULF 140000,REAB,170000,510,7,6,12000,73
205 .TITLE BEAR DEC/X11 SYSTEM EXERCISER MODULE
206 ; DDXCOM VERSION 6 23-MAV-78
207 .LIST BIN
208 ;*****
209 000000- BEGIN:
210 000000- 042502 041101 040 MODNAM: .ASCII /REAB / ;MODULE NAME.
211 000000- 000 XFLAG: .RBYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
212 000000- 170000 ADDR: 170000+0 ;1ST DEVICE ADDR
213 000010- 000510 VECTOP: 510+0 ;1ST DEVICE VECTOR.
214 000013- 340 BR1: .RYTE PRTV7+0 ;1ST RR LEVEL.
215 000013- 300 BR2: .RYTE PRTV6+0 ;2ND RR LEVEL.
216 000013- 000001 DTID1: +1 ;DEVICE INDICATOR 1.
217 000016- 000000 SR1: OPEN ;SWITCH REGISTER 1.
218 000020- 000000 SR2: OPEN ;SWITCH REGISTER 2.
219 000022- 000000 SR3: OPEN ;SWITCH REGISTER 3.
220 000024- 000000 SR4: OPEN ;SWITCH REGISTER 4.
221 ;*****
222 000026- 140000 STAT: 140000 ;STATUS WORD.
223 000030- 000410- INIT: START ;MODULE START ADDR.
224 000032- 000224- SPOINT: MODSP ;MODULE STACK POINTER.
225 000034- 000000 PASCNT: 0 ;PASS COUNTER.
226 000036- 012000 ICOUNT: 12000 ;# OF ITERATIONS PER PASS=12000
227 000040- 000000 ICOUNT: 0 ;LOC TO COUNT ITERATIONS
228 000042- 000000 SOFCHT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
229 000044- 000000 HRDCHT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
230 000046- 000000 SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
231 000050- 000000 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
232 000052- 000000 SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
233 000054- 000000 RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
234 000056- 000000 CONFIG: 0 ;RESERVED FOR MONITOR USE
235 000058- 000000 RES1: 0 ;RESERVED FOR MONITOR USE
236 000060- 000000 RES2: 0 ;RESERVED FOR MONITOR USE
237 000062- 000000 SVR0: OPEN ;LOC TO SAVE R0.
238 000064- 000000 SVR1: OPEN ;LOC TO SAVE R1.
239 000066- 000000 SVR2: OPEN ;LOC TO SAVE R2.
240 000070- 000000 SVR3: OPEN ;LOC TO SAVE R3.
241 000072- 000000 SVR4: OPEN ;LOC TO SAVE R4.
242 000074- 000000 SVR5: OPEN ;LOC TO SAVE R5.
243 000076- 000000 SVR6: OPEN ;LOC TO SAVE R6.
244 000100- 000000 CSRA: OPEN ;ADDR OF CURRENT CSR.
245 000102- 000000 SRADR: OPEN ;ADDR OF GOOD DATA, OP
246 000104- 000000 ACSR: OPEN ;CONTENTS OF CSR.
247 000106- 000000 WASADR: OPEN ;ADDR OF BAD DATA, OR
248 000108- 000000 ASSTAT: OPEN ;STATUS RFG CONTENTS.
249 000106- 000000 ERRTVP: OPEN ;TYPE OF ERROR
250 000106- 000000 ASB: OPEN ;EXPECTED DATA.
251 000110- 000000 AAS: OPEN ;ACTUAL DATA.
252 000112- 000534- RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
253 000114- 000000 WRTN: OPEN ;WORDS TO MEMORY PER ITERATION
254 000116- 000000 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
255 000120- 000000 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
256 000122- 000073 IONUM: 73 ;MODULE IDENTIFICATION NUMBFR=73
257 .REPT SPSTZ ;MODULE STACK STARTS HERE.
258 .NLIST
```

```
259 .WORD 0
260 .LIST
261 .ENDR
262 000224- MODSP:
263 ;*****
264
```

```

265
266
267
268 000224 000000
269 000226 137777
270 000230 000000
271 000233 100000
272 000234 000000
273 000236 170014
274 000240 000000
275 000242 000000
276 000244 000000
277 000256 170000
278 000258 000740
279 000259 000360
280 000254 000240
281 000256 000170
282 000260 000140
283 000262 000120
284 000264 000000
285 000266 000000
286 000270 000000
287 000272 000000
288 000274 000000
289 000276 000000
290 000300 000000
291 000302 000000
292 000304 000000
293 000306 000000
294 000310 000000
295 000312 000000
296 000314 000000
297 000316 000352
298 000320 000000
299 000322 000000
300 000324 004324
301 000326 000000
302 000330 000000
303 000332 000360
304 000334 000000
305 000336 000000
306 000338 000000
307 000342 000000
308 000344 004100
309 000346 000500
310 000350 022500
311 000352 003100
312 000354 000060
313 000356 007740
314 000360 000000
315 000362 000000
316 000364 000000
317 000366 000000
318 000368 000000
319 000370 000000
320 000372 000374

```

```

.ENABL AMA
P1: 0 ;1ST DATA PATTERN
P2: 137777 ;2ND DATA PATTERN
P3: 0 ;3RD DATA PATTERN
P4: 100000 ;4TH DATA PATTERN
ROTCNT: 0 ;COUNTER FOR # OF TIMES DATA ROTATED
STWCGN: 170014 ;ADDR TO SET OFF ALL DEVS SIMULTANEOUSLY
DV: 0 ;TEMP STORAGE FOR DVIDI
MASK: 0 ;USED TO DROP ANY DEV NOT INTERRUPTING
MORE: 0 ;WORKING STORAGE FOR DVIDI
B12T15: 170000 ;USED TO CLEAR BITS 12 THRU 15
RBUFSZ: 740 ;SIZE FOR 1 DEV
360 ;SIZE FOR 2 DEVS
240 ;SIZE FOR 3 DEVS
170 ;SIZE FOR 4 DEVS
140 ;SIZE FOR 5 DEVS
120 ;SIZE FOR 6 DEVS
DEV CNT: 0 ;TOTAL NUMBER OF DEVS
DODI: 0 ;DATA OR DATI INDICATOR
SVDDDI: 0 ;STORAGE FOR DODI
SAVRI: 0 ;LOC TO SAVE R1
SAVR2: 0 ;LOC TO SAVE R2
CYCLE: 0 ;CYCLE COUNT FOR A BYTE TRANSFER
MCC: 0 ;CYCLE COUNT FOR A DATA TRANSFER
RCC: 0 ;CYCLE COUNT FOR A DATI TRANSFER
DOCNT: 0 ;# OF DEVS DOING A DATI/P(S)
DTCNT: 0 ;# OF DEVS DOING A DATI/P(S)
EABIT: 0 ;USED TO SET EXTENDED MEMORY ADDR BITS
RRUF: 0 ;SIZE OF BUFFER FOR DATI/P(S)
WRUF: 0 ;SIZE OF BUFFER FOR DATI/P(S)
WRUFVA: 0 ;VIRTUAL ADDR OF WRITE BUFFER
WRUFPA: OPEN ;PHYSICAL ADDR OF WRITE BUFFER
WRUFEA: OPEN ;EXT MEM ADDR BITS SET BY MONITOR
RBUFVA: REEDIV ;VIRTUAL ADDR OF READ BUFFER
RBUFEA: OPEN ;EXT MEM ADDR OF READ BUFFER
RBUFEA: OPEN ;EXT MEM ADDR BITS SET BY MONITOR
RBUFSZ: 360 ;TOTAL SIZE OF BUFFER RESERVED FOR READS
RRFADR: 0 ;USED TO POINT TO LOC WITHIN RBUFSZ
ENRBUF: 0 ;USED FOR LAST ADDR(PHYSICAL) OF READ BUFF
WRUFADR: 0 ;USED TO POINT TO LOC WITHIN WRUFSZ
ENWRBF: 0 ;USED FOR LAST ADDR(PHYSICAL) OF WRITE RUFF
XPR1: 4100 ;FUN 2-DATI-INTR
XPR2: 2500 ;FUN 2-DAT0-INTR
XPR3: 22500 ;FUN 1-DATI/NO ROT-INTR
XPR4: 3100 ;FUN 1-DAT0-INTR
RQVTL: 60 ;USED TO SET REQUEST LEVELS OF DEVS
ERRIT: 7740 ;USED TO CHECK IF ANY ERROR BITS SET
DVRREGS: 0
DR: 0 ;DEV DATA REG CONTENTS
CC: 0 ;DEV CYCLE COUNT CONTENTS
CG: 0 ;DEV ADDR REG CONTENTS
CR1: 0 ;DEV CR1 REG CONTENTS
CR2: 0 ;DEV CR2 REG CONTENTS
BADMEM: BADR ;BAD MEMORY ADDR

```

```

321 000374 000000
322 000376 177777
323 000400 000000
324 000400 000240
325 000402 000474
326 000404 000474
327 000406 000000
328
329 000410 012737 000361 000114
330 000416 012737 000361 000116
331 000424 012737 000001 000120
332 000432 013700 000014
333 000440 006200
334 000440 001412
335 000442 103375
336 000444 062737 000361 000114
337 000452 062737 000361 000116
338 000460 005237 000120
339 000464 000764
340 000466 013737 000014 000240
341 000474 043737 000246 000240
342 000502 013737 000240 000242
343 000510 013737 000240 000244
344 000516 001002
345 000520 000137 001404
346 000524
347 000524 004737 001546
348 000530 004737 001410
349
350
351
352
353
354
355
356
357
358
359
360
361 000534
362 000534 104415 000000 000324
363 000542 013737 000326 000336
364 000550 062737 000736 000336
365
366 000556 104415 000000 000316
367 000564 013737 000320 000342
368 000572 062737 000736 000342
369
370
371 000600 004737 002234
372
373 000604 012737 000001 000266
374
375 000612 005037 000234
376 000616 012737 000224 000272

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```

BADR: 0 ;CONTENTS OF BAD MEMORY
DVMASK: 177777 ;THIS POINTER IS USED SO THE NEXT
PTR1: DV ;TWO LOCATIONS WILL BE PRINTED OUT FOR
PTR2: MASK ;AN ERR WHICH DEVICES DID NOT INTERRUPT
TOCNT: 1 ;TIME OUT COUNTER
START: MOV #361,WDT0 ;AT LEAST 361 WORDS TO MEM/ITERATION
MOV #361,WDR ;AT LEAST 361 WORDS FROM MEM/ITERATION
MOV DVIDI,R0 ;AT LEAST INTERRUPT / ITERATION
2S: ASR R0 ;SHIFT IN A COUNT
JEQ 3S ;RR OUT IF NONE LEFT
JNE 2S ;GO BACK IF NO BIT IN THIS POSITION
ADD #361,WDT0 ;361 MORE WORDS TO MEM
ADD #361,WDR ;361 MORE WORDS FROM MEM
INC INTR ;1 MORE INTERRUPT
BR 2S ;GO CHECK FOR MORE
3S: MOV DVIDI,DV ;MOVE DVIDI TO TEMP STORAGE
BIC B12T15,DV ;ENSURE BITS 12-15 ARE CLEARED
MOV DV,MASK ;INIT MASK BITS EQUAL TO DVIDI
MOV DV,MORE ;THEN SET UP WORKING AREA
BNE IS ;GO TO IS IF THERE ARE DEVS
JMP DROP ;ELSE DROP THE MODULE
1S: JSR PC,CLRREG ;CLEAR ALL DEV REGS
JSR PC,SETUP ;SET UP AVAILABLE REG LOCATIONS AND
;GET BUFFER SIZES FOR DATI(S) & DATI(S)
;*****
;NOTE: IN THE FOLLOWING COMMENTS THE TERM READ IS USED
;TO DENOTE DATI OR DATI AND THE TERM WRITE DENOTES
;*****
;THIS IS THE LOCATION THE PROGRAM WILL PROCEED
;WITH AFTER AN END OF PASS
RESTR: GETPAS,REGIN,RRUFVA ;GET PHYSICAL ADDRESS FROM 16-BIT RRUFVA
MOV RRUFPA,ENRBUF ;MOVE READ BUFF ADDR TO END-OF-READ BUFF REG
ADD #736,ENRBUF ;ADD 736 TO GET LAST ADDR OF BUFFER
GETPAS,REGIN,WRUFVA ;GET PHYSICAL ADDRESS FROM 16-BIT WRUFVA
MOV WRUFPA,ENWRBF ;MOVE WRITE BUFF ADDR TO END-OF-WRITE BUFF REG
ADD #736,ENWRBF ;ADD 736 TO GET LAST ADDR OF BUFFER
JSR PC,FARITS ;SET BITS 0 & 1 OF REA EQUAL TO
;BITS 4 & 5 OF RRUFPA RESPECTIVELY
MOV #1,DODI ;INIT XFER INDICATOR TO 1 SO THE
;1ST INC WILL SIGNIFY A DATI
CLR ROTCNT ;CLEAR PATTERN ROTATION COUNT
MOV #P1,SAVR1 ;INIT SAVRI TO 1ST PATTERN

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377 000624 012737 000344 000274 40V #XFR1,SAVR5 ;INIT SAVRS TO 1ST XFER FUNCTION
378 000632 012737 000060 000354 40V #60,RQLVL ;INIT REQUEST LEVEL TO NPP/TNTR BR7
379
380 ;THIS IS THE LOCATION THE PROGRAM WILL PROCEED WITH
381 ;AFTER AN ITERATION
382
383 000640 023737 000242 000240 RRRST:
384 000646 001402 CMP MASK,DV ;# OF DEVS THAT SHOULD INTR=# OF DEVS THAT DID?
385 000650 004737 JSR PC,SETUP ;IF EQUAL,GO TO 2S
386 ;ELSE GET NEW DEV COUNTS & RUPFFR SIZES
387
388 000654 013737 000266 000270 1S: 40V DDDI,SVD001 ;SAVE DDDI SETTING
389 000662 013737 000242 000244 40V MASK,MORE ;RESET MORE BITS
390 000670 013737 000242 000240 40V MASK,DV ;IF ANY DEVS DROPPED, ALTER DV
391 000676 010102 JNE 3S ;IF NOT, CONTINUE ON
392 000700 000137 001404 40V DROP ;ELSE DROP MODULE
393
394 000704 004737 002402 2S: JSR PC,RESDAT ;RESTORE DATA IN READ BUFFER
395 ;AND CLEAR WRITE BUFFER
396 000710 013700 000006 40V ADDR,R0 ;PUT DEVICE ADDR INTO REG. 0
397 000714 013737 000320 000340 40V WRUPPA,WBPADR ;USE WBPADR AS WRITE BUFF ADDR
398 000722 163737 000314 000340 SUB WRUP,WBPADR ;INIT TO WRITE BUFF LESS WBP
399 000730 013737 000326 000334 SUB RBP,WBPADR ;USE RBPADR AS READ BUFF ADDR
400 000736 163737 000312 000334 SUB RBP,WBPADR ;INIT TO READ BUFF LESS RBP
401
402 000744 006237 000244 3S: ASR MORE ;CHECK FOR ANOTHER DEV
403 000750 103404 RCS 4S ;IF THERE, CONTINUE
404 000752 001440 GO ;AND IF NOT, GO TO GO
405 000754 062700 000020 ADD #20,R0 ;ELSE ADD 20 FOR NEXT ADDR
406 000766 000771 BR 3S ;AND CHECK FOR MORE
407
408 000762 004737 001742 4S: JSR PC,CHEKPX ;SEE IF R1 & R5 NEED INITIALIZATION
409 000766 005237 000266 INCI DDDI ;INC XFER INDICATOR
410 000772 000001 000266 BIT #R10,DDD1 ;BIT 0 OF DDD1 IS 1
411 001000 001003 JNE 5S ;GO SET UP REGS FOR DATO(S)
412 001002 004737 002070 JSR PC,RRRGS ;ELSE SET UP REGS FOR DATI(S)
413 001006 000402 BR 6S ;GO LOAD THE REGISTERS
414
415 001010 004737 002016 5S: JSR PC,WRRGS ;SET REGS FOR WRITE XFER INFO
416
417 ;AT THIS POINT THE DEVICE REGISTERS WILL BE LOADED TO DO
418 ;TRANSFERS
419
420 6S:
421 001014 40V (R1)+(R0)+ ;SET UP DATA BUFFER REG
422 001016 012120 40V (R2)+(R0)+ ;SET UP CYCLE COUNT REG.
423 001020 011320 40V #R3,ADR ;SET UP ADDR REG
424 001022 013760 000310 40V CABIT,10(R0) ;SET UP EXT ADDR BITS IN CR2
425 001030 012510 40V (R5)+(R0) ;SET UP CR1
426 001032 005710 31S RQLVL,(R0) ;SET REQUEST LEVELS
427 001036 062700 000012 ADD #12,R0 ;ADD 12 TO GET NEXT DEV ADDR
428
429 001042 010137 000272 40V R1,SAVR1 ;SAVE CONTENTS OF R1(LAST DATA PATTERN)
430 001046 000734 40V R5,SAVR5 ;SAVE CONTENTS OF R5(LAST XFER INSTR.)
431
432 001052 BR 3S ;GO LOAD ANOTHER DEVICE
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433 001054 60: CLR TOCNT ;CLEAR TIME OUT COUNT
434 001054 005037 000406 CLR MASK ;CLEAR ALL BITS IN MASK
435 001060 005037 000242 ;TO BE RESET IN INTR SERV RTNS
436 ;SET OFF ALL DEVS
437 001064 005277 177146 BRK: INC @SIMLGO
438
439 001070 104407 000000 BREAKS,REGIN ;TEMPORARY RETURN TO MONITOR....
440 001074 104407 000000 BREAKS,REGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
441 001100 023737 000240 000242 CMC D,MASK ;ALL DEVS INTR?
442 001106 001611 BRQ D,DATA ;IF YES, GO TO DATABK
443 001110 005337 000406 DFC TOCNT ;ELSE DEC TIME OUT COUNT
444 001114 001365 BNE BRK ;AND BREAK IF NOT 0
445 001116 104603 000000 003122 MSGNS,BPGIN,NOINTR ;ASCII MESSAGE CALL WITH COMMON HEADER
446 001124 005703 CLR PC,GETDEV ;R4 INVALID IN ERR REPORT
447 001126 004737 002502 JSR PC,GETDEV ;REPORT NON-INTR'G DEVICES
448
449 001132 60:
450 001132 013737 000240 000244 DATA: 40V DV,MORE ;SET UP MORE
451 001140 013737 000270 000266 40V SVD001,DDD1 ;RESTORE DDDI TO SETTING WHEN
452 ;LOADING 1ST DEV
453 001146 013700 000006 40V ADDR,R0 ;INIT R0 TO 1ST DEV ADDR
454 001152 012737 004324 40V #RFDI,WBPADR ;RBPADR=1ST LOC IN READ BUFFER
455 001160 012737 003362 000340 40V #RTOU,WBPADR ;WBPADR=1ST LOC IN WRITE BUFFER
456 001166 005003 CLR R3 ;USED IN DODATA RTN
457
458 001170 006237 000244 MORDEV: ASR MORE ;CHECK FOR OTHER DEVS
459 001174 103405 RCS 2S ;IF THERE, CONTINUE
460 001176 001001 JNE 1S ;IF SOME LEFT, GO CHECK FOR MORE
461 001200 000447 BR NPASS ;ELSE GO TO END OF PASS RTN
462
463 001202 062700 000020 1S: ADD #20,R0 ;GET NEXT DEV ADDR
464 001206 000770 BR MORDEV ;GO CHECK FOR THAT DEV
465
466 001210 000006 2S:
467 001214 105760 TSTR 6(R0) ;IS READY BIT SET IN CR1?
468 001216 100407 BMI 3S ;IF SET, CONTINUE
469 001216 104403 000000 003102 MSGNS,BPGIN,NOINTR ;ASCII MESSAGE CALL WITH COMMON HEADER
470 001224 005004 CLR R4 ;R4 INVALID IN ERR REPORT
471 001232 000426 JSR PC,RDDATA ;ELSE TYPE OUT CONTENTS OF ALL DEV REGS
472 001234 BR NXT ;AND GO CHECK FOR NEXT DEV
473
474 001234 033760 000356 000016 3S: BIT #R15,16(R0) ;SEE IF ANY ERROR BITS SET
475 001242 001407 BFC 4S ;IF NONE SET, CONTINUE
476 001244 104403 000000 003106 MSGNS,BPGIN,ERRSET ;ASCII MESSAGE CALL WITH COMMON HEADER
477 001252 005004 CLR R4 ;R4 INVALID IN ERR REPORT
478 001254 004737 002556 JSR PC,RDDATA ;ELSE TYPE OUT CONTENTS OF ALL DEV REGS
479 001260 000413 BR NXT ;AND GO CHECK FOR NEXT DEV
480
481 001262 005237 000266 4S: INCI DDDI ;INC XFER INDICATOR
482 001266 032737 000001 000266 BIT #R10,DDD1 ;DATI OR DATO ?
483 001274 001003 BNE 5S ;IF DATO, GO DO IT
484 001276 004737 002110 JSR PC,DIDATA ;CHECK DATI XFRS
485 001302 000402 BR 5S ;CHECK FOR NEXT DEV
486
487 001304 004737 002162 5S: JSR PC,DODATA ;CHECK DATO XFRS
488 001310 062700 000020 NXT: ADD #20,R0 ;ADD 20 FOR NEXT DEV ADDR
```



```
489 001314* 000137 001170*      JMP      MORDEV      ;GO CHECK FOR ANOTHER DEV
490 001320* 000137 001170*      NPASS:  JSR      PC,CLRREG  ;CLEAR ALL DEV RECS
491 001321* 004737 001546*      JSR      PC,ROTREG  ;SHIFT REQUEST TO NEXT RR LEVEL
492 001322* 004737 002276*      CMP      #R4,SAVR5  ;HAS LAST XFER BEEN DONE?
493 001330* 022737 000352* 000274* BNE      IS          ;IF NOT, CONTINUE
494 001336* 001010                ;*****
495 ;*****
496 ;*****
497 ;*****
498 ;*****
499 ;*****
500 ;*****
501 ;*****
502 001340* 022737 000020 000234*  CMP      #16,,ROTCNT ;ELSE HAS P2&P4 BEEN ROTATED 16 TIMES?
503 001346* 001404                BEQ      IS          ;IF YES DON'T ROTATE PATTERNS ANYMORE
504 001350* 005237 000234*      INC      ROTCNT    ;ELSE INC ROTATION COUNT, AND
505 001354* 004737 002342*      JSR      PC,OUTPAT ;ROTATE DATA PATTERNS
506 001360* 000001 000264*      1S:      BIT      #R10,DEVcnt ;IS THERE AN ODD # OF DEVS?
507 001366* 032737 000001 000264*      BFC      2S        ;IF NOT, GO TO 5S
508 001370* 004737 002704*      JSR      PC,SWAPCC ;ELSE SWAP RCC WITH WCC & RBUF
509 ;*****
510 ;*****
511 001374* 104413 000000*      2S:      ENDDITS,BEGIN ;SIGNAL END OF ITERATION
512 001374* 104413 000000*      ;MONITOR SHALL TEST END OF PASS
513 ;*****
514 001400* 000137 000640*      JMP      REREST    ;ELSE GO TO REREST
515 ;*****
516 ;*****
517 001404* 104410 000000*      DRUP:   ENDS,BEGIN ;
518 ;*****
519 ;*****
520 ;*****
521 ;*****
522 ;*****
523 ;*****
524 ;*****
525 ;*****
526 ;*****
527 ;*****
528 ;*****
529 ;*****
530 001410* 005037 000306*      SETUP:  CLR      DICNT    ;CLEAR REG COUNTING DEVS DOING DATI(S)
531 001411* 005037 000304*      CLR      DOCNT    ;CLEAR REG COUNTING DEVS DOING DATO(S)
532 001420* 012700 002742*      MOV      #RSP1,R0 ;SET R0 TO 1ST INTR SERVICE RTN
533 001421* 013701 000010 000266*      MOV      VECTOR,R1 ;SET R1 TO 1ST INTR VEC ADDR
534 001430* 012737 000001 000266*      MOV      #1,DDDI   ;INIT XFR INDICATOR TO 1
535 ;*****
536 001436* 006237 000244*      CKMORE: ASR      MORE    ;CHECK FOR A DEVICE
537 001441* 001420                BFC      1S        ;IF THERE, CONTINUE
538 001444* 001420                BFC      2S        ;IF NONE, LEFT AT ALL, GET OUT
539 001446* 062700 000010 000010  ADD      #10,R0     ;ELSE INC INTR SERV POINTER BY 10
540 001452* 062701 000004 000004  ADD      #4,R1      ;AND INC INTR VEC LOC BY 4
541 001456* 000767                RR         ;GO BACK AND CHECK FOR ANOTHER DEV
542 ;*****
543 001460* 005237 000266*      1S:      INC      DDDI     ;INC XFR INDICATOR
544 001464* 004737 001622*      JSR      PC,INCCNT ;INC DATI OR DATO COUNT
545 001470* 010021                MOV      R0,(R1)+  ;MOVE ADDR OF INTR SERV RTN TO VEC LOC
```

```
545 001472* 113721 000012*      MOVBR   RRI,(R1)+ ;MOVE RRI VALUE TO PSW VEC
546 001476* 105721                TSTR    (R1)+     ;GET BACK TO AN EVEN ADDR
547 001500* 062700 000010 000010  ADD      #10,R0     ;ADD 10 TO R0 FOR NEXT INTR SERV RTN
548 001504* 000754                RR      CKMORE    ;SEE IF THERE ARE MORE DEVS
549 ;*****
550 001506* 013737 000266* 000264*  2S:      MOV      DDDI,DEVcnt ;DEV XFR INDICATOR HAS # OF DEVS
551 001514* 005337 000264*      DFC      DEVcnt   ;+ 1, SO DEC DEVcnt BY 1
552 ;*****
553 001520* 012700 000246*      GETRUF: MOV      #RUFsz-2,R0 ;SET R0 TO SIZE TABLE
554 001524* 005001                CLR      R1        ;CLEAR R1
555 ;*****
556 001526* 005720                1S:      TST      (R0)+    ;MOVE POINTER TO NEXT VALUE
557 001530* 005201                LNC     R1         ;INC COUNTER
558 001532* 020137 000306*      CMB     R1,DICNT  ;# OF DATI DEVS ?
559 001536* 103773                BLO     IS        ;IF NOT, INC COUNTER & POINTER
560 001540* 011037 000312*      MOV      (R0),RBUF ;ELSE R0 HAS SIZE OF READ BUFF PER DEV
561 ;*****
562 001544* 011037 000302*      MOV      (R0),RCC ;1 WORD = 2 LOCATIONS(OR BYTES)
563 001550* 006237 000302*      ASR      RCC      ;AND ALSO IS DATI XFER COUNT
564 001554* 005437 000302*      NEG     RCC       ;WHEN IT IS HALVED
565 001560* 005737 000304*      NEG     RCC       ;GET 2'S COMP FOR DEV CC REG
566 001564* 001405                TST     DDCNT    ;IF DDCNT IS 0
567 ;*****
568 001566* 020137 000304*      CMP     R1,DDCNT  ;R1=# OF DATO DEVS ?
569 001572* 001402                BFC     2S        ;YES, CONTINUE
570 001574* 005301                DEC     R1        ;NO, DEC R1
571 001576* 005740                TST    -(R0)     ;AND DEC POINTER
572 ;*****
573 001600* 011037 000314*      2S:      MOV      (R0),RBUF ;R0 HAS SIZE OF WRITE BUFF PER DEV
574 ;*****
575 001604* 011037 000300*      MOV      (R0),WCC ;AND ALSO IS DATO XFER COUNT
576 001610* 006237 000300*      ASR     WCC      ;WHEN IT IS HALVED
577 001614* 005437 000300*      NEG     WCC      ;GET 2'S COMP FOR DEV CC REG
578 001620* 000207                RTS     PC        ;RETURN
579 ;*****
580 ;*****
581 ;*****
582 ;*****
583 ;*****
584 ;*****
585 ;*****
586 ;*****
587 ;*****
588 001622* 032737 000001 000266*  INCCNT: BIT      #R10,DDDI ;IF BIT 0 IS 1
589 001630* 001003                BNE     INCD0     ;GO INC DDCNT
590 001632* 005237 000306*      INC     DICNT    ;ELSE INC DICNT
591 001636* 000402                BR     EXINC     ;AND EXIT RTN
592 ;*****
593 001640* 005237 000304*      INCDU: INC     DDCNT ;INC DDCNT
594 001644* 000207                RTS     PC        ;RETURN
595 ;*****
596 ;*****
597 ;*****
598 ;*****
599 ;*****
600 ;*****
```

```

601 ;THIS ROUTINE WILL CLEAR ALL THE REGISTERS OF ALL EXISTING DEVICES
602 ;AS DEFINED BY DVID1
603 ;*****
604 ;*****
605
606 001646- CLRRREG:
607 001646- 013737 000240- 000244- MOV DV, MORE ;RESTORE MORE
608 001654- 013701 000006- 000244- MOV ADDR, R1 ;INIT R1 TO 1ST DEV ADDR
609
610 001660- 1S:
611 001660- 006237 000244- ASR MORE ;IS THERE A DEVICE?
612 001664- 103404 BCS ZS ;IF VCS GO SET IT UP
613 001670- 062701 000020 ADD #20, R1 ;IF MORE LEFT, - GET OUT
614 001674- 000771 BR IS ;ELSE INC R1 TO NEXT DEV ADDR
615 ;AND GO SEE IF IT'S THERE
616
617 001700- 005011 000002 CLR (R1) ;CLEAR DATA BUFFER REG
618 001704- 005061 000004 CLR 2(R1) ;CLEAR CYCLE COUNT REG
619 001710- 005061 000006 CLR 4(R1) ;CLEAR BUFFER ADDR REG
620 001714- 005061 000010 CLR 6(R1) ;CLEAR CR1 REG
621 001720- 005061 000016 CLR 10(R1) ;CLEAR ERROR CLEAR REG
622 001724- 062701 000020 ADD #20, R1 ;CLEAR CR2 REG
623 001730- 000753 BR IS ;INC R1 TO NEXT DEV ADDR
624 ;GO CHECK FOR ANOTHER DEV
625
626 001732- 013737 000240- 000244- MOV DV, MORE ;RESTORE MORE
627 001740- 000207 RTS PC ;RETURN
628 ;*****
629 ;*****
630 ;*****
631 ;THIS ROUTINE WILL KEEP TRACK OF THE DATA PATTERNS AND TRANSFER
632 ;FUNCTIONS USED TO LOAD THE DEVICES
633 ;*****
634 ;*****
635 ;*****
636
637 001742- CHECKPX:
638 001742- 013701 000272- MOV SAVR1, R1 ;RESTORE R1 TO PATTERN IN SAVR1
639 001746- 013705 000274- MOV SAVR5, R5 ;RESTORE R5 TO XPR FUNCTION IN SAVR5
640 001750- 027705 000352- CMP #R4, R5 ;DOES R5 POINT TO LAST XFER?
641 001756- 100002 BPL IS ;IF NOT, CONTINUE
642 001760- 012705 000344- MOV #XFR1, R5 ;ELSE INIT TO 1ST XFER
643
644 001764- 1S:
645 001770- 022701 000232- CMP #4, R1 ;IF R1 DOES NOT EXCEED LAST PATTERN
646 001774- 100011 BPL CS ;ADDRESS, EXIT ROUTINE
647 001778- 022737 000020 000234- CMP #16, ROTCNT ;HAVE P2&P4 BEEN ROTATED 16 TIMES?
648 002002- 001003 BNE ;IF NOT, INIT R1
649 002006- 100002 CBR #R0RBF, R1 ;DOES R1 LAST ADDR OF READ BUFF?
650 002010- 000000 BPL CS ;IF LESS, GO TO EXINIT
651
652 2S: MOV #P1, R1 ;ELSE INIT R1 TO 1ST DATA PATTERN
653
654 3S: RTS PC ;RETURN
655 ;*****
656 ;*****

```

```

657 ;*****
658 ;THIS ROUTINE WILL DETERMINE IF THE TRANSFER IS A DATO OR A DATOB
659 ;AND SET THE CYCLE COUNT APPROXIMATELY VIA R2 AND ALSO SET R3 TO
660 ;THE CORRECT WRITE BUFFER ADDRESS
661 ;*****
662 ;*****
663 ;*****
664
665 002016- WREGS:
666 002016- 012702 000300- MOV #WCC, R2 ;SET R2 FOR A DATO CYCLE COUNT
667 002022- 032712 000400 BIT 1, (R5) ;IF BIT 8 OF XFER FUNC IS 0
668 002030- 033715 001000 BEQ IS ;ALL SET 6 FINISH UP
669 002034- 001407 BIT 9, (R5) ;IF BIT 9 OF XFER FUNCTION
670 002036- 012702 BFC IS ;ALL SET FINISH UP
671 002042- 013737 000300- 000276- MOV #BYTCC, R2 ;ELSE SET R2=ADDR OF BYTCC
672 002050- 006337 000276- ASL WCC, BYTCC ;MOVE VALUE OF WCC TO BYTCC AND
673 ;DOUBLE BY SHIFTING LEFT
674 002054- 1S:
675 002062- 063737 000314- 000340- ADD #WBUF, WBFADR ;INC WBFADR ADDR BY VALUE IN WBUF
676 002066- 000207 MOV #WBFADR, R3 ;SET R3 = TO ADDR OF WBFADR
677 RTS PC ;RETURN
678 ;*****
679 ;*****
680 ;*****
681 ;THIS ROUTINE WILL SET UP R3 WITH VALUES FOR DOING READS
682 ;*****
683 ;*****
684 ;*****
685
686 002070- RREGS:
687 002074- 063737 000302- 000334- MOV #RCC, R2 ;SET R2 FOR A DATI CYCLE COUNT
688 002102- 012703 000334- ADD #RBUF, RBFADR ;INC RBFADR ADDR BY VALUE IN RBUF
689 002106- 000207 MOV #RBFADR, R3 ;SET R3 = TO VALUE IN RBFADR
690 RTS PC ;RETURN
691 ;*****
692 ;*****
693 ;*****
694 ;THIS ROUTINE CHECKS FOR A CORRECT DATI TRANSFER SINCE ALL OF THE
695 ;READ TRANSFER BUFFER SIZE CONTAINS THE SAME DATA, ONLY ONE LOCATION
696 ;IS COMPARED WITH THE CONTENTS IN THE DEVICE DATA BUFFER REGISTER.
697 ;*****
698 ;*****
699 ;*****
700
701 002110- DIDATA:
702 002114- 063703 000334- MOV RBFADR, R3 ;SET R3 = 1ST READ BUFF ADDR
703 002120- 013704 000334- ADD #RBUF, R3 ;GET LAST ADDR BY ADDING BUFF SIZE
704 002124- MOV RBFADR, R4 ;SET R4=PRESENT READ BUFF ADDR
705
706 002124- 1S:
707 002126- 021024 CMP (R0), (R4)+ ;IS VALUE IN DEV DATA REG=TO THAT IN READ BUFF?
708 002130- 000000- 003112- BFC ZS ;IF EQUAL, CONTINUE
709 002136- 005744 MSGNS, BFCIN, DTERR ;ASCII MESSAGE CALL WITH COMMON HEADER
710 002140- 004737 002556- JSR PC, R0DATA ;RETURN R4 TO ERR LOCATION
711 002144- 000402 BR ;ADD TYPE OUT CONTENTS OF REGS
712 002146- 020403 CMP R4, R3 ;GO EXIT

```

```
713 002150 103765
714 002152 063737 000312 000334
715 002154 063737
716 002160 000207
717
718
719
720
721
722
723
724
725
726
727
728
729 002162
730 002162 013703 000340
731 002166 053703 000314
732 002172 013704 000340
733 002176
734 002176 021024
735 002200 001407
736 002202 104403 000000 003116
737 002210 005744
738 002212 004737 002556
739 002220 000402
740 002220
741 002220 020403
742 002222 103765
743 002224
744 002224 063737 000314 000340
745 002232 000207
746
747
748
749
750
751
752
753
754
755
756 002234
757 002234 005037 000310
758 002240 032737 000020 000330
759 002246 001403
760 002250 052737 000001 000310
761 002256
762 002256 032737 000040 000330
763 002264 021403
764 002266 052737 000002 000310
765 002274
766 002274 000207
767
768
```

3S: BLD 1S ;IF NOT,CHECK NEXT ADDR  
ADD RBUF,RBFADR ;INC READ BUFF ADDR BY RBUF VALUE  
PC ;RETURN  
\*\*\*\*\*

\*\*\*\*\*  
THIS ROUTINE CHECKS FOR CORRECT DATA TRANSFERS ALL VALUES IN THE  
WRITE TRANSFER BUFFER IS COMPARED WITH THE CONTENTS IN THE DEVICE  
DATA BUFFER REGISTER  
\*\*\*\*\*

ODDATA: MOV WBFADR,R3 ;SET R3 = PRESENT WRITE BUFF ADDR  
ADD WBUF,R3 ;INC R3 BY # OF XFER LOCATIONS  
MOV WBFADR,R4 ;SET R4 = PRESENT WRITE BUFF ADDR

1S: CMP (R0),(R4)+ ;IS DFV DATA REG = VALUE IN BUFF ADDR?  
BFG 2S ;IF EQUAL CONTINUE  
MSGNS,BEGIN,DOERR ;ASCII MESSAGE CALL WITH COMMON HEADER  
TST -(R4) ;RETURN R4 TO ERR LOCATION  
JSR PC,ODDATA  
BR 3S

2S: CMP R4,R3 ;IS R4 AT LAST XFER LOCATION?  
BLD 1S ;IF NOT,GO CHECK NEXT ADDR

3S: ADD WBUF,WBFADR ;INC WRITE RUFF BY VALUE IN WBUF  
PC ;RETURN  
\*\*\*\*\*

\*\*\*\*\*  
THIS ROUTINE WILL SET THE CORRECT EXTENDED MEMORY ADDRESS BITS  
OF THE CR2 REGISTER IN ACCORDANCE WITH THE EXTENDED MEMORY ADDRESS  
BITS AS SET BY THE MONITOR IN RBUFEA.  
\*\*\*\*\*

EABITS: CLR EABIT ;CLEAR EXT MEM BITS  
BIT #BIT4,RBUFEA ;IS BIT 4 OF RBUFEA SET?  
BFI 1S ;IF NOT, CONTINUE  
BFS #BIT0,EABIT ;ELSE SET BIT 0 OF EABIT

1S: BIT #BIT5,RBUFEA ;IS BIT 5 OF RBUFEA SET?  
BFI 1S ;IF NOT, RETURN  
BFS #BIT1,EABIT ;ELSE SET BIT 1 OF EABIT

2S: RTS PC ;RETURN  
\*\*\*\*\*

```
769
770
771
772
773
774 002276
775 002276 022737 000060 000354
776 002304 001004
777 002306 012737 000020 000354
778 002314 000411
779 002316
780 002316 006237 000354
781 002320 022737 000001 000354
782 002330 001003
783 002332 012737 000060 000354
784 002340
785 002340 000207
786
787
788
789
790
791
792
793
794
795 002342
796 002342 012701 000226
797 002348
798 002348 032711 000001
799 002352 001402
800 002354 000261
801 002356 000401
802 002360
803 002360 000241
804 002362
805 002364 006011
806 002364 022701 000232
807 002370 001403
808 002372 012701 000232
809 002376 000763
810 002400
811 002400 000207
812
813
814
815
816
817
818
819
820 002402
821 002402 012702 004324
822
823 002406 013703 000226
824 002412 012704 004324
```

\*\*\*\*\*  
THIS ROUTINE WILL CAUSE THE REQUEST LEVEL OF ALL DEVICES TO  
BE AT A LOWER LEVEL THAN IN THE PREVIOUS ITERATION---ONLY  
ONE LEVEL IS SET AT ANY ONE TIME EXCEPT FOR AN NPR ,THEN AN INTERRUPT  
LEVEL MUST BE SET FOR HARDWARE CONSIDERATIONS  
\*\*\*\*\*

ROTRQS: CMP #60,RQLVL ;IS REQUEST AT NPR WITH BR7 INTR?  
BNE 1S ;IF NOT, CONTINUE  
MOV #20,RQLVL ;ELSE SET RREQUEST LEVEL TO RR7  
BR 2S ;AND RETURN

1S: ASR RQLVL ;SHIFT REQUEST LEVEL TO NEXT RR  
CMP #1,RQLVL ;IF REQUEST LEVEL DOES NOT = 1  
BNE 2S ;THEN RETURN  
MOV #60,RQLVL ;ELSE RESET REQUEST TO NPR/RR7 INTR

2S: RTS PC ;RETURN  
\*\*\*\*\*

\*\*\*\*\*  
THIS ROUTINE WILL ROTATE THE DATA PATTERNS TO TRY TO EXERCISE  
THE DATA LINES.  
\*\*\*\*\*

ROTPAT: MOV #P2,R1 ;INIT R1 TO ADDR OF P2

1S: BIT #BIT0,(R1) ;IF BIT 0 OF PATTERN IS 0  
BFI 2S ;GO CLEAR C-BIT  
SEC ;ELSE SET C-BIT  
BR 3S ;AND CONTINUE

2S: CLC ;CLEAR C-BIT

3S: ROR (R1) ;ROTATE DATA TO THE RIGHT  
CMP #P4,R1 ;DOES R1 POINT TO LAST PATTERN?  
BNE 4S ;IF IT DOES,EXIT  
MOV #P4,R1 ;ELSE SET R1=LAST PATTERN ADDR  
BR 1S ;AND GO BACK FOR MORE

4S: RTS PC ;RETURN  
\*\*\*\*\*

\*\*\*\*\*  
THIS ROUTINE WILL SET UP THE READ BUFFER WITH ONE OR BOTH DATA  
PATTERNS DEPENDING ON THE NUMBER OF DEVICES AND CLEAR THE WRITE  
BUFFER SO THE FOLLOWING TRANSFERS CAN BE COMPARED CORRECTLY.  
\*\*\*\*\*

RESDAT: MOV #PEEDIN,R2 ;SET R2=1ST ADDR OF READ RUFF  
MOV P2,R3 ;SET R3= VALUE IN P2  
MOV #REEDIN,R4 ;SET R4= ADDR OF READ BUFFER



```

937 002762 052737 000004 000242  BTS      #BIT2,MASK    ;SET BIT2 TO INDICATE DEV 3 INTERR'D
938 002770 000002          RTI          ;RETURN
939 002772 052737 000010 000242  ISR4:    #BIT3,MASK    ;SET BIT3 TO INDICATE DEV 4 INTERR'D
940 002772 000002          RTI          ;RETURN
941 003000 052737 000020 000242  ISR5:    #BIT4,MASK    ;SET BIT4 TO INDICATE DEV 5 INTERR'D
942 003002 000002          RTI          ;RETURN
943 003002 052737 000040 000242  ISR6:    #BIT5,MASK    ;SET BIT5 TO INDICATE DEV 6 INTERR'D
944 003010 000002          RTI          ;RETURN
945 003012 052737 000100 000242  ISR7:    #BIT6,MASK    ;SET BIT6 TO INDICATE DEV 7 INTERR'D
946 003012 000002          RTI          ;RETURN
947 003020 052737 000200 000242  ISR8:    #BIT7,MASK    ;SET BIT7 TO INDICATE DEV 8 INTERR'D
948 003022 000002          RTI          ;RETURN
949 003030 052737 000400 000242  ISR9:    #BIT8,MASK    ;SET BIT8 TO INDICATE DEV 9 INTERR'D
950 003030 000002          RTI          ;RETURN
951 003032 052737 001000 000242  ISR10:   #BIT9,MASK    ;SET BIT9 TO INDICATE DEV 10 INTERR'D
952 003032 000002          RTI          ;RETURN
953 003040 052737 002000 000242  ISR11:   #BIT10,MASK   ;SET BIT10 TO INDICATE DEV 11 INTERR'D
954 003042 000002          RTI          ;RETURN
955 003052 052737 004000 000242  ISR12:   #BIT11,MASK   ;SET BIT11 TO INDICATE DEV 12 INTERR'D
956 003052 000002          RTI          ;RETURN
957 003102 003200          NOSET:  MMSG3   -1
958 003104 003232          ERRSET: MMSG4   -1
959 003106 003232          DIERR:  MMSG1   -1
960 003110 177777          DOERR:  MMSG2   -1
961 003112 003126          NOINTR: MMSG5   -1
962 003114 177777          MMSG1:  .ASCIZ  "%DATI OR DATIP ERROR"
963 003116 003153          MMSG2:  .ASCIZ  "%DATO OR DATOB ERROR"
964 003118 177777          MMSG3:  .ASCIZ  "%DEVICE READY BIT NOT SET"
965 003120 003304          MMSG4:  .ASCIZ  "%DEVICE INTERRUPTED ON ERROR--NOT ON DONE"
966 003122 003304          MMSG5:  .ASCIZ  "%THE FOLLOWING DEVICE(S) DID NOT INTERRUPT:"
967 003124 042045 052101 020111  MMSG5:  .ASCIZ  "%THE FOLLOWING DEVICE(S) DID NOT INTERRUPT:"
968 003126 051117 042040 052101
969 003128 050111 042440 051122
970 003130 051117 000000 000000
971 003132 045045 040504 047524  MMSG5:  .ASCIZ  "%THE FOLLOWING DEVICE(S) DID NOT INTERRUPT:"
972 003134 051117 042040 052101
973 003136 047440 020122 040504
974 003138 047524 020102 051105
975 003140 047522 000122 000122
976 003142 042045 053105 041511  MMSG5:  .ASCIZ  "%THE FOLLOWING DEVICE(S) DID NOT INTERRUPT:"
977 003144 020105 042522 042101
978 003146 020131 044502 020124
979 003148 000124 042523 042523
980 003150 000124 000000 000000
981 003152 042045 053105 041511  MMSG5:  .ASCIZ  "%THE FOLLOWING DEVICE(S) DID NOT INTERRUPT:"
982 003154 020105 042522 042101
983 003156 020131 044502 020124
984 003158 000124 042523 042523
985 003200 042045 053105 041511  MMSG5:  .ASCIZ  "%THE FOLLOWING DEVICE(S) DID NOT INTERRUPT:"
986 003202 020105 042522 042101
987 003204 020131 044502 020124
988 003206 047516 020124 042523
989 003208 000124 000000 000000
990 003210 042045 053105 041511  MMSG5:  .ASCIZ  "%THE FOLLOWING DEVICE(S) DID NOT INTERRUPT:"
991 003212 020105 047111 042524
992 003214 050125 042524 042524
  
```

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993 003254 020104 047117 042440
994 003256 051122 051117 026455
995 003270 047516 020124 047117
996 003276 042040 047117 000105
997 003304 052045 042510 043040  MMSG5:  .ASCIZ  "%THE FOLLOWING DEVICE(S) DID NOT INTERRUPT:"
998 003312 046117 047514 044527
999 003320 043516 042040 053105
1000 003326 041511 024105 024523
1001 003334 042040 042111 047040
1002 003342 052117 044440 052116
1003 003350 051105 052522 052120
1004 003356 022472 000000 000000
1005 003362 003362          .EVEN
1006 003362 000361          #RTOUT:  .RLKW  361
1007 004324 000361          #REDIN:  .RLKW  361
1008
1009 005266          .=.
1010
1011 000001          .FND
  
```





BEAR DEC/X11 SYSTEM EXERCISER MODULE  
X0EABO.P11 12-OCT-78 11:46  
RUN-TIME RATIO: 18/5=3.3  
CORE USED: 7K (13 PAGES)

MACY11 30A(1052) 12-OCT-78 16:18 PAGE 28  
CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0026