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IDENTIFICATION

PRODUCT CODE: AC-E866L-MC
PRODUCT NAME: CXDHALO DH11 16-LNE PROG
PRODUCT DATE: FEB 1979
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT:

DHA IS AN IOMOD THAT EXERCISES UP TO FOUR (CONSECUTIVELY ADDRESSED) DH11 ASYNCHRONOUS INTERFACES. IT USES MAINTENANCE MODE TO TRANSMIT AND RECEIVE A BINARY COUNT PATTERN OUTPUTTED AND RECEIVED IN 64 CHARACTER BURSTS. THE MAJOR PORTION OF THE ERROR CHECKING IS DEFERRED TO LEVEL 0. ALL LINES SELECTED FOR TEST ARE ACTIVATED AND RUN CONCURRENTLY.

2. REQUIREMENTS:

HARDWARE: AT LEAST ONE DH11 INTERFACE

STORAGE:: DHA REQUIRES:

1. DECIMAL WORDS: 1066
2. OCTAL WORDS: 02052
3. OCTAL BYTES: 4124

3. PASS DEFINITION:

ONE PASS OF THE DHA MODULE CONSISTS OF TRANSMITTING AND RECEIVING 425984. CHARACTERS FOR EACH DH11 SELECTED

4. EXECUTION TIME:

VARIABLES WITH BAUD RATE BUT SHOULD TAKE AN AVERAGE OF ONE MINUTE TO COMPLETE ONE PASS WHEN RUNNING ALONE.

5. CONFIGURATION PARAMETERS:

DEFAULT PARAMETERS:

DVA: 1, VCT: 1, BR1: 5, BR2: 5, DVC: 1, SR1: 0

REQUIRED PARAMETERS:

AT CONFIGURATION TIME THE USER MUST SPECIFY:

DVA: ADDRESS OF FIRST DH11 CSR REG.
VCT: VECTOR ADDRESS OF FIRST DH11
DVC: NO OF DH11'S IF GREATER THAN 1
SR1: BIT15 MUST BE SET TO A "1" FOR
8. WORDS BETWEEN ADJACENT VECTORS
(2040 CONFIGURATION)

6. DEVICE OPTION SETUP:

NONE REQUIRED

7. MODULE OPERATION:

START: DETERMINE IF ANY ERRORS ARE SELECTED. DO NOT RUN THE MODULE IF NO DEVICES ARE SELECTED. IF THERE ARE SELECTED DEVICES, INITIALIZE THE BINARY COUNT PATTERN AT 0. CONTINUE PROCESSING.

RESTART: INITIALIZE THE ITERATION COUNTER TO 6656. DETERMINE IF ANY DH11'S ARE SELECTED. LOAD THE INTERRUPT VECTORS TO POINT TO THE JSR LINKING TABLE.

SETUP2: INITIALIZE THE QUEUE POINTERS. CLEAR ALL THE BUFFERS AND QUEUES. CLEAR THE BUFFER ACCESS FLAG (LCKOUT) IN CASE IT WAS STILL SET BY A CONTROL C INTERRUPT OF THE PROGRAM.

ACTIVATE: THIS BLOCK OF CODE GETS THE PHYSICAL ADDRESS OF THE TRANSMITTER TEXT BUFFER. IT INITIALIZES EACH DH11 SELECTED. IT SAVES THE EXTENDED ADDRESS INFORMATION IN THE APPROPRIATE SYSTEM CONTROL REGISTER BITS. EACH OF THE SIXTEEN LINES IS LOADED WITH THE PHYSICAL ADDRESS AND LENGTH OF THE TRANSMITTER BUFFER. ALSO, EIGHT BITS PER CHARACTER IS SELECTED. IF A BAUDRATE IS SELECTED, IT IS CALCULATED AND ASSIGNED. OTHERWISE THE DEFAULT RATE OF 9600 BAUD IS ASSUMED.

INITIAL: THE DATA PATTERN IS LOADED INTO THE TRANSMITTER BUFFER. IT IS A BINARY COUNT PATTERN WHICH, ON SUCCESSIVE ITERATIONS, BEGINS 0, 4, 10, 14, 20, ..., 177774. THE SILO ALARM LEVEL IS PLACED IN THE SILO STATUS REGISTER TRANSMITTER AND ERROR INTERRUPTS ARE ENABLED. ALL THE TRANSMITTERS FOR EACH SELECTED DH11 ARE ENABLED.

TMRSET: TMRCNT. IS USED AS A MULTIPLYING FACTOR TO DETERMINE THE WAITING LENGTH FOR THE WATCHDOG TIMER. IT IS PRESENTLY SET AT 5 TO ALLOW SEVENTY-FIVE SECONDS TO ELAPSE BEFORE TAKING FURTHER ACTION.

TIMER: THIS IS THE WATCHDOG TIMER LOOP. IT IS CONTROLLED BY R4 AND TMRCNT. IF ALL DH11'S SELECTED GENERATED BOTH TRANSMIT AND RECEIVE INTERRUPTS, THE APPROPRIATE BIT IN DONFLG FOR THAT DH11 WILL BE CLEARED. IF THIS DOES NOT OCCUR IN THE GIVEN TIME, THE DEVICE NUMBER OF THE OFFENDING DH11 WILL BE CALCULATED, AND THIS WILL BE REPORTED IN A MODULE MESSAGE. THE

OFFENDING DEVICE IS DROPPED FROM THE EXERCISE. IF NO MORE DH11'S ARE SELECTED, THE MODULE ITSELF IS DROPPED FROM THE RUN. IF MORE REMAIN TO BE EXERCISED, HOWEVER, CONTROL IS TRANSFERRED TO "FINISH."

FINISH: CONTROL COMES HERE IF ALL SELECTED DH11'S WERE SUCCESSFULLY EXERCISED OR IF MORE DH11'S REMAIN AFTER ONE WAS HUNG. THE ITERATION COUNT IS DECREASED. IF THE COUNT DOES NOT REACH ZERO, CONTROL IS PASSED TO SETUP2 AND THE MODULE IS RUN AGAIN. WHEN THE COUNT REACHES ZERO, AN END OF PASS IS SIGNALLED.

XMTINT: THIS SEGMENT SAVES A POINTER TO THE CSR ADDRESS OF THE INTERRUPTING DH11 IN A FIRST-IN, FIRST-OUT, WRAPAROUND BUFFER. THE TRANSMITTER QUEUE. THE ENTRY POINTER IS THEN UPDATED TO POINT TO THE NEXT ENTRY IN THE QUEUE.

XMTSRV: THIS BLOCK FETCHES A POINTER TO A CSR ADDRESS FROM THE TRANSMITTER QUEUE, AND THE QUEUE IS UPDATED TO THE NEXT ENTRY. THE CSR IS TESTED TO DETERMINE WHAT KIND OF INTERRUPT OCCURRED. FALSE INTERRUPT AND NON-EXISTENT MEMORY INTERRUPT ARE REPORTED. IF EVERYTHING IS CORRECT, THE RECEIVER FOR THIS DH11 IS ENABLED.

RCVINT: THIS SEGMENT SAVES A POINTER TO THE CSR ADDRESS OF THE INTERRUPTING DH11 IN THE RECEIVER QUEUE. IT UPDATES THE QUEUE ENTRY AND RESTORES THE VALUE OF R5 WHICH WAS SAVED BY THE JSR INSTRUCTION FROM THE LINKAGE TABLE.

RCVSRV: THE FIRST TASK IS TO PREVENT VOLATILE REGISTER INFORMATION FROM BEING DESTROYED. THIS IS DONE BY TESTING A SEMAPHORE, "LCKOUT." IF IT IS SET, CONTROL IS RETURNED TO THE MONITJR TO WAIT FOR A WHILE. IF IT IS CLEAR, ACCESS IS PERMITTED. THE FLAG IS SET TO DENY OTHER ACCESSES TO THIS DEFERRED ROUTINE. A CSR ADDRESS IS OBTAINED FROM THE QUEUE, AND THE QUEUE ENTRY IS UPDATED. THE RECEIVER INTERRUPT AND INTERRUPT ENABLE ARE CLEARED. THE REGISTERS ARE SET UP TO RETRIEVE AS QUICKLY AS POSSIBLE THE DATA FROM THE DH11 SILO. EACH FETCH IS CHECKED TO SEE IF THE INFORMATION IS VALID. IF IT IS NOT, THE REGISTERS ARE SAVED AND A BREAK LOOP IS USED TO ALLOW MORE TIME FOR VALID INFORMATION TO BECOME AVAILABLE. IF AFTER THE ALLOTTED TIME ALL THE CHARACTERS ARE STILL NOT RECEIVED, AN ERROR MESSAGE IS REPORTED. IN THE MESSAGE, THE NUMBER FOLLOWING STATC IS THE OCTAL NUMBER OF CHARACTERS MISSING.

CKDATA: THIS SEGMENT INITIALIZES THE LINE CHECK BUFFER (LNCKBF) TO THE FIRST DATUM THAT WAS TRANSMITTED. THE DEVICE NUMBER IS SAVED FOR LATER USAGE. THE RECEIVED INFORMATION IS CHECKED FOR VALIDITY AND TRANSMISSION ERRORS. ERRORS ARE HANDLED BY THE

"STATERR" (STATUS ERROR) AND "DERRJP" (DATA ERROR) ROUTINES.

RCVDONE: THIS BLOCK CLEARS THE ACCESS SEMAPHORE TO ALLOW OTHER DEVICES TO USE THE LINE CHECK BUFFER. IT THEN BUILDS A ONE BIT MASK USING R0 AND THE CARRY BIT TO DELETE THE APPROPRIATE BIT IN THE WATCHDOG TIMER FLAG (DONFLG). WHEN THIS IS DONE, PROCESSING CONTROL IS RETURNED TO THE MONITOR.

SUBROUTINES

VCTLOAD: THIS ROUTINE IS CALLED IN "SETUP1". IT IS USED TO LOAD THE ADDRESS OF THE LINKING INSTRUCTION FOR INTERRUPT SERVICING INTO THE CORRESPONDING VECTOR SPACE. IT ALSO LOADS THE PRIORITY LEVEL AND THE DEVICE ADDRESS. THE LATTER IS LOADED INTO THE APPROPRIATE JSR TABLE ENTRY.

SAVREG: THIS ROUTINE SAVES THE FIVE VOLATILE INFORMATION REGISTERS IN A FIRST-IN, FIRST-OUT WRAPAROUND BUFFER, THE ERROR QUEUE.

GOTREG: THIS ROUTINE RETRIEVES THOSE SAME REGISTERS.

BAUDRTE: THIS ROUTINE CALCULATES THE BAUD RATE, ASSIGNS IT, AND SELECTS 8 BITS/CHARACTER COMMUNICATION MODE. IF SR1=0, THE DEFAULT RATE OF 9600. BAUD IS ASSIGNED. THE BAUD RATE SELECTED IS DETERMINED BY THE LEAST SIGNIFICANT (RIGHTMOST) SET BIT IN SR1.

STATERR: THIS ROUTINE DETERMINES WHETHER AN ERROR INDICATED IN THE RECEIVED CHARACTER INFORMATION WAS AN OVERRUN ERROR, A FRAMING ERROR, OR A PARITY ERROR. THE DEVICE NUMBER OF THE ERRING DEVICE IS REPORTED AS STATC. CSRA WILL BE CLEAR.

DERROR: THIS ROUTINE REPORTS A DATA ERROR.

8. OPERATOR OPTIONS

MODULE LOCATION DVID1 MAY BE MODIFIED (MOD. CMMD) TO EXERCISE ANY COMBINATION OF DH11'S.

MODULE LOCATION SR1 MAY BE MODIFIED TO SELECT A DIFFERENT BAUD RATE. THE FOLLOWING TABLE SHOULD BE USED:

FOR THE BAUD RATE	SR1=
9600	0
EXT. B	1
EXT. A	2

9600	4
4800	10
2400	20
1800	40
1200	100
600	200
300	400
200	1000
150	2000
134.5	4000
110	10000
75	20000
50	40000
2040 CONFIGURATION	100000 (8. WORDS BETWEEN VECTORS)

BIT15 MAY BE SET IN CONJUNCTION WITH ONE OF BITS
00 THRU 14 TO INDICATE SPECIFIC BAUD RATE OTHER THAN
DEFAULT OF 9600 BAUD. (SR<14:00> = 0)

THE DEFAULT RATE IS 9600 BAUD(SR1=0).

9. NON-STANDARD PRINTOUTS:

WHEN A STATUS ERROR IS DETECTED, DHA USES THE ERROR
CALL TO REPORT IT. THE LINE NUMBER IS
REPORTED IN STATC.

ALL OTHER PRINTOUT IS STANDARD.

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302 .LIST SFQ,LOC,BIN
303 LMOD <DHAL>1,1,5,5,17000,25
304 000000- 000000- 140000,DHAL,1,1,5,5,17000,25
305 .MODULE DHAL DEC/X11 SYSTEM EXERCISER MODULE
306 ;
307 .DDXCDW VERSION 6 23-MAY-78
308 .LIST BIN
*****
309 BEGIN:
310 MODNAM: .ASCII /DHAL / ;MODULE NAME
311 XFLAG: .RYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
312 ADDR: 1+0 ;ICE ADDR
313 VECTOR: 1+0 ;1ST DEVICE VECTOR.
314 BR1: .RYTE PRTV5+0 ;1ST BR LEVEL.
315 BR2: .RYTE PRTV5+0 ;2ND BR LEVEL.
316 DVID1: +1 ;DEVICE INDICATOR 1.
317 SR1: OPEN ;SWITCH REGISTER 1
318 SR2: OPEN ;SWITCH REGISTER 2
319 SR3: OPEN ;SWITCH REGISTER 3
320 SR4: OPEN ;SWITCH REGISTER 4
*****
321 STAT: 140000 ;STATUS WORD
322 INIT: START ;MODULE START ADDR.
323 PPOINT: MODSP ;MODULE STACK POINTER.
324 PASCNT: 0 ;PASS COUNTER.
325 ICOUNT: 17000 ;# OF ITERATIONS PER PASS=17000
326 ICOUNT: 0 ;LOC TO COUNT ITERATIONS
327 SPCCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
328 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
329 SOPPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
330 HRUPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
331 SVSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
332 RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
333 CONFIG:
334 RES1: 0 ;RESERVED FOR MONITOR USE
335 RES2: 0 ;RESERVED FOR MONITOR USE
336 SVR0: OPEN ;LOC TO SAVE R0.
337 SVR1: OPEN ;LOC TO SAVE R1.
338 SVR2: OPEN ;LOC TO SAVE R2.
339 SVR3: OPEN ;LOC TO SAVE R3.
340 SVR4: OPEN ;LOC TO SAVE R4.
341 SVR5: OPEN ;LOC TO SAVE R5.
342 SVR6: OPEN ;LOC TO SAVE R6.
343 CSRA: OPEN ;ADDR OF CURRENT CSR.
344 SBADR: ;ADDR OF GOOD DATA, OR
345 ACSR: OPEN ;CONTENTS OF CSR
346 WASADR: ;ADDR OF BAD DATA, OR
347 ASTAT: OPEN ;STATUS REG CONTENTS.
348 ERR1VP: ;TYPE OF ERROR
349 ASR: OPEN ;EXPECTED DATA.
350 AWAS: OPEN ;ACTUAL DATA.
351 RSRRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
352 WDRT: OPEN ;WORDS TO MEMORY PER ITERATION
353 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
354 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
355 IDNUM: 25 ;MODULE IDENTIFICATION NUMBER=25
356 .REPT SPSIZ ;MODULE STACK STARTS HERE.
357
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358 .NLIST 0
359 .WORD 0
360 .LIST
361 .ENDR
362 MODSP:
363 *****
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364 ;START ROUTINE. DETERMINE IF ANY DH'S ARE SELECTED
365 ;IF SO, BEGIN MODULE PROCESSING... IF NOT, DROP THE MODULE FROM THE RUN
366 START: CLR B DATA ;INITIALIZE THE DATA PATTERN WORD
367 MOV DV101,SELECT ;COPY THE DEVICE SELECTION PARAMETER. ARE
368 ;ANY DEVICES SELECTED?
369 BNE RERSTRT ;IF SO, BEGIN PROCESSING. IF NOT, DROP THE MODULE
370 DROP: ENDS,REGIN ;
371 ;
372 ;INITIALIZE THE NUMBER OF PASSES TO BE MADE. SET UP THE DH11 INTERRUPT
373 ;VECTORS TO INTERRUPT IN THE SUBROUTINE LINKING TABLE. CALL SUBROUTINE
374 ;VCTLOAD FOR THIS PURPOSE.
375
376
377
378 000244* 005000 RERSTRT: CLR R0 ;INIT POINTER
379 000246* 016701 MOV SRI,R0 ;GET BAUD RATE
380 000252* 042701 BIC #100000,R1 ;THROW 2040 CNF BIT AWAY
381 000256* 001405 BEQ Z5,R0 ;LEAVE IF SRI IS 0
382 000260* 062700 1S: ADD #2,R0 ;ELSE BUMP POINTER
383 000264* 006001 ROR R1 ;LOOK FOR THE SRI BIT
384 000266* 103401 BCS Z5 ;LEAVE IF WE FOUND IT
385 000270* 000773 BR Z5 ;ELSE DO IT AGAIN
386 000272* 016067 2S: MOV CNTTBL(R0),ICONT ;SET ITERATION COUNT
387 000300* 042767 BIC #177760,SELECT ;REMOVE UNWANTED BITS FROM THE DEVICE SELECTION
388 ;PARAMETER
389 000306* 016701 SETUP1: MOV SELECT,R1 ;COPY THE DEVICE SELECTION PARAMETER INTO R1
390 000312* 001752 BEQ DROP ;IF NO DEVICES SELECTED (ALL FLAGS CLEAR) DROP THE
391 ;MODULE
392 000314* 016700 MOV VECTOR,R0 ;LOAD THE VECTOR ADDRESS IN R0
393 000320* 016702 MOV ADDR,R2 ;LOAD R2 WITH ADDRESS OF FIRST DH11
394 000324* 012703 MOV #LNKTAB,R3 ;POINT R3 TO THE BEGINNING OF JSR LINKING TABLE
395 000328* 006201 ASR Z5 ;ISOLATE A SELECTION FLAG IN THE CARRY BIT
396 000332* 103415 BCS Z5 ;IF THE FLAG IS SET, GO SET UP THE VECTORS
397 000334* 001430 SETUP2: BEQ ;IF NO FLAGS ARE LEFT, GO SET UP THE BUFFERS
398 000336* 062700 ADD #10,R0 ;IF MORE FLAGS ARE SET, ADJUST POINTERS. THE
399 ;VECTOR POINTER...
400 000342* 005767 TST SRI ;EIGHT WORDS BETWEEN VECTORS ?
401 000346* 100002 BPL Z5 ;BR IF NOT
402 000350* 062700 ADD #10,R0 ;FIX VECTOR POINTER
403 000354* 062703 4S: ADD #20,R3 ;THE ADDRESS POINTER...
404 000360* 062702 3S: ADD #20,R2 ;AND THE LINKAGE TABLE POINTER
405 000364* 007761 BR Z5 ;GO SET UP THE NEXT DH11 ADDRESSES
406 000366* 004567 2S: JSR R5,VCTLOAD ;CALL THE VECTOR SETUP ROUTINE FOR RECEIVER
407 000372* 000013 BR Z5 ;VECTOR, PASSING THE RECEIVER BR LEVEL AS THE
408 ;ARGUMENT
409 000374* 004567 JSR R5,VCTLOAD ;SET UP THE TRANSMITTER VECTOR PASSING THE
410 000400* 000012 BR Z5 ;TRANSMITTER BR LEVEL AS THE ARGUMENT
411 000402* 005767 TST SRI ;EIGHT WORDS BETWEEN VECTORS ?
412 000406* 100002 BPL Z5 ;BR IF NOT
413 000410* 062700 5S: ADD #10,R0 ;FIX VCTR POINTER
414 000414* 000761 BR Z5 ;GO SETUP THE NEXT DH11
415
416 ;THIS BLOCK RESETS ALL THE QUEUE POINTERS, CLEARS THE TRANSMITTER TEXT BUFFER, THE
417 ;RECEIVER BUFFERS, AND THE QUEUES. THIS IS THE BEGINNING OF THE ITERATIVE PART OF THE
418 ;PROGRAM.
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420 000416* 012767 SETUP2: MOV #XMTQUE,XMTQPI ;POINT THE TRANSMIT QUEUE ENTRY (IN) POINTER
421 000422* 012767 MOV #XMTQUE,XMTQPO ;TO THE BEGINNING OF THE QUEUE
422 000424* 012767 003360 MOV #RCVQUE,RCVQPI ;POINT TO THE BEGINNING OF RETRIEVAL (OUT)
423 000432* 012767 002662* 003354 MOV #RCVQUE,RCVQPO ;POINT TO THE BEGINNING OF THE QUEUE
424 000440* 012767 002662* 003350 MOV #ERRQUE,ERRQPI ;SET UP THE RECEIVER QUEUE POINTERS
425 000446* 012767 002703* 003344 MOV #ERRQUE,ERRQPO ;
426 000454* 012767 002703* 003340 MOV #XMTQUE,R3 ;SETUP THE ERROR QUEUE POINTERS
427 000462* 012703 002642* MOV #294,R4 ;POINT R3 TO THE BEGINNING OF THE QUEUE AREA
428 000466* 012704 000446 MOV #294,R4 ;USING R4 AS A COUNTER CLEAR
429 000472* 005023 1S: CLR (R3)+ ;TRANSMITTER, RECEIVERS, AND RROR QUEUES....
430 000474* 005304 DEC R4 ;TRANSMITTER TEXT BUFFER...
431 000476* 000761 BNE Z5 ;RECEIVER SIB BUFFERS
432 000500* 105067 CLR R LCKOHT ;CLEAR THE BUFFER ACCESS FLAG
433
434 ;THIS BLOCK SETS THE WATCHDOG TIMER FLAG, INITIALIZES THE DH11 CONDITIONS, SETS THE CHAR
435 ;AND BAUD RATE, AND SETS THE EXTENDED ADDRESSING BITS FOR THE ACTIVE DH11S
436
437
438 000504* 016700 ACTVATE: MOV SELECT,R0 ;COPY THE DEVICE SELECTION PARAMETER
439 000510* 110067 MOV R0,DONFLG ;SET THE WATCHDOG TIMER DEVICE COMPLETION FLAG
440 000514* 016701 MOV ADDR,R1 ;LOAD THE ADDRESS OF THE FIRST DH11
441 000520* 012767 002764* 003304 MOV #XMTBUF,VA ;GET THE PHYSICAL ADDRESS OF THE TRANSMITTER
442 ;BUFFER
443 000526* 104415 000000* 004032* GETPAS,BEGIN, VA ;GET PHYSICAL ADDRESS FROM 16-BIT VA
444 000534* 006200 1S: ASP R0 ;ISOLATE A SELECTION FLAG IN THE CARRY BIT
445 000536* 103404 BCS Z5 ;IF SELECTED, GO SET UP THE DEVICE REGISTERS
446 000540* 001431 BEQ ;IF NO MORE SELECTED, GO SET UP THE DATA
447 000542* 062701 000020 2S: ADD #20,R1 ;POINT R1 TO THE NEXT DH11
448 000546* 000772 BR Z5 ;PROCESS NEXT DH11
449 000550* 012711 004000 3S: MOV #RIT11,(R1) ;MASTER CLEAR THIS DH11 AND SELECT LINE 0
450
451 000554* 052711 001000 BIS #RIT9,(R1) ;ENABLE MAINTENANCE MODE OPERATION
452 000560* 042711 000060 BIC #60,(R1) ;ERASE INVALID INFORMATION FROM THE EA BITS IN THE CSR
453 000564* 056711 003246 EA,(R1) ;SET THE EXTENDED ADDRESS BITS OF THE TEXT
454 000570* 012702 000020 MOV #20,R2 ;USE R2 TO COUNT 16 LINES BEING SET UP
455 000574* 016761 003234 000006 4S: MOV #PA,6(R1) ;LOAD THE PHYSICAL ADDRESS OF THE TRANSMITTER
456 ;BUFFER INTO THE CURRENT ADDRESS REGISTER
457 000602* 012761 177774 000010 MOV #-4,10(R1) ;LOAD THE BYTE COUNT REGISTER WITH THE NUMBER
458 ;OF CHARACTERS TO BE TRANSMITTED
459 000610* 004767 JSR PC,BAURTE ;GO CALCULATE THE BAUD RATE TO BE USED
460 000614* 005302 DEC R2 ;REDUCE THE COUNT. ARE ALL 16 LINES SET?
461 000616* 001751 BFC Z5 ;IF YES, GO PROCESS NEXT DH11
462 000620* 005211 INC (R1) ;IF NO, INCREMENT THE LINE SELECTION PARAMETER
463 000622* 000764 BR Z5 ;GO SET UP THE NEXT LINE
464
465 ;THIS BLOCK SETS UP THE TRANSMITTER TEXT WITH THE TEST DATA. IT INSURES THAT THE
466 ;EXTENDED ADDRESS BITS ARE SET, THEN STARTS EACH RECEIVER AND EACH TRANSMITTER SELECTED
467
468 000624* 012701 002764* INITIAL:MOV #XMTBUF,R1 ;POINT R1 TO THE START OF THE BUFFER AREA
469 000630* 116767 003212* 003204 MOV R0,RGNDATA ;COPY THE FIRST DATUM BEING TRANSMITTED IN THIS
470 000636* 116767 003204 003177 MOV R0,RGNDATA+1 ;GROUP RGNDATA WILL BE USED TO SET
471 ;UP LNCB (THE LINE CHECK BUFFER) IN THE CKDATA ROUTINE
472 000644* 012702 000004 MOV #4,R2 ;USE R2 TO COUNT THE LOOP ITERATIONS
473 000650* 116721 003172 1S: MOV DATA,(R1)+ ;PUT A CHARACTER IN THE BUFFER, BUILDING THEM
474 000654* 105267 003166 INCR DATA ;IN DATA, WHICH WILL CREATE A BINARY INCREMENT PATTERN.
475 000660* 005302 DEC R2 ;REDUCE COUNT. HAVE ALL CHARACTERS BEEN MADE?

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476 000662 001372 BNE 1S ;IF NO, GO LOAD THE NEXT CHARACTER
477 000664 016700 003134 MOV R0, R1 ;COPY THE DEVICE SELECTION PARAMETER
478 000670 016701 177117 MOV ADDR, R1 ;LOAD THE ADDRESS OF THE FIRST DH11
479 000674 006200 2S: ASR R0 ;ISOLATE A SELECTION FLAG IN THE CARRY BIT
480 000676 103404 BCS 4S ;IF SELECTED, GO START THE DEVICE
481 000700 001414 BEQ TMRSET ;IF NO MORE SELECTED, GO START WATCHDOG TIMER
482 000702 052701 000020 ADD R20, R1 ;POINT R1 TO THE NEXT DH11
483 000706 000772 BR 2S ;GO PROCESS THE NEXT DH11
484 000710 012761 000040 000015 4S: MOV #32, 16(R1) ;LOAD THE SILO ALARM LEVEL INTO THE SILO STATUS REGISTER
485 BIS #BIT13, (R1) ;ENABLE TRANSMITTER AND NON-EXISTENT MEMORY INTERRUPTS
486 000716 052711 020000 MOV #-1, 12(R1) ;ENABLE THE TRANSMITTERS FOR ALL 16. LINES
487 BR 3S ;PROCESS THE NEXT DH11
488
489
490
491 ;THIS IS THE WATCHDOG TIMER. WHEN A DEVICE HAS SUCCESSFULLY TRANSMITTED AND
492 ;RECEIVED ALL DATA, IT CLEARS ITS CORRESPONDING BIT IN DONFLG. IF THIS
493 ;DOES NOT OCCUR, AN ERROR MESSAGE IS REPORTED. STATC IN THE
494 ;ERROR MESSAGE TELLS WHICH DEVICE WAS SLOW OR HUNG. CSRA WILL BE ZERO.
495 ;A MODULE MESSAGE IS REPORTED. IF MORE DEVICES ARE TO BE PROCESSED, THIS
496 ;ITERATION IS CONSIDERED COMPLETE. IF NO DEVICES REMAIN TO BE PROCESSED,
497 ;THE MODULE IS DROPPED.
498
499 000732 012767 000005 003070 TMRSET: MOV #5, TMRCNT ;SET THE TIMER COUNT FOR THE BREAK LOOP
500 000740 005004 TIMER: CLR R4 ;USING R4, RETURN TO MONITOR 65535 TIMES
501 1S: BREAKS, BEGIN ;TEMPORARY RETURN TO MONITOR...
502 000742 104407 000000 BREAKS, BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
503 000745 104407 000000 TSTR DONFLG ;IF DONFLG IS CLEAR, EACH SELECTED DEVICE WAS
504 000752 105767 003071 ;SUCCESSFUL
505 BEQ FINISH ;IF SO, PERFORM ENDPASS PROCESSING
506 000760 005704 DEC R4 ;IF NO, REDUCE COUNT AND BREAK AGAIN
507 000762 001367 RNE 1S ;BREAK IF COUNT NOT EXCEEDED
508 000764 005367 003040 DEC TMRCNT ;REDUCE THE OVERALL TIME. IS IT EXCEEDED?
509 000770 001363 BNE TIMER ;IF NO, START ANOTHER BREAK LOOP
510
511 000772 116703 003051 MOVB DONFLG, R3 ;IF TIMEOUT OCCURRED, SAVE THE REMAINING FLAG
512 ;IN R3
513 000776 040367 003022 BIC R3, SELECT ;CLEAR THE SELECTION FLAG FOR THIS DEVICE
514 001002 005067 177077 CLR CSRA ;SETUP TO REPORT CSR
515 001006 006003 2S: ROR R3 ;DETERMINE WHICH DEVICE WAS READ FOR REPORTING
516 ;PURPOSES
517 BCS 3S ;IF THIS IS THE DEVICE, R4 CONTAINS THE CORRECT
518 001012 103405 ;LINE NUMBER... GO REPORT IT
519 001012 005204 INC R4 ;IF NOT, INCREMENT R4, WHICH WAS INITIALLY 0
520 ;FROM THE PREVIOUS LOOP
521 001014 052767 000020 177056 ADD #20, CSRA ;ADD DEVICE OFFSET FOR CORRECT DH11
522 001022 000771 ;GO SEE IF THIS IS THE DEVICE
523 001024 010467 003022 3S: MOV R4, NUMBA1 ;THE DEVICE NUMBER WILL BE REPORTED AS STATC
524 ;*****
525 ;CONVERT NUMBA1 TO ASCII AND
526 ;STORE AT M2
527 OTOAS, BEGIN, NUMBA1, M2
528 001030 104420 000000 004052*
529 001036 004067*
530
531 001040 004767 000772 JSR PC, SAVREG ;SAVE THE REGISTERS BEFORE LEAVING THE MODULE
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532 001044 066767 176736 177026 ADD ADDR, CSRA ;ADD BASE ADDRESS TO OFFSET FOR ERROR CALL
533 001052 005067 177030 CLR ERRPT ;UNKNOWN
534 ;*****
535 001056 104405 000000 000000 ;*****
536 ;*****
537 001064 104403 000000 004054* MSGNS, BEGIN, HUNG ;ASCII MESSAGE CALL WITH COMMON HEADER
538 001072 004767 000776 JSR PC, GETREG ;RESTORE THE PREVIOUS REGISTER VALUES
539 001076 005767 002722 TST SELECT ;ARE THERE DEVICES LEFT ACTIVE?
540 001102 001002 BNE FINISH ;IF YES, REDUCE COUNT AND CONTINUE
541 ;IF NO, DROP THE MODULE
542 001104 104410 000000* ENDS, BEGIN
543
544 ;THIS BLOCK REDUCES THE ITERATION COUNT AND GOES TO SETUP2 IF THE COUNT IS NOT EXCEEDED
545
546 001110* FINISH:
547 001110* 104413 000000* ENDTIS, BEGIN ;SIGNAL END OF ITERATION.
548 001114* 000167 177276 JMP SETUP2 ;MONITOR SHALL TEST END OF PASS
549
550 ;TRANSMITTER INTERRUPT SERVICE ROUTINE
551 ;THIS ROUTINE STORES THE ADDRESS OF THE CSR POINTER (OFFSET IN THE JSR LINKAGE
552 ;TABLE) IN THE TRANSMITTER QUEUE. IT DETERMINES THIS A NON-EXISTENT MEMORY ERROR
553 ;OR A DATA INTERRUPT. IF IT IS THE FORMER, IT IS SETUP FOR REPORTING. IF IT
554 ;IS THE LATTER, FURTHER SERVICE IS DEFERRED TO PRIORITY BY A PROGRAM
555 ;INTERRUPT REQUEST. THE ROUTINE THEN ENABLES INTERRUPTS FOR THE CORRESPONDING RECEIVER
556 ;AND RETURNS CONTROL TO THE MONITOR.
557
558 001120 010577 002664 XMTINT: MOV R5, XMTQPI ;LOAD THE OFFSET TO THE CSR INTO TRANSMITTER QUEUE
559 001124 062767 000002 002656 ADD #2, XMTQPI ;UPDATE THE QUEUE ENTRY POINTER
560 001132 022767 002660 002650 CMP #XMTQOE+16, XMTQPI ;HAS THE QUEUE BOUNDARY BEEN EXCEEDED?
561 BHS 1S ;IF NO, CONTINUE PROCESSING
562 001140 013003 MOV #XMTQOE, XMTQPI ;IF YES, RESET THE POINTER TO QUEUE BEGINNING
563 001142 012767 002642 002640 1S: MOV (SP)+, R5 ;RESTORE THE PREVIOUS R5 VALUE
564 001150 012605 MOV R0, -(SP)
565 001152 010048 MOV R1, -(SP)
566 001154 010146 MOV R2, -(SP)
567 001156 010246 MOV R3, -(SP)
568 001160 010348 MOV R4, -(SP)
569 001162 010448 MOV #XMTQPO, R1 ;FETCH THE OFFSET FROM THE QUEUE
570 001164 017701 002622 ADD #2, XMTQPO ;UPDATE THE QUEUE RETRIEVAL POINTER
571 001170 062767 000002 002614 CMP #XMTQOE+16, XMTQPO ;HAS THE QUEUE BOUNDARY BEEN EXCEEDED?
572 001176 022767 002660 002605 BHS 1S ;IF NO, CONTINUE PROCESSING
573 001204 103003 MOV #XMTQOE, XMTQPO ;IF YES, RESET POINTER TO BEGINNING OF THE QUEUE
574 001206 012767 002642 002576 1S: MOV (R1), R0 ;LOAD CSR ADDRESS INTO R0
575 001214 011100 TST (R0) ;IS THIS A VALID INTERRUPT?
576 001216 005710 BMI 3S ;IF YES, GO ENABLE RECEIVER INTERRUPT
577 001220 100467 3S: BIC #BIT10, (R0) ;IF NO, DETERMINE THE TYPE OF ERROR
578 001222 032710 BIT 2S ;IF NON-EXISTENT MEMORY LEVEL 0 BY A PROGRAM
579 001226 001032 BNE 2S ;THAT TYPE OF ERROR. OTHERWISE IT IS A FALSE INTERRUPT
580
581 001230 010067 176644 MOV R0, CSRA ;LOAD THE DEVICE ADDRESS
582 001234 011067 176642 MOV (R0), ACSR ;SAVE THE CSR CONTENTS
583 001240 004767 000577 JSR PC, SAVREG ;SAVE THE REGISTERS BEFORE ERROR MESSAGE
584 001244 012604 MOV (SP)+, R4
585 001246 012603 MOV (SP)+, R3
586 001250 012602 MOV (SP)+, R2
587 001252 012601 MOV (SP)+, R1
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588 001254* 012600          MOV      (SP)+,R0
589 001256* 000004 000000* 001264* PIRQS,BEGIN,10$          ; QUEUE UP TO CONTINUE AT 10$ AND RTI
590 001264* 012767 000011 176614 10$: MOV      #11,ERRTYP      ;ILLEGAL INTERRUPT
591 001272* 104405 000000* 000000 HRDRS,BEGIN,NULL      ;FALSE INTERRUPT REQUEST FROM TRANSMITTER
592 001300* 004767 000570          JSR      PC,GETREG      ;RESTORE THE REGISTERS
593 001304* 042710 120000          BIC      #120000,(R0)    ;DELETE THE FALSE INTERRUPT AND THE INTERRUPT
594 001310* 104400 000000*          EXITS,BEGIN            ;ENABLE
595 001314* 010067 176560          MOV      R0,CSRA        ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
596 001320* 011067 176556          MOV      R0,ACSR        ;LOAD THE DEVICE ADDRESS
597 001324* 004767 000506          JSR      PC,SAVREG      ;SAVE THE CSR CONTENTS
598 001330* 012604          PC,SAVREG            ;SAVE THE REGISTER VALUE BEFORE ERROR MESS.
599 001332* 012603          MOV      (SP)+,R4
600 001334* 012602          MOV      (SP)+,R3
601 001336* 012601          MOV      (SP)+,R2
602 001340* 012600          MOV      (SP)+,R1
603 001342* 000004 000000* 001350* PIRQS,BEGIN,11$          ; QUEUE UP TO CONTINUE AT 11$ AND RTI
604 001350* 012767 000010 176530 11$: MOV      #10,ERRTYP      ;BAD ADDRESS
605 001356* 104405 000000* 000000 HRDRS,BEGIN,NULL      ;NON-EXISTENT MEMORY ADDRESSING FAILURE
606 001364* 004767 000504          JSR      PC,GETREG      ;RESTORE THE REGISTERS
607 001370* 042710 120000          BIC      #120000,(R0)    ;CLEAR INTERRUPT ENABLE AND INTERRUPT BITS
608 001374* 104400 000000*          EXITS,BEGIN            ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
609 001400* 052710 000100          BIS      #BIT6,(R0)     ;ENABLE RECEIVER INTERRUPTS
610 001404* 012604          MOV      (SP)+,R4
611 001406* 012603          MOV      (SP)+,R3
612 001408* 012602          MOV      (SP)+,R2
613 001410* 012601          MOV      (SP)+,R1
614 001412* 012600          MOV      (SP)+,R0
615 001414* 000002          RTI
616
617 ;RECEIVER INTERRUPT SERVICE ROUTINE
618 ;THIS ROUTINE STORES A POINTER TO THE CSR ADDRESS OF THE INTERRUPTING DEVICE IN THE
619 ;RECEIVER QUEUE. SERVICE IS DEFERRED TO PRIORITY LEVEL 0. AT DEFERRED SERVICE (RCVSRV),
620 ;THE OFFSET IS FETCHED FROM THE QUEUE. THE INTERRUPT AND INTERRUPT ENABLE BITS ARE
621 ;CLEARED AND THE SILO IS EMPTIED INTO THE SOFTWARE BUFFER. IF ALL CHARACTERS
622 ;WERE NOT RECEIVED, THIS IS REPORTED. CHECK THE BUFFER AND REPORT DATA ERRORS. IF
623 ;ANOTHER RECEIVER IS ALREADY USING THE LINE CHECK BUFFER (LNCKBF), SUBSEQUENT
624 ;RECEIVERS WILL NOT BE PERMITTED ACCESS UNTIL THE FIRST IS COMPLETE. IF THERE
625 ;ARE ANY CHARACTER ERRORS REPORTED ALL DATA ARE CHECKED. IT RELEASES
626 ;THE LINE CHECK BUFFER AND CLEAR THE CORRESPONDING BIT IN DONFLG.
627
628 RCVINT: MOV      R5,RCVQPI      ;STORE THE OFFSET IN THE RETRIEVAL QUEUE
629          ADD      #2,RCVQPI    ;UPDATE THE QUEUE ENTRY POINTER
630          CMP      RCVQVE+16,RCVQPI ;HAS THE QUEUE BOUNDARY BEEN EXCEEDED?
631          BHS     IS          ;IF NOT, CONTINUE PROCESSING
632          MOV      #RCVQVE,RCVQPI ;RESET THE POINTER TO QUEUE BEGINNING
633          MOV      (SP)+,R5     ;RESTORE THE PREVIOUS VALUE OF R5
634
635 1$:

```

```

644 001452* 000004 000000* 001460* PIRQS,BEGIN,RCVSRV      ; QUEUE UP TO CONTINUE AT RCVSRV AND RTI
645
646 RCVSRV: TSTB     LCKOUT          ;TEST THE BUFFER LOCK FLAG. IS ACCESS PERMITTED?
647          BCS     #1,RCVQPI ;IF YES, GO SET THE LOCK AND CHECK THE DATA
648          BREAKS,BEGIN    ;TEMPORARY RETURN TO MONITOR...
649          BREAKS,BEGIN    ;THEN CONTINUE AT NEXT INSTRUCTION.
650          BR      RCVSRV   ;TRY TO ACCESS AGAIN
651          DECB     LCKOUT   ;DENY OTHER ACCESSES TO THE BUFFER
652          MOV      R0,RCVQD,R0 ;FETCH THE OFFSET FROM THE QUEUE
653          ADD      #RCVQD,R0 ;UPDATE THE QUEUE RETRIEVAL POINTER
654          CMP      RCVQVE+16,RCVQD ;HAS THE QUEUE BOUNDARY BEEN EXCEEDED?
655          BHS     IS          ;IF NOT, CONTINUE PROCESSING
656          MOV      #RCVQVE,RCVQD ;RESET THE POINTER TO THE QUEUE BEGINNING
657          MOV      #R4,R3    ;LOAD THE CSR ADDRESS INTO R1
658          MOV      #300,(R1) ;DISABLE RECEIVER INTERRUPT AND INTERRUPT ENABLE
659          BIC      #300,(R1) ;AND POINT R1 TO THE NEXT RECEIVED CHAR. REGISTER
660
661          MOV      (R0),R2    ;LOAD THE SOFTWARE SILO BUFFER ADDRESS
662          MOV      #77000,RCVTMR ;SET THE RECEIVER BREAK LOOP TIMER
663          MOV      #R1,(R2)+ ;SET THE COUNT FOR STORING THE DATA
664          MOV      #R1,(R2)+ ;STORE A WORD IN THE SOFTWARE SILO
665          DEC      R3        ;IF THE DATA WASN'T VALID, GO ALLOW A LITTLE TIME FOR IT
666          BNE     R3        ;IF IT WAS VALID, REDUCE THE COUNT.
667          BR      R3        ;IF NOT ZERO, GO STORE THE NEXT WORD
668          CKNDA    R2        ;IF ALL CHARACTERS RECEIVED, GO CHECK THEM
669          JSR      PC,SAVREG ;RESET R2 TO THE PROPER BUFFER LOCATION
670          JSR      PC,SAVREG ;SAVE THE REGISTER VALUES
671          BREAKS,BEGIN    ;TEMPORARY RETURN TO MONITOR...
672          BREAKS,BEGIN    ;THEN CONTINUE AT NEXT INSTRUCTION.
673          JSR      PC,GETREG ;RESTORE THE REGISTER VALUES
674          DEC      RCVTMR   ;HAS ENOUGH TIME BEEN PERMITTED FOR A CHARACTER?
675          BPL     R2        ;TRY TO GET ONE.
676          MOV      R3,ASTAT ;STATC WILL SHOW HOW MANY CHARACTERS
677          MOV      -(R0),CSRA ;WERE NOT RECEIVED
678          MOV      R0,(R0)+,ACSR ;LOAD THE DEVICE ADDRESS
679          JSR      PC,SAVREG ;LOAD THE CSR CONTENTS
680          JSR      PC,SAVREG ;SAVE THE REGISTER VALUES
681          MOV      #17,ERRTYP ;UNKNOWN RCVR ERROR
682          HRDRS,BEGIN,NULL ;*****
683          JSR      PC,GETREG ;DID NOT RECEIVE ALL 64. CHARACTERS-STATC=NO. MISSING
684          JSR      PC,GETREG ;*****
685
686 ;THIS ROUTINE PERMITS ONLY ONE DEVICE TO HAVE ITS DATA CHECKED AT A TIME. PENDING
687 ;DEVICES RETURN TO THE MONITOR IN A WAIT LOOP. WHEN ACCESS IS PERMITTED, THE DATA IS CHE
688 ;FOR EACH LINE OF THE DEVICE. DATA ERRORS ARE REPORTED.
689
690 CKDATA: MOV      #R3,R3      ;SET UP A COUNT FOR CLEARING THE BUFFER
691          MOV      #LNCKBF,R5 ;POINT TO THE BEGINNING OF THE BUFFER
692          CNDATA,(R5)+      ;SET UP TWO LINE CHECK BYTES
693          DEC      R3        ;REDUCE THE COUNT. HAVE 16. BYTES BEEN CLEARED?
694          BNE     R3        ;IF NO, GO CLEAR THE REMAINING
695          MOV      (R0),R2    ;LOAD THE SOFTWARE SILO ADDRESS IN R2
696          MOV      #0,(R0),DVCNMBR ;SAVE THE NUMBER OF THIS DEVICE
697          MOV      (R2)+,R0   ;LOAD COUNT TO COMPARE 64. CHARACTERS
698          MOV      (R2)+,R0   ;FETCH A WORD FROM THE SILO INTO R0
699          BGE     RCVDOONE  ;IF IT'S INVALID, GO CLEAN UP BUFFERS

```

700 001720* 032700 070000
 701 001724* 001402
 702 001726* 004767 000304
 703 001732* 110067 002112
 704 001736* 009300
 705 001740* 042700 177750
 706 001744* 126067 003770* 002075
 707 001752* 001402
 708 001754* 004767 000422
 709 001760* 105260 003770*
 710 001764* 005303
 711 001766* 001352
 712
 713
 714
 715
 716
 717 001770* 105067 002055
 718 001774* 005000
 719 001776* 008261
 720 002000* 066100
 721 002002* 005367 002032
 722 002006* 002374
 723 002010* 140067 002033
 724 002014* 104400 000000*
 725
 726
 727
 728
 729
 730
 731 002020* 010320
 732 002022* 113520
 733 002024* 005200
 734 002026* 010320
 735 002030* 010223
 736 002032* 005723
 737 002034* 000205
 738
 739
 740
 741
 742 002036* 016705 001756
 743 002042* 010025
 744 002044* 011100
 745 002046* 010225
 746 002050* 010325
 747 002052* 010425
 748 002054* 022705 002762*
 749 002056* 103700
 750 002062* 012705 002702*
 751 002066* 010567 001726
 752 002072* 000207
 753
 754
 755

```

BIT #70000,R0 ;ARE THERE ANY TRANSMISSION ERRORS?
BEQ 45 ;IF NO, GO DETERMINE THE LINE NUMBER
JSR PC,STATERR ;IF YES, GO DETERMINE THE ERROR TYPE
MOV R0,RCVDATA ;SAVE THE RECEIVED CHARACTER
SWAB R0 ;PUT THE LINE NUMBER IN R0'S LOWER BYTE
MOV #177760,R0 ;ISOLATE THE LINE NUMBER IN R0
CMPR LCKBFF(R0),RCVDATA ;IS THE DATA CORRECT?
BEQ 55 ;IF YES, GO CHECK THE NEXT CHARACTER
JSR PC,DERROR ;IF NO, GO REPORT A DATA ERROR
INCR LCKBFF(R0) ;SET UP THE NEXT CHARACTER FOR THIS LINE
BNE R3 ;REDUCE THE COUNT, ARE ALL 64 CHARACTERS CHECKED?
;IF NO, GO CHECK THE NEXT CHARACTER.

;THIS ROUTINE UNLOCKS THE LINE CHECK BUFFER TO PERMIT ACCESS BY OTHER DEVICES.
;IT THEN COMPUTES THE LINE NUMBER AND CLEARS THE APPROPRIATE FLAG FROM THE WATCHDOG
;TIMER BYTE.

RCVDONE:CLRR LCKOUT ;RESET THE BUFFER ACCESS FLAG
CLR R0 ;THE FLAG WILL PROPAGATE IN R0
SEC ;USE THE CARRY BIT TO BUILD THE DELETION FLAG
ROL ;MOVE THE FLAG TO THE NEXT DEVICE
1S: DEC DVCNMBR ;WHEN THIS NUMBER IS NEGATIVE, THE CORRECT
BGE 1S ;DEVICE IS POINTED TO.
BITCB R0,DONFLG ;CLEAR THIS FLAG
EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.

;SUBROUTINES
;THE FIRST IS THE VECTOR LOADING SUBROUTINE. THE VECTOR ADDRESS IS PASSED IN R0,
;THE DEVICE ADDRESS IS PASSED IN R2. THE LINKING TABLE ADDRESS IS PASSED IN R3.
;THE BUSS REQUEST PRIORITY LEVEL IS A PARAMETER TAKEN INDIRECTLY THROUGH R5.
VCTLOAD:MOV R3,(R0)+ ;LOAD THE ADDRESS OF THE LINKING INSTRUCTION
MOV R5,(R5)+(R0)+ ;AND THE PRIORITY LEVEL INTO THE VECTOR
LNC R0 ;POINT R0 TO THE NEXT VECTOR ADDRESS
CMP R3,(R3)+ ;POINT R3 TO THE CSR INSERT LOCATION
MOV R3,(R3)+ ;LOAD THE DEVICE ADDRESS IN THE LINK TABLE
TST R5+ ;ALIGN R3 FOR THE NEXT DEVICE
RTS R5 ;RETURN TO CALLING BLOCK

;THIS ROUTINE SAVES THE REGISTER VALUES IN THE ERROR QUEUE, A FIRST-IN,
;FIRST-OUT DISCIPLINE WRAPAROUND BUFFER.
SAVREG: MOV ERROPI,R5 ;USE R5 FOR INDEXING CAPABILITY
MOV R0,(R5)+ ;SAVE R0...
MOV R1,(R5)+ ;SAVE R1...
MOV R2,(R5)+ ;SAVE R2...
MOV R3,(R5)+ ;SAVE R3...
MOV R4,(R5)+ ;AND R4
CMP #ERRQUE+60,R5 ;HAS THE QUEUE BOUNDARY BEEN EXCEEDED?
BHS 1S ;IF NO, GO RELOAD THE POINTER
MOV #ERRQUE,R5 ;RESET R5 TO THE QUEUE BEGINNING
MOV R5,ERRQPI ;SET THE ENTRY POINTER TO NEXT ENTRY POINT
RTS PC ;RETURN TO THE CALLING BLOCK

;THIS ROUTINE RETRIEVES REGISTER VALUES FROM THE ERROR QUEUE

```

756 002074* 016705 001722
 757 002100* 012500
 758 002102* 012501
 759 002104* 012502
 760 002106* 012503
 761 002110* 012504
 762 002112* 022705 002762*
 763 002116* 103002
 764 002120* 012705 002702*
 765 002124* 010567 001675
 766 002130* 000207
 767
 768
 769
 770
 771 002132* 005767 175660
 772 002136* 001433
 773 002140* 016704 175652
 774 002144* 100003
 775 002146* 042704 100000
 776 002152* 001425
 777 002154* 012703 000017
 778 002160* 006004
 779 002162* 103402
 780 002164* 005303
 781 002166* 000394
 782 002170* 000257
 783 002172* 010305
 784 002174* 006105
 785 002176* 006105
 786 002200* 006105
 787 002202* 006105
 788 002204* 006105
 789 002206* 000305
 790 002210* 006005
 791 002212* 000005
 792 002214* 052705 000003
 793 002220* 050561 000004
 794 002224* 000207
 795 002226* 052761 033503 000004
 796 002234* 000207
 797
 798
 799
 800
 801 002236* 004767 177574
 802 002242* 010167 175634
 803 002246* 005004
 804 002250* 016703 001564
 805 002254* 001404
 806 002256* 062704 000020
 807 002262* 005303
 808 002264* 001374
 809 002266* 066704 175514
 810 002272* 010467 175602
 811 002276* 010001

```

GETREG: MOV ERROPI,R5 ;USE R5 FOR INDEXING CAPABILITY
MOV R0,(R5)+60 ;RETRIEVE R0...
MOV R1,(R5)+R1 ;R1...
MOV R2,(R5)+R2 ;R2...
MOV R3,(R5)+R3 ;R3...
MOV R4,(R5)+R4 ;AND R4
CMP #ERRQUE+60,R5 ;HAS THE QUEUE BOUNDARY BEEN EXCEEDED?
BHS 1S ;IF NO, GO RELOAD THE POINTER
MOV #ERRQUE,R5 ;RESET R5 TO THE QUEUE BEGINNING
MOV R5,ERRQPI ;SET THE RETRIEVAL POINTER TO THE NEXT FETCH POINT.
RTS PC ;RETURN TO THE CALLING BLOCK

;THIS ROUTINE CALCULATES THE BAUD RATE FROM THE SRI OPTION. IF SRI IS 0,THE DEFAULT
;RATES OF 9600 ARE ASSIGNED. THE LEAST SIGNIFICANT BIT IS THE ONLY ONE CONSIDERED.
;THE BAUD RATES ARE THEN LOADED INTO THE PROPER LINE PARAMETER REGISTER.
BAUDRTE:TST SRI ;IS A SPECIFIC RATE SELECTED?
BEQ 3S ;IF NO,GO ASSIGN THE DEFAULT RATE
MOV SRI,R4 ;IF YES COPY SRI
BPL 4S ;RR IF NOT R4 WORDS BETWEEN VECTORS
BIT 15,R4 ;CLEAR VECTOR SELECT BIT
BEQ 3S ;RR IF DEFAULT - USE 9600 BAUD
MOV #17,R3 ;SET UP THE BIT CONFIGURATION FOR A BAUD RATE SELECTION
ROR R4 ;ISOLATE THE NEXT BIT IN THE CARRY BIT
BCS 2S ;IF THIS IS THE BIT, PUT TOGETHER THE BAUD RATE
DEC R3 ;IF NOT, CALCULATE THE NEXT BIT CONFIGURATION
BR 1S ;GO CHECK THE NEXT ALTERNATIVE
2S: CCC ;ELIMINATE EXTRANEOUS FLAGS BEFORE ROTATING
MOV R3,R5 ;MAKE A DUPLICATE COPY OF THE BAUD RATE
ROL R5 ;BUILD THE TRANSMITTER RATE IN R5
ROL R5
ROL R5
ADD R3,R5 ;ADD THE RECEIVER RATE TO THE TRANSMITTER RATE
SWAR R5 ;ALIGN THE RATE TO BIT POSITIONS 6-13
ROR R5
ROR R5
BIS #3,R5
BIS #4,(R1) ;PLACE THE BAUD RATES IN THE LINE PARAMETER REGISTER
RTS PC ;RETURN TO THE CALLING PROCEDURE
3S: BIT #33503,4(R1) ;SET THE DEFAULT RATE(9600, BAUD)
RTS PC ;RETURN TO CALLING PROCEDURE

;THIS ROUTINE DETERMINES WHAT TYPE OF ERROR WAS INDICATED BY THE SILO AND REPORTS EACH
STATERR:JSR PC,SAVREG ;SAVE THE REGISTERS
MOV R1,ACSR ;REPORT THE CSR CONTENTS
CLR R4 ;SETUP TO CALCULATE
MOV DVCNMBR,R3 ;THE CSR
BEQ 2S ;DEVICE 0 JUST ADD ADDRESS
MOV #20,R4 ;POINT NEXT DH11
DEC R4 ;DECREMENT DH NUMBER
BNE 1S ;/RIGHT DH11?
ADD #R4 ;ADD BASE ADDRESS TO OFFSET
MOV R4,CSRA ;REPORT CSR ADDRESS
MOV R0,R1 ;CALC LINE #

```


924 004010 000000
925 004012 000000
926 004014 000000
927 004016 000000
928 004020 000000
929 004022 000000
930
931
932
933 004024 000000
934 004026 000000
935 004030 000000
936 004032 000000
937 004034 000000
938 004036 000000
939 004040 000000
940 004042 000000
941 004044 000000
942 004046 000
943 004047 000
944 004050 000
945 004051 000
946 004052 000000
947
948
949
950
951 004054 004060
952 004056 177777
953
954 004060 042504 044526 042503
955 004066 040
956 004067 000006
957 004075 040 052510 043515
958 004102 044445 020124 040527
959 004110 051104 050117
960 004116 042520 022504 000
961
962 000001

XMTQPI: OPEN ; TRANSMITTER QUEUE ENTRY (IN) POINTER
XMTQPO: OPEN ; TRANSMITTER QUEUE RETRIEVAL POINTER (OUT)
RCVQPI: OPEN ; RECEIVER QUEUE POINTERS
RCVQPO: OPEN
ERRQPI: OPEN
ERRQPO: OPEN
; THESE ARE THE PROGRAM PARAMETERS
SELECT: OPEN ; ACTIVE DEVICE SELECTION PARAMETER
COUNT: OPEN ; COUNTER FOR THE NUMBER OF ITERATIONS
TMRcnt: OPEN ; COUNTER FOR WATCHDOG TIMER
VA: OPEN ; LOCATION FOR VIRTUAL ADDRESS (USED BY GETPA)
PA: OPEN ; ARGUMENT RETURNED BY GETPA (PHYSICAL ADDRESS)
EA: OPEN ; ARGUMENT RETURNED BY GETPA (EXTENDED ADDRESS BITS)
DVCNMBR: OPEN ; NUMBER OF DEVICE BEING PROCESSED
BCNDATA: OPEN ; TWO COPIES OF THE FIRST CHARACTER IN TRANSMIT PATTERN
RCVTMR: 0 ; RECEIVER BREAK LOOP TIMER
DATA: - BYTE OPEN ; CHARACTER BUILDING BYTE
DONFLG: - BYTE OPEN ; WATCHDOG FLAG FOR BUSY DEVICES
RCVDATA: - BYTE OPEN ; BUFFER FOR CHARACTER CHECKING
LCKOUT: - BYTE OPEN ; BUFFER ACCESS FLAG
NUMBAL: OPEN
; EVEN
; THIS IS THE ASCII MESSAGE
HUNG: M1
-1
M1: .ASCII "DEVICE"
M2: .BLKB 6
M3: .ASCIZ " HUNG&IT WAS DROPPED&"
; EVEN
; FND

ACSR 000102R 346# 582*
ACTVAT 000504R 438#
ADDR21 000006R 312#
ADDR22= 001000 364#
ASB 000106R 350# 848*
ASTAT 000104R 348# 675*
AWAS 000110R 352# 849*
BAURPT 000112R 354# 772#
BEGIN 000000R 309# 372#
859# 613
859# 470*
469# 692 940#
BIT0 = 000001 364#
BIT1 = 000002 364#
BIT10 = 002000 364# 578
BIT11 = 004000 364# 449
BIT12 = 010000 364#
BIT13 = 020000 364# 486
BIT14 = 040000 364#
BIT15 = 100000 364# 776
BIT2 = 000004 364#
BIT3 = 000010 364#
BIT4 = 000020 364#
BIT5 = 000040 364#
BIT6 = 000100 364# 618
BIT7 = 000200 364#
BIT8 = 000400 364#
BIT9 = 001000 364#
BREAKS= 104407 364# 502
BPI 000012R 314# 410
BR2 000013R 315# 407
RTODS = 104412 364#
CDATA= 104412 364#
CKDATA 001660R 667 690#
CNTTBL 002602R 386 890#
CONFIG 000056R 334#
COUNT 004026R 934#
CSR 000100R 344#
DATA 004046R 367* 469
364# 515*
DATCK= 104411 364# 470 522*
DATERS= 104404 364# 473 532*
DERROR 002402R 705 839#
DONFLG 004047R 439* 512 723*
DRIP 000240R 371# 390 943#
DVCNMBR 004040R 696* 804 841 939#
DVTD1 000014R 316# 368#
EA 004036R 453 938#
ENDIT= 104413 364# 547
ENDS = 104410 364# 372 542
ERRQPI 004020R 364# 742 751*
ERRQPO 004022R 425* 928#
ERRQUE 002702R 425 756 929#
ERRTYP 000106R 349# 427 748 750
EXITS = 104400 364# 533* 762 764 911#
FINISH 001110R 506 617 611* 830*
GFTPAS= 104415 364# 443 546#

601* 678* 802*
440 478 532 809 846
814*
443 502 503 528 535 537 542 547 590 594 599
617 644 648 649 670 671 682 724 820 826 832
470* 692 940#
451 503 648 649 670 671
690# 890#
515* 522* 532* 581* 600* 677* 810* 847*
470 473 474* 942#
512 723* 943#
804 841 939#
542 751* 928#
426* 742 756 929#
425 748 750 762 764 911#
533* 592* 611* 830*
617 724

. ARS. 000000 000
004124 001

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

XDHALO, XDHALO/SOL/CRF:SYM=DDXCOM, XDHALO
RUN-TIME: 1 2 3 SECONDS
RUN-TIME RATIO: 24/5=4.8
CORE USED: 7K (13 PAGES)