

IDENTIFICATION

SEQ 0001

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1.0 GENERAL INFORMATION

1.1 PROGRAM ABSTRACT

1.1.1 STRUCTURE OF PROGRAM

THIS DIAGNOSTIC OCCUPIES 14.5K WORDS OF MEMORY AND IS COMPATIBLE WITH BOTH XXDP AND ACT. IT CAN BE RUN STANDALONE UNDER XXDP, AND CAN BE CHAINED UNDER XXDP, ACT AND APT IN ACT MODE (SEE "CREATE CORE IMAGE" COMMAND BELOW FOR DETAILS OF CHAINING PROCEDURE). IT IS A SINGLE PROGRAM FROM THE STANDPOINT OF THE DIAGNOSTIC USER, BUT WE HAVE INCORPORATED INTO IT A CONTROL MODULE WHICH WILL LATER BE RELEASED INDEPENDENTLY AS A DIAGNOSTIC SUPERVISOR.

WHEN THIS DIAGNOSTIC IS STARTED AT ADDRESS 200, CONTROL GOES FIRST TO THE SUPERVISOR PORTION, WHICH WILL ASK CERTAIN "HARD CORE" QUESTIONS ABOUT THE ENVIRONMENT. THEN IT WILL ENTER COMMAND MODE, INDICATED BY A PROMPT CHARACTER (DS B>). AT COMMAND MODE THE OPERATOR MAY ENTER ANY OF SEVERAL COMMANDS AS DESCRIBED BELOW.

THE SUPERVISOR CODING FOLLOWS IMMEDIATELY THE DIAGNOSTIC TEST CODING, BUT THE SUPERVISOR LISTING HAS BEEN SUPPRESSED FOR GENERAL DISTRIBUTION. A LIMITED DISTRIBUTION HAS BEEN MADE TO FIELD SERVICE OF THE SUPERVISOR ASSEMBLY LISTING, AND IT MAY BE CONSULTED IN EVENT OF A SOFTWARE PROBLEM.

1.1.2 DIAGNOSTIC INFORMATION

THE RL11/RLV11 CONTROLLER TEST (PART 2) IS A PDP-11 (LSI-11) BASED PROGRAM THAT WILL TEST THE CONTROLLER. IT COMPLIMENTS PART 1 BY EXTENDING THE TEST COVERAGE TO INCLUDE WRITE DATA, READ DATA, WRITE CHECK AND READ DATA WITHOUT HEADER COMPARE. IT IS AIMED AT FULLY TESTING THE CONTROLLER IN THESE AREAS, BUT BY DEFAULT ALSO EXERCISES THE DRIVE. THE TEST COVERAGE OF THE PROGRAM IS EXTREMELY HIGH.

1.2 SYSTEM REQUIREMENTS

1.2.1 HARDWARE REQUIREMENTS

PDP-11/LSI-11 PROCESSOR WITH 16K OR MORE OF MEMORY
 CONSOLE DEVICE (LA30, LA36, VT50, ETC.)
 RL11/RLV11 CONTROLLER(S)
 1 - 8 RLO1 DRIVES
 1 - 8 RLO1K CARTRIDGES WITH BAD SECTOR FILE
 KW11P, KW11L (OPTIONAL)
 LINEPRINTER (OPTIONAL)

1.2.2 SOFTWARE REQUIREMENTS

CZRLBB RL11/RLV11 CTRL 2

(FORMERLY MD-11-DZRLB-A)

1.3 RELATED DOCUMENTS AND STANDARDS

RL01 USERS MANUAL (EK-RL01-UG-PRE)
XXDP USERS MANUAL

1.4 DIAGNOSTIC HIERARCY PREREQUISITES

THE RL01 SUBSYSTEM SHOULD HAVE SUCCESSFULLY RUN THE FOLLOWING PROGRAMS:

CZRLABO RL11/RLV11 RL01 CONTROLLER TEST (PART 1)

1.5 ASSUMPTIONS

THE HARDWARE OTHER THAN THE RL01 SUBSYSTEM IS ASSUMED TO WORK PROPERLY. FALSE ERRORS MAY BE REPORTED IF THE PROCESSOR, ETC., DO NOT FUNCTION PROPERLY.

2.0 OPERATING INSTRUCTIONS

2.1 HOW TO RUN THIS DIAGNOSTIC

2.1.1 THE SIX STEPS OF EXECUTION

THIS DIAGNOSTIC SHOULD BE LOADED AND STARTED USING NORMAL XXDP PROCEDURES. THE START COMMAND SHOULD NOT SPECIFY AN ADDRESS, BECAUSE THE DIAGNOSTIC HAS THE PROPER TRANSFER ADDRESS CODED INTO IT.

WHEN THIS DIAGNOSTIC IS STARTED, THE FOLLOWING STEPS WILL OCCUR:

* STEP 1 *

A SHORT SERIES OF "HARDCORE QUESTIONS" WILL BE ASKED:

QUESTION	MEANING
L-CLK (L) N ?	IS THERE AN L-CLOCK?
P-CLK (L) N ?	" " " P-CLOCK?
50HZ (L) N ?	IS THE POWER 50 CYCLES (AS IN EUROPE)?
LSI (L) N ?	IS MACHINE AN LSI?
LPT (L) N ?	IS THERE A LINE PRINTER?
MEM (K) (D) 16 ?	HOW MANY K OF MEMORY ARE THERE?

THE DEFAULTS (SHOWN AFTER EACH QUESTION) CAN BE SELECTED BY HITTING CARRIAGE RETURN. IT IS POSSIBLE THAT NOT ALL OF THE QUESTIONS WILL BE ASKED: FOR EXAMPLE, IF YOU SAY "YES" TO THE L-CLOCK QUESTION, THE P-CLOCK QUESTION WILL NOT BE ASKED.

IF NEITHER P OR L CLOCK ARE ANSWERED YES THE OPERATOR WILL BE ASKED TO TYPE TWO CHARACTERS 4 SECONDS APART.

* STEP 2 *

WHEN YOU HAVE ANSWERED ALL THE HARDCORE QUESTIONS, THE DIAGNOSTIC WILL ISSUE THE PROMPT "DS-B>". FROM THIS POINT UNTIL THE TIME WHEN YOU RESTART XXDP, YOU WILL BE TALKING TO THE DIAGNOSTIC, NOT XXDP. WE WILL REFER TO THE PRESENCE OF THIS PROMPT AS BEING IN DIAGNOSTIC COMMAND MODE, AS OPPOSED TO XXDP COMMAND MODE.

AT THIS POINT YOU WILL ENTER A "START" COMMAND. THIS IS NOT THE SAME AS THE XXDP "START" COMMAND, WHICH YOU ALREADY ISSUED IN RESPONSE TO THE XXDP DOT PROMPT. THIS "START" COMMAND CAN TAKE A NUMBER OF SWITCHES AND FLAGS (ALL OPTIONAL) AND THE DETAILS OF THESE ARE SET FORTH IN "2.3 DETAILS OF COMMANDS AND SYNTAX". HOWEVER, IN ORDER TO USE THE PROGRAM, ALL YOU NEED TO SAY IS SOMETHING LIKE THIS:

STA/PASS:1/FLAGS:HOE

THINGS TO NOTE HERE:

1. ONLY THE FIRST THREE CHARACTERS OF THIS OR ANY COMMAND AT THE "DS-B>" LEVEL NEED TO BE TYPED.
2. THE "PASS" SWITCH SPECIFIES HOW MANY PASSES YOU DESIRE. A PASS CONSISTS OF RUNNING THE FULL DIAGNOSTIC AGAINST ALL UNITS BEING TESTED (THIS WILL BE EXPLAINED SHORTLY). ONE PASS IS SPECIFIED IN THE ABOVE EXAMPLE.
3. THE "FLAGS" SWITCH MAY SPECIFY ANY OF A NUMBER OF FLAGS, BUT THE MAIN USEFUL ONES ARE:

LOE	LOOP ONE ERROR
HOE	HALT ON ERROR
IER	INHIBIT ERROR PRINTOUT

THE HOE FLAG IS SPECIFIED IN THE ABOVE EXAMPLE (WE'LL SEE WHY SHORTLY).

* STEP 3 *

WHEN YOU HAVE TYPED IN A "START" COMMAND, THE DIAGNOSTIC WILL COME BACK WITH THE QUESTION "# UNITS?" TO WHICH YOU SHOULD RESPOND BY TYPING IN THE NUMBER OF DEVICES YOU WISH TO TEST.

A WORD OF WARNING HERE: THE NUMBER OF UNITS DEPENDS ON THE TARGET DEVICE OF THE DIAGNOSTIC. FOR EXAMPLE, IF THE DIAGNOSTIC IS DIRECTED

AT A DISK DRIVE, THEN THE NUMBER OF UNITS WOULD BE THE NUMBER OF DRIVES TO BE TESTED. WHEREAS IF THE DIAGNOSTIC WAS DIRECTED AT THE DISK CONTROLLER, THEN THE NUMBER OF UNITS WOULD BE THE NUMBER OF CONTROLLERS. THE TARGET DEVICE OF A DIAGNOSTIC CAN ALWAYS BE DETERMINED BY INSPECTING THE "HEADER" STATEMENT NEAR THE BEGINNING OF THE SOURCE CODE. ONE OF THE OPERANDS OF THIS "HEADER" STATEMENT SHOULD BE THE DEVICE TYPE OF THE DIAGNOSTIC.

* STEP 4 *

WHEN YOU HAVE TYPED IN THE NUMBER OF UNITS TO BE TESTED, THE DIAGNOSTIC WILL ASK YOU THE "HARDWARE QUESTIONS". THE ANSWERS TO THESE QUESTIONS ARE USED TO BUILD TABLES IN CORE, CALLED "HARDWARE P-TABLES". ONE HARDWARE P-TABLE WILL BE BUILT FOR EACH UNIT TO BE TESTED.

THERE ARE SEVERAL HARDWARE QUESTIONS AND THE ENTIRE SERIES WILL BE POSED N TIMES, WHERE N IS THE NUMBER OF UNITS.

THIS REPRESENTS A NEW PHILOSOPHY IN DIAGNOSTIC ENGINEERING. DIAGNOSTICS IN THE FUTURE WILL NOT BE WRITTEN TO AUTOSIZE OR ASSUME STANDARD ADDRESSES; INSTEAD, THEY WILL ASK THE OPERATOR FOR ALL THE INFORMATION THEY NEED TO TEST THE DEVICE.

* STEP 5 *

AFTER YOU HAVE ANSWERED ALL THE HARDWARE QUESTIONS (SEC 2.5) FOR ALL THE UNITS, YOU WILL BE ASKED "CHANGE SW?" IF YOU WANT TO BE ASKED THE SOFTWARE QUESTIONS THAT DETERMINE THE BEHAVIOR OF THIS PROGRAM, TYPE "Y". IF YOU WANT TO TAKE ALL THE DEFAULTS TO THESE QUESTIONS, TYPE "N". IF YOU TYPE "Y" YOU WILL BE ASKED THE SOFTWARE QUESTIONS (SEC 2.6), AND THE ANSWERS WILL BE PUT INTO THE SOFTWARE P-TABLE IN THE PROGRAM. THE SERIES OF QUESTIONS WILL BE ASKED JUST ONCE, REGARDLESS OF THE NUMBER OF UNITS TO BE TESTED.

* STEP 6 *

AFTER YOU HAVE ANSWERED THE SOFTWARE QUESTIONS, THE DIAGNOSTIC WILL BEGIN TO EXECUTE THE HARDWARE TEST CODE. THERE ARE SEVERAL THINGS THAT CAN HAPPEN NEXT, DEPENDING ON WHETHER A HARDWARE ERROR IS ENCOUNTERED AND ALSO ON WHAT SWITCH VALUES YOU SELECTED ON THE START COMMAND. CONSIDER THE POSSIBILITIES:

1. IF NO ERROR IS ENCOUNTERED, THEN THE DIAGNOSTIC WILL SIMPLY EXECUTE THE DESIRED NUMBER OF PASSES AND RETURN TO COMMAND MODE (PROMPT DS-B>).

2. IF AN ERROR IS ENCOUNTERED, THEN ONE OF THREE THINGS HAPPENS, DEPENDING ON THE SETTINGS OF THE HOE AND LOE FLAGS.

HOE SET: THE ERROR WILL BE REPORTED ON THE CONSOLE AND THE DIAGNOSTIC WILL RETURN TO COMMAND MODE.
 LOE SET: THE DIAGNOSTIC WILL LOOP ENLESSLY ON THE BLOCK OF CODE THAT DETECTED THE ERROR.
 NEITHER HOE NOR LOE SET: THE ERROR WILL BE REPORTED ON THE CONSOLE AND NORMAL EXECUTION WILL RESUME AS IF NO ERROR HAD OCCURED.

2.1.2 SAMPLE RUN-THROUGH

LET'S SEE HOW ALL THIS WORKS IN A REAL SITUATION. RECALL THAT WE ENTERED THE COMMAND "STA/PASS:1/FLAGS:HOE". THIS WOULD BE A VERY TYPICAL WAY TO RUN THE DIAGNOSTIC. IF NO ERRORS ARE ENCOUNTERED, THE SINGLE REQUESTED PASS WILL BE EXECUTED AND THE PROMPT WILL BE REISSUED.

IF AN ERROR IS ENCOUNTERED, THE ERROR WILL BE REPORTED AND THE PROMPT WILL BE REISSUED (BECAUSE THE HOE FLAG IS SET). AT THIS POINT THERE ARE FOUR DIFFERENT WAYS YOU CAN GET THE PROGRAM GOING AGAIN:

1. ISSUE ANOTHER "START" COMMAND (THUS GOING THRU ALL OF STEPS 2, 3, 4, 5, AND 6 AGAIN)
2. ISSUE A "RESTART" COMMAND (SAME AS START COMMAND EXCEPT THAT THE HARDWARE QUESTIONS ARE NOT ASKED)
3. ISSUE A "CONTINUE" COMMAND (EXECUTION WILL RESUME AT THE BEGINNING OF THE PARTICULAR HARDWARE TEST (MOST DIAGNOSTICS CONSIST OF A NUMBER OF THESE) THAT IT WAS IN WHEN THE ERROR HALT OCCURED. NO QUESTIONS ASKED.)
4. ISSUE A "PROCEED" COMMAND: EXECUTION WILL RESUME AT THE INSTRUCTION FOLLOWING THE ERROR REPORT (THIS IS A SPECIAL COMMAND AND CAN BE ISSUED ONLY AT A HALT ON ERROR).

THE MOST TYPICAL THING TO DO HERE IS TO ISSUE THE PROCEED, BUT WITH DIFFERENT FLAG SETTINGS. PROBABLY YOU WOULD WANT TO SAY

PRO/FLAGS:IER:LOE:HOE=0

THIS WILL DO THE FOLLOWING:

1. TURN ON THE IER (INHIBIT ERROR PRINTOUT) FLAG
2. TURN ON THE LOE FLAG
3. TURN OFF THE HOE FLAG
4. RESUME EXECUTION AT INSTRUCTION AFTER ERROR REPORT

THE DIAGNOSTIC WILL NOW LOOP ON THE BLOCK OF CODE THAT DETECTED AND REPORTED THE ERROR, BUT NO ERROR PRINTOUT WILL OCCUR. THUS YOU CAN STUDY THE ERROR OR SCOPE IT OR WHATEVER.

WHEN YOU'VE SEEN ENOUGH, YOU MAY HIT CONTROL/C. THIS WILL TAKE YOU OUT OF THE LOOP AND PUT YOU BACK INTO COMMAND MODE. YOU NOW HAVE THREE CHOICES:

1. START
2. RESTART
3. CONTINUE

LET'S SAY YOU'VE REPAIRED THE DEFECT FOUND ABOVE AND WANT TO FINISH RUNNING THE DIAGNOSTIC. YOU WOULD TYPE

CON/FLAGS:HOE:IER=0:LOE=0

THIS WILL RESTORE THE FLAGS TO THEIR ORIGINAL VALUES AND RESUME EXECUTION AT THE BEGINNING OF THE HARDWARE TEST YOU WERE IN. IF THE ERROR DOES NOT RECUR, THE EXECUTION WILL FLOW RIGHT ON THRU TO THE NEXT ERROR OR TO END OF PASS.

IF AT END OF PASS YOU WANT TO RUN THE DIAGNOSTIC AGAIN, YOU HAVE TWO CHOICES:

1. START
2. RESTART

YOU WOULD CHOOSE ONE, DEPENDING ON WHETHER YOU WANTED TO ANSWER THE HARDWARE QUESTIONS AGAIN.

THE FULL PRINT-OUT FROM THE ABOVE DIALOGUE MIGHT LOOK LIKE THIS:

.	R	DZRKXX	
	DZRKXX		BY
	L-CLK	(L) N ? Y	WHOM
	50HZ	(L) N ?	ENTERED:
	LSI	(L) N ?	O
	LPT	(L) N ?	D
	MEM	(K) (D) 16 ?	D,0
	DS-B>STA/PASS:1/	FLAGS:HOE	D
	# UNITS	(D) ? 2	D
	UNIT 1		D
	CSR	(O) ?	D,0
	VECTOR	(O) ?	D,0
	BR LEVEL	(O) ?	D,0
	DRIVE	(O) ? 0	D,0
	UNIT 2		D
	CSR	(O) ?	D,0
	VECTOR	(O) ?	D,0
	BR LEVEL	(O) ?	D,0
	DRIVE	(O) ? 1	D,0
	CHANGE SW	(L) ? N	D,0
	DZRKXX	HARD ERR 00004 TST 003 SUB 002 PC:004130	D
	ERR	HLT	D
	DS-B>PRO/	FLAGS:IER:LOE:HOE=0	D,0

AT THIS POINT THE DIAGNOSTIC IS LOOPING ON THE			
ERROR WITHOUT PRINTING ANYTHING. YOU CAN SCOPE			
THE ERROR UNTIL YOU HAVE LOCATED IT, THEN ^C OUT			

^C			
	DS-B>CON/	FLAGS:HOE:IER:LOE=0	O
	CHANGE SW	(L) ? N	D,0
	DZRKXX	EOP 1	D
	DS-B>RESTART/	PASS:1	D,0
	CHANGE SW	(L) ? N	D,0

2.2 HOW TO CREATE A CHAINABLE FILE

THE DIAGNOSTIC AS RECEIVED FROM RELEASE ENGINEERING CANNOT BE RUN IN CHAIN MODE. THAT IS WHY IT BEARS THE EXTENSION "BIN" INSTEAD OF "BIC". THERE IS A WAY, HOWEVER, TO CREATE A CHAINABLE PROGRAM FROM WHAT YOU'VE GOT.

IT CONSISTS OF RUNNING THE PROGRAM WITH THE SPECIAL COMMAND "CCI" ISSUED WHERE YOU WOULD NORMALLY ISSUE A START COMMAND (TO THE PROMPT DS-B>). THIS COMMAND CAUSES THE DIAGNOSTIC TO GO THRU ALL THE QUESTIONS AND ANSWERS AND THEN TO HALT, JUST WHERE IT WOULD ORDINARILY BEGIN EXECUTION OF THE HARDWARE TEST CODE. AT THIS POINT YOU CAN DUMP THE PROGRAM AS IT SITS IN CORE TO THE LOAD MEDIUM, WITH THE NEW EXTENSION "BIC".

HERE IS A SAMPLE DIALOGUE TO ACCOMPLISH THIS:

```
.R UPD2
RESTART:  XXXXXX
*CLR
*LOAD DIAG.BIN
XFER:200  CORE:0,60602
*START 200
L-CLK (L) N ?
-----
DS-B>CCI
# UNITS (D) ? 4
-----
CHANGE SW (L) ? N
PTAB END: 60632

*****
*AT THIS POINT THE MACHINE HALTS AND*
*YOU MUST RESTART AT ADDRESS XXXXXX*
*****

*HICORE 60632
CORE: 0,60632
*DUMP DK0: DIAG.BIC
```

THE RESULT OF DOING THIS IS THAT YOU CAN NOW BUILD AN XXDP CHAIN FILE CONTAINING THE XXDP COMMAND

```
.R DIAG.BIC
```

AND THE DIAGNOSTIC WILL EXECUTE WITHOUT MANUAL INTERVENTION, USING THE ANSWERS THAT YOU GAVE IT WHEN YOU DID THE CCI COMMAND.

2.3 DETAILS OF COMMANDS AND SYNTAX

2.3.1 TABLE OF COMMAND VALIDITY

THERE ARE FOUR WAYS OF ENTERING DIAGNOSTIC COMMAND MODE, AND DIFFERENT SUBSETS OF THE DIAG COMMAND SET ARE AVAILABLE WITH EACH:

HOW ENTERED	LEGAL COMMANDS
1. OPERATOR ENTERED "RUN DIAG"	START PRINT DISPLAY FLAGS ZFLAGS
2. DIAGNOSTIC HAS FINISHED ALL ITS REQUESTED PASSED	START RESTART PRINT DISPLAY FLAGS ZFLAGS
3. OPERATOR INTERRUPTED THE DIAGNOSTIC WITH CTRL/C	START RESTART CONTINUE PRINT DISPLAY FLAGS ZFLAGS
4. AN ERROR WAS ENCOUNTERED WITH THE HOE FLAG SET SET	START RESTART CONTINUE PROCEED PRINT DISPLAY FLAGS ZFLAGS

2.3.2 COMMAND SYNTAX

```
*****
STA(RT)/TESTS:TEST-LIST/PASS:PASS-CNT/FLAGS:FLAG-LIST/EOP:EOP-INCR
*****
```

THE DIAGNOSTIC IN CORE IS EXECUTED IN ACCORDANCE WITH THE SWITCHES SPECIFIED. THE MESSAGE "# UNITS?" IS PRINTED. THE START COMMAND MAY BE ISSUED WHEN DIAGNOSTIC COMMAND MODE HAS BEEN ENTERED VIA ONE OF THE FOLLOWING: A) OPERATOR TYPED "RUN DIAGNOSTIC" B) DIAGNOSTIC FINISHED EXECUTING C) ERROR WAS ENCOUNTERED WITH HOE FLAG SET D) OPERATOR ENTERED CONTROL/C.

AFTER THE OPERATOR RESPONDS TO "# UNITS?", THE HARDWARE DIALOGUE IS INITIATED. WHEN IT IS COMPLETED, THE QUESTIONS "CHANGE SW?" IS ISSUED, AND THE ANSWERS, IF GIVEN, BECOME THE NEW DEFAULTS. THEREFORE IT IS NECESSARY TO RELOAD THE PROGRAM IN ORDER TO RETURN TO THE LOAD DEFAULTS.

THE SWITCH ARGUMENTS ARE AS FOLLOWS:

"TEST-LIST" IS A SEQUENCE OF DECIMAL NUMBERS (1:2 ETC.) OR RANGES OF DECIMAL NUMBERS (1-5:8-10 ETC.) THAT SPECIFY THE TESTS TO BE EXECUTED. THE NUMBERS ARE SEPARATED BY COLONS. THE NUMBERS RANGE FROM 1 TO THE LARGEST TEST NUMBER IN THE DIAGNOSTIC. THEY MAY BE SPECIFIED IN ANY ORDER. TESTS WILL BE EXECUTED IN NUMERICAL ORDER REGARDLESS OF THE ORDER OF SPECIFICATION. THE DEFAULT IS TO EXECUTE ALL TESTS.

"PASS-CNT" IS A DECIMAL NUMBER INDICATING THE DESIRED NUMBER OF PASSES. A PASS IS DEFINED AS THE EXECUTION OF THE FULL DIAGNOSTIC (ALL SELECTED TESTS) AGAINST ALL UNITS SUBMITTED. THE DEFAULT IS NON-ENDING EXECUTION..B "FLAG-LIST" IS A SEQUENCE OF ELEMENTS OF THE FORM <FLAG>, <FLAG=1>, OR <FLAG=0>, SEPARATED BY COLONS, WHERE <FLAG> HAS ONE OF THE FOLLOWING VALUES:

HOE	HALT ON ERROR, CAUSING COMMAND MODE TO BE ENTERED WHEN AN ERROR IS ENCOUNTERED
LOE	LOOP ON ERROR, CAUSING THE DIAGNOSTIC TO LOOP CONTINUOUSLY WITHIN THE SMALLEST DEFINED BLOCK OF CODING (SEGMENT, SUBTEST, OR TEST) CONTAINING THE ERROR
IER	INHIBIT ERROR REPORTING
IBE	INHIBIT BASIC ERROR REPORTS
IXE	INHIBIT EXTENDED ERROR REPORTS
PRI	DIRECT ALL MESSAGES TO A LINE PRINTER
PNT	PRINT NUMBER OF TES BEING EXECUTED
BOE	BELL ON ERROR
UAM	RUN IN UNATTENDED MODE, BYPASSING MANUAL INTERVENTION TESTS
ISR	INHIBIT STATISTICAL REPORTS
IDU	INHIBIT DROPPING OF UNITS BY DIAGNOSTIC

THE FLAGS NAMED OR EQUATED TO 1 ARE SET, THOSE EQUATED TO 0 ARE CLEARED. A FLAG NOT SPECIFIED IS CLEARED. IF THE FLAGS SWITCH IS NOT GIVEN ALL FLAGS ARE CLEARED.

"EOP-INCR" IS A DECIMAL NUMBER INDICATING HOW OFTEN (IN TERMS OF PASSES) IT IS DESIRED THAT THE END OF PASS MESSAGE BE PRINTED. THE DEFAULT IS AT THE END OF EVERY PASS.

 RES(TART)/TEST:TEST-LIST/PASS:PASS-CNT/FLAGS:FLAG-LIST/EOP:EOP-INCR/UNITS:UNIT-LIST

THE DIAGNOSTIC IN CORE IS EXECUTED IN ACCORDANCE WITH THE SWITCHES SPECIFIED. HOWEVER, NEW P-TABLES ARE NOT BUILT. INSTEAD, THE ONES IN CORE ARE USED. THE QUESTION "CHANGE SW?" IS ASKED, AND THE ANSWERS IF GIVEN BECOME THE NEW DEFAULTS. THE COMMAND MAY BE ISSUED WHEN COMMAND MODE HAS BEEN ENTERED VIA A) DIAGNOSTIC IS FINISHED B) HALT ON ERROR C) CONTROL/C.

THE SWITCH ARGUMENTS ARE AS IN THE START COMMAND EXCEPT:

1. "UNIT-LIST" IS A SEQUENCE OF LOGICAL UNIT NUMBERS RANGING FROM 1 THRU N (N = NUMBER OF UNITS BEING TESTED) SPECIFYING WHICH UNITS ARE TO BE TESTED. THE LOGICAL UNIT NUMBER DESIGNATES THE POSITION OF THE P-TABLE IN CORE, ACCORDING TO THE ORDER IN WHICH THEY WERE BUILT. THE UNITS SPECIFIED MUST NOT HAVE BEEN DROPPED BY THE OPERATOR DROP COMMAND. THE UNIT-LIST DEFAULTS TO "ALL THAT HAVE NOT BEEN DROPPED BY OPERATOR COMMAND". THE EFFECT OF THE UNIT-LIST LASTS UNTIL THE NEXT START (WHERE IT IS AUTOMATICALLY RESET TO "ALL") OR THE NEXT RESTART.
2. ALL UNSPECIFIED FLAG SETTINGS ARE UNCHANGED.

 CON(TINUE)/PASS:<PASS-CNT/FLAGS:<FLAG-LIST>

COMMAND MODE MUST HAVE BEEN ENTERED DUE TO A HALT ON ERROR OR A CONTROL/C. THE EFFECT OF THE COMMAND IS TO GO TO THE BEGINNING OF THE TEST THAT WAS BEING EXECUTED WHEN THE HALT OR CONTROL/C TOOK PLACE. SOFTWARE DIALOGUE MAY OPTIONALLY BE REEXECUTED. HARDWARE PARAMETERS MAY NOT BE CHANGED.

THE SWITCH ARGUMENTS ARE AS IN THE START COMMAND EXCEPT:

1. DEFAULT FOR PASS-CNT IS THE UNSATISFIED PASS-CNT FROM THE PREVIOUS START OR RESTART
2. UNSPECIFIED FLAG SETTINGS ARE UNCHANGED

 PRO(CEED)/FLAGS:<FLAG-LIST>

COMMAND MODE MUST HAVE BEEN ENTERED VIA A HALT ON ERROR. THE EFFECT OF THE COMMAND IS TO BEGIN EXECUTION AT THE LOCATION FOLLOWING THE ERROR CALL. NEITHER HARDWARE NOR SOFTWARE PARAMETERS MAY BE ALTERED.

THE SWITCH ARGUMENTS ARE THE SAME AS THE START COMMAND EXCEPT:

1. UNSPECIFIED FLAG SETTINGS ARE UNCHANGED

 CCI/TEST:TEST-LIST/PASS:PASS-CNT/FLAGS:FLAG-LIST/EOP:EOP-INCR

THE DIAGNOSTIC EXECUTES THRU ALL OPERATOR DIALOGUE AND HALTS AT THE HARDWARE TEST CODE. NOW THE OPERATOR CAN DUMP THE CORE IMAGE TO THE MEDIUM WITH A BIC EXTENSION.

THE BIC FILE MUST BE HANDLED DIFFERENTLY DEPENDING ON WHETHER IT IS RUN MANUALLY OR IN CHAIN MODE. IF RUN MANUALLY IT CAN BE INVOKED EITHER WITH A "START" (IN WHICH CASE IT WILL BEHAVE LIKE THE BIN FILE; THE PRE-GENERATED ANSWERS TO OPERATOR QUESTIONS WILL BE IGNORED) OR WITH A "RESTART" (IN WHICH CASE THE PRE-GENERATED OPERATOR ANSWERS WILL BE USED).

IF RUN IN CHAIN MODE, AUTOMATIC EXECUTION WILL COMMENCE IMMEDIATELY FROM THE XXDP COMMAND ".R DIAG". THE COMMAND PROMPT "DS-B>" WILL NOT BE ISSUED.

ANY SWITCHES SPECIFIED ON THE CCI COMMAND WILL CARRY OVER WHEN THE BIC FILE IS RUN IN CHAIN MODE (EXCEPT THAT UAM IS ALWAYS SET THERE) BUT WILL NOT CARRY OVER WHEN IT IS RUN MANUALLY.

TO DO A CCI ON A FULL SIZED DIAGNOSTIC (14.5K WORDS), A MACHINE SIZE LARGER THAN 16K IS REQUIRED. THE EXACT SIZE NEEDED DEPENDS ON WHICH UTILITY IS USED TO EXECUTE THE DIAGNOSTIC AT CCI TIME.

 DRO(P)/UNITS:UNIT-LIST

THE UNITS SPECIFIED ARE DROPPED FROM TESTING UNTIL THEY ARE ADDED BACK OR UNTIL A START COMMAND IS GIVEN. A DROP CANNOT BE FOLLOWED BY A PROCEED.

THERE IS ALSO A "DROP" MACRO INTERNAL TO THE DIAGNOSTIC, WHICH GIVES THE FACILITY OF AUTO-DROPPING. THE DURATION OF A PROGRAM DROP, HOWEVER, IS ONLY UNTIL THE NEXT START OR RESTART.

ADD/UNITS:UNIT-LIST

THE UNITS SPECIFIED ARE ADDED BACK (THEY MUST HAVE BEEN PREVIOUSLY DROPPED BY THE DROP COMMAND) TO THE TEST SEQUENCE. AN ADD CANNOT BE FOLLOWED BY A PROCEED.

PRI(NT)

ALL STATISTICS TABLES ACCUMULATED BY THE DIAGNOSTIC ARE PRINTED. THE ISR (INHIBIT STATISTICAL REPORTING) FLAG IS CLEARED.

DIS(PLAY)/UNITS:<UNIT-LIST>

THE HARDWARE P-TABLES FOR ALL UNITS UNDER TEST ARE PRINTED OUT IN THE FORMAT IN WHICH THEY WERE ENTERED. ANY UNITS THAT WERE DROPPED BY THE OPERATOR "DROP" COMMAND ARE SO DESIGNATED.

FLA(GS)

THE CURRENT SETTINGS OF ALL FLAGS ARE PRINTED.

ZFL(AGS)

ALL FLAGS ARE CLEARED.

2.4 EXTENDED P-TABLE DIALOGUE

THE FULL CAPABILITY OF THE HARDWARE DIALOGUE IS REVEALED BY THE FOLLOWING DISCUSSION OF WHAT HAPPENS INTERNALLY.

AS SOON AS THE QUESTION "# UNITS?" IS ANSWERED (WITH THE NUMBER N, SAY) SPACE IN CORE IS ALLOCATED FOR N P-TABLES. ALL OF THE P-TABLES ARE OF THE SAME FORMAT, AND THERE IS A ONE-TO-ONE CORRESPONDENCE BETWEEN THE HARDWARE PARAMETER QUESTIONS AND THE SLOTS IN THE P-TABLE FORMAT.

ON THE FIRST TRIP THRU THE QUESTIONS, ALL OF THE SLOTS IN ALL OF THE P-TABLES ARE FILLED. IF THE OPERATOR TYPES IN LESS THAN N EXPLICIT VALUES IN RESPONSE TO A PARTICULAR QUESTION, THESE VALUES ARE PLACED IN THE P-TABLES (ONE VALUE GOING INTO THE PROPER SLOT OF EACH P-TABLE BEGINNING WITH THE FIRST P-TABLE) UNTIL THE STRING OF VALUES IS EXHAUSTED. THE LAST VALUE THAT SLOT IN THE REMAINING P-TABLES.

ON SUBSEQUENT TRIPS THRU THE QUESTIONS, THE SAME PROCESS IS CARRIED OUT, EXCEPT THAT THE EARLIEST P-TABLE NOT TO HAVE RECEIVED AN EXPLICIT VALUE IN ANY OF ITS SLOTS NOW ASSUMES THE ROLE THAT TABLE NUMBER ONE PLAYED IN THE FIRST TRIP.

THE SERIES OF QUESTIONS IS REISSUED UNTIL AT LEAST ONE QUESTION HAS RECEIVED N EXPLICIT VALUES FROM THE OPERATOR.

IN GIVING A STRING OF VALUES, COMMAS WITHOUT INTERVENING VALUES MAY BE USED TO INDICATE A REPETITION OF THE LAST NAMED VALUE.

A STRING OF VALUES MAY BE GIVEN AS A RANGE (6-10 FOR EXAMPLE). IF THE VALUES REPRESENT PURE NUMERICAL DATA, THIS SAMPLE RANGE TRANSLATES TO THE STRING 6,7,8,9,10 (AN INCREMENT OF 1). IF THE VALUES ARE ADDRESSES, THE SAMPLE RANGE TRANSLATES TO THE STRING 6,8,10 (AN INCREMENT OF 2).

NOW LET US SEE HOW WE COULD USE THESE CAPABILITIES TO CONSTRUCT A SET OF P-TABLES. ASSUME THAT WE HAVE 64 UNITS, AND THAT THERE ARE THREE HARDWARE PARAMETERS FOR EACH (THREE SLOTS IN THE P-TABLE, THREE HARDWARE QUESTIONS IN THE DIALOGUE). LET THE DESIRED VALUE FOR THE FIRST PARAMETER BE THE NUMBER 75 FOR ALL 64 TABLES. LET THE DESIRED VALUE FOR THE SECOND PARAMETER BE EQUAL TO THE UNIT NUMBER (1,2,3,...,64) EXCEPT FOR UNIT 50, WHICH SHOULD RECEIVE THE VALUE 49. LET THE DESIRED VALUE FOR THE THIRD PARAMETER BE THE NUMBER 76 FOR THE FIRST 20 UNITS AND THE NUMBER 77 FOR THE LAST 44 UNITS.

THE FOLLOWING DIALOGUE WOULD ACCOMPLISH THIS GOAL:

UNITS (D) ? 64

UNIT 1

<QUESTION 1> ? 75
<QUESTION 2> ? 1-20
<QUESTION 3> ? 76

UNIT 21

<QUESTION 1> ?
<QUESTION 2> ? 21-49,,51-64
<QUESTION 3> ? 77

THE FIRST TIME THE SERIES IS ASKED, SLOT ONE RECEIVES A 75 IN ALL 64 TABLES. SLOT TWO RECEIVES THE VALUES 1,2,3,...,20 IN TABLES 1 THRU 20 AND A CONSTANT 20 IN TABLES 21 THRU 64. SLOT THREE RECEIVES A CONSTANT 76 IN ALL 64 TABLES.

THE SECOND TIME THRU THE SERIES, TABLES 21 THRU THE END ARE GOING TO BE AFFECTED (NOTE THAT THIS PIECE OF INFORMATION IS PRINTED OUT FOR THE OPERATOR IN THE FORM "UNIT XX" AT THE BEGINNING OF EACH SERIES). QUESTION 1 IS RESPONDED TO BY A <CR>, SO SLOT ONE STAYS A CONSTANT 75 IN TABLES 21 THRU 64, SINCE NO NEW EXPLICIT VALUES ARE TYPED IN. SLOT TWO GETS THE VALUES 21,22,23,...,49 IN TABLES 21 THRU 49, AND GETS A 49 IN SLOT 50, AND GETS THE VALUES 51,52,53,...,64 IN TABLES 51 THRU 64. SLOT THREE GETS THE VALUE 77 IN TABLES 21 THRU 64.

THE DIALOGUE IS TERMINATED WHEN THE SOFTWARE RECOGNIZES THAT 64 EXPLICIT VALUES HAVE BEEN GIVEN FOR AT LEAST ON QUESTION (NAMELY QUESTION 2).

2.5 HARDWARE PARAMETERS

THE FOLLOWING QUESTIONS WILL BE ASKED ON A START COMMAND. THE VALUE LOCATED TO THE LEFT OF THE QUESTION MARK IS THE DEFAULT VALUE THAT WILL BE TAKEN ON A CARRIAGE RETURN RESPONSE.

RL11 (L) Y?

ANSWER YES(Y) IF YOU HAVE AN RL11 CONTROLLER, NO(N) IF YOU HAVE AN RLV11 CONTROLLER.

BUS ADDRESS (O) 174400?

ANSWER WITH THE BUS ADDRESS OF THE CONTROLLER.

VECTOR (O) 330?

ANSWER WITH THE INTERRUPT VECTOR OF THE CONTROLLER.

BR LEVEL (O) 5?

ANSWER WITH THE INTERRUPT PRIORITY OF THE CONTROLLER.

DRIVE (O) 0?

ANSWER WITH THE DRIVE(S) CONNECTED TO THE CONTROLLER.

2.6 SOFTWARE PARAMETERS

THE FOLLOWING QUESTIONS ARE ASKED IF REQUESTED ON A START, RESTART, OR CONTINUE. THEY ALLOW FLEXABILITY IN THE WAY THE PROGRAM BEHAVES. THE SOFTWARE PARAMETERS GIVE THE PROGRAM FLEXIBILITY IN THE WAY IT RUNS. THE PARAMETERS CAN BE MODIFIED ON A START, RESTART, OR CONTINUE BY ANSWERING (Y)ES TO THE FOLLOWING QUESTION:

CHANGE S.W. ?

A YES ANSWER WILL ASK THE FOLLOWING SOFTWARE PARAMETER QUESTIONS, WITH THE PRESENT DEFAULT VALUE PRINTED TO THE LEFT OF THE QUESTION MARK. (THE LAST ANSWER GIVEN IS THE DEFAULT) THE DEFAULT IS TAKEN ON A <CR>. CONTROL Z (^Z) WILL DEFAULT ALL REMAINING QUESTIONS AND START THE TEST.

DROP ON ERROR LIMIT (L) Y?

TO ALLOW THE UNIT TO BE DROPPED ONCE A PREDETERMINED NUMBER OF ERRORS ARE ENCOUNTERED.

ANSWER Y OR N

ERROR LIMIT (D) 10?

NUMBER OF ERRORS ALLOWED BEFORE DROPPING UNIT.

ANSWER 1 TO 65K

AUTOSIZE (L) N?

TO CHECK TO SEE IF UNIT SPECIFIED ACTUALLY EXISTS BEFORE TESTING IT (VIA DRIVE READY), IF NOT UNIT WILL NOT BE TESTED.

ANSWER Y OR N

COMPARE DATA ON DCK (L) N?

WHEN A DATA CHECK IS ENCOUNTERED AND DATA IS KNOWN, ALLOW AN INCORE COMPARISON OF DATA.

ANSWER Y OR N

OF WORDS IN ERROR REPORTED (D) 3?

NUMBER OF MISCOMPARES TO BE PRINTED ON CONSOLE DEVICE.

ANSWER 0 - 128

3.0 ERROR INFORMATION

ALL ERROR INFORMATION IS PRINTED ON THE CONSOLE DECIVE. ERROR REPORTS ARE AIMED AT BEING SELF EXPLANATORY. THE GENERAL FORMAT IS:

DZRL? XXX ERR YYYYY TST ZZZ SUB PPP PC: RRRRRR

WHERE:

? IS PROGRAM LETTER
XXX IS SFT - SOFT ERROR
HRD - HARD ERROR

DV FAT - DEVICE FATAL ERROR
 SYS FAT - SYSTEM FATAL ERROR
 VVVVY IS THE ERROR NUMBER
 ZZZ IS THE TEST NUMBER
 PPP IS THE SUBTEST NUMBER
 RRRRRR IS THE PROGRAM LISTING LOCATION

ERRORS GIVE THE REGISTER CONTENTS BEFORE AND AFTER THE ERROR
 ALONG WITH A ONE LINE DESCRIPTION AND RELEVANT DATA.

EXAMPLE:

ONE LINE DESCRIPTION
 (OPTIONAL SECOND LINE)
 (OPTIONAL THIRD LINE)
 BEFORE COMMAND: CS:XXXXXX BA:XXXXXX DA:XXXXXX MP:XXXXXX
 TIME OF ERROR: CS:XXXXXX BA:XXXXXX DA:XXXXXX MP:XXXXXX XXXXXX XXXXXX

3.2 ERROR HALTS

ERROR HALTS ARE SUPPORTED PER DESCRIBED IN THE PREVIOUS SECTION WITH
 /FLAG:HOE. THERE ARE NO OTHER HALTS.

4.0 PERFORMANCE AND PROGRESS REPORTS

4.1 PERFORMANCE REPORTS

THIS PROGRAM WILL NOT GIVE ANY PERFORMANCE REPORTS.

4.2 PROGRESS REPORTS

THIS PROGRAM WILL NOT GIVE ANY PROGRESS REPORTS.

5.0 DEVICE INFORMATION TABLES

THE RL11/RLV11 CONTROLLER HAS THE FOLLOWING FOUR(4) REGISTERS FOR
 CONTROL OF THE SUBSYSTEM.

RLCS - CONTROL AND STATUS REGISTER (XXXXX0)

 BIT 15 - COMPOSITE ERROR
 BIT 14 - DRIVE ERROR
 BIT 13 - NON EXISTANT MEMORY ERROR
 BIT 12 - HEADER NOT FOUND (WITH BIT 10 SET)
 - DATA LATE (WITH BIT 10 CLEAR)
 BIT 11 - HEADER CRC (WITH BIT 10 SET)
 - DATA CRC (WITH BIT 10 CLEAR)
 BIT 10 - OPERATION INCOMPLETE
 BIT 9/8 - DRIVE SELECT (0-3)

BIT 7 - CONTROLLER READY
 BIT 6 - INTERRUPT ENABLE
 BIT 5 - EXTENDED BUS ADDRESS (BIT 17)
 BIT 4 - EXTENDED BUS ADDRESS (BIT 16)
 BIT 3-1 - FUNCTION CODE
 0 - NOP (PDP-11) MAINT (LSI-11)
 1 - WRITE CHECK
 2 - GET DRIVE STATUS
 3 - SEEK
 4 - READ HEADER
 5 - WRITE DATA
 6 - READ DATA
 7 - READ WITHOUT HEADER COMPARE

BIT 0 - DRIVE READY

RLBA - BUS ADDRESS REGISTER (XXXXX2)

BITS 15-1 BUS ADDRESS OF DATA TRANSFER
 BIT 0 SHOULD BE 0

RLDA - DISK ADDRESS REGISTER (XXXXX4)

FOR READ/WRITE FUNCTIONS

BIT 15 - MUST BE ZERO(0)
 BIT 14-7 - CYLINDER ADDRESS FOR TRANSFER
 BIT 6 - SURFACE FOR TRANSFER
 BIT 5-0 - SECTOR FOR TRANSFER (0-47)

FOR SEEK FUNCTION

BIT 15 - MUST BE ZERO(0)
 BIT 14-7 - DIFFERENCE TO NEW CYLINDER
 BIT 6-5 - MUST BE ZERO(0)
 BIT 4 - SURFACE
 BIT 3 - MUST BE ZERO
 BIT 2 - SEEK DIRECTION(1 - IN / 0 - OUT)
 BIT 1 - MUST BE ZERO
 BIT 0 - MUST BE ONE(1)

FOR GET STATUS FUNCTION

BIT 15-4 - IGNORED SHOULD BE ZERO
 BIT 3 - DRIVE RESET
 BIT 2 - MUST BE ZERO
 BIT 1 - MUST BE ONE
 BIT 0 - MUST BE ONE

RLMP - MULTIPURPOSE REGISTERFOR READ/WRITE FUNCTION

BIT 15 - 0 - WORD COUNT(TWO'S COMPLIMENT)

FOR READ HEADER FUNCTION

BIT 15-0 - DISK HEADER OF SECTOR (FIRST READ)
 - ZERO WORD (SECOND READ)
 - HEADER CRC (THIRD READ)

FOR GET STATUS FUNCTION

HAS DRIVE STATUS

BIT 15 - WRITE DATA ERROR
 BIT 14 - CURRENT HEAD ERROR(CHE)
 BIT 13 - WRITE LOCK STATUS(WL)
 BIT 12 - SEEK TIME OUT(SKTO)
 BIT 11 - SPIN ERROR(SPE)
 BIT 10 - WRITE GATE ERROR(WGE)
 BIT 9 - VOLUME CHECK(VC)
 BIT 8 - DRIVE SELECT ERROR(DSE)
 BIT 7 - RESERVED(0)
 BIT 6 - SURFACE
 BIT 5 - COVER OPEN
 BIT 4 - HEADS HOME
 BIT 3 - BRUSHES HOME
 BIT 2-0 - STATE BITS
 0 - LOAD STATE
 1 - SPIN UP
 2 - BRUSH CYCLE
 3 - LOAD HEADS
 4 - SEEK - TRACK COUNTING
 5 - SEEK - LINEAR MODE
 6 - UNLOAD HEADS
 7 - SPIN DOWN

6.0 TEST SUMMARIES

TEST 01 - WRITE NPR INTEGRITY

THIS TEST WILL VERIFY THAT THE WRITE FUNCTION WILL NOT CAUSE
 A BUS TRAP THEREFORE VERIFYING THE NPR LOGIC BETWEEN THE
 CONTROLLER AND PROCESSOR.

TEST 02 - WRITE FUNCTION

THIS TEST WILL VERIFY THAT THE WRITE FUNCTION WILL RESET CONTROLLER READY AND POST NO ERRORS.

TEST 03 - WRITE FUNCTION INTERRUPT

THIS TEST WILL VERIFY THAT THE WRITE FUNCTION WILL GENERATE AN INTERRUPT ON COMPLETION.

TEST 04 - PROPER INCREMENT OF RLBA ON WRITE

THIS TEST WILL VERIFY THAT THE BUS ADDRESS REGISTER INCREMENTS PROPERLY ON A WRITE FUNCTION.

TEST 05 - PROPER INCREMENT OF RLDA ON WRITE

THIS TEST WILL VERIFY THAT THE DISK ADDRESS REGISTER INCREMENTS PROPERLY ON A WRITE FUNCTION.

TEST 06 - FORCE HEADER NOT FOUND WITH WRITE

THIS TEST WILL FORCE A HEADER NOT FOUND ERROR ON A WRITE. THE RLDA IS SET UP TO LOOK FOR SECTOR 40, A WRITE IS THEN ISSUED. THE HEADER NOT FOUND ERROR SHOULD THEN SET.

TEST 07 - FORCE INTERRUPT WITH HNF

THIS TEST WILL FORCE A HEADER NOT FOUND ERROR UNDER INTERRUPT CONTROL.

TEST 08 - CHECK OPI TIME WITH HNF

THIS TEST WILL TIME THE SETTING OF HNF (OPI) FROM ISSUANCE. THIS IS DONE BY ISSUING A WRITE TO SECTOR 40. THE TIME OF OPI SHOULD BE AROUND 200 MILLISECONDS.

TEST 09 - MULTIPLE SECTOR TRANSFER ON WRITE

THIS TEST THE ABILITY FOR THE WRITE FUNCTION TO WRITE MORE THAN ONE SECTOR. WE SET UP FOR A TWO SECTOR WRITE.

TEST 10 - CHECK DIRECTION OF WRITE NPR

THIS TEST WILL VERIFY THAT THE NPR DIRECTION OF A WRITE FUNCTION IS FROM MEMORY TO THE CONTROLLER. THIS IS DONE BY WRITING A PATTERN IN MEMORY AND ISSUING A WRITE, THEN CHECKING MEMORY TO VERIFY THAT IT DID NOT GET DISTURBED.

TEST 11 - CHECK FULL INCREMENT OF RLBA

THIS TEST WILL CHECK THAT THE RLBA CAN INCREMENT OF THE FULL 16 BIT RANGE. THIS IS DONE BY ISSUING A ONE WORD WRITE TO CHECK EACH BIT TOGGLE FROM 1-0 AND 0-1. THIS IS DONE FROM 0 TO 177776 REGARDLESS OF MEMORY SIZE.

TEST 12 - BA BIT 16 INCREMENT

THIS TEST WILL CHECK THAT BUS ADDRESS BIT 16 WILL SET WHEN THE RLBA IS 177776. AND THAT THE RLBA GOES TO 0.

TEST 13 - BA BIT 17 INCREMENT

THIS TEST WILL CHECK THAT BUS ADDRESS BIT 17 WILL SET WHEN BIT 16 AND THE RLBA ARE SET. THE RLBA AND BIT 16 ARE CHECKED TO GO TO ZERO.

TEST 14 - READ NPR INTEGRITY

THIS TEST WILL VERIFY THAT THE READ FUNCTION WILL NOT CAUSE A BUS TRAP THEREFORE VERIFYING THE NPR LOGIC BETWEEN THE CONTROLLER AND PROCESSOR.

TEST 15 - READ FUNCTION

THIS TEST WILL VERIFY THAT THE READ FUNCTION WILL RESET CONTROLLER READY AND POST NO ERRORS.

TEST 16 - READ FUNCTION INTERRUPT

THIS TEST WILL VERIFY THAT THE READ FUNCTION WILL GENERATE AN INTERRUPT ON COMPLETION.

TEST 17 - CHECK DIRECTION OF READ NPR

THIS TEST WILL VERIFY THAT THE NPR DIRECTION OF A READ FUNCTION IS FROM CONTROLLER TO THE MEMORY. THIS IS DONE BY WRITING A PATTERN IN MEMORY AND ISSUING A READ, THEN CHECKING MEMORY TO VERIFY THAT IT DID NOT GET DISTURBED.

TEST 18 - PROPER INCREMENT OF RLBA ON READ

THIS TEST WILL VERIFY THAT THE BUS ADDRESS REGISTER INCREMENTS PROPERLY ON A READ FUNCTION.

TEST 19 - PROPER INCREMENT OF RLDA ON READ

THIS TEST WILL VERIFY THAT THE DISK ADDRESS REGISTER INCREMENTS PROPERLY ON A READ FUNCTION.

TEST 20 - FORCE HEADER NOT FOUND WITH READ

THIS TEST WILL FORCE A HEADER NOT FOUND ERROR ON A READ. THE RLDA IS SET UP TO LOOK FOR SECTOR 40, A READ IS THEN ISSUED. THE HEADER NOT FOUND ERROR SHOULD THEN SET.

TEST 21 - FORCE INTERRUPT WITH HNF

THIS TEST WILL FORCE A HEADER NOT FOUND ERROR UNDER INTERRUPT CONTROL.

TEST 22 - CHECK HEADER COMPARE LOGIC

THIS TEST WILL EXTENSIVELY CHECK THE CYLINDER AND HEAD BITS OF THE HEADER WORD TO COMPARE CORRECTLY. THIS IS DONE BY WALKING AND GROWING 0'S AND 1'S THRU THE PROPER RLDA BITS AND ISSUING READ TO SEE IF ALL BIT POSITIONS CAN COMPARE.

TEST 23 - MULTIPLE SECTOR TRANSFER ON READ

THIS TEST THE ABILITY FOR THE READ FUNCTION TO WRITE MORE THAN ONE SECTOR. WE SET UP FOR A TWO SECTOR READ.

TEST 24 - FORCE HNF AT END OF TRACK

THIS TEST WILL CHECK THE ABILITY TO DETECT HEADER NOT FOUND AT THE END OF A TRACK. THIS DONE BY SETTING UP FOR A TWO SECTOR READ AT SECTOR 39.

TEST 25 - FORCE NON-EXISTANT MEMORY ERROR

THIS TEST WILL CHECK THAT THE NON-EXISTANT MEMORY ERROR (NXM) CAN SET. WE WILL ISSUE A READ TO THE MAXIMUM ADDRESS AND EXPECT A NXM ERRCR. (THIS TEST WILL NOT BE DONE ON A 128K MACHINE.)

TEST 26 - FORCE NXM UNDER INTERRUPT

THIS TEST WILL ATTEMPT TO FORCE AN INTERRUPT VIA NXM. (THIS TEST WILL NOT BE DONE ON A 128K MACHINE.)

TEST 27 - CHECK READ WRITE LOOP

THIS TEST WILL WRITE A PATTERN TO SECTOR 0 AND TRY TO RECOVER IT WITH A WRITE.

TEST 28 - CHECK OF SILO LINES

THIS TEST WILL CHECK THAT WE CAN WRITE AND READ UNIQUE BIT PATTERNS VERIFY THAT THE LINES ON THE SILO ARE NOT STUCK OR TIED TOGETHER. THIS IS DONE WITH WALKING AND GROWING 0'S AND 1'S.

TEST 29 - CHECK THROUGHPUT OF SILO

THIS TEST WILL ATTEMPT TO CHECK THAT THE FALL THROUGH OF THE SILO IS WORKING CORRECTLY. WE WRITE A SECTOR OF 128 UNIQUE PATTERNS AND READ IT BACK CHECKING THAT EACH LOCATION IS UNIQUE AND CORRECT.

TEST 30 - CHECK ZERO FILL ON WRITE

THIS TEST WILL CHECK THE ABILITY OF THE CONTROLLER TO FILL THE REMAINING SECTOR WITH ZEROS ON A WRITE. WE WRITE A SECTOR WITH FROM 1 TO 127 WORDS, READ IT BACK AND VERIFY THAT THE NON WRITTEN WORDS ARE ZERO.

TEST 31 - CHECK SECTOR BITS ON HEADER COMPARE

THIS TEST WILL CHECK THAT THE SECTOR BITS CAN COMPARE CORRECTLY. THIS IS DONE BY WRITING THE SECTORS ADDRESS INTO THE SECTOR FOR A FULL TRACK. EACH SECTOR IS READ TO VERIFY THE SECTOR HAS THE CORRECT DATA, IF NOT THEN THE SECTOR BITS ARE NOT COMPARING CORRECTLY.

TEST 32 - WRITE CHECK NPP INTEGRITY

THIS TEST WILL CHECK THAT THE WRITE CHECK WILL FUNCTION WITHOUT CAUSING A BUS TRAP. TEST IS SET UP TO HANDLE BUS TRAPS.

TEST 33 - WRITE CHECK FUNCTION

THIS TEST WILL CHECK THAT A WRITE CHECK FUNCTION WILL COMPLETE WITH THE SPECIFIED TIME WITHOUT POSTING ERRORS.

TEST 34 - WRITE CHECK FUNCTION INTERRUPT

THIS TEST WILL CHECK THAT AN INTERRUPT CAN BE GENERATED FROM ISSUING A WRITE CHECK.

TEST 35 - PROPER INCREMENT OF RLBA ON WRITE CHECK

THIS TEST WILL CHECK THAT THE RLBA INCREMENTS PROPERLY DURING A

WRITE CHECK.

TEST 36 - PROPER INCREMENT OF RLDA ON WRITE CHECK

THIS TEST WILL CHECK THAT THE RLDA INCREMENTS PROPERLY DURING A WRITE CHECK.

TEST 37 - MULTIPLE SECTOR WRITE CHECK

THIS TEST WILL CHECK THAT WE CAN WRITE CHECK MORE THAN ONE SECTOR AT A TIME.

TEST 38 - FORCE DCK WITH WRITE CHECK

THIS TEST WILL CHECK THAT WE CAN DETECT A DCK DURING A WRITE CHECK. THIS IS DONE BY MODIFYING MEMORY BETWEEN A WRITE AND A WRITE CHECK.

TEST 39 - FORCE DCK WITH WRITE CHECK INTERRUPT

THIS TEST WILL CHECK THAT A DCK DURING A WRITE CHECK WILL CAUSE AN INTERRUPT TO OCCUR.

TEST 40 - CHECK ZERO FILL ON WRITE WITH WRITE CHECK

THIS TEST WILL VERIFY THAT WE CAN SUCCESSFULLY WRITE CHECK ALL WORD COUNTS FROM 1 - 127.

TEST 41 - 42 - EXTENDED CHECK OF WRITE CHECK

THESE TESTS VERIFY THAT WE CAN WRITE CHECK SUCCESSFULLY ALL PATTERNS. PATTERNS USED ARE WALKING 1'S, 0'S, GROWING 1'S, 0'S.

TEST 43 - READ WITHOUT HEADER COMPARE

THIS TEST VERIFIES THAT THE FUNCTION READ WITHOUT HEADER COMPARE (7) RESETS THE CONTROLLER READY AND POSTS NO ERRORS. THE DISK ADDRESS IS SET TO ALL ONES.

TEST 44 - READ WITHOUT HEADER COMPARE INTERRUPT

THIS TEST WILL VERIFY THAT THE FUNCTION READ WITHOUT HEADER COMPARE (7) CAN GENERATE AN INTERRUPT ON COMPLETION.

TEST 45 - CHECK RD W/O HDR CMP READS

THIS TEST CHECKS THAT THE FUNCTION CAN ACTUALLY RECOVER DATA. WE WRITE A PATTERN IN MEMORY AND CHECK THAT THE FUNCTION CAN OVERLAY IT WITH DATA.

TEST 46 - CHECK RLBA INCREMENT WITH RD W/O HDR CMP

THIS TEST CHECKS THAT THE RLBA CAN INCREMENT PROPERLY ON THE
FUNCTION.

TEST 47 - CHECK RLDA DOES INCREMENT

THIS TEST CHECKS THAT THE RLDA DOES INCREMENT WITH THE
FUNCTION READ WITHOUT HEADER COMPARE.

```

1          .ENABLE AMA
2          .ENABLE ABS
3          .WLIST ME,CND,HD
4
5
6
7
8
9
10         002000          .-=2000
11
12         002000          SVC
13         000000          SVCINS=0
14         000000          SVCTAG=0
15
16         002000          POINTER BCNSM,BCNSFT,BCNDU
17
18         002000          BGNMOD MHEDR
19
20         002000          HEADER CZRLB,B,0,60,60,4,RL01
21         002001          .ASCII /C/
22         002002          .ASCII /Z/
23         002003          .ASCII /R/
24         002004          .ASCII /L/
25         002005          .ASCII /B/
26         002006          .BYTE 0
27         002007          .BYTE 0
28         002010          .ASCII /B/
29         002011          .ASCII /O/
30         002012          .WORD 0
31         002013          .WORD 4
32         002014          .WORD L$HARD
33         002015          .WORD L$SOFT
34         002016          .WORD L$HW
35         002017          .WORD L$SW
36         002018          .WORD L$LAST
37         002019          .WORD 0
38         002020          .WORD 0
39         002021          .WORD 0
40         002022          .WORD L$DISPATCH
41         002023          .WORD 0
42         002024          .WORD 0
43         002025          .WORD 0
44         002026          .WORD 0
45         002027          .WORD 0
46         002028          .WORD 0
47         002029          .WORD 0
48         002030          .WORD 0
49         002031          .WORD 0
50         002032          .WORD 0
51         002033          .WORD 0
52         002034          .WORD 0
53         002035          .WORD 0
54         002036          .WORD 0
55         002037          .WORD 0
56         002038          .WORD 0
57         002039          .WORD 0
58         002040          .WORD 0
59         002041          .WORD 0
60         002042          .WORD 0
61         002043          .WORD 0
62         002044          .WORD 0
63         002045          .WORD 0
64         002046          .WORD 0
65         002047          .WORD 0
66         002048          .WORD 0
67         002049          .WORD 0
68         002050          .WORD 0
69         002051          .WORD 0
70         002052          .WORD 0
71         002053          .WORD 0
72         002054          .WORD 0
73         002055          .WORD 0
74         002056          .WORD 0
75         002057          .WORD 0
76         002058          .WORD 0
77         002059          .WORD 0
78         002060          .WORD 0
79         002061          .WORD 0
80         002062          .WORD 0
81         002063          .WORD 0
82         002064          .WORD 0
83         002065          .WORD 0
84         002066          .WORD 0
85         002067          .WORD 0
86         002068          .WORD 0
87         002069          .WORD 0
88         002070          .WORD 0
89         002071          .WORD 0
90         002072          .WORD 0
91         002073          .WORD 0
92         002074          .WORD 0
    
```

```

(4) 002076 020104          .WORD L$DU
(4) 002100 000000          .WORD 14
(4) 002102 000000          .WORD 0
(4) 002104 017140          .WORD L$INIT
(4) 002106 020010          .WORD L$CLEAN
31
32 002110          ENDMOD
33
34
35 002110          DEVREG
36 002110 000000          .WORD 0
37 002112 000001          .BLKW
38
39 002114          DEVTYP <RL01>
40 002114 046122 030460 000 .ASCIIZ /RL01/
41 002122          .EVEN
42 002122          BGNMOD GLBEQAT
43 002122          EQUALS
44 DRDY=BIT0          ;DRIVE READY (RLCS)
45 INTEN=BIT6         ;INTERRUPT ENABLE (RLCS)
46 ERR=BIT15         ;RL11 ERROR (RLCS)
47 DERR=BIT14        ;RL01 DRIVE ERROR (RLCS)
48 OPI=BIT10         ;OPERATION INCOMPLETE (RLCS)
49 CRDY=BIT7         ;CONTROLLER READY (RLCS)
50 BA17=BIT5         ;EXTENDED ADDRESS BIT 17 (RLCS)
51 BA16=BIT4         ;EXTENDED ADDRESS BIT 16 (RLCS)
52 NXM=BIT13        ;NON-EXISTANT MEMORY (RLCS)
53 DSO=0            ;DRIVE SELECT 0 (RLCS)
54 DS1=BIT8         ;DRIVE SELECT 1 (RLCS)
55 DS2=BIT9         ;DRIVE SELECT 2 (RLCS)
56 DS3=BIT8#BIT9    ;DRIVE SELECT 3 (RLCS)
57 NODPO=0         ;FUNCTION-NOOP(0)
58 WRCHK=BIT1       ;WRITE CHECK FUNCTION
59 GSTAT=BIT2       ;GET STATUS FUNCTION
60 SEEK=BIT2#BIT1   ;SEEK FUNCTION
61 RDHDR=BIT3       ;READ HEADER FUNCTION
62 WRITE=BIT3#BIT1  ;WRITE DATA FUNCTION
63 READ=BIT3#BIT2   ;READ DATA FUNCTION
64 RDNRHD=BIT3#BIT2#BIT1 ;READ W/O HEADER VERIFICATION
65 GODRVR=BIT1#BIT7 ;CRDY AND DRDY
66 DRST=BIT3       ;DRIVE RESET (RLDA)
67 CSBIT=BIT1       ;GET STATUS BIT (RLDA)
68 MK=BIT0          ;MARKER BIT (RLDA)
69 SIGN=BIT2       ;SIGN BIT (RLDA)
70 RHNS=BIT6       ;HEAD SELECT IN READ HEADER
71 STHS=BIT6       ;HEAD SELECT IN STATUS BACK
72 DAHS=BIT4       ;HEAD SELECT IN SEEK
73 ;OFFSET FOR HARDWARE P-TABLE
74
75 CSR=0
76 VECT=2
77 PRIOR=4
78 DRP=6
79 CNT=10
80
81 ;OFFSET FOR SOFTWARE P-TABLE
    
```

88	GLOBAL DATA
154	LIST TO CHECK HEADER COMPARE LOGIC
221	BUFFER FOR READ/WRITE
227	GLOBAL TEXT
334	GLOBAL ERRORS
580	INITIALIZATION CODE
723	GLOBAL SUBROUTINES
757	ROUTINE TO CHECK FOR CONTROLLER ERRORS
819	LOAD RLCS
1056	**TEST 1** - WRITE NPR INTEGRITY
1107	**TEST 2** - WRITE FUNCTION
1163	**TEST 3** - WRITE FUNCTION INTERRUPT
1203	**TEST 4** - PROPER INCREMENT OF RLBA ON WRITE
1248	**TEST 5** - PROPER INCREMENT OF RLDA ON WRITE
1291	**TEST 6** - FORCE HEADER NOT FOUND WITH WRITE
1334	**TEST 7** - FORCE HEADER NOT FOUND WITH WRITE INTERRUPT
1390	**TEST 8** - CHECK DPI TIME WITH HDR NT FND
1453	**TEST 9** - MULTIPLE SECTOR TRANSFER ON WRITE
1506	**TEST 10** - CHECK DIRECTION OF WRITE NPR
1564	**TEST 11** - CHECK FULL RLBA INCREMENT
1614	**TEST 12** - BA BIT 16 INCREMENT
1670	**TEST 13** - BA BIT 17 INCREMENT
1726	**TEST 14** - TEST READ NPR INTEGRITY
1769	**TEST 15** - READ FUNCTION
1803	**TEST 16** - READ FUNCTION INTERRUPT
1843	**TEST 17** - CHECK READ NPR DIRECTION
1909	**TEST 18** - PROPER INCREMENT OF RLBA ON READ
1945	**TEST 19** - PROPER INCREMENT OF RLDA ON READ
1987	**TEST 20** - FORCE HEADER NOT FOUND WITH READ
2026	**TEST 21** - FORCE HEADER NOT FOUND WITH READ INTERRUPT
2075	**TEST 22** - CHECK HEADER COMPARE LOGIC
2206	**TEST 23** - CHECK MULTIPLE SECTORS ON READ
2265	**TEST 24** - FORCE HDR NT FND AT END OF TRACK
2301	**TEST 25** - FORCE NON-EXISTANT MEMORY ERROR
2344	**TEST 26** - FORCE NON-EXISTANT MEMORY ERROR INTERRUPT
2391	**TEST 27** - CHECK READ WRITE LOOP
2477	**TEST 28** - CHECK SILO LINES
2574	**TEST 29** - CHECK THROUGHPUT OF SILO
2670	**TEST 30** - CHECK ZERO FILL ON WRITE
2775	**TEST 31** - CHECK SECTOR BITS OF HEADER COMPARE
2887	**TEST 32** - WRITE CHECK NPR INTEGRITY
2970	**TEST 33** - WRITE CHECK FUNCTION
3035	**TEST 34** - WRITE CHECK FUNCTION INTERRUPT
3106	**TEST 35** - PROPER INCREMENT OF RLBA ON WRITE CHECK
3179	**TEST 36** - PROPER INCREMENT OF RLDA ON WRITE CHECK
3252	**TEST 37** - MULTIPLE SECTOR WRITE CHECK
3338	**TEST 38** - FORCE DCK WITH WRITE CHECK
3411	**TEST 39** - FORCE DCK WITH WRITE CHECK INTERRUPT
3495	**TEST 40** - CHECK ZERO FILL ON WRITE WITH WRITE CHECK
3572	**TEST 41** - EXTENDED CHECK OF WRITE CHECK FUNCTION
3653	**TEST 42** - EXTENDED CHECK OF WRITE CHECK FUNCTION
3734	**TEST 43** - READ WITHOUT HEADER COMPARE FUNCTION
3764	**TEST 44** - READ WITHOUT HEADER COMPARE FUNCTION INTERRUPT
3800	**TEST 45** - CHECK RD W/O HDR CMP ACTUALLY READS
3862	**TEST 46** - CHECK RLBA INCREMENT WITH RD W/O HDR CMP
3908	**TEST 47** - CHECK RLDA DOES INCREMENT WITH RD W/O HDR CMP

4015	DIAGNOSTIC SUPERVISOR -- LOW CORE SET UP
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```
78          000000          DLT=0
79          000002          ELT=2
80          000004          SIZE=4
81          000006          DMPCK=6
82          000010          DLMT=10
83
84
85 002122          ENDMOD
86 002122          GLBDAT
87
88          .SBTTL  GLOBAL DATA
89
90 002122 000000          CHECK:  .WORD  0
91 002124 000000          T.CRC:  .WORD  0
92 002126 000000          WHY:    .WORD  0
93 002130 000000          CDCNT:  .WORD  0
94 002132 000004          ERRVEC:  .WORD  4
95 002134 000000          DRIVE:  .WORD  0
96 002136 000000          UUT:    .WORD  0
97 002140 000000          UNITST: .WORD  0
98 002142 000000          TRPFLG: .WORD  0
99 002144 000000          INTELG: .WORD  0
100 002146 000000          LDCSR:  .WORD  0
101 002150 000077          SECMSK: .WORD  77
102 002152 120001          XPOLY:  .WORD 120001
103 002154 000000          BCCFBK: .WORD  0
104 002156 000000          CALBCC: .WORD  0
105 002160 000000          TMP0:   .WORD  0
106 002162 000000          TMP1:   .WORD  0
107 002164 000000          TMP2:   .WORD  0
108 002166 000000          GDDAT:  .WORD  0
109 002170 000000          RDDAT:  .WORD  0
110 002172 000000          TEMP3:  .WORD  0
111 002174 000000          TEMP4:  .WORD  0
112 002176 000000          TEMP5:  .WORD  0
113 002200 000000          FIRST:  .WORD  0
114 002202 177700          CYLMSK: .WORD 177700
115 002204 000050          MXSECT: .WORD  40
116 002206 000047          MAXSEC: .WORD  39
117 002210 000040          DIBRD:  .WORD  0
118 002212 077600          MAXCYL: .WORD  77600
119 002214 000000          SVHD:   .WORD  0
120 002216 000000          B-CS:   .WORD  0
121 002220 000000          B-BA:   .WORD  0
122 002224 000000          B-DA:   .WORD  0
123 002226 000000          B-MP:   .WORD  0
124 002230 000000          E-CS:   .WORD  0
125 002232 000000          E-BA:   .WORD  0
126 002234 000000          E-DA:   .WORD  0
127 002236 000000          E-MP:   .WORD  0
128 002240 000000          DRD1:   .WORD  0
129 002242 000000          E-MP2:  .WORD  0
130 002244 000000          RLCS:   .WORD  0
131 002246 000000          RLBA:   .WORD  0
132 002248 000000          RLDA:   .WORD  0
133 002250 000000          RLMP:   .WORD  0

;INTERRUPT OCCURANCE FLAG
;LOCATION TO FORM RLCS
;MASK OUT SECTOR
;POLYNOMIAL FOR CRC 16
;LOCATION USED BY *SIMBCC*
;LOCATION USED BY *SIMBCC*

;LOCATION USED BY *SIMBCC*
;LOCATION USED BY *SIMBCC*
;LOCATION USED BY *SIMBCC*
;FIRST SECTOR READ
;MASK CYLINDER AND HEAD SELECT
;MAX SECTOR ADDRESS +1
;MAX SECTOR ADDRESS
;DIFFERENCE WORD (SEEK)
;MAXIMUM CYLINDER ADDRESS
;SAVE CURRENT HEAD SELECT
;CS - BEFORE OPERATION
;BA - BEFORE OPERATION
;DA - BEFORE OPERATION
;MP - BEFORE OPERATION
;CS - AT OCCURANCE OF ERROR
;BA - AT OCCURANCE OF ERROR
;DA - AT OCCURANCE OF ERROR
;MP - AT OCCURANCE OF ERROR
```

```
134 002252 000000          BCSR:   .WORD  0
135 002254 000000          BVEC:   .WORD  0
136 002256 000000          BPRIOR: .WORD  0
137 002260 000000          PNDPNC: .WORD  0
138 002262 000000          XHEW:   .WORD  0
139 002264 000000          TRVFLG: .WORD  0
140 002266 000000          ERFLG:  .WORD  0
141 002270 001212          LOPIWX: .WORD  650
142 002272 000233          LOPIWN: .WORD  155
143 002274 000620          UOPIWX: .WORD  400
144 002276 000740          UOPIWN: .WORD  160
145 002300 000000          OPIWX:  .WORD  0
146 002302 000000          OPIWN:  .WORD  0
147 002304 000000          PWRFLG: .WORD  0
148 002306 000000          T.CHTLR: .WORD  0
149 002310 000000          DERFLG: .WORD  0
150 002312 000000          ERDIWT: .WORD  0
151 002314 000074          ERCOUNT: .BLKW 60
152
153          .SBTTL  LIST TO CHECK HEADER COMPARE LOGIC
154          HDRTAB: .WORD  0 ;WALK 1
155 002504 000000          .WORD  BIT0
156 002506 000001          .WORD  BIT1
157 002510 000002          .WORD  BIT1
158 002512 000004          .WORD  BIT2
159 002514 000010          .WORD  BIT3
160 002516 000020          .WORD  BIT4
161 002518 000040          .WORD  BIT5
162 002522 000100          .WORD  BIT6
163 002524 000200          .WORD  BIT7
164 002526 000400          .WORD  BIT8
165 002530 001000          .WORD  BIT9
166 002532 002000          .WORD  BIT10
167 002534 004000          .WORD  BIT11
168 002536 010000          .WORD  BIT12
169 002540 020000          .WORD  BIT13
170 002542 040000          .WORD  BIT14
171 002544 000003          .WORD  3
172 002546 000007          .WORD  7
173 002550 000017          .WORD  17
174 002552 000037          .WORD  37
175 002554 000137          .WORD  137
176 002556 000337          .WORD  337
177 002560 000737          .WORD  737
178 002562 001337          .WORD  1337
179 002564 002737          .WORD  2737
180 002566 007737          .WORD  7737
181 002570 017737          .WORD  17737
182 002572 037737          .WORD  37737
183 002574 077737          .WORD  77737
184 002576 077736          .WORD  77736
185 002600 077734          .WORD  77734
186 002602 077730          .WORD  77730
187 002604 077720          .WORD  77720
188 002606 077700          .WORD  77700
189 002610 077600          .WORD  77600

;GROW 1
;GROW 0
```

190	002612	077400				.WORD	77400
191	002613	077400				.WORD	77000
192	002614	076000				.WORD	76000
193	002615	070000				.WORD	70000
194	002616	070000				.WORD	70000
195	002617	060000				.WORD	60000
196	002618	077735				.WORD	77735
197	002619	077733				.WORD	77733
198	002620	077727				.WORD	77727
199	002621	077437				.WORD	77437
200	002622	077437				.WORD	77437
201	002623	077537				.WORD	77537
202	002624	077337				.WORD	77337
203	002625	078337				.WORD	78337
204	002626	078337				.WORD	78337
205	002627	078337				.WORD	78337
206	002628	078337				.WORD	78337
207	002629	057737				.WORD	57737
208	002630	037737				.WORD	37737
209	002631	000000				.WORD	0
210	002632	000000				.WORD	0
211	002662	000000				.WORD	0
212	002670	000000				.WORD	0
213	002733	177777				.WORD	177777
214	002734	177777				.WORD	177777
215	002776	177777				.WORD	177777
216	002776	177777				.WORD	177777
217	003033	000000				.WORD	0
218	003034	017777				.WORD	017777
219	003035	000000				.WORD	0
220	003036	000000				.WORD	0
221	003052	002000				.WORD	002000
222	007052					.WORD	
223	007052	047516	041440	047117		.WORD	047516 041440 047117
224	007052					.WORD	
225	007052					.WORD	
226	007052					.WORD	
227	007052					.WORD	
228	007052					.WORD	
229	007052					.WORD	
230	007052					.WORD	

233	007070	047516	042040	044522		WORDDY:	.ASCIZ	/NO DRIVE/
234	007101	103	035123	000040		ARLCS:	.ASCIZ	/CS/
235	007101	041104	035101	000040		ARLBA:	.ASCIZ	/BA:/
236	007101	042440	035101	000040		ARLDA:	.ASCIZ	/DA:/
237	007130	042506	042522	000040		BEREC:	.ASCIZ	/BEFORE COMMAND/
238	007151	124	046511	020105		AFREC:	.ASCIZ	/TIME OF ERROR/
239	007177	047503	052116	047522		CRATM:	.ASCIZ	/CONTROLLER TIMED OUT/
240	007177	104	044214	002349		DRITH:	.ASCIZ	/DRIVE READY TIMED OUT/
241	007225	047040	048530	000000		WTMMES:	.ASCIZ	/WTM/
242	007257	040	050117	000111		OPINES:	.ASCIZ	/OPT/
243	007264	044040	051103	000103		HCRMES:	.ASCIZ	/HCR/
244	007274	044040	043216	000000		HNFMES:	.ASCIZ	/HNF/
245	007304	042040	052314	000113		DCMES:	.ASCIZ	/DC/
246	007311	015	000000	000000		LF:	.ASCIZ	<15>
247	007313	015	000012			MSCRLF:	.ASCIZ	<15><12>
248	007313	041440	046517	000120		COMP:	.ASCIZ	/COMP/
249	007324	047506	047322	042105		OPIERR:	.ASCIZ	/FORCED OPI(GET STATUS) CAUSED OTHER ERRORS/
250	007324	116	047317	020120		NOPIES:	.ASCIZ	/NOOP OPERATION-INTR. MODE/
251	007430	047516	050117	047440		NOPIW:	.ASCIZ	/NOOP OPERATION-INTR. MODE/
252	007430	051127	052111	020105		WCKMES:	.ASCIZ	/WRITE CHECK OPERATION-FLAG MODE/
253	007522	051127	052111	020105		WCKINT:	.ASCIZ	/WRITE CHECK OPERATION-INTR. MODE/
254	007563	133	040505	020104		RHDWES:	.ASCIZ	/READ HEADER OPERATION-FLAG MODE/
255	007622	042533	040505	020104		RHDINT:	.ASCIZ	/READ HEADER OPERATION-INTR. MODE/
256	007663	042533	040505	047440		SEKMES:	.ASCIZ	/SEEK OPERATION-FLAG MODE/
257	007663	042533	040505	020113		SEKINT:	.ASCIZ	/SEEK OPERATION-INTR. MODE/
258	007747	107	052105	051440		GSTMES:	.ASCIZ	/GET STATUS OPERATION-FLAG MODE/
259	010006	042507	020124	052123		GSTINT:	.ASCIZ	/GET STATUS OPERATION-INTR. MODE/
260	010004	042533	040505	020104		RDDWES:	.ASCIZ	/READ OPERATION-FLAG MODE/
261	010007	042533	042501	047440		RDDINT:	.ASCIZ	/READ OPERATION-INTR. MODE/
262	010121	172	044201	042524		WRMES:	.ASCIZ	/WRITE OPERATION-FLAG MODE/
263	010161	157	044522	042524		WRINT:	.ASCIZ	/WRITE OPERATION-INTR. MODE/
264	010213	172	040505	020104		RDMWES:	.ASCIZ	/READ W/O HEADER - FLAG MODE/
265	010247	122	040505	020104		RDMINT:	.ASCIZ	/READ W/O HEADER - INTR. MODE/
266	010303	103	047101	052047		SKHOME:	.ASCIZ	/CAN'T SEEK TO TRACK 0/
267	010325	127	044522	042524		WRLOCK:	.ASCIZ	/WRITE LOCK ERROR/
268	010325	046152	051543	041440		EM1:	.ASCIZ	/RLCS CONTAINED FOLLOWING ERROR(S): /
269	010417	000170				EM100:	.BLKB	120-
270	010607	116	020117	047111		EM4:	.ASCIZ	/NO INTERRUPT ON READ OPERATION/
271	010646	042542	042101	047440		EM5:	.ASCIZ	/READ OPERATION DID NOT WRITE MEMORY/
272	010712	048523	040502	042040		EM9:	.ASCIZ	/RLBA DID NOT INCREMENT PROPERLY DURING READ/
273	010712	048523	040502	042040		EM10:	.ASCIZ	/HEADER NOT FOUND COULD NOT BE FORCED/
274	011043	110	040505	042504		EM11:	.ASCIZ	/WRONG CYLINDER ON SEEK/
275	011110	051127	047117	020107		EM12:	.ASCIZ	/HEADER NOT FOUND WOULD NOT SET/
276	011137	110	040505	042504		EM13:	.ASCIZ	/DRIVE READY WOULD NOT SET/
277	011176	051104	053111	020105		EM14:	.ASCIZ	/DISK ADDRESS INCORRECT AFTER MULTIPLE SECTOR READ/
278	011330	044504	045523	040440		EM15:	.ASCIZ	/WRITE ERROR ON WRITE OPERATION/
279	011341	116	020117	047111		EM16:	.ASCIZ	/NO INTERRUPT ON WRITE OPERATION/
280	011411	172	041114	020101		EM20:	.ASCIZ	/RLBA DID NOT INCREMENT PROPERLY DURING WRITE/
281	011466	042523	052103	051117		EM21:	.ASCIZ	/SECTOR DID NOT INCREMENT PROPERLY AFTER WRITE/
282	011544	044504	045523	040440		EM22:	.ASCIZ	/DISK ADDRESS (RLDA) INCORRECT AFTER MULTIPLE SECTOR WRITE/
283	011633	047516	020442	045109		EM23:	.ASCIZ	/HDR NOT FND COULD NOT BE FORCED AT END OF TRACK/
284	011716	047516	026516	054105		EM24:	.ASCIZ	/NOW-EXISTANT MEMORY ERROR COULD NOT BE FORCED/

289	011774	040504	040524	041440	EM25:	.ASCIZ	%DATA COMPARISON ERROR - READ/WRITE ERRORS
290							
291	012045	127	044522	042524	EM26:	.ASCIZ	/WRITE OPERATION MODIFIED MEMORY/
292	012105	125	051122	051117	EM27:	.ASCIZ	/ERROR ON PARTIAL SECTOR WRITE - ZERO FILL CHECK/
293	012165	122	041114	020101	EM30:	.ASCIZ	/RLBA DID NOT INCREMENT PROPERLY/
294	012225	102	020101	044502	EM31:	.ASCIZ	/BA BIT 16 DID NOT SET ON INCREMENT/
295	012270	040502	041040	052111	EM32:	.ASCIZ	/BA BIT 17 SET ON BA16 INCREMENT TEST/
296	012332	124	041114	020101	EM33:	.ASCIZ	/RLBA DID NOT INCREMENT WITH BA16/
297	012372	040502	041040	052111	EM34:	.ASCIZ	/BA BIT 17 DID NOT SET ON INCREMENT/
298	012441	102	020101	044502	EM35:	.ASCIZ	/BA BIT 16 DID NOT CLEAR ON INCREMENT/
299	012506	046122	040502	042040	EM36:	.ASCIZ	/RLBA DID NOT INCREMENT WITH BA17/
300	012547	122	040505	024104	EM40:	.ASCIZ	/READ(FUNCTION 7) DID NOT INTERRUPT/
301	012612	042522	042101	043050	EM41:	.ASCIZ	/READ(FUNCTION 7) ERROR - BAD DATA/
302	012653	042522	042101	043050	EM42:	.ASCIZ	/READ(FUNCTION 7) ERROR AT END OF TRACK/
303	012717	116	020117	047111	EM43:	.ASCIZ	/NO INTERRUPT WITH HDR WT FND FORCED/
304	012767	116	020117	047111	EM44:	.ASCIZ	/NO INTERRUPT WITH WFN FORCED/
305	013024	051105	047522	020122	EM45:	.ASCIZ	/ERROR ON BIT BANG OF SILOS
306	013056	044523	047514	047440	EM47:	.ASCIZ	/SILO OPERATION FAILURE/
307	013105	110	040505	042504	EM50:	.ASCIZ	/HEADER COMPARE FAILURE - SECTOR/
308	013145	127	044522	042524	EM51:	.ASCIZ	/WRITE WPR CAUSED BUS TRAP/
309	013179	122	040505	020104	EM52:	.ASCIZ	/READ WPR CAUSED BUS TRAP/
310	013230	042522	042101	053440	EM53:	.ASCIZ	/READ W/O HDR CMP OPERATION DID NOT WRITE MEMORY?
311	013310	046122	040502	042040	EM54:	.ASCIZ	?RLBA DID NOT INCREMENT PROPERLY DURING READ W/O HDR CMP?
312	013400	046122	040504	042040	EM55:	.ASCIZ	?RLDA DID NOT INCREMENT AFTER READ W/O HDR CMP?
313	013456	050117	020111	044524	EM56:	.ASCIZ	/OPT TIMING ERROR/
314	013479	127	044522	042524	EM57:	.ASCIZ	/WRITE CHECK WPR CAUSED BUS TRAP/
315	013537	127	044522	042524	EM60:	.ASCIZ	/WRITE CHECK DID NOT INTERRUPT/
316	013575	122	041114	020101	EM61:	.ASCIZ	/RLBA DID NOT INCREMENT PROPERLY DURING WRCHK/
317	013652	046122	040504	042040	EM62:	.ASCIZ	/RLDA DID NOT INCREMENT PROPERLY DURING WRCHK/
318	013727	122	042114	020101	EM63:	.ASCIZ	/RLDA DID NOT INCREMENT PROPERLY AFTER A MULTIPLE SECTOR WRITE CHK/
319	014031	127	044522	042524	EM64:	.ASCIZ	/WRITE CHECK OF PARTIAL SECTOR WRITE FAILURE/
320	014104	103	047101	047040	EM65:	.ASCIZ	/CAN NOT FORCE DCK ON WRITE CHECK/
321	014146	040503	020116	047516	EM66:	.ASCIZ	/CAN NOT FORCE INTERRUPT WITH DCK ON WRCHK/
322	014220	051127	052111	020105	EM70:	.ASCIZ	/WRITE CHECK FAILURE/
323							
324							
325							
326							
327							
328							
329							
330							
331	014244						ENDMOD
332	014244						BGNMOD GLBERR
333							
334							
335	014244						.SBTFL GLOBAL ERRORS
336							BGNMSG ERRO
337	014244	004737	015256				JSR PC,LINE1
338	014250	004737	015312				JSR PC,LINE2
339							
340							
341	014254	004537	020126				JSR R5,CKERLT ;INCREMENT ERROR AND CHECK LIMIT
342							
343	014260						ENDMSG
344	014260						L10000: EMT C\$MSG
345	014260	104023					EMT C\$MSG
346	014262						BGNMSG ERR1

46							
47	014262	004737	015256				JSR PC,LINE1
48							
49							
50	014266	004537	020126				JSR R5,CKERLT ;INCREMENT ERROR AND CHECK LIMIT
51							
52	014272						ENDMSG
53	014272	104023					L10001: EMT C\$MSG
54	014274						BGNMSG ERR2
55							
56	014274	004737	015256				JSR PC,LINE1
57	014300						PRINTB #FRMT4,GDDAT,BDDAT
58	014300	013746	002170				MOV GDDAT,-(SP)
59	014304	013746	002166				MOV BDDAT,-(SP)
60	014310	012746	015733				MOV #FRMT4,-(SP)
61	014314	012746	000003				MOV #3,-(SP)
62	014320	010600					MOV SP,R0
63	014324	104014					EMT C\$PNTB
64	014324	062706	000010				ADD #10,SP
65							
66	014330	004537	020126				JSR R5,CKERLT ;INCREMENT ERROR AND CHECK LIMIT
67							
68	014334						ENDMSG
69	014334						L10002: EMT C\$MSG
70	014334	104023					EMT C\$MSG
71	014336						BGNMSG ERR3
72							
73	014336	004737	015256				JSR PC,LINE1
74	014342	004737	015312				JSR PC,LINE2
75	014346						PRINTB #FRMT5,TMPO,BDDAT,GDDAT
76	014346	013746	002166				MOV GDDAT,-(SP)
77	014352	013746	002170				MOV BDDAT,-(SP)
78	014356	012746	015750				MOV TMPO,-(SP)
79	014362	012746	015771				MOV #FRMT5,-(SP)
80	014366	012746	000004				MOV #4,-(SP)
81	014372	010600					MOV SP,R0
82	014374	104014					EMT C\$PNTB
83	014376	062706	000012				ADD #12,SP
84							
85							
86							
87							
88							
89							
90	014402	004537	020126				JSR R5,CKERLT ;INCREMENT ERROR AND CHECK LIMIT
91							
92	014406						ENDMSG
93	014406						L10003: EMT C\$MSG
94	014406	104023					EMT C\$MSG
95	014410						BGNMSG ERR4
96							
97	014410	004737	015256				JSR PC,LINE1
98	014410	004737	015312				JSR PC,LINE2
99	014420						PRINTB #FRMT6,GDDAT,BDDAT
100	014420	013746	002170				MOV BDDAT,-(SP)


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(8) 014424 013746 002166      MOV      GDDAT,-(SP)
(7) 014430 012746 015733      MOV      #FRMT4,-(SP)
(6) 014434 012746 000003      MOV      SP,RO
(5) 014440 010600      EMT      CS,PNTB
(4) 014442 104014      EMT      CS,PNTB
(4) 014444 062706 000010      ADD      #10,SP
380
381 014450 004537 020126      JSR      R5,CKERLT      ;INCREMENT ERROR AND CHECK LIMIT
382
383
384 014454      ENDMSG
(3) 014454      L10004: EMT      C$MSG
(3) 014454 104023
385
386 014456      BGNMSG ERR5
387
388 014456 004737 015256      JSR      PC,LINE1
389 014462      PRINTB  #FRMT3,RESTMS
(8) 014462 013746 020440      MOV      RESTMS,-(SP)
(7) 014465 012746 015726      MOV      #FRMT3,-(SP)
(6) 014468 010600 000002      MOV      SP,RO
(5) 014470 104014      EMT      CS,PNTB
(4) 014500 104014      EMT      CS,PNTB
(4) 014502 062706 000006      ADD      #6,SP
390
391 014506 004537 020126      JSR      R5,CKERLT      ;INCREMENT ERROR AND CHECK LIMIT
392
393
394 014512      ENDMSG
(3) 014512      L10005: EMT      C$MSG
(3) 014512 104023
395
396 014514      BGNMSG ERR6
397
398 014514 004737 015256      JSR      PC,LINE1
399 014520 004737 015534      JSR      PC,LINE3
400 014524 004737 015312      JSR      PC,LINE2
401
402
403 014530      PRINTB  #FRMT99
(7) 014530 012746 016667      MOV      #FRMT99,-(SP)
(6) 014534 012746 000001      MOV      #1,-(SP)
(5) 014540 010600      MOV      SP,RO
(4) 014542 104014      EMT      CS,PNTB
(4) 014544 062706 000004      ADD      #4,SP
404 014550 004537 020126      JSR      R5,CKERLT      ;INCREMENT ERROR AND CHECK LIMIT
405
406 014554      ENDMSG
(3) 014554      L10006: EMT      C$MSG
(3) 014554 104023
407
408 014556      BGNMSG ERR7
409
410
411
412 014556 004537 020126      JSR      R5,CKERLT      ;INCREMENT ERROR AND CHECK LIMIT

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413
414 014562      L10007: ENDMSG
(3) 014562      EMT      C$MSG
(3) 014562 104023
415
416
417 014564      BGNMSG ERR8
418
419
420 014564 004737 015256      JSR      PC,LINE1
421 014570 004737 015312      JSR      PC,LINE2
422 014574      PRINTB  #FRMT6,TMP1,GDDAT,BDDAT
(10) 014574 013746 002170      MOV      BDDAT,-(SP)
(9) 014600 013746 002166      MOV      GDDAT,-(SP)
(8) 014610 013746 016842      MOV      TMP1,-(SP)
(7) 014614 012746 000004      MOV      #4,-(SP)
(6) 014620 010600      MOV      SP,RO
(4) 014622 104014      EMT      CS,PNTB
(4) 014624 062706 000012      ADD      #12,SP
424
425 014630 004537 020126      JSR      R5,CKERLT      ;INCREMENT ERROR AND CHECK LIMIT
426
427 014634      ENDMSG
(3) 014634      L10010: EMT      C$MSG
(3) 014634 104023
428
429 014636      BGNMSG ERR9
430
431 014636 004737 015256      JSR      PC,LINE1
432 014642 004737 015312      JSR      PC,LINE2
433 014646      PRINTB  #FRMT4,TMP0,R2
(9) 014646 010246      MOV      R2,-(SP)
(8) 014650 013746 002160      MOV      TMP0,-(SP)
(7) 014654 012746 015733      MOV      #FRMT4,-(SP)
(6) 014660 012746 000003      MOV      #3,-(SP)
(5) 014664 010600      MOV      SP,RO
(4) 014666 104014      EMT      CS,PNTB
(4) 014670 062706 000010      ADD      #10,SP
434
435 014674 004537 020126      JSR      R5,CKERLT      ;INCREMENT ERROR AND CHECK LIMIT
436
437
438 014700      ENDMSG
(3) 014700      L10011: EMT      C$MSG
(3) 014700 104023
439
440 014702      BGNMSG ERR10
441
442 014702 004737 015256      JSR      PC,LINE1
443 014706 004737 015312      JSR      PC,LINE2
444 014712      PRINTB  #FRMT7,TMP1,GDDAT,BDDAT
(10) 014712 013746 002170      MOV      BDDAT,-(SP)
(9) 014716 013746 002166      MOV      GDDAT,-(SP)
(8) 014722 013746 002162      MOV      TMP1,-(SP)

```

```

(7) 014726 012746 016117      MOV    #FRMT7,-(SP)
(3) 014736 012746 000004      MOV    SP,R0
(4) 014740 104014                EMT    C$PNTB
(4) 014742 062706 000012      ADD    #12,SP
445
446      014746 004537 020126                JSR    R5,CKERLT                ;INCREMENT ERROR AND CHECK LIMIT
447
448      014752                ENDMMSG
(3) 014752                L10012: EMT    C$MSG
(3) 014752 104023
450
451      014754                BGNMSG ERR11
452
453 014754 004737 015256      JSR    PC,LINE1
454 014760 004737 015312      JSR    PC,LINE2
455 014764                PRINTB #FRMT8,TMP0,GDDAT,BDDAT
(10) 014764 013746 002170      MOV    BDDAT,-(SP)
(9) 014770 013746 002166      MOV    GDDAT,-(SP)
(8) 014774 013746 002160      MOV    TMP0,-(SP)
(7) 015000 012746 016171      MOV    #FRMT8,-(SP)
(6) 015004 012746 000004      MOV    #4,-(SP)
(3) 015010 010600                MOV    SP,R0
(4) 015012 104014                EMT    C$PNTB
(4) 015014 062706 000012      ADD    #12,SP
456
457      015020 004537 020126                JSR    R5,CKERLT                ;INCREMENT ERROR AND CHECK LIMIT
458
459      015024                ENDMMSG
(3) 015024                L10013: EMT    C$MSG
(3) 015024 104023
461
462      015026                BGNMSG ERR12
463
464 015026 004737 015256      JSR    PC,LINE1
465 015032 004737 015312      JSR    PC,LINE2
466 015036                PRINTB #FRMT9,TMP1,R3,GDDAT,BDDAT
(11) 015036 013746 002170      MOV    BDDAT,-(SP)
(10) 015042 013746 002166      MOV    GDDAT,-(SP)
(9) 015046 013746 002160      MOV    R3,-(SP)
(8) 015050 012746 002162      MOV    TMP1,-(SP)
(7) 015054 012746 016312      MOV    #FRMT9,-(SP)
(6) 015060 012746 000005      MOV    #5,-(SP)
(3) 015064 010600                MOV    SP,R0
(4) 015066 104014                EMT    C$PNTB
(4) 015070 062706 000014      ADD    #14,SP
467
468      015074 004537 020126                JSR    R5,CKERLT                ;INCREMENT ERROR AND CHECK LIMIT
469
470      015100                ENDMMSG
(3) 015100                L10014: EMT    C$MSG
(3) 015100 104023
472
    
```

```

473 015102                BGNMSG ERR13
474
475 015102 004737 015256      JSR    PC,LINE1
476 015106                PRINTB #FRMT10,OPIMN,OPIMX,BDDAT
(10) 015106 013746 002170      MOV    BDDAT,-(SP)
(9) 015112 013746 002302      MOV    OPIMX,-(SP)
(8) 015116 013746 002300      MOV    OPIMN,-(SP)
(7) 015122 012746 016415      MOV    #FRMT10,-(SP)
(5) 015126 010600                MOV    SP,R0
(4) 015134 104014                EMT    C$PNTB
(4) 015136 062706 000012      ADD    #12,SP
477
478      015142 004537 020126                JSR    R5,CKERLT                ;INCREMENT ERROR AND CHECK LIMIT
479
480      015146                ENDMMSG
(3) 015146                L10015: EMT    C$MSG
(3) 015146 104023
482
483 015150                BGNMSG ERR14
484
485 015150 004737 015256      JSR    PC,LINE1
486 015154 004737 015312      JSR    PC,LINE2
487 015160                PRINTB #FRMT11,TMP1,#BUF
(8) 015160 012746 003052      MOV    #BUF,-(SP)
(6) 015164 013746 002162      MOV    TMP1,-(SP)
(7) 015170 012746 016241      MOV    #FRMT11,-(SP)
(6) 015174 012746 000003      MOV    #3,-(SP)
(3) 015200 010600                MOV    SP,R0
(4) 015202 104014                EMT    C$PNTB
(4) 015204 062706 000010      ADD    #10,SP
488
489      015210 004537 020126                JSR    R5,CKERLT                ;INCREMENT ERROR AND CHECK LIMIT
490
491      015214                ENDMMSG
(3) 015214                L10016: EMT    C$MSG
(3) 015214 104023
493
494 015216                BGNMSG ERR15
495
496 015216 004737 015256      JSR    PC,LINE1
497 015222 004737 015312      JSR    PC,LINE2
498 015226                PRINTB #FRMT15,R2
(8) 015226 010246 016723      MOV    R2,-(SP)
(7) 015230 012746 000002      MOV    #2,-(SP)
(5) 015234 010600                MOV    SP,R0
(4) 015240 104014                EMT    C$PNTB
(4) 015242 062706 000006      ADD    #6,SP
(4) 015244 062706 020126      JSR    R5,CKERLT
499
500      015254                ENDMMSG
(3) 015254                L10017: EMT    C$MSG
(3) 015254 104023
    
```

```
502 015256 005046 LINE1: PRINTB #FRMT1,RLCS,<B,DRIVE+1>
503 015256 CLR -(SP)
504 015260 153716 002135 BISB DRIVE+1,(SP)
505 015264 013746 002242 MOV RLC<S>,-(SP)
506 015270 012746 015606 MOV #FRMT1,-(SP)
507 015274 012746 000003 MOV SP,RO
508 015302 104014 EMT C$PNTB
509 015304 062706 000010 ADD #10,SP
510 015310 000207 RTS PC

511 015312 LINE2: PRINTB #FRMT2,#BEREG,#ARLCS,B,CS,#ARLBA,B,BA
512 015312 MOV B,BA,-(SP)
513 015316 013746 002220 MOV #ARLBA,-(SP)
514 015322 013746 002216 MOV B,CS,-(SP)
515 015326 012746 007101 MOV #ARLCS,-(SP)
516 015332 012746 007130 MOV #BEREG,-(SP)
517 015336 012746 015640 MOV #FRMT2,-(SP)
518 015342 012746 000008 MOV #B,-(SP)
519 015346 010600 MOV SP,RO
520 015350 104014 EMT C$PNTB
521 015352 062706 000016 ADD #10,SP
522 015356 013746 002224 PRINTB #FRMT2A,#ARLDA,B,DA,#ARLMP,B,MP
523 015362 012746 007122 MOV B,MP,-(SP)
524 015366 013746 002222 MOV #ARLMP,-(SP)
525 015372 012746 007114 MOV B,DA,-(SP)
526 015376 012746 015664 MOV #ARLDA,-(SP)
527 015382 012746 000005 MOV #FRMT2A,-(SP)
528 015406 010600 MOV SP,RO
529 015410 104014 EMT C$PNTB
530 015412 062706 000014 ADD #14,SP
531 015416 LINE3: PRINTB #FRMT2,#AFREG,#ARLCS,E,CS,#ARLBA,E,BA
532 015416 MOV B,BA,-(SP)
533 015422 013746 002226 MOV #ARLBA,-(SP)
534 015426 012746 002226 MOV E,CS,-(SP)
535 015432 012746 007101 MOV #ARLCS,-(SP)
536 015436 012746 007151 MOV #AFREG,-(SP)
537 015442 012746 015645 MOV #FRMT2,-(SP)
538 015446 010600 MOV SP,RO
539 015454 104014 EMT C$PNTB
540 015456 062706 000016 ADD #16,SP
541 015462 LINE4: PRINTB #FRMT2B,#ARLDA,E,DA,#ARLMP,E,MP,E,MP1,E,MP2
542 015462 MOV B,MP1,-(SP)
543 015466 013746 002236 MOV E,MP,-(SP)
544 015472 012746 002234 MOV E,MP,-(SP)
545 015476 012746 007122 MOV #ARLMP,-(SP)
546 015502 013746 002232 MOV E,DA,-(SP)
547 015506 012746 007114 MOV #ARLDA,-(SP)
548 015512 012746 000007 MOV #FRMT2B,-(SP)
549 015516 010600 MOV SP,RO
550 015524 104014 EMT C$PNTB
551 015526 062706 000020 ADD #20,SP
```

```
510 015532 000207 RTS PC
511 015534 LINE3: PRINTB #FRMT3,#EM1
512 015534 MOV #EM1,-(SP)
513 015540 012746 010352 MOV #FRMT3,-(SP)
514 015544 012746 015726 MOV SP,RO
515 015550 010600 000002 MOV SP,RO
516 015552 104014 EMT C$PNTB
517 015554 062706 000006 ADD #6,SP
518 015560 PRINTB #FRMT3,#EM100
519 015560 MOV #EM100,-(SP)
520 015566 012746 015726 MOV #FRMT3,-(SP)
521 015570 012746 000002 MOV SP,RO
522 015574 010600 MOV SP,RO
523 015576 104014 EMT C$PNTB
524 015600 062706 000006 ADD #6,SP
525 015604 000207 RTS PC

526 015606 040445 047503 FRMT1: -ASCIZ /%ACONTROLLER: %06%A DRIVE: %01/
527 015645 04 022516 FRMT2: -ASCIZ /%N%T%T%06%T%06/
528 015664 052045 047445 FRMT2A: -ASCIZ /%T%06%T%06/
529 015677 04 022524 FRMT2B: -ASCIZ /%T%06%T%06% %06% %06/
530 015726 04704 052045 FRMT3: -ASCIZ /%N%T%T%06%T%06% %06% %06/
531 015733 045 022516 FRMT4: -ASCIZ /%N%EXP%D: %06%A REC%D: %06%N/
532 015771 04 022516 FRMT5: -ASCIZ /%N%ALAST: %06%A PRES: %06%A EXP%D: %06%N/
533 016042 04704 040445 FRMT6: -ASCIZ /%N%ABUS ADR: %06%A EXP%D: %06%A REC%D: %06%N/
534 016117 04 022516 FRMT7: -ASCIZ /%N%WORD: %D3%A EXP%D: %06%A REC%D: %06%N/
535 016171 04 022516 FRMT8: -ASCIZ /%N%ADA: %06%A REC%D: %06%A EXP%D: %06%N/
536 016241 045 022516 FRMT14: -ASCIZ /%N%WORDS WRITTEN: %D3%A BUS ADDR: %06%N/
537 016312 04704 040445 FRMT9: -ASCIZ /%N%WORDS WRITTEN: %D3%A BUS ADDR: %06%A EXP%D: %06%A REC%D: %06%N/
538 016415 04 022516 FRMT10: -ASCIZ /%N%ARANGE %D3%A - %D3%A MILLISECONDS WAS %D6%N/
539 016474 049445 040516 FRMT11: -ASCIZ /%N%MAXIMUM TIMEOUT OF PROGRAM IS %3 SECONDS%N/
540 016550 04704 040445 FRMT12: -ASCIZ /%N%ERROR LIMIT EXCEEDED - DROPPED%N/
541 016615 04 042101 FRMT9B: -ASCIZ /%ADRIIVE DID NOT RECOVER FROM POWER FAILURE/
542 016667 045 000116 FRMT99: -ASCIZ /%N/
543 016672 04704 052045 FRMT13: -ASCIZ /%N%T%A - WILL NOT TEST%N/
544 016723 045 022516 FRMT15: -ASCIZ /%N%APATTERN WAS: %06/

545 .EVEN
546
547 016750 BGNMOD ENDMOD
548 016750 HPTCODE
549
550 BGNHW
551 016750 L10020-L$HW/2
552 016750 -WORD 174400 ;CSR
553 016754 000160 -WORD 160 ;VECTOR
554 016756 000240 -WORD 040 ;PRIORITY
555 016762 000001 -WORD 040 ;DRIVE (BITS 8,9,10)
556 016762 000001 -WORD 1 ;RL11=1 RL11=0
557 016764 ENDNW
558 (3) 016764 L10020:
```

```

558      016764      ENDMOD
559      016764      BGNMOD SPTCODE
560      016764      BGNSW      .WORD      L10021-L$SW/2
561      016766      000000      DROP:      .WORD      0
562      016770      000012      MERLMT:    .WORD      10.
563      016772      000000      T.SIZE:    .WORD      0
564      016774      000000      T.DMP:     .WORD      0
565      016776      000000      T.LMT:     .WORD      0
566      017000      ENDSW
567      017000      L10021:
568      017000      ENDMOD
569      017000      BGNMOD DSPCODE
570      017000      DISPATCH   47
571      017000      .WORD      47
572      017002      .WORD      T1
573      017004      .WORD      T2
574      017006      .WORD      T3
575      017010      .WORD      T4
576      017012      .WORD      T5
577      017014      .WORD      T6
578      017016      .WORD      T7
579      017020      .WORD      T8
580      017022      .WORD      T9
581      017024      .WORD      T10
582      017026      .WORD      T11
583      017030      .WORD      T12
584      017032      .WORD      T13
585      017034      .WORD      T14
586      017036      .WORD      T15
587      017040      .WORD      T16
588      017042      .WORD      T17
589      017044      .WORD      T18
590      017046      .WORD      T19
591      017050      .WORD      T20
592      017052      .WORD      T21
593      017054      .WORD      T22
594      017056      .WORD      T23
595      017060      .WORD      T24
596      017062      .WORD      T25
597      017064      .WORD      T26
598      017066      .WORD      T27
599      017068      .WORD      T28
600      017072      .WORD      T29
601      017074      .WORD      T30
602      017076      .WORD      T31
603      017100      .WORD      T32
604      017102      .WORD      T33
    
```

```

(6) 017104      033274      .WORD      T34
(6) 017106      033564      .WORD      T35
(6) 017110      034060      .WORD      T36
(6) 017112      034352      .WORD      T37
(6) 017114      034644      .WORD      T38
(6) 017116      035244      .WORD      T39
(6) 017120      035604      .WORD      T40
(6) 017122      036116      .WORD      T41
(6) 017124      036402      .WORD      T42
(6) 017130      036912      .WORD      T43
(6) 017132      037114      .WORD      T44
(6) 017134      037312      .WORD      T45
(6) 017136      037450      .WORD      T46
(6) 017140      ENDMOD
580      .SBTTL  INITIALIZATION CODE
581      BGNMOD  INITCODE
582      017140      BGNINIT
583      017140      SETPRI  #PRI07
584      017140      MOV     #PRI07,R0
585      017144      EMT     C$SPRI
586      017146      012700      000340      READEF  #EF.PWR
587      017152      104050      MOV     #EF.PWR,R0
588      017154      103004      EMT     CSREFG
589      017156      103004      BCC     NOPWR
590      017160      002012      002304      MOV     L$UNIT,PWRFLG
591      017166      000473      BR      CONT
592      017166      012700      000037      NOPWR:  READEF  #EF.RESTART
593      017172      104050      MOV     #EF.RESTART,R0
594      017174      103404      EMT     CSREFG
595      017176      103404      BCC     START1
596      017176      012700      000040      READEF  #EF.START
597      017202      104050      MOV     #EF.START,R0
598      017204      103010      EMT     CSREFG
599      017204      103010      BCC     CONTINUET
600      017206      002314      START1: MOV     #RCOUNT,R0
601      017212      000100      NOV     #64,R1
602      017216      005020      1$:    CLR     (R0)+4
603      017220      005301      DEC     R1
604      017222      001375      BNE     1$
605      017224      000407      BR      START
606      017226      000036      CONTINUE: READEF  #EF.CONTINUE
607      017232      104050      MOV     #EF.CONTINUE,R0
608      017234      103447      EMT     CSREFG
609      BCC     CONT
610      BCC     CONT
    
```

```

606 017236 005737 002136      NXT:   TST   UUT
607 017242 001011              BNE   XXX
608 017244 012737 177777      START: MOV   #-1,UNITST
609 017252 013737 002012     MOV   L$UNIT,UUT
610 017260 012737 002312     MOV   #ERCOUNT-2,ERPOINT
611
613 017266 005237 002140      XXX:   INC   UNITST
614 017272 062737 000002     ADD   #2,ERPOINT
615 017300 005337 002136     DEC   004
616 017304 013700 002140      REST: GPHARD UNITST,RO
617 017310 104042              MOV   UNITST,RO
618 017312 103406              EMT   C$GPHRD
619 017320 001746              BCS   COMPLETE 2$
620 017322 005337 002304      TST   PWRFLG
621 017326 000743              BEQ   NXT
622 017334 012037 002252      2$:   MOV   (RO)+,BCSR
623 017340 012037 002254      MOV   (RO)+,BVEC
624 017344 012037 002256      MOV   (RO)+,BPRIOR
625 017350 012037 002134      MOV   (RO)+,DRIVE
626 017354 012037 002306      MOV   (RO)+,T.CNTRLR
627
628 017354 013700 002252      CONT: MOV   BCSR,RO
629 017360 010037 002242      MOV   RO,RLCS
630 017364 062700 000002      ADD   #2,RO
631 017370 010037 002244      MOV   RO,RLBA
632 017374 062700 000002      ADD   #2,RO
633 017400 010037 002246      MOV   RO,RLDA
634 017404 062700 000002      ADD   #2,RO
635 017410 010037 002250      MOV   RO,RLMP
636
637 017414 005737 002304      TST   PWRFLG
638 017420 001069              BNE   5$
639 017426 001461              BEQ   5$
640 017430 005037 002142      CLR   TRPFLG
641 017434 012746 000340      SETVEC ERRVEC,#TRPHAN,#340
642 017440 012746 001350      MOV   #340,-(SP)
643 017444 012746 003132      MOV   ERRVEC,-(SP)
644 017450 012746 000003      MOV   #3,-(SP)
645 017454 104037              EMT   C$SVEC
646 017456 062706 000010      ADD   #10,SP
647 017462 005777 162554      TST   RLCS
648 017466 013700 002132      CLREVC CLREVC,RO
649 017472 104036              MOV   ERRVEC,RO
650 017474 005737 002142      TST   CSCVEC
651 017500 001404              BEQ   7$
652 017502 012737 007052     7$:   MOV   #NORES,WHY
653 017510 000415              BR    8$
654
655 017512 012777 000200     7$:   MOV   #200,@RLCS
656 017520 053777 002134     BIS   DRIVE,@RLCS

```

```

;DONE WITH ALL UNITS
;NO
;GET BUS ADDRESS
;GET VECTOR
;GET PRIORITY
;GET DRIVE
;GET CONTROLLER TYPE
;CREATE REGISTERS
;DO WE WANT TO CHECK UNITS??
;NO
;CLEAR OUT TRAP INDICATOR
;SETUP TO CATCH TIMEOUT
;ACCESS CONTROLLER
;DID TRAP OCCUR??
;NO, CHECK DRIVE
;NOW CHECK DRIVE FOR READY

```

```

651 017526 032777 000001 162506      BIT   #1,@RLCS
652 017534 001016              BNE   8$
653 017536 012737 007070      MOV   #NORDY,WHY
654 017544 013746 002126     8$:   PRINTB #FRMT13,WHY
655 017548 012746 016672      MOV   WHY,-(SP)
656 017550 010600 000002      MOV   #3,-(SP)
657 017552 104014              EMT   C$PNTB
658 017554 062706 000006      ADD   #6,SP
659 017570 000444              BR    5$
660
661 017572 005737 002304     5$:   TST   PWRFLG
662 017576 001451              BEQ   END
663 017600 012777 000200 162434      MOV   #200,@RLCS
664 017606 053777 002134 162426      BIS   DRIVE,@RLCS
665 017614 012701 000074     3$:   MOV   #60,-R1
666 017620 012700 000012      MOV   R10,RO
667 017624 104026              EMT   C$WTM
668 017626 032777 000001 162406      BIT   #1,@RLCS
669 017634 001032              BNE   END
670 017636 005301              DEC   R1
671 017640 001367              BNE   3$
672
673 017642 012746 016667      PRINTF #FRMT99
674 017644 012746 000001      MOV   #1,-(SP)
675 017646 012746              MOV   #3,RO
676 017648 104017              EMT   C$PNTF
677 017650 062706 000004      ADD   #4,SP
678 017652 012746 016615      PRINTF #FRMT98
679 017654 012746 000001      MOV   #1,-(SP)
680 017656 010600              MOV   #3,RO
681 017658 104017              EMT   C$PNTF
682 017660 062706 000004      ADD   #4,SP
683 017662 004737 015256     6$:   JSR   PC,LINE1
684 017706 013700 002140      DODU  UNITST,RO
685 017710 104053              MOV   UNITST,RO
686 017714 104044              EMT   C$DODU
687 017716 000137              DOCLN C$DCLN
688 017718 000137              JMP   NXT
689
690 017722 013737 002276 002300      END:  MOV   UOPIHN,OPIHN
691 017730 013737 002274 002302      MOV   UOPIHX,OPIHX
692 017736 005737 002306      TST   T.CNTRLR
693 017742 001006              BNE   1$
694 017744 013737 002272 002300      MOV   LOPIHN,OPIHN
695 017746 013737 002270 002302      MOV   LOPIHX,OPIHX
696
697 017760 012746 000340      SETVEC BVEC,#INTSRV,#340
698 017762 012746 002010      MOV   #340,-(SP)
699 017764 012746 002010      MOV   INTSRV,-(SP)
700 017770 013746 062254      MOV   BVEC,-(SP)

```

```

;POWER UP
;NO
;RL11??
;YES, THEN KEEP LIMITS SET

```

```

(4) 017774 012746 000003      MOV    #3-(SP)
(2) 020000 104037 000010      BMT    C$VEEC
(3) 020002 062706 000010      ADD    #10,SP
683
684
685
686
(3) 020006 104011      L10022:  ENDINIT
(3) 020006 104011      EMT    C$INIT
687
688 020010      ENDMOD
689
690 020010      BGNMOD  CLNCODE
691
692 020010      BGNCLN
693
694
695 020010      SETVEC  ERRVEC, #TRPHAN, #340
(7) 020010      MOV    #340-(SP)
(6) 020014 012746 021350      MOV    #TRPHAN, -(SP)
(5) 020020 012746 002132      MOV    ERRVEC, -(SP)
(4) 020024 012746 000003      MOV    #3-(SP)
(3) 020030 104037 000010      EMT    C$SVEC
(3) 020032 062706 000010      ADD    #10,SP
(3) 020036 032777 000200 162176 1$:  BIT    #RDY, @RLCS
697 020044 001774      BEQ    1$
698
699 020046 042777 000100 162166      BIC    #INTEN, @RLCS
700
701 020054      CLRVEC  BVEC
(3) 020054 013700 002254      MOV    BVEC, R0
(3) 020060 104036      EMT    C$CVEC
702 020062 005737 002304      TST    PWRFLG
703 020066 001402      BEQ    2$
704 020070 005337 002304      DEC    PWRFLG
705 020074 013700 002132      CLRVEC  ERRVEC
(3) 020100 104036      MOV    ERRVEC, R0
(3) 020100 104036      EMT    C$CVEC
706
707
708
709 020102      ENDCLN
(3) 020102      L10023:  EMT    C$CLEAN
(3) 020102 104012      EMT    C$CLEAN
710
711 020104      ENDMOD
712
713 020104      BGNMOD  DRPCODE
714
715 020104      BGNDU
716
717 020104 000240      NOP
718
719 020106      ENDDU
(3) 020106      L10024:  EMT    C$DU
(3) 020106 104055      EMT    C$DU

```

```

720
721 020110      ENDMOD
722
723      .SBTTL  GLOBAL SUBROUTINES
724
725 020110      BGNMOD  GLBSUB
726
727 020110      BGNSRV
728 020110 005237 002144      INTSRV:  INC    INTFLG      ;SET INTERRUPT OCCURANCE FLAG
729
730 020114      ENDSRV
(3) 020114      L10025:  RTI
(3) 020114 000002      RTI
731
732      ;ROUTINE USED IN TIMING OPI
733
734 020116 005237 002144      TIMSRV:  INC    INTFLG
735 020122 104021      ABRDWAIT
(3) 020124 000002      BMT    C$ABRT
736
737
738 020126 000240      CKERLT:  NOP
739 020130      INLOOP
(3) 020130 104020      EMT    C$INLP
740 020132      BCOMPLETE 99$
(2) 020132 103427      BCS    99$
741
742 020134 005737 016766      TST    DROP
743 020140 001424      BEQ    99$
744 020142 005277 162144      INC    @RRPOINT
745 020146 027737 162140 016770      CMP    @RRPOINT, MERLMT
746 020154 002416      BLT    99$
747
748 020156      PRINTF #FRMT11
(7) 020156 012746 016550      MOV    #FRMT11, -(SP)
(6) 020156 012746 000001      MOV    #3-(SP)
(3) 020166 010600      MOV    SP, R0
(4) 020170 104017      EMT    C$BNTF
(4) 020172 062706 000004      ADD    #4, SP
749 020176 004737 015256      JSR    PC, LINE1
750 020202      DODU   UNITST, DROP THIS UNIT
(3) 020206 013700 002140      MOV    UNITST, R0
(3) 020206 104053      EMT    C$DODU
751 020210      DOCLN
(3) 020210 104044      EMT    C$DCLN
752
753 020212      99$:
754 020212 000205      RTS    R5
755
756
757      .SBTTL  ROUTINE TO CHECK FOR CONTROLLER ERRORS
758
759      ;*****
760      ;*THIS ROUTINE WILL CHECK RLCS FOR ERRORS AND PRINT THEM
761      ;*ACCORDINGLY. IT WILL MERGE THE ERROR PRINTOUT WITH THE TEST
762      ;*ERROR MESSAGE.

```

```

763          ;*
764          ;* ROUTINE USES R0,R1 AND PICKS HEADER FROM R3
765          ;*
766          ;* CALL JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
767          ;*
768          ;*
769          ;*
770
771 020214 005037 002124          CHERR: CLR T,CRC
772 020220 032737 176000 002226 BIT #176000,E.CS ;ANY ERROR BITS SET?
773 020226 001001          BNE #2          ;YES, FIND OUT WHICH
774 020230 000205          RTS R5          ;NO EXIT
775 020232 012701          MOV #EM100,R1 ;GET START OF STRING
776 020236 005737 002226 TST #CS          ;IS COMPOSITE ERROR SET?(BETTER BE)
777 020242 100003          BPL #99$       ;IF NOT SOMETHING IS WRONG
778 020244 004537 020752 JSR R5,FIX      ;YES, PUT "COMP" IN STRING
779 020250 007316          COMP          ;"COMP"
780 020252 032737 040000 002226 99$: BIT #DERR,E.CS ;DRIVE ERROR SET?
781 020260 001405          BEQ #3$        ;NO, CONTINUE
782 020262 005237          INC DERFLG     ;YES, PUT "DRV" INTO STRING
783 020266 004537 020752 JSR R5,FIX      ;"DRV"
784 020272 007245          DEMES         ;NON-EXISTENT MEMORY ERROR?
785 020274 032737 020000 002226 3$: BIT #NXM,E.CS ;NO, CONTINUE
786 020302 001403          BEQ #4$        ;YES, PUT "NXM" INTO STRING
787 020304 004537          JSR R5,FIX     ;"NXM"
788 020310 007237          NXMES         ;IS OPI SET?
789 020312 012737 020200 002226 4$: BIT #OPI,E.CS ;NO, GO CHECK BITS 11 & 12
790 020320 001422          BEQ #5$        ;PUT "OPI" INTO STRING
791 020322 004537 020752 JSR R5,FIX      ;"OPI"
792 020326 007257          OPMES         ;HEADERCRC ERROR?
793 020330 032737 004000 002226 BIT #BIT11,E.CS ;NO, GO CHECK HEADER NOT FOUND
794 020334 001403          BEQ #6$        ;GO, PUT "HCRC" IN STRING
795 020340 004537 020752 JSR R5,FIX      ;"HCRC"
796 020344 007264          HRCRCMES     ;HEADER NOT FOUND?
797 020346 032737 010000 002226 5$: BIT #BIT12,E.CS ;NO, GO PUT "CRLF" IN STRING
798 020354 001424          BEQ #7$        ;PUT "HNF" IN STRING
799 020356 004537 020752 JSR R5,FIX      ;"HNF"
800 020362 007272          HNFES         ;PUT "CRLF" IN STRING
801 020364 000420          BR #8$        ;DATA CRC ERROR?
802 020366 032737 004000 002226 6$: BIT #BIT11,E.CS ;NO, GO CHECK DATA LATE
803 020374 001405          BEQ #7$        ;PUT "DCK" IN STRING
804 020376 005237          INC DCKMES    ;"DCK"
805 020402 007237          DCKMES       ;DATA LATE ERROR?
806 020406 007237 010000 002226 7$: BIT #BIT12,E.CS ;NO, GO PUT IN "CRLF"
807 020410 032737          BEQ #8$        ;PUT "DLT" IN STRING
808 020416 001403          BEQ #9$        ;"DLT"
809 020420 004537 020752 JSR R5,FIX      ;PUT "CRLF" INTO STRING
810 020424 007304          DLTMS        ;"CRLF"
811 020426 004537 020752 8$: JSR R5,FIX      ;MOVE HEADER
812 020432 007313          MSCRFL       ;HEADER FROM TEST
813 020434 004537 020752 JSR R5,FIX      ;PUT TERMINATOR IN
814 020440 000000          RESTMS: .WORD 0
815 020442 105011          CLR B         ;(R1)
816 020444 004537          ERDF         ;(R0) LF ERR6
817 020446 104462          TRDF         ;SERCODE
818          .WORD 300
    
```

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(5) 020450 007311          .WORD LF ERR6
(6) 020452 014514          .WORD ERR6
(7) 020454 000205          RTS R5          ;EXIT ROUTINE
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871 020722 007747 GSTMES
872 020724 010006 GSTINT
873 020726 007664 SEKMES
874 020730 007715 SEKINT
875 020732 007563 RDMES
876 020734 007623 RHDINT
877 020736 010127 WRTMES
878 020740 010161 WRTINT
879 020742 010045 RDDMES
880 020744 010076 RDDINT
881 020746 010213 RDMES
882 020750 010247 RDNINT
883
884
885 ;*****
886 ;ROUTINE TO MOVE ASCII STRINGS
887 ;USES REGISTERS R1 - WHERE STRING IS BEING BUILT
888 ;*
889 ;* CALL JSR R5,FIX ;ADDRESS OF STRING TO MOVE
890 ;*
891
892 020752 012504 FIX: MOV (R5)+,R4 ;GET ADDRESS AND MOVE RETURN
893 020754 115421 15: MOVB (R4)+,(R1)+ ;GET BYTE AND UPDATE
894 020756 001376 BNE 15 ;WATCH 0 BYTE TERMINATOR
895 020760 105741 TSTB -(R1) ;BACK UP OVER ZERO BYTE
896 020762 000205 RTS R5 ;EXIT
897
898 ;ROUTINE TO READ REGISTERS PRIOR TO OPERATION
899 ;CALL: JSR R5,BEFORE
900
901 020764 017737 161252 002216 BEFORE: MOV @RLCS,B:CS ;READ CS
902 020772 017737 161346 002220 MOV @RLBA,B:BA ; BA
903 021000 017737 161349 002222 MOV @RLDA,E:DA ; DA
904 021006 017737 161236 002224 MOV @RLMP,E:MP ; MP
905 021014 000205 RTS R5
906
907 ;ROUTINE TO READ REGISTERS AT TIME OF ERROR
908 ;CALL: JSR R5,AFTER
909
910 021016 017737 161220 002226 AFTER: MOV @RLCS,E:CS ;READ CS
911 021024 017737 161214 002230 MOV @RLBA,E:BA ; BA
912 021032 017737 161210 002232 MOV @RLDA,E:DA ; DA
913 021040 017737 161204 002234 MOV @RLMP,E:MP ; MP
914 021046 017737 161176 002236 MOV @RLMP,E:MP1 ; MP
915 021054 017737 161170 002240 MOV @RLMP,E:MP2 ; MP
916 021062 000205 RTS R5
917
918
919 021064 010046 SIMBCC: MOV R0,-(SP) ;SAVE R0
920 021070 010146 MOV R1,-(SP) ;SAVE R1
921 021076 010246 MOV R2,-(SP) ;SAVE R2
922 021072 012537 002172 MOV (R5)+,TEMP2 ;GET NUMBER OF BITS
923 021076 012537 002174 MOV (R5)+,TEMP3 ;GET DATA FOR CRC CALCULATION
924 021102 012537 002176 MOV (R5)+,TEMP4 ;GET STARTING CRC
925 021108 013700 002178 15: CTRB @R0,TEMP4 ;GET PRESENT CRC
926 021112 013700 002176 MOV TEMP4,R0
    
```

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927 021116 006037 002174 ROR TEMP3 ;ROTATE NEW DATA
928 021122 005500 ADC R0 ;MERGE NEW WITH OLD
929 021134 032700 000001 BIT R1,R0 ;BIT 0 SET
930 021136 001472 BEO ;IF NOT CONTINUE
931 021132 005137 002154 COM BCCFBK ;
932 021136 013700 002152 2$: MOV XPOLY,R0 ;GET CRC POLYNOMIAL (CRC-16)
933 021142 005100 COM R0 ;COMPLIMENT POLYNOMIAL
934 021144 040037 002154 CLC R0,BCCFBK ;
935 021150 000241 CLC ;CLEAR CARRY
936 021156 006037 002176 MOV TEMP4 ;
937 021156 013700 002154 MOV BCCFBK,R0 ;
938 021162 013701 002176 MOV TEMP4,R1 ;
939 021166 010102 MOV R1,R2 ;
940 021170 040100 BIC R1,R0 ;
941 021176 043702 002154 BIC BCCFBK,R2 ;
942 021178 050200 BIC R2,R0 ;
943 021200 043737 002152 002176 BIC XPOLY,TEMP4 ;
944 021206 050037 002176 BIS R0,TEMP4 ;
945 021212 005337 002172 DEC TEMP2 ;
946 021216 001333 BNE 15 ;
947
948 021220 013737 002176 002156 MOV TEMP4,CALBCC ;
949 021226 012602 MOV (SP)+,R2 ;
950 021230 012601 MOV (SP)+,R1 ;
951 021232 012600 MOV (SP)+,R0 ;
952 021234 000205 RTS R5 ;RETURN
953
954 ;ROUTINE TO WAIT FOR DRIVE READY
955
956
957
958
959
960 021236 012701 000144 160772 WTRDY: MOV #100,R1
961 021242 032777 000001 15: BIT @RDRDY,@RLCS
962 021250 001011 BNE 2$
963
964 021252 WAITUS #20.
965 (3) 021255 MOV #20,R0
966 (3) 021256 EMT CSWT0
967 021260 005301 DEC R1
968 021262 001367 BNE 15
969
970 021264 ERRDF 200.,DRTIM,ERR5
971 (3) 021264 TRAP #ERRCODE
972 (5) 021266 .WORD 200
973 (5) 021270 .WORD DRTIM
974 (5) 021272 .WORD ERR5
975
976 021274 000205 2$: RTS R5
977
978 ;ROUTINE TO WAIT FOR CONTROLLER
979
980
981 021276 012701 000620 160732 WTCRDY: MOV #400,R1
982 021302 032777 000200 15: BIT @RDRDY,@RLCS
983 021310 001014 BNE 2$
    
```



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977
978 021312          WAITUS #20, R0
(3) 021312 012700 000024  MOV #20, R0
(3) 021316 104027  EMT C$WTO
979 021320 005301  DEC RI
980 021322 001367  BNE 1$
981 021324 004537  JSR R5, AFTER
983 021330          ERRDF 100, CRTIM, ERR5
(3) 021330 104462  TRAP T$ERRCODE
(5) 021332 000144  .WORD 100
(5) 021334 007172  .WORD CRTIM
984 021336 014458  .WORD ERR5
(3) 021340 000205  RTS R5
986 021342 004537 021016 2$: JSR R5, AFTER
987 021346 000205  RTS R5
989
990 021350 005237 002142  TRPHAN: INC TRPFLG
991 021354 000002  RTI
992
993 021356          HDHOME:
994
995 021356          BGNSEG          ;%%START OF SEGMENT%%
(3) 021356 104004  EMT C$BSEG
996          ;ISSUE DRIVE RESET
997
998 021360 012737 000001 002266  MOV #1, ERFLG          ;SET ERROR FLAG
999 021366 012777 000013 160652  MOV #RSTIMIGSBIT, @RLDA
1000 021374 004537 020456  JSR R5, LDFUNC          ;LOAD THE FUNCTION IN NEXT WORD
1001 021400 000004  GSTAT
1002 021402 004537 021276  JSR R5, WTCRDY
1003 021406          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 021406 104010  EMT C$ESCAPE
(3) 021410 000174  .WORD 10000$-
1004 021412 004537 020214  JSR R5, CHERR          ;CHECK CNTLR FOR ERRORS
1005 021416          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 021416 104010  EMT C$ESCAPE
(3) 021420 000206  .WORD 10000$-
1006
1007
1008 021422 004537 020456  JSR R5, LDFUNC          ;LOAD THE FUNCTION IN NEXT WORD
1009 021426 000010  RDHDR
1010 021430          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 021430 104010  EMT C$ESCAPE
(3) 021432 000174  .WORD 10000$-
1011 021434 004537 021276  JSR R5, WTCRDY
1012 021440          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 021440 104010  EMT C$ESCAPE
(3) 021442 000164  .WORD 10000$-
1013
1014 021444 004537 020214  JSR R5, CHERR          ;CHECK CNTLR FOR ERRORS
1015 021450          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 021450 104010  EMT C$ESCAPE
(3) 021452 000154  .WORD 10000$-
  
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```

1016
1018 021462 042737 000077 002160  BIC #77, T$MPO          ;GET HEADER
1019 021470 001424  BEQ 99$          ;SEEK IS NOT NECESSARY
1020 021472 042737 000100 002160  BIC #100, T$MPO
1021 021500 012777 000001 160540  MOV #R$K, @RLDA          ;SET TO SEEK
1022 021506 053777 002160 160532  BIS T$MPO, @RLDA          ;SET IN DIFFERENCE
1023
1024 021514 004537 020456  JSR R5, LDFUNC          ;LOAD THE FUNCTION IN NEXT WORD
1025 021520 000006  SEEK
1026 021522 004537 021276  JSR R5, WTCRDY
1027 021526          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 021526 104010  EMT C$ESCAPE
(3) 021530 000076  .WORD 10000$-
1028
1029 021532 004537 020214  JSR R5, CHERR          ;CHECK CNTLR FOR ERRORS
1030 021536          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 021536 104010  EMT C$ESCAPE
(3) 021540 000066  .WORD 10000$-
1031
1032 021542 004537 020456 99$: JSR R5, LDFUNC          ;LOAD THE FUNCTION IN NEXT WORD
1033 021546 000010  RDHDR
1034 021550 004537 021276  JSR R5, WTCRDY
1035 021554          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 021554 104010  EMT C$ESCAPE
(3) 021556 000050  .WORD 10000$-
1036 021560 004537 020214  JSR R5, CHERR
1037 021564          ESCAPE SEG
(3) 021564 104010  EMT C$ESCAPE
(3) 021566 000040  .WORD 10000$-
1038
1039 021570 013737 002234 002160  MOV E.M$P, T$MPO          ;GET HEADER
1040 021576 043737 002150 002160  BIC SECM$K, T$MPO          ;IGNORE SECTOR
1041 021604 001404  BEQ 1$          ;ON ZERO
1042
1043 021606          ERRDF 400, SKHOME, ERRO          ;CAN'T SEEK TO TRACK 0
(3) 021606 104462  TRAP T$ERRCODE
(5) 021610 000620  .WORD 400
(5) 021612 010303  .WORD SKHOME
1044 021614 014244  .WORD ERRO
1045 021616          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 021616 104010  EMT C$ESCAPE
(3) 021620 000006  .WORD 10000$-
1046 021622 005037 002266  CLR ERFLG          ;INDICATE SUCCESS BACK TO MAIN PROGRAM
1047
1048
1049
1050 021626          ENDSEG          ;%%END OF SEGMENT%%
(3) 021626 104005  EMT C$ESEG
(3) 021626 000207  RTS PC
1051
1052 021630 000207  RTS PC
1053
1054 021632          ENDMOD
1055
  
```

```

1056 .SBTTL **TEST 1** - WRITE NPR INTEGRITY
1057 BGNSTST ;**START OF TEST**
1058
1059 021632
1060 021632
1061 STARS
1062 ;*****
1063 ;CHECK THAT NPR WILL NOT INTERFERE WITH THE OPERATION OF THE
1064 ;UNIBUS. WE SET UP LOCATION 4 TO HANDLE THE TRAP IF IT HAPPENS.
1065 STARS
1066 ;*****
1067
1068 021632 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0
1069 021636 CKERFG ;HEADS GO HOME OKAY
1070 021644 104032 EMT C$EXIT
1071 021646 000232 .WORD L10026-.
1072
1073 021650 BGNSEG ;**START OF SEGMENT**
1074 021650 104004 EMT C$BSEG
1075
1076 1$: SETVEC ERRVEC,#TRPHAN,#340 ;SET UP FOR TRAP
1077 021652 012746 000340 MOV #340,-(SP)
1078 021656 012746 021350 MOV #TRPHAN,-(SP)
1079 021662 013746 002132 MOV ERRVEC,-(SP)
1080 021666 012746 000003 MOV #3,-(SP)
1081 021674 104037 ADD C$SVEC
1082 021700 062706 000010 TRPFLG ;CLEAR TRAP OCCURANCE
1083 021704 012777 003052 MOV #BUF,@RLBA ;BUS ADDRESS
1084 021712 005077 160332 CLR @RLDA ;LOAD DISK ADDRESS
1085 021716 012777 160324 CLR @RLMP ;WORD COUNT OF 1
1086 021730 005037 002166 CLR GDDAT ;SET UP CSR TO LOAD
1087 021734 013737 002134 MOV DRIVE,GDDAT ;SET IN DRIVE
1088 021736 052737 000012 BIS #WRITE,GDDAT ;SET IN FUNCTION
1089 021744 004537 020764 JSR R5,BEFORE ;LOAD FOR ERROR PRINTOUT
1090 021750 013737 002166 MOV GDDAT,B.CS ;SET IN COMMAND
1091 021756 052737 000201 BIS #201,B.CS ;LOAD CRDY
1092 021762 043737 002000 BIC @R14,C ;CLEAR (BIT 10)
1093 021772 013737 002166 MOV @R14,@RLCS ;ISSUE WRITE
1094 022000 012701 000144 MOV #100,R1 ;WAIT FOR CRDY
1095 022004 032777 000200 BIT @CRDY,@RLCS ;NPR DONE
1096 022012 001013 BNE 6$ ;YES 6$
1097 022014 012700 WAITUS #20 ;WAIT A WHILE
1098 022016 014027 MOV #20,R0
1099 022022 005301 EMT C$WTU
1100 022024 001367 DEC R1 ;A WHILE UP
1101 BNE 5$ ;NO, GO BACK
1102
1103 022026 004537 021016 JSR R5,AFTER ;CONTROLLER TIMED OUT
1104 022030 014462 ERRDF 0,CRTIM,ERR5 ;CONTROLLER TIMED OUT
1105 022034 000000 TRAP T$ERRCODE
1106 022036 007172 .WORD 0
1107 022040 014456 .WORD CRTIM
1108 022042 013700 002132 6$: CLR ERR5 ;CLEAR VECTOR
1109 MOV ERRVEC,ERR5
1110 MOV ERRVEC,R0

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1094 022046 104036 EMT C$CVEC
1095 022050 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
1096 022052 000024 EMT C$ESCAPE
1097 .WORD 10001$-.
1098
1099 022054 005737 002142 TST TRPFLG ;DID TRAP OCCUR?
1100 BEQ 7$ ;NO
1101 JSR R5,AFTER
1102 ERRSF 1,C$MS1,ERR0 ;TRAP ON WRITE
1103 TRAP T$ERRCODE
1104 .WORD 1
1105 .WORD EMS1
1106 .WORD ERR0
1107
1108 7$:
1109
1110 10001$: ENDSEG ;**END OF SEGMENT**
1111 EMT C$ESEG
1112
1113 ENDTST
1114 LI0026: EMT C$ETST ;**END OF TEST**
1115
1116 .SBTTL **TEST 2** - WRITE FUNCTION
1117 BGNSTST ;**START OF TEST**
1118
1119 022102
1120
1121 STARS
1122 ;*****
1123 ;CHECK OF WRITE LOGIC UNDER FLAG MODE. WE WILL FIRST ISSUE A
1124 ;READ HEADER SO THAT WE DON'T WRITE ON THE BAD SECTOR
1125 ;FILE TRACK. WE WILL WRITE A FULL SECTOR (128 WORDS) FROM
1126 ;MEMORY (BUF). WE CHECK THAT NO ERRORS OCCUR. IF WE
1127 ;HAVE A DRIVE ERROR WE WILL DO A "GET STATUS" TO SEE
1128 ;IF WRITE PROTECT IS SET IF IT IS WE WILL ABORT THE
1129 ;TEST. AN ERROR ON THE WRITE WILL LOOP ON JUST THE
1130 ;WRITE PORTION. LOOP ON TEST WILL READ HEADER, SEEK (IF
1131 ;NECESSARY) AND WRITE.
1132 STARS
1133 ;*****
1134
1135 022102 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0
1136 022106 CKERFG ;HEADS GO HOME OKAY
1137 022114 104032 EMT C$EXIT
1138 022116 000126 .WORD L10027-.
1139
1140 022120 BGNSEG ;**START OF SEGMENT**
1141 022120 104004 EMT C$BSEG
1142
1143 3$: CLR @RLDA ;SET DISK ADDRESS
1144 MOV #-128,@RLMP ;WORD COUNT

```

```

1134 022134 012777 003052 160102      MOV    #BUF,@RLBA    ;BUS ADDRESS
1135 022142 004537 020456      JSR    R5,LDFUNC     ;LOAD THE FUNCTION IN NEXT WORD
1136 022146 000012                WRITE                ;WRITE
1137
1138 022150 004537 021276      JSR    R5,WTCRDY     ;WAIT FOR CONTROLLER READY
1139 022154                ESCAPE  SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
1140 022156 104010 000064      EMT    C$ESCAPE     10000$-.
1141
1142 022160 032777 040000 160054      BIT    #DERR,@RLCS   ;DRIVE ERROR SET?
1143 022166 001425                BEQ                    ;BRANCH IF NOT
1144
1145 022170 012777 000003 160050      MOV    #MKIGSBIT,@RLDA ;SET GET STATUS OF DRIVE
1146 022176 004537 020456      JSR    R5,LDFUNC     ;LOAD THE FUNCTION IN NEXT WORD
1147 022202 000004                GSTAT                ;GET STATUS
1148 022204 004537 021276      JSR    R5,WTCRDY     ;WAIT FOR CONTROLLER READY
1149 022210                ESCAPE  SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
1150 022212 104010 000030      EMT    C$ESCAPE     10000$-.
1151
1152 022214 013737 002234 002166      MOV    E,MP,GDDAT    ;READ DRIVE STATUS
1153 022220 032737 020000 002166      BIT    #BIT13,GDDAT  ;WRITE LOCK ERROR?
1154 022230 001404                BEQ                    ;NO, BRANCH
1155
1156 022232                ERRSF  3,WRLOCK,ERRO ;WRITE LOCK ERROR
1157 022234                TRAP   1,$ERCODE
1158 022236                .WORD 0
1159 022240                .WORD WRLOCK
1160 022242                .WORD ERRO
1161
1162 4$:
1163
1164 022242                ENDSEG                ;%%END OF SEGMENT%%
1165 022242 10000$: EMT    C$ESEG
1166 022244                ENDTST                ;**END OF TEST**
1167 022244 L10027: EMT    C$SETST
1168 022244 104001
1169
1170 .SBTTL **TEST 3** - WRITE FUNCTION INTERRUPT
1171
1172 022246                BGNST                ;**START OF TEST**
1173
1174 STARS
1175 ;*****
;CHECK OF WRITE LOGIC UNDER INTERRUPT MODE, WE WILL ISSUE A
;READ HEADER SO THAT WE DON'T WRITE ON THE BAD SECTOR FILE
;TRACK. WE WILL WRITE A FULL SECTOR (128 WORDS) FROM MEMORY (BUF).
;WE CHECK THAT NO ERRORS OCCUR. WE DO NOT CHECK RLDA OR RLBA
;INCREMENT AT THIS TIME.
;*****
;*****

```

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1176 022246 004737 021356      JSR    PC,HDHOME     ;HEADS OVER TRACK 0
1177 022252                CKERFG                ;HEADS GO HOME OKAY
1178 022260 104032 000112      EMT    C$EXIT        L10030-.
1179 022262 000112                .WORD
1180
1181 022264 104004                BGNSEG                ;%%START OF SEGMENT%%
1182 022264 EMT    C$BSEG
1183
1184 022266 005037 002144      CLR    INTPLG        ;CLEAR INTERRUPT OCCURANCE FLAG
1185 022272 005077 157750      MOV    @RLDA         ;SET UP WORD COUNT
1186 022276 012777 177600 157744      MOV    #128,@RLMP    ;SET UP BUS ADDRESS
1187 022280 012777 003052 157732      MOV    #BUF,@RLBA
1188
1189 022312                SETPRI #PRI00        ;PRIORITY TO 0
1190 022316 012700 000000      MOV    #PRI00,R0
1191 022320 104041                EMT    C$PRI
1192 022324 004537 020456      JSR    R5,LDFUNC     ;LOAD THE FUNCTION IN NEXT WORD
1193 022326 000112                WRITEINTEN           ;WRITE UNDER INTERRUPT
1194 022328 004537 021276      JSR    R5,WTCRDY     ;WAIT FOR INTERRUPT
1195 022332                ESCAPE  SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
1196 022334 104010 000036      EMT    C$ESCAPE     10000$-.
1197
1198 022336                SETPRI #PRI07        ;SET PRIORITY TO 7
1199 022336 012700 000340      MOV    #PRI07,R0
1200 022342 104041                EMT    C$PRI
1201 022344 005737 002144      TST    INTPLG        ;DID INTERRUPT OCCUR?
1202 022350 001004                BNE    2$            ;YES-BRANCH NO-REPORT
1203
1204 022352                ERDF  4,EM17,ERRO   ;WRITE DID NOT INTERRUPT
1205 022352                TRAP   1,$ERCODE
1206 022354                .WORD 4
1207 022356                .WORD EM17
1208 022360                .WORD ERRO
1209
1210 022362 104010 000006      ESCAPE  SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
1211 022364 EMT    C$ESCAPE     10000$-.
1212
1213 022366 004537 020214      JSR    R5,CHERR      ;CHECK CNTLR FOR ERRORS
1214
1215 022372                ENDSEG                ;%%END OF SEGMENT%%
1216 022372 10000$: EMT    C$ESEG
1217 022374                ENDTST                ;**END OF TEST**
1218 022374 L10030: EMT    C$SETST
1219 022374 104001
1220
1221 .SBTTL **TEST 4** - PROPER INCREMENT OF RLBA ON WRITE
1222
1223 022376                BGNST                ;**START OF TEST**
1224
1225 STARS
1226 ;*****
;CHECK THAT THE RLBA WILL INCREMENT PROPERLY AFTER THE

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ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-30
CZRLBB.P11 22-NOV-78 15:28 **TEST 4** - PROPER INCREMENT OF RLBA ON WRITE SEQ 0060

1212 ;WRITE WAS FINISHED THE RLBA SHOULD BE 128 WORDS (256 BYTES)
1213 ;CREATOR. STARTING RLBA IS "BUP", ENDING SHOULD BE "BUP" + 256."
1214 ;WE WILL MONITOR ALL ERRORS AND REPORT THEM ACCORDINGLY
1215 STARS
1216 ;*****
1217
1218 022376 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0
1219 022402 CKERFG ;HEADS GO HOME OKAY
1220 (4) 022410 104032 EMT C$EXIT
1221 (4) 022412 000116 .WORD L10031-.
1222
1223 BGNSEG ;**START OF SEGMENT**
1224 EMT C$BSEG
1225
1226 3$:
1227 CLR RDLA
1228 MOV #BUP,@RLBA ;SET UP BUS ADDRESS
1229 MOV #-128,@RLMP ;WORD COUNT
1230 MOV #BUP,@DDAT ;FORM EXPECTED BUS ADDRESS
1231 ADD #256,@DDAT ;AFTER WRITE
1232
1233 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
1234 WRITE R5,WTCRDY ;WRITE
1235 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
1236 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
1237 EMT C$ESCAPE
1238 .WORD 10000$-.
1239
1240 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
1241 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
1242 EMT C$ESCAPE
1243 .WORD 10000$-.
1244 MOV RDLA,BDDAT ;READ "RLBA" FOR PRESENT ADDRESS
1245 CMP BDDAT,BDDAT ;DID "BA" INCREMENT PROPERLY?
1246 BEQ Z$ ;YES, CONTINUE
1247
1248 ERRDF 5,EM20,ERR4 ;BA DID NOT INCREMENT
1249 TRAP T$ERRCODE
1250 .WORD 6
1251 .WORD EM20
1252 .WORD ERR4
1253
1254 2$:
1255 ENDSEG ;**END OF SEGMENT**
1256 EMT C$ESEG
1257 10000$:
1258 EMT C$ESEG
1259 ENDTST ;**END OF TEST**
1260 L10031:
1261 EMT C$ETST
1262
1263 .SBTTL **TEST 5** - PROPER INCREMENT OF RLDA ON WRITE
1264 BGNST ;**START OF TEST**
1265
1266 022532

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ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-31
CZRLBB.P11 22-NOV-78 15:28 **TEST 5** - PROPER INCREMENT OF RLDA ON WRITE SEQ 0061

1252 022532
1253 STARS
1254 ;*****
1255 ;CHECK THAT THE SECTOR INCREMENTS AFTER THE WRITE WAS FINISHED.
1256 ;WE RANDOMLY PICK A SECTOR (OTHER THAN LAST TRACK) AND ISSUE
1257 ;A FULL SECTOR WRITE THE RLDA SHOULD REFLECT AN INCREMENT
1258 ;OF THE SECTOR. "GDDAT" WAS THE EXPECTED RLDA.
1259 STARS
1260 ;*****
1261
1262 022532 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0
1263 022536 CKERFG ;HEADS GO HOME OKAY
1264 (4) 022544 104032 EMT C$EXIT
1265 (4) 022546 000114 .WORD L10032-.
1266
1267 BGNSEG ;**START OF SEGMENT**
1268 EMT C$BSEG
1269
1270 3$:
1271 CLR GDDAT
1272 MOV GDDAT,@RLDA ;SETUP DISK ADDRESS
1273 INC GDDAT ;CREATE EXPECTED SECTOR
1274 MOV #-128,@RLMP ;WORD COUNT
1275 MOV #BUP,@RLBA ;SETUP BUS ADDRESS
1276
1277 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
1278 WRITE R5,WTCRDY ;WRITE
1279 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
1280 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
1281 EMT C$ESCAPE
1282 .WORD 10000$-.
1283
1284 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
1285 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
1286 EMT C$ESCAPE
1287 .WORD 10000$-.
1288 MOV E,DA,BDDAT ;READ DISK ADDRESS
1289 CMP GDDAT,BDDAT ;DID SECTOR INCREMENT PROPERLY
1290 BEQ Z$ ;YES, BRANCH NO, REPORT ERROR
1291
1292 ERRDF 6,EM21,ERR4 ;DA DID NOT INCREMENT
1293 TRAP T$ERRCODE
1294 .WORD 6
1295 .WORD EM21
1296 .WORD ERR4
1297
1298 2$:
1299 ENDSEG ;**END OF SEGMENT**
1300 EMT C$ESEG
1301 10000$:
1302 EMT C$ESEG
1303 ENDTST ;**END OF TEST**
1304 L10032:
1305 EMT C$ETST
1306
1307 022660

```

```

1291
1292
1293 022664
1294
1295 022664
1296
1297
1298
1299
1300 022664
1301
1302 022664 004737 021356
1303 022670
(4) 022676 104032
(4) 022700 000126
1305 022702
(3) 022702 104004
1306
1307
1308 022704 012777 000050 157334
1309 022712 012777 003052 157334
1310 022720 012777 177777 157322
1311
1312 022726 004537 020456
1313 022732 000012
1314 022734 004537 021276
1315 022740
(3) 022740 104010
(3) 022742 000054
1316
1317 022744 013737 002226 002160
1318 022752 042737 001777 002160
1319 022760 022737 112000 002160
1320 022766 001402
1321
1322 022770 004537 020214
1323 022774 104006
1324
1325 022776 022737 112000 002160
1326 023004 001404
1327 023006
(5) 023010 104462
(5) 023012 000027
(5) 023014 011043
(5) 023014 014244
1328
1329 023016
1330
1331 023016
(3) 023016
(3) 023016 104005
1332 023020

```

```

.SBTL **TEST 6** - FORCE HEADER NOT FOUND WITH WRITE
BGNTST
**START OF TEST**
STARS
*****
; FORCE HEADER NOT FOUND ERROR TO OCCUR. THIS IS DONE
; BY SETTING SECTOR 40 OF THE RLDA AND ISSUING A
; WRITE. SECTOR 40 DOES NOT EXIST ON THE L01 PACK
; THEREFORE HDR NT FOUND SHOULD SET.
STARS
*****
JSR PC,HDHOME ;HEADS OVER TRACK 0
CKFRFG ;HEADS GO HOME OKAY
EMT C$EXIT
.WORD L10033-.
BGNSEG ;%%START OF SEGMENT%%
EMT C$BSEG
MOV #40,0RLDA ;INSURE NOT TO FIND HEADER BY
MOV #BUF,0RLBA ;SETTING SECTOR 40 OF CYL. ADDR.
MOV #-1,0RLMP ;WORD COUNT
JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
WRITE ;WRITE
R5,WTCRDY ;WAIT FOR CONTROLLER READY
ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
EMT C$ESCAPE
.WORD 10000$-.
MOV E.CS,TMPO ;GET RLCS
BIC #177,TMPO ;SAVE ERROR BITS
CMP #BIT15|BIT12|BIT10,TMPO ;HDR NOT FOUND SET.
BEQ IS ;YES, CONTINUE
1$: JSR R5,CHERR
CKLOOP
EMT C$CLP1
CMP #BIT15|BIT12|BIT10,TMPO
BEQ 2$
ERRDF 2$,EM10,ERR0
TRAP #SERCODE
.WORD 23
.WORD EM10
.WORD ERRO ;WHEN FORCED
2$:
10000$: ENDSEG ;%%END OF SEGMENT%%
EMT C$ESEG
ENDTST **END OF TEST**

```

```

(3) 023020
(3) 023020 104001
1333
1334
1335 023022
1336
1337 023022
1338
1339 023022
1340
1341
1342
1343 023022
1344
1345 023022 004737 021356
1346 023026
(4) 023034 104032
(4) 023036 000160
1349
1350 023040
(3) 023040 104004
1351
1352 023042
(3) 023042 012700 000000
(3) 023046 104041
1353 023050 005037 002144
1354 023054 012777 000050 157164
1355 023062 012777 003052 157154
1356 023070 012777 177777 157152
1357
1358 023076 004537 020456
1359 023102 000112
1360 023104 004537 021276
1361
1362 023110
(3) 023110 104006
(3) 023112
(3) 023114 012700 000340
(3) 023116 104041
1363
1364 023120 005737 002144
1365 023124 001004
1366
1367 023126
(3) 023126 104462
(5) 023130 000030
(5) 023132 012723
(5) 023134 014244
1368
1369 023136
(3) 023136 104010
(3) 023140 000054
1370

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```

L10033: EMT C$SETST
.SBTL **TEST 7** - FORCE HEADER NOT FOUND WITH WRITE INTERRUPT
BGNTST
**START OF TEST**
STARS
*****
; FIRST THAT HEADER NOT FOUND ERROR WILL GENERATE AN INTERRUPT
; ON OCCURANCE. HEADER NOT FOUND WILL BE FORCED BY SETTING
; SECTOR 40 OF RLDA AND ISSUING A WRITE
STARS
*****
JSR PC,HDHOME ;HEADS OVER TRACK 0
CKFRFG ;HEADS GO HOME OKAY
EMT C$EXIT
.WORD L10034-.
BGNSEG ;%%START OF SEGMENT%%
EMT C$BSEG
SETPRI #PRI00
MOV #PRI00,R0
EMT C$SPRI
CLR INTFLG ;CLEAR INTERRUPT OCCURANCE FLAG
MOV #40,0RLDA ;INSURE NOT TO FIND HEADER BY
MOV #BUF,0RLBA ;SETTING SECTOR 40 OF CYL. ADDR.
MOV #-1,0RLMP ;WORD COUNT
JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
WRITE ;WRITE
R5,WTCRDY ;WAIT FOR CONTROLLER READY
CKLOOP
EMT C$CLP1
SETPRI #PRI07,R0
MOV #PRI07,R0
EMT C$SPRI
TST INTFLG ;DID INTERRUPT OCCUR
BNE 2$ ;YES OKAY
ERRDF 2$,EM43,ERR0 ;NO INTERRUPT FROM OPI
TRAP #SERCODE
.WORD 24
.WORD EM43
.WORD ERRO
2$: ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
EMT C$ESCAPE
.WORD 10000$-.

```

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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-34
CZRLBB.P11 22-NOV-78 15:28 **TEST 7** - FORCE HEADER NOT FOUND WITH WRITE INTERRUPT
                                                                    SEQ 0064
1371 023142 013737 002226 002160      MOV     E,CS,TMPO      ;SET RLCS
1372 023150 043737 001777 002160      BIC    #177,TMPO      ;SAVE ERROR BITS
1373 023156 023737 112000 002160      CMP    #BIT15|BIT12|BIT10,TMPO ;WDR NOT FOUND SET.
1374 023164 001402                BEQ    IS              ;YES, CONTINUE
1375
1376 023166 004537 020214                JSR    R5,CHERR
1377 023172 104006                1$:    C%LOOP
1378                EMT
1379 023174 022737 112000 002160      CMP    #BIT15|BIT12|BIT10,TMPO
1380 023202 001404                BEQ    IS
1381 023204                ERROF  #2,EM10,ERRO
1382 023206 000031                TRAP  #2,ERRCODE
1383 023210 011043                .WORD 25
1384 023212 014244                .WORD EM10
1385                .WORD ERRO                ;WHEN FORCED
1386
1387                3$:
1388                ENDSEG                ;**END OF SEGMENT**
1389
1390                10000$:
1391 023214                EMT    C%SEEG
1392 023216                ENDTST ;**END OF TEST**
1393 L10034:
1394                EMT    C%ETST
1395
1396                .SBTTL **TEST 8** - CHECK OPI TIME WITH HDR NT FND
1397                BGNTST                ;**START OF TEST**
1398
1399                STARS
1400                ;*****
1401                ;CHECK OPI TIME IT SHOULD BE AROUND 200 MILLISECONDS (ON UNIBUS)
1402                ;CHECK THIS BY TIMING OPI ON A FORCED HEADER NOT FOUND
1403                ;ISSUE WRITE WITH SECTOR 40 SET IN THE DISK ADDRESS
1404                STARS
1405                ;*****
1406
1407 023220 004737 021356                JSR    PC,HDHOME      ;HEADS OVER TRACK 0
1408 023222 104032                CKERFG                ;HEADS GO HOME OKAY
1409 023234 000264                EMT    C%EXIT
1410                .WORD L10035-.
1411
1412                BGMSEG                ;**START OF SEGMENT**
1413                EMT    C%BSEG
1414
1415                CLRVEC BVEC,RO                ;CLEAR PRESENT INTERRUPT VECTOR
1416                MOV    BVEC,RO
1417                EMT    C%VEC
1418                SETVEC BVEC,#INTSRV,#340        ;SET INTR. VEC. WITH ABORT WAIT
1419                MOV    #340,-(SP)
1420                MOV    #INTSRV,-(SP)
1421                MOV    BVEC,-(SP)
1422                MOV    #3,-(SP)
1423
1424 023220 004737 021356                JSR    PC,HDHOME      ;HEADS OVER TRACK 0
1425 023222 104032                CKERFG                ;HEADS GO HOME OKAY
1426 023234 000264                EMT    C%EXIT
1427                .WORD L10035-.
1428
1429                BGMSEG                ;**START OF SEGMENT**
1430                EMT    C%BSEG
1431
1432                CLRVEC BVEC,RO                ;CLEAR PRESENT INTERRUPT VECTOR
1433                MOV    BVEC,RO
1434                EMT    C%VEC
1435                SETVEC BVEC,#INTSRV,#340        ;SET INTR. VEC. WITH ABORT WAIT
1436                MOV    #340,-(SP)
1437                MOV    #INTSRV,-(SP)
1438                MOV    BVEC,-(SP)
1439                MOV    #3,-(SP)
1440
1441 023220 004737 021356                JSR    PC,HDHOME      ;HEADS OVER TRACK 0
1442 023222 104032                CKERFG                ;HEADS GO HOME OKAY
1443 023234 000264                EMT    C%EXIT
1444                .WORD L10035-.
1445
1446                BGMSEG                ;**START OF SEGMENT**
1447                EMT    C%BSEG
1448
1449                CLRVEC BVEC,RO                ;CLEAR PRESENT INTERRUPT VECTOR
1450                MOV    BVEC,RO
1451                EMT    C%VEC
1452                SETVEC BVEC,#INTSRV,#340        ;SET INTR. VEC. WITH ABORT WAIT
1453                MOV    #340,-(SP)
1454                MOV    #INTSRV,-(SP)
1455                MOV    BVEC,-(SP)
1456                MOV    #3,-(SP)

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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-35
CZRLBB.P11 22-NOV-78 15:28 **TEST 8** - CHECK OPI TIME WITH HDR NT FND
                                                                    SEQ 0065
1407 023266 104037 000010                EMT    C%SEVEC
1408 023270 062706 000010                ADD    #10,SP
1409 023274                SETPRI #PRI00
1410 023274 012700 000000                MOV    #PRI00,R0
1411 023300 104041                MOV    C%SPRI
1412 023302 005037 002144                CLR    INTFLG        ;CLEAR INTERRUPT FLAG
1413 023306 012777 000050 156732                MOV    #40,&RLDA     ;SET UP FOR HDR NT FND
1414 023306 012777 000050 156732                MOV    #40,&RLDA     ;BUS ADDRESS
1415 023322 012777 177777 156720                MOV    #-1,&RLMP     ;WORD COUNT
1416
1417 023330 004537 020456                JSR    R5,LDFUNC     ;LOAD THE FUNCTION IN NEXT WORD
1418 023334 000112                WRITEINTEN
1419
1420 023336 013700 002302                MOV    OPIMX,R0
1421 023342 006300                ASL    RO
1422 023344 006300                ASL    RO
1423 023346 006300                ASL    RO
1424 023350 063700 002302                ADD    OPIMX,R0
1425 023354 063700 002302                ADD    OPIMX,R0
1426
1427 023360                WAITUS RO                ;WAIT MAX MILLISECONDS
1428 023360 104027                EMT    C%WTU
1429 023362 010037 002170                MOV    RO,BDDAT     ;SETUP FOR WORST CASE
1430 023366 005737 002144                TST   INTFLG        ;DID INTERRUPT OCCUR
1431 023372 001427                BEQ    4$            ;NO, REPORT ERROR
1432
1433 023374                GETTIM BDDAT                ;GET TIME EXPIRED
1434 023374                EMT    C%GTIM
1435 023376 010037 002170                MOV    RO,BDDAT
1436 023402 005000                CLR    RO            ;DIVIDE
1437 023404 162737 000012 002170 1$:    SUB    #10.,BDDAT    ;ANSWER
1438 023412 100402                BMI    4$            ;BY 10 TO GET
1439 023414 005200                INC    RO            ;RIGHT ANSWER
1440 023416 000772                BR    1$
1441 023420 010037 002170                3$:    MOV    RO,BDDAT
1442
1443                ;CHECK THAT OPI TIME IS WITHIN LIMITS
1444
1445                2$:
1446 023424                SETPRI #PRI07
1447 023424 012700 000340                MOV    #PRI07,R0
1448 023430 104041                EMT    C%SPRI
1449 023430 023737 002302 002170                CMP    OPIMX,BDDAT    ;IS IT WITHIN LIMITS
1450 023440 002404                BLT    4$            ;NO, REPORT ERROR
1451
1452 023442 023737 002300 002170                CMP    OPIMN,BDDAT    ;WITHIN LIMITS
1453 023450 003404                BLE    5$            ;YES
1454
1455 023452                4$:
1456 023452 104462                ERROF  974.,EM56,ERR13 ;OPI TIMING INCORRECT
1457 023452 001716                TRAP  #2,ERRCODE
1458 023454 013456                .WORD 974
1459 023456 013456                .WORD EM56
1460 023460 015102                .WORD ERR13
1461
1462 023462                5$:
1463 023462 013700 002254                CLRVEC BVEC,RO        ;CLEAR PRESENT VECTOR
1464 023466 104036                MOV    BVEC,RO
1465 023470                EMT    C%VEC
1466                SETVEC BVEC,#INTSRV,#340        ;SET IN OLD VECTOR

```

```

(7) 023470 012746 000340      MOV     #340,-(SP)
(6) 023474 012746 020110      MOV     #INTSRV,-(SP)
(5) 023500 012746 002254      MOV     #VEC,-(SP)
(4) 023504 012746 000003      MOV     #3,-(SP)
(3) 023510 104037 000003      EMT     C$VEC
(2) 023512 062706 000010      ADD     #10,SP
1448
1449 023516
1450 023516 104005      10000$: ENDSEG                ;**END OF SEGMENT**
1451 023520      EMT     C$ESEG
1452 023520 104001      ENDTST L10036:                ;**END OF TEST**
1453 023520      EMT     C$ESET
1454
1455 .SBTTL **TEST 9** - MULTIPLE SECTOR TRANSFER ON WRITE
1456 BGNTST                ;**START OF TEST**
1457
1458 STARS
1459 ;*****
1460 ;CHECK FOR MULTIPLE SECTOR TRANSFER ON WRITE. THIS TEST CHECKS
1461 ;THAT TWO SECTORS CAN BE SUCCESSFULLY WRITTEN. WE LOAD
1462 ;A WORD COUNT OF 129 WORDS (ONE SECTOR + 1 WORD) STARTING AT
1463 ;SECTOR 0 THRU SECTOR 37 AND VERIFY THAT THE RLDA DOES
1464 ;A DOUBLE INCREMENT EACH TIME.
1465 STARS
1466 ;*****
1467
1468 023522 004737 021356      JSR     PC,HDHOME          ;HEADS OVER TRACK 0
1469 023526      CKERFG                    ;HEADS GO HOME OKAY
1470 (4) 023534 104032      EMT     C$EXIT
1471 (4) 023536 000152      .WORD  L10036-.
1472
1473 023540 005037 002160      CLR     TMO0              ;CLEAR TEMP LOCATIONS
1474 023544 005037 002162      CLR     TMO1
1475
1476 023550 104004      BGNSEG                    ;**START OF SEGMENT**
1477 (3) 023550      EMT     C$BSEG
1478
1479 023552 013737 002162 002166 1$:  MOV     TMO1,GDDAT        ;GET CYLINDER
1480 023560 053737 002160 002166      BIS     TMO0,GDDAT        ;GET SECTOR
1481 023566 013777 002166 156452      MOV     GDDAT,RLDA        ;SET DISK ADDRESS-SECTOR 0
1482 023574 062737 000002 002166      ADD     #2,GDDAT          ;SET EXPECTED + 2
1483 023602 012777 003954 156434      MOV     #BUF,RLBA         ;SET BUS ADDRESS
1484 023610 012777 179574 156432      MOV     #129,@RLMP        ;WORD COUNT-SECTOR+1 WORD
1485
1486 023616 004537 020456      JSR     RS,LDFUNC          ;LOAD THE FUNCTION IN NEXT WORD
1487 023622 000012      WRITE
1488 023624 004537 021276      JSR     RS,WTCRDY         ;WRITE
1489 023630      ESCAPE                    ;WAIT FOR CONTROLLER READY?
1490 023632 104010      EMT     C$ESCAPE         ;CHECK FOR FL:LOE, ELSE EXIT SEG
1491 (3) 023632 000054      .WORD  10000$-
    
```

```

1487 023634 004537 020214      JSR     RS,CHERR          ;CHECK CNTLR FOR ERRORS
1488 023638      ESCAPE                    ;CHECK FOR FL:LOE, ELSE EXIT SEG
1489 (3) 023640 104010      EMT     C$ESCAPE
1490 (3) 023642 000044      .WORD  10000$-
1491
1492 023644 013737 002232 002170      MOV     E,DA,BDDAT        ;READ DISK ADDRESS
1493 023652 037937 002170 002166      CMP     BDDAT,GDDAT      ;IS DISK ADDRESS CORRECT
1494 023660 001404      BEQ     2$                ;YES, BRANCH NO, REPORT ERROR
1495
1496 023662 104462      ERDF  7,EN22,ERR4        ;DISK ADDRESS NOT CORRECT
1497 (3) 023662      TRAP  1,ERRCODE
1498 (5) 023664 000007      .WORD  1
1499 (5) 023666 011544      .WORD  0
1500 (5) 023670 014410      .WORD  0
1501
1502 023672 2$:
1503 023672 005237 002160      INC     TMO0              ;NEXT SECTOR
1504 023676 023437 000046 002160      CMP     #46,TMO0         ;AT END?
1505 023704 001322      BNE     1$                ;NO, GO BACK
1506
1507 023706
1508 023706 104005      10000$: ENDSEG                ;**END OF SEGMENT**
1509 (3) 023706      EMT     C$ESEG
1510 023710      ENDTST L10036:                ;**END OF TEST**
1511 (3) 023710      EMT     C$ESET
1512
1513 .SBTTL **TEST 10** - CHECK DIRECTION OF WRITE NPR
1514 BGNTST                ;**START OF TEST**
1515
1516 STARS
1517 ;*****
1518 ;VERIFY THAT A WRITE IS WRITING NOT READING. WE WRITE A
1519 ;KNOWN PATTERN IN "BUF" (128 WORD), WE THEN ISSUE A WRITE.
1520 ;ONCE THE WRITE IS FINISHED WE CHECK THAT "BUF" IS INTACT.
1521 ;THIS IS DONE TO PROVE THAT THE NPR IS GOING THE RIGHT
1522 ;WAY.
1523 STARS
1524 ;*****
1525
1526 023712 004737 021356      JSR     PC,HDHOME          ;HEADS OVER TRACK 0
1527 023716      CKERFG                    ;HEADS GO HOME OKAY
1528 (4) 023724 104032      EMT     C$EXIT
1529 (4) 023726 000160      .WORD  L10037-.
1530
1531 023730 104004      BGNSEG                    ;**START OF SEGMENT**
1532 (3) 023730      EMT     C$BSEG
1533
1534 023732 2$:
1535 023732 012702 003052      MOV     #BUF,R2           ;WRITE BUFFER FOR WRITE OPERATION
1536 023734 012701 000200      MOV     #128,R1          ;ONE SECTOR'S WORTH
1537 023742 012722 125252      MOV     #125252,(R2)+    ;WRITE BUFFER
    
```

```

1528 023746 005301      DEC R1          ;DONE?
1529 023750 001374      BNE 3$         ;NO, GO BACK
1530
1531 023752 005077 156270 CLR @RLDA      ;LOAD DISK ADDRESS
1532 023756 012777 177600 MOV #128,@RLMP ;WORD COUNT
1533 023764 012777 003052 MOV @R1,@RLBA ;BUS ADDRESS
1534 023772 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
1535 023776 000012      WRITE          ;WRITE SOME DATA
1536 024000 004537 021276 JSR R5,WTCRDY ;WAIT FOR IT TO FINISH
1537 024004      ESCAPE SEG    ;CHECK FOR FL:LOE, ELSE EXIT SEG
1538 024006      EMT 104010 ;
1539      .WORD 10000$-.
1540
1541 024010 004537 020214 JSR R5,CHERR  ;CHECK CNTLR FOR ERRORS
1542 024014      ESCAPE SEG    ;CHECK FOR FL:LOE, ELSE EXIT SEG
1543 024016      EMT C$ESCAPE ;
1544      .WORD 10000$-.
1545
1546 024020 012702 003052 MOV #BUF,R2   ;SET UP TO CHECK BUFFER
1547 024024 012701 000200 MOV #128,R1  ;CHECK 128 WORDS
1548
1549 024030      BGNSEG          ;**START OF SEGMENT**
1550 024030      EMT C$BSEG          ;
1551
1552 024032 012737 125252 002166 MOV #125252,GDDAT ;DATA SHOULD BE 125252
1553 024034 011237 025252 002170 MOV @R1,BDDAT  ;LOAD DATA INTO BDDAT
1554 024044 023937 002166 002170 4$: CMP GDDAT,BDDAT ;IS IT OKAY?
1555 024052 001406      BEQ 5$          ;YES, CONTINUE
1556
1557 024054 010237 002162 MOV R2,TMP1  ;LOAD MEMORY LOCATION OF FAILURE
1558 024060      ERDF 6,EM26,ERR8 ;
1559 024062      TRAP T$ERRCODE ;
1560 024064      .WORD 6 ;
1561 024066      .WORD EM26 ;
1562      .WORD ERR8 ;
1563
1564 024070      ESCAPE SEG    ;CHECK FOR FL:LOE, ELSE EXIT SEG
1565 024072      EMT C$ESCAPE ;
1566 024074      .WORD 100013$-.
1567 024076      TST (R2)+      ;NEXT!
1568 024100      DEC R1        ;DONE?
1569      BNE 4$          ;NO, GO BACK
1570
1571 024102      ENDSEG          ;**END OF SEGMENT**
1572 024102      EMT C$ESEG          ;
1573 024104      ENDSEG          ;**END OF SEGMENT**
1574 024104      .WORD 10000$ ;
1575 024106      EMT C$ESEG          ;
1576 024106      ENDTST L10037: ;**END OF TEST**
1577      EMT C$SETST ;
1578
1579 .SBTTL **TEST 11** - CHECK FULL RLBA INCREMENT
1580
1581 024110      BGNST          ;**START OF TEST**
    
```

```

1567
1568 024110      STARS
1569 ;*****
1570 ;TEST THAT THE RLBA WILL INCREMENT, WE DO NOT DO A FULL 16
1571 ;BIT INCREMENT WE CHECK THAT EACH BIT WILL TOGGLE 0 TO 1
1572 ;AND 1 TO 0. WE DO CHECK ALL BITS EVEN IF ALL MEMORY
1573 ;IS NOT AVAILABLE. (WE IGNORE NON-EXISTANT MEMORY ERRORS).
1574 ;WE USE THE SAME DISK ADDRESS (RANDOM) AND A 1 WORD TRANSFER.
1575 ;*****
1576
1577 024110 004737 021356 JSR PC,HDDHME ;HEADS OVER TRACK 0
1578 024114      CKERFG      ;HEADS GO HOME OKAY
1579 024124 104032      EMT C$EXIT ;
1580 024124 000134      .WORD L10040-.
1581
1582 024126 005037 002162 CLR TMP1      ;CLEAR LOCATION
1583
1584 024132      BGNSEG          ;**START OF SEGMENT**
1585 024132      EMT C$BSEG          ;
1586
1587 024134 012777 177777 156106 3$: MOV #-1,@RLMP ;ONLY ONE (1) WORD
1588 024142 005077 156100 CLR @RLDA    ;LOAD DISK ADDRESS
1589 024146 013777 002162 156070 MOV @R1,TMP1 ;BUS ADDRESS
1590
1591 024154 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
1592 024160      WRITE          ;WRITE SOME DATA
1593 024162 004537 021276 JSR R5,WTCRDY ;WAIT FOR WRITE TO FINISH
1594 024166      ESCAPE SEG    ;CHECK FOR FL:LOE, ELSE EXIT SEG
1595 024170      EMT 104010 ;
1596      .WORD 10000$-.
1597
1598 024172 013737 002162 002166 4$: MOV TMP1,GDDAT ;SET UP EXPECTED RLBA
1599 024200 062737 000002 002166 ADD #2,GDDAT  ;PREVIOUS RLBA+2
1600 024206 013737 002230 002170 MOV #BA,BDDAT ;READ RLBA
1601 024214 023737 002166 002170 CMP GDDAT,BDDAT ;WAS IT UPDATED PROPERLY?
1602 024222 001404      BEQ 5$          ;YES, CONTINUE
1603
1604 024224      ERDF 9,EM30,ERR4 ;BA INCREMENT ERROR
1605 024226      TRAP T$ERRCODE ;
1606 024230      .WORD 9 ;
1607 024232      .WORD EM30 ;
1608 024234      .WORD ERR4 ;
1609 024236      ESCAPE SEG    ;CHECK FOR FL:LOE, ELSE EXIT SEG
1610      EMT C$ESCAPE ;
1611      .WORD 10000$-.
1612
1613 024240 006337 002162 ASL TMP1      ;NEXT PATTERN TO TEST RLBA
1614 024244 103404      BCS 6$          ;DONE?
1615 024246 052737 000002 002162 BIS #BIT1,TMP1 ;NO, SET IN BIT 1
1616 024254 000727      BR 3$          ;GO CHECK NEXT.
1617
1618 024256      6$:          ;END TEST
    
```



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1610 1611 024256 104005 10000$: ENDSEG ;**END OF SEGMENT**
1612 024256 104005 10000$: EMT C$ESEG ;**END OF TEST**
1613 024260 104001 ENDTST L10040:
1614 024260 104001 EMT C$ETST
1615 024262 104001 .SBTTL **TEST 12** - BA BIT 16 INCREMENT
1616 024262 104001 BGNSTST ;**START OF TEST**
1617 024262 104001 STARS
1618 ;*****
1619 ;CHECK THAT BA BIT 16 WILL INCREMENT. WE WILL LOAD THE
1620 ;RLBA WITH 177776 AND ISSUE A ONE WORD WRITE WE THEN
1621 ;CHECK BA BIT 16 TO SET, BA 17 TO STAY A 0 AND THE RLBA
1622 ;TO GO TO ZERO
1623 024262 104001 STARS
1624 ;*****
1625 024262 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0
1626 024266 004737 CKERFG ;HEADS GO HOME OKAY
1627 024274 104032 EMT C$EXIT
1628 024276 000160 .WORD L10041-.
1629 024300 104004 BGNSEG ;**START OF SEGMENT**
1630 024300 104004 EMT C$BSEG
1631 024302 012777 177776 155734 2$: MOV #177776,@RLBA ;SET MAX BA TO INC. BA16
1632 024310 005037 002262 CLR MEM ;WE DON'T WANT TO LOAD ANY EA
1633 024314 012777 177777 155726 MOV #-1,@RLMP ;ONE WORD TRANSFER
1634 024322 005077 155720 CLR @RLDA
1635 024326 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
1636 024332 000012 WRITE R5,WTCRDY
1637 024334 004537 021276 EMT ESCAPE SEG ;WAIT FOR WRITE TO FINISH
1638 024340 104010 EMT ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
1639 024342 000112 .WORD 10000$-.
1640 024344 032737 020000 002226 BIT #NXM,E.CS ;NON-EXISTANT MEMORY ERROR?
1641 024352 001002 BNE 3$ ;YES, CONTINUE
1642 024354 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
1643 024360 104010 3$: ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
1644 024360 104010 EMT C$ESCAPE
1645 024362 000072 .WORD 10000$-.
1646 024364 032737 000020 002226 BIT #BA16,E.CS ;DID BA16 SET?
1647 024372 001004 BNE 4$ ;YES, CONTINUE
1648 024374 104462 ERDF 11,EM31,ERR0 ;BA 16 DID NOT INCREMENT
1649 024374 000012 TRAP T$ERRCODE
1650 024400 012225 .WORD 10
1651 .WORD EM31
1652 .WORD ERR0
1653 .WORD 10
1654 .WORD EM31
1655 .WORD ERR0
1656 .WORD 10
1657 .WORD EM31
1658 .WORD ERR0
1659 .WORD 10
1660 .WORD EM31
1661 .WORD ERR0
1662 .WORD 10
1663 .WORD EM31
1664 .WORD ERR0
1665 .WORD 10
1666 .WORD EM31
1667 .WORD ERR0
1668 .WORD 10
1669 .WORD EM31
1670 .WORD ERR0
1671 .WORD 10
1672 .WORD EM31
1673 .WORD ERR0
1674 .WORD 10
1675 .WORD EM31
1676 .WORD ERR0
1677 .WORD 10
1678 .WORD EM31
1679 .WORD ERR0
1680 .WORD 10
1681 .WORD EM31
1682 .WORD ERR0
1683 .WORD 10
1684 .WORD EM31
1685 .WORD ERR0
1686 .WORD 10
1687 .WORD EM31
1688 .WORD ERR0
1689 .WORD 10
1690 .WORD EM31
1691 .WORD ERR0
1692 .WORD 10
1693 .WORD EM31
1694 .WORD ERR0
1695 .WORD 10
1696 .WORD EM31
1697 .WORD ERR0
1698 .WORD 10
1699 .WORD EM31
1700 .WORD ERR0
    
```

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1650 024402 014244 .WORD ERRO
1651 024404 104006 4$: CKLOOP
1652 024404 104006 EMT C$CLP1
1653 024406 032737 000040 002226 BIT #BA17,E.CS ;DID BA17 SET ALSO?
1654 024414 001404 BEQ 5$ ;NO, GOOD CONTINUE
1655 024416 104462 ERDF 11,EM32,ERR0 ;BA 17 GOT CARRIED AWAY
1656 024416 104462 TRAP T$ERRCODE
1657 024420 000013 .WORD 11
1658 024422 012270 .WORD EM32
1659 024424 014244 .WORD ERRO
1660 024426 104006 5$: CKLOOP
1661 024426 104006 EMT C$CLP1
1662 024430 005037 002166 CLR GDDAT ;CHECK THAT BA15-BA0 IS CLEAR
1663 024434 013737 002230 002170 MOV E,BA,BDDAT ;READ BA
1664 024442 001404 BEQ 6$ ;IS BA ZERO?
1665 024444 104462 ERDF 12,EM33,ERR4 ;BA SHOULD BE ZERO
1666 024446 000014 TRAP T$ERRCODE
1667 024450 012335 .WORD 12
1668 024452 014410 .WORD EM33
1669 024452 014410 .WORD ERR4
1670 024454 104005 6$:
1671 024454 104005 10000$: ENDSEG ;**END OF SEGMENT**
1672 024454 104005 10000$: EMT C$ESEG ;**END OF TEST**
1673 024456 104001 ENDTST L10041:
1674 024456 104001 EMT C$ETST
1675 024460 104001 .SBTTL **TEST 13** - BA BIT 17 INCREMENT
1676 024460 104001 BGNSTST ;**START OF TEST**
1677 024460 104001 STARS
1678 ;*****
1679 ;CHECK THAT BA BIT 17 WILL INCREMENT. WE WILL LOAD THE
1680 ;RLBA WITH 177776 AND BA 16 SET, WE WILL ISSUE A ONE WORD
1681 ;WRITE. WE THEN CHECK BA17 TO SET, BA16 TO CLEAR AND
1682 ;BA15 - BAO TO CLEAR.
1683 024460 104001 STARS
1684 ;*****
1685 024460 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0
1686 024464 004737 CKERFG ;HEADS GO HOME OKAY
1687 024472 104032 EMT C$EXIT
1688 024474 000162 .WORD L10042-.
1689 024476 104004 BGNSEG ;**START OF SEGMENT**
    
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(3) 024476 104004 EMT C$BSEG
1687
1688 024500
1689 024500 012777 177776 155536 2$: MOV #177776, R1LBA ;SET MAX BA TO IMC. BA16
1690 024506 012777 000020 022526 MOV #BA16, MEM ;SET BA16 IN RLCS
1691 024514 012777 177777 155526 MOV #1, R1LMP ;ONE WORD TRANSFER
1692 024522 012777 155526 CLR R1LMP
1693 024526 004537 020456 JSR R5, LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
1694 024532 000012 WRITE R5, WTCRDY
1695 024534 004537 021276 JSR R5, WTCRDY ;WAIT FOR WRITE TO FINISH
1696 024540 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 024542 104010 EMT C$ESCAPE
(3) 024547 000119 EMT -WORD 10000$-
1697 024544 032737 020000 002226 BIT #NXM, E.CS ;NON-EXISTANT MEMORY ERROR?
1698 024552 001002 BNE 3$ ;YES, CONTINUE
1699
1700 024554 004537 020214 JSR R5, CHERR ;CHECK CNTLR FOR ERRORS
1701 024560 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 024562 104010 EMT C$ESCAPE
(3) 024562 000072 EMT -WORD 10000$-
1702
1703 024564 032737 000040 002226 BIT #BA17, E.CS ;DID BA17SET?
1704 024572 001004 BNE 4$ ;YES, CONTINUE
1705
1706 024574 ERRDF 13, EM34, ERRO ;BA 17 DID NOT SET
(3) 024574 104462 TRAP TRAPCODE
(5) 024576 000019 EMT -WORD 13
(2) 024600 012376 EMT -WORD EM34
(3) 024602 014244 EMT -WORD ERRO
1707
1708 024604 4$: CKLOOP
(3) 024604 104006 EMT C$CLP1
1709
1710 024606 032737 000020 002226 BIT #BA16, E.CS ;DID BA16 SET ALSO?
1711 024614 001404 BEQ 5$ ;NO, GOOD CONTINUE
1712
1713 024616 ERRDF 14, EM35, ERRO ;BA 16 DIDN'T KNOW WHEN TO QUIT.
(3) 024616 104462 TRAP TRAPCODE
(5) 024620 000016 EMT -WORD 14
(2) 024622 012376 EMT -WORD EM35
(3) 024624 014244 EMT -WORD ERRO
1714
1714 024626 5$: CKLOOP
(3) 024626 104006 EMT C$CLP1
1715
1716 024630 005037 002166 CLR GDDAT ;CHECK THAT BA15-BA0 IS CLEAR
1717 024634 013737 002230 002170 MOV #BA, BDDAT ;READ BA
1718 024640 001404 BEQ 6$ ;IS BA ZERO?
1719 024644 ERRDF 15, EM36, ERR4 ;BA SHOULD BE ZERO
(3) 024644 104462 TRAP TRAPCODE
(5) 024646 000017 EMT -WORD 15
(2) 024650 012506 EMT -WORD EM36
(3) 024652 014410 EMT -WORD ERR4
1720
1721 024654 6$: ;
1722
1723 024654 ENDSEG ;%%END OF SEGMENT%%
  
```

```

(3) 024654 10000$: EMT C$ESEG
(3) 024654 104005 ENDTST ;**END OF TEST**
1724 024656 L10042: EMT C$ETST
(3) 024656 104001 .SBTTL **TEST 14** - TEST READ NPR INTEGRITY
1725
1726 024660 BGNTST ;**START OF TEST**
1727
1728
1729
1730
1731
1732
1733 024660 STARS
(2) *****
1734 ;CHECK THAT NPR WILL NOT INTERFERE WITH THE OPERATION OF THE UNIBUS
1735 ;WE SETUP LOCATION 4 TO HANDLE THE TRAP IF IT HAPPENS
1736 024660 STARS
(2) *****
1737
1738 024660 004737 021356 JSR PC, HDHOME ;HEADS OVER TRACK 0
1739 024664 CKRFG ;HEADS GO HOME OKAY
(4) 024672 104032 EMT C$EXIT
(4) 024674 000132 EMT -WORD L10043-
1741
1742 024676 HGNSEG ;%%START OF SEGMENT%%
(3) 024676 104004 EMT C$BSEG
1743
1744
1745 024700 1$: SETVEC ERRVEC, #TRPHAN, #340 ;SET UP VECTOR
(7) 024700 MOV #340, -(SP)
(6) 024704 012746 000340 MOV #TRPHAN, -(SP)
(2) 024710 013746 002132 MOV ERRVEC, -(SP)
(3) 024714 012746 000003 MOV #3, -(SP)
(3) 024720 104037 EMT C$SVEC
(2) 024722 062706 ADD #10, SP ;CLEAR TRAP PLAY
1746 024726 005037 002142 CLR TRPFLG ;LOAD BA
1747 024732 012777 003652 MOV #BUF, R1LBA ;LOAD DA
1748 024740 005077 155304 CLR R1LDA ;LOAD WC
1749 024744 012777 155276 MOV #1, R1LMP ;LOAD THE FUNCTION IN NEXT WORD
1750 024752 004537 020456 JSR R5, LDFUNC
1751 024756 000014 READ
1752 024760 004537 021276 JSR R5, WTCRDY ;CLEAR OUT VECTOR
1753 024764 CLRVEC ERRVEC, R0
(3) 024770 013700 002132 MOV ERRVEC, R0
(3) 024770 104036 EMT C$CVEC
1754 024772 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 024772 104010 EMT C$ESCAPE
(3) 024774 000030 EMT -WORD 10000$-
1756 024776 004537 020214 JSR R5, CHERR ;CHECK CNTLR FOR ERRORS
(3) 025002 104010 EMT C$ESCAPE ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 025004 000020 EMT -WORD 10000$-
1757
1758 025006 005737 002142 TST TRPFLG ;DID TRAP OCCUR?
  
```

```

1759 025012 001404          REQ      7$              ;NO, OKAY
1760 025014          ERRDF  17,EM52,ERRO ;YES, PRINT ERROR
      (3) 025014          TRAP  T$ERRCODE
      (3) 025016          .WORD  17
      (3) 025020          .WORD  EM52
      (3) 025022          .WORD  ERRO
1761 025024          7$:
1762
1763
1764 025024          10000$: ENDSEGE          ;**END OF SEGMENT**
      (3) 025024          EMT      C$ESEG
1765
1766 025026          ENDTST          ;**END OF TEST**
      (3) 025026          L10043: EMT      C$ETST
1768
1769          .SBTTL  **TEST 15** - READ FUNCTION
1770
1771 025030          BGNST          ;**START OF TEST**
1772
1773 025030          STARS
      (2)
      ;*****
      ;CHECK OF THE READ FUNCTION. WE WILL FIRST DO A READ
      ;HEADER TO FIND OUT WHERE WE ARE AND THEN ISSUE
      ;A FULL SECTOR READ, WAIT FOR READY AND CHECK FOR
      ;ANY ERRORS
1774          STARS
      (2)
      ;*****
1775
1776 025030          004737 021356          JSR      PC,HDHOME          ;HEADS OVER TRACK 0
      (3) 025034          CKERFG          ;HEADS GO HOME OKAY
      (4) 025042          EMT      C$EXIT
      (4) 025044          .WORD  L10044-.
1783
1784 025046          BGNSEGE          ;**START OF SEGMENT**
      (3) 025046          EMT      C$BSEG
1785
1786 025050          012737 001750 002160          1$: MOV      #1000-,TMP0
      (3) 025056          CLR      @RLDA          ;LOAD DISK ADDRESS
      (3) 025062          MOV      #-128,@RLMP ;SET WORD LENGTH
      (3) 025070          MOV      #BUF,@RLBA ;SET BUS ADDRESS
1787
1788 025076          JSR      R5,LDFUNC          ;LOAD THE FUNCTION IN NEXT WORD
      (3) 025102          READ          ;READ
      (3) 025104          JSR      R5,WTCRDY          ;WAIT FOR CONTROLLER READY
      (3) 025110          ESCAPE          ;CHECK FOR FLOE, ELSE EXIT SEG
      (3) 025112          EMT      C$ESCAPE
      (3) 025112          .WORD  10000$-.
1795
1796 025114          004537 020214          JSR      R5,CHERR          ;CHECK CNTLR FOR ERRORS
1797
1798 025120          005337 002160          DEC      TMP0
1799 025124          001354          BNE     1$
    
```

```

1800 025126          ENDSEGE          ;**END OF SEGMENT**
      (3) 025126          10000$: EMT      C$ESEG
1801 025130          ENDTST          ;**END OF TEST**
      (3) 025130          L10044: EMT      C$ETST
1802
1803          .SBTTL  **TEST 16** - READ FUNCTION INTERRUPT
1804
1805 025132          BGNST          ;**START OF TEST**
1806
1807 025132          STARS
      (2)
      ;*****
      ;CHECK OF THE READ FUNCTION UNDER INTERRUPT CONTROL, WE WILL
      ;ISSUE A READ HEADER TO GET POSITION AND THEN READ
      ;A FULL SECTOR WAITING FOR THE INTERRUPT. CHECK FOR
      ;ERRORS ON INTERRUPT.
1808          STARS
      (2)
      ;*****
1809
1810 025132          004737 021356          JSR      PC,HDHOME          ;HEADS OVER TRACK 0
      (3) 025136          CKERFG          ;HEADS GO HOME OKAY
      (4) 025144          EMT      C$EXIT
      (4) 025146          .WORD  L10045-.
1817
1818 025150          BGNSEGE          ;**START OF SEGMENT**
      (3) 025150          EMT      C$BSEG
1819
1820 025152          005037 002144          CLR      INTFLG          ;CLEAR INTERRUPT INDICATOR
      (3) 025156          CLR      @RLDA          ;SET DISK ADDRESS
      (3) 025162          MOV      #-128,@RLMP ;SET UP WORD COUNT
      (3) 025170          MOV      #BUF,@RLBA ;SET UP BUS ADDRESS
1821
1822 025176          SETPRI  #PRI00,R0          ;PRIORITY TO 0
      (3) 025176          MOV      #PRI00,R0
      (3) 025200          EMT      C$SPRI
1823 025204          JSR      R5,LDFUNC          ;LOAD THE FUNCTION IN NEXT WORD
      (3) 025210          READIINTEN ;READ UNDER INTERRUPT
      (3) 025212          JSR      R5,WTCRDY          ;WAIT FOR INTERRUPT
1824
1825 025216          CKLOOP          ;PRIORITY TO 7
      (3) 025216          EMT      C$CLP1
      (3) 025220          SETPRI  #PRI07,R0
      (3) 025224          EMT      C$SPRI
1831
1832 025226          005737 002144          TST      INTFLG          ;DID INTERRUPT OCCUR?
1833 025232          001004          BNE     1$              ;YES-BRANCH NO-REPORT
1834
1835 025234          ERRDF  19,EM4,ERRO ;READ DID NOT INTERRUPT
      (3) 025234          TRAP  T$ERRCODE
      (3) 025236          .WORD  19
      (3) 025240          .WORD  EM4
      (3) 025242          .WORD  ERRO
1836 025244          1$: CKLOOP          ;CHECK FOR LOOP
      (3) 025244          EMT      C$CLP1
    
```

```

1837
1838 025246 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
1839
1840 025252 ENDSEG ;%%END OF SEGMENT%%
1841 025252 104005 10000$: EMT C$ESEG
1842 025254 104005 ENDTST ;**END OF TEST**
1843 025254 104001 L1004$: EMT C$ETST
1844
1845 .SBTTL **TEST 17** - CHECK READ NPR DIRECTION
1846 BGNTST ;**START OF TEST**
1847 025256
1848 STARS
1849 ;*****
1850 ;CHECK THAT THE READ FUNCTION ACTUALLY READS (INTO MEMORY)
1851 ;WE WILL WRITE A PATTERN INTO MEMORY AND THEN ISSUE
1852 ;A READ TO OVERLAY THAT PATTERN. AFTER THE READ
1853 ;WE CHECK TO SEE IF THE WRITTEN PATTERN HAS CHANGED.
1854 ;IF NOT WE ISSUE IT AGAIN AT THE SAME SECTION AFTER
1855 ;HAVING MODIFIED OUR PATTERN IN MEMORY (SINCE THERE IS
1856 ;ONE CHANCE THAT THE DISK COULD HAVE OUR PATTERN). AFTER
1857 ;THE SECOND READ WE CHECK THE BUFFER AGAIN. IF IT'S
1858 ;NO CHANGED WE REPORT AN ERROR. IF IT'S
1859 STARS
1860 ;*****
1861 025256 004737 021356 JSR PC,HDDHOME ;HEADS OVER TRACK 0
1862 025262 CKERFG ;HEADS GO HOME OKAY
1863 025270 104032 EMT C$EXIT
1864 025274 000156 .WORD L10046-.
1865
1866 025274 104004 BGNSEG ;%%START OF SEGMENT%%
1867 025274 104004 EMT C$BSEG
1868
1869 025276 012737 123456 002160 MOV #123456,TMPO ;SET PATTERN TO WRITE
1870 025304 005037 002162 CLR ;CLEAR PASS INDICATOR
1871 025310 012700 003052 1$: MOV #BUF,R0 ;SET UP BUFFER BEGINNING
1872 025314 012701 002200 MOV #12,R1
1873 025320 013720 002160 2$: MOV TMPO,(R0)+ ;WRITE BUFFER
1874 025324 005301 DEC R1 ;DONE?
1875 025326 001374 BNE ZS ;NO, GO BACK
1876 025330 005077 154712 CLR @RLDA ;LOAD DISK ADDRESS
1877 025334 012777 177600 MOV #128,@RLMP ;SET WORD COUNT
1878 025342 012777 154674 MOV #BUF,@RLBA ;LOAD BUS ADDRESS
1879 025350 012737 003052 002166 MOV #BUF,@DDAT ;FOR ERROR PRINT
1880
1881 025356 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
1882 025362 000014 READ ;READ
1883 025364 004537 021276 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
1884 025370 104010 ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
1885 025372 000054 EMT C$ESCAPE
1886 .WORD 10000$-.
    
```

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1887 025374 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
1888 025400 ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
1889 025400 104010 EMT C$ESCAPE
1890 025402 000044 .WORD 10000$-.
1891
1892 025404 012702 003052 4$: MOV #BUF,R2 ;SET TO START COMPARING DATA
1893 025410 032237 002160 CMP (R2)+,TMPO ;DID DATA CHANGE?
1894 025414 001014 BNE CS ;YES, CHECK FOR END
1895
1896 025416 005737 002162 TST TMP1 ;DATA DIDN'T CHANGE, CHECK
1897 025422 001005 BNE SS ;IF 1ST OR 2ND TIME?
1898 ;2ND-REPORT 1ST-TRY AGAIN
1899
1900 025424 005237 002162 INC TMP1 ;INC PASS COUNT
1901 025430 005137 002160 COM TMPO ;COMPLIMENT PATTERN
1902 025434 000725 BR SS ;GO DO IT AGAIN
1903
1904 025436 20: 005:ERR9 5$: ERDF 20: 005:ERR9 ;READ DID NOT MODIFY MEMORY
1905 025436 TRAP 005:ERR9 ;SERCODE
1906 025440 20: 006:ERR9 .WORD 20:
1907 025442 006:ERR9 .WORD 005:
1908 025444 006:ERR9 .WORD 005:
1909
1910 025446 10000$: ENDSEG ;%%END OF SEGMENT%%
1911 025446 104005 10000$: EMT C$ESEG
1912 025450 104001 ENDTST ;**END OF TEST**
1913 025450 104001 L10046$: EMT C$ETST
1914
1915 .SBTTL **TEST 18** - PROPER INCREMENT OF RLBA ON READ
1916 BGNTST ;**START OF TEST**
1917 025452
1918 STARS
1919 ;*****
1920 ;CHECK THAT THE RLBA WILL INCREMENT WITH THE READ
1921 ;THE RLBA SHOULD CONTAIN "BUF +256." AFTER A FULL SECTOR
1922 ;READ.
1923 STARS
1924 ;*****
1925 025452 004737 021356 JSR PC,HDDHOME ;HEADS OVER TRACK 0
1926 025456 CKERFG ;HEADS GO HOME OKAY
1927 025464 104032 EMT C$EXIT
1928 025466 000116 .WORD L10047-.
1929
1930 025470 104004 BGNSEG ;%%START OF SEGMENT%%
1931 025470 104004 EMT C$BSEG
1932
1933 025472 005077 154550 CLR @RLDA ;SET UP DISK ADDRESS
1934 025476 012777 003052 154540 MOV #BUF,@RLBA ;SET UP BUS ADDRESS
    
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1923 025504 012777 177600 154536 MOV #128,@RLMP ;WORD COUNT
1924 025512 012777 003052 002166 MOV #BUF,GDDAT ;FORM EXPECTED BUS ADDRESS
1925 025520 062737 000400 002166 ADD #256,GDDAT ;AFTER READ
1926
1927 025526 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
1928 025532 000014 READ ;READ
1929 025534 004537 021276 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
1930 025540 ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 025540 104010 EMT C$ESCAPE
(3) 025542 000040 .WORD 10000$-
1931
1932 025544 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
1933 025550 ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 025550 104010 EMT C$ESCAPE
(3) 025552 000030 .WORD 10000$-
1934 025554 013737 002230 002170 MOV E.BA,BDDAT ;READ "RLBA" FOR PRESENT ADDRESS
1935 025562 023737 002170 002166 CMP GDDAT,GDDAT ;DID "BA" INCREMENT PROPERLY?
1936 025570 001404 BEQ 1$ ;YES, CONTINUE
1937
1938 025572 ERRDF 21,EM6,ERR4 ;BA DID NOT INCREMENT PROPERLY
(3) 025572 104462 TRAP T$ERRCODE
(5) 025574 000025 .WORD 21
(5) 025576 010712 .WORD EM6
(5) 025600 014410 .WORD ERR4
1939
1940 025602 1$:
1941
1942 025602 10000$: ENDSEG ;**END OF SEGMENT**
(3) 025602 104005 EMT C$ESEG
(3) 025604 1943: ENDTST L10047: ;**END OF TEST**
(3) 025604 104001 EMT C$ETST
1944
1945 .SBTTL **TEST 19** - PROPER INCREMENT OF RLDA ON READ
1946
1947 025606 BGNST ;**START OF TEST**
1948
1949 025606 STARS
(2) ;*****
1950 ;CHECK THAT THE RLDA INCREMENTS BY ONE AFTER A
1951 ;FULL SECTOR READ, WE FIRST READ A HEADER TO FIND
1952 ;OUT WHERE WE ARE, THEN ISSUE A READ AFTER
1953 ;THE READ THE RLDA SHOULD BE RLDA (START) + 1
1954 025606 STARS
(2) ;*****
1955
1956 025606 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0
1957 025612 CKERFG ;HEADS GO HOME OKAY
(4) 025620 104032 EMT C$EXIT
(4) 025622 000114 .WORD L10050-
1958
1959 025624 104004 BGNSEG ;**START OF SEGMENT**
(3) 025624 EMT C$BSEG
1960
1961
    
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1962 025626 005037 002166 CLR GDDAT
1963 025632 013777 002166 MOV GDDAT,@RLDA ;SETUP DISK ADDRESS
1964 025640 005237 002166 INC GDDAT ;CREATE EXPECTED SECTOR
1965 025644 012777 177600 154376 MOV #128,@RLMP ;WORD COUNT
1966 025652 012777 003052 154364 MOV #BUF,@RLBA ;SETUP BUS ADDRESS
1967
1968 025660 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
1969 025664 000014 READ ;READ
1970 025666 004537 021276 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
1971 025672 ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 025672 104010 EMT C$ESCAPE
(3) 025674 000040 .WORD 10000$-
1972
1973 025676 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
1974 025702 ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 025702 104010 EMT C$ESCAPE
(3) 025704 000030 .WORD 10000$-
1975
1976 025706 013737 002232 002170 MOV E.DA,BDDAT ;READ DISK ADDRESS
1977 025714 023737 002166 002170 CMP GDDAT,BDDAT ;DID SECTOR INCREMENT PROPERLY
1978 025722 001404 BEQ 1$ ;YES, BRANCH NO, REPORT ERROR
1979
1980 025724 ERRDF 22,EM7,ERR4 ;DISK ADDRESS DID NOT INCREMENT
(3) 025724 104462 TRAP T$ERRCODE
(5) 025726 000026 .WORD 22
(5) 025730 010766 .WORD EM7
(5) 025732 014410 .WORD ERR4
1981
1982 025734 1$:
1983
1984 025734 10000$: ENDSEG ;**END OF SEGMENT**
(3) 025734 104005 EMT C$ESEG
(3) 025736 1985: ENDTST L10050: ;**END OF TEST**
(3) 025736 104001 EMT C$ETST
1986
1987 .SBTTL **TEST 20** - FORCE HEADER NOT FOUND WITH READ
1988
1989 025740 BGNST ;**START OF TEST**
1990
1991 025740 STARS
(2) ;*****
1992 ;FORCE HEADER NOT FOUND ERROR TO OCCUR. THIS IS DONE
1993 ;BY SETTING SECTOR 40 OF THE RLDA AND ISSUING A
1994 ;READ. SECTOR 40 DOES NOT EXIST ON THE RL01 PACK
1995 ;THEREFORE HDR NOT FOUND SHOULD SET.
1996 025740 STARS
(2) ;*****
1997
1998 025740 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0
1999 025744 CKERFG ;HEADS GO HOME OKAY
(4) 025752 104032 EMT C$EXIT
(4) 025754 000102 .WORD L10051-
2000
2001 025756 BGNSEG ;**START OF SEGMENT**
    
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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-50
CZRLBB-P11 22-NOV-78 15:28 **TEST 20** - FORCE HEADER NOT FOUND WITH READ          SEQ 0080
(3) 025756 104004          EMT          C$BSEG
2002
2003
2004 025760 012777 000050 154260      MOV          #40, @RLDA          ;INSURE NOT TO FIND HEADER BY
2005 025766 012777 003052 154250      MOV          #BUF, @RLBA         ;SETTING SECTOR 40 OF CYL. ADDR.
2006 025774 012777 177777 154246      MOV          #-1, @RLMP         ;WORD COUNT
2007
2008 026002 004537 020456          JSR          R5, LDFUNC          ;LOAD THE FUNCTION IN NEXT WORD
2009 026006 000014          READ          ;READ
2010 026010 004537 021276          JSR          R5, WTCRDY         ;WAIT FOR CONTROLLER READY
2011 026014          ESCAPE       SEG              ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 026014 104010          EMT          C$ESCAPE
(3) 026016 000036          EMT          10000$-.
2012
2013 026020 013737 002226 002160      MOV          E, CS, TMP0         ;GET RLCS
2014 026026 042737 001777 002160      BIC          #1777, TMP0        ;SAVE ERROR BITS
2015 026034 022737 112000 002160      CMP          #BIT15|BIT12|BIT10, TMP0 ;HDR NOT FOUND SET.
2016 026042 001404          BEQ          IS                ;YES, CONTINUE
2017
2018 026044          ERDF        23, EM10, ERRO      ;HEADER NOT FOUND WOULD NOT SET
(3) 026044 104462          TRAP        15, ERCODE
(5) 026046 000027          .WORD      23
(5) 026050 011043          .WORD      EM10
(5) 026052 014244          .WORD      ERRO
2019
2020 026054          IS:
2021
2022
2023 026054          ENDSEG          ;**END OF SEGMENT**
(3) 026054          10000$:
(3) 026054 104005          EMT          C$ESEG
2024 026056          ENDTST         ;**END OF TEST**
(3) 026056          L10051:
(3) 026056 104001          EMT          C$ETST
2025
2026          .SBTTL **TEST 21** - FORCE HEADER NOT FOUND WITH READ INTERRUPT
2027
2028 026060          BGNTST         ;**START OF TEST**
2029
2030
2031 026060          STARS
(2) ;*****
2032 ;TEST THAT HEADER NOT FOUND ERROR WILL GENERATE AN INTERRUPT
2033 ;ON OCCURANCE. HEADER NOT FOUND WILL BE FORCED BY SETTING
2034 ;SECTOR 40 OF RLDA AND ISSUING A READ
2035 026060          STARS
(2) ;*****
2036
2037 026060 004737 021356          JSR          PC, HDHOME         ;HEADS OVER TRACK 0
2038 026064          CKERFC        ;HEADS GO HOME OKAY
(4) 026072 104032          EMT          C$EXIT
(4) 026074 000142          .WORD      L10052-.
2040
2041 026076          BGNSEG          ;**START OF SEGMENT**
(3) 026076 104004          EMT          C$BSEG

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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-51
CZRLBB-P11 22-NOV-78 15:28 **TEST 21** - FORCE HEADER NOT FOUND WITH READ INTERRUPT          SEQ 0081
2042
2043 026100          SETPRI        #PRI00
(3) 026100          MOV          #PRI00, R0
(3) 026104 104041          EMT          C$SPRI
2045 026106 012777 000050 154126      CLR          INTPLG            ;CLEAR INTERRUPT OCCURANCE FLAG
2046 026120 012777 003052 154116      MOV          #40, @RLDA         ;INSURE NOT TO FIND HEADER BY
2047 026126 012777 177777 154114      MOV          #BUF, @RLBA         ;SETTING SECTOR 40 OF CYL. ADDR.
2048          MOV          #-1, @RLMP         ;WORD COUNT
2049 026134 004537 020456          JSR          R5, LDFUNC          ;LOAD THE FUNCTION IN NEXT WORD
2050 026138 001114          READ          ;READ
2051 026142 004537 021276          JSR          R5, WTCRDY         ;WAIT FOR CONTROLLER READY
2052 026146          CKLOOP
(3) 026146 104006          EMT          C$CLP1
2053 026150          SETPRI        #PRI07
(3) 026150 012700 000340          MOV          #PRI07, R0
(3) 026154 104041          EMT          C$SPRI
2054
2055 026156 005737 002144          IST          INTPLG            ;DID INTERRUPT OCCUR
2056 026162 001004          BNE          2$              ;YES
2057
2058 026164          ERDF        24, EM43, ERRO      ;HNF DID NOT INTERRUPT
(3) 026164 104462          TRAP        15, ERCODE
(5) 026166 000030          .WORD      24
(5) 026170 012723          .WORD      EM43
(5) 026172 014244          .WORD      ERRO
2059
2060 026174          2$:
(3) 026174 104010          ESCAPE       SEG              ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 026176 000036          EMT          C$ESCAPE
(3) 026176 000036          .WORD      10000$-.
2061
2062
2063 026200 013737 002226 002160      MOV          E, CS, TMP0         ;GET RLCS
2064 026206 042737 001777 002160      BIC          #1777, TMP0        ;SAVE ERROR BITS
2065 026214 022737 112000 002160      CMP          #BIT15|BIT12|BIT10, TMP0 ;HDR NOT FOUND SET.
2066 026222 001404          BEQ          IS                ;YES, CONTINUE
2067
2068 026224          ERDF        25, EM10, ERRO      ;WHEN FORCED
(3) 026224 104462          TRAP        15, ERCODE
(5) 026226 000031          .WORD      25
(5) 026230 011043          .WORD      EM10
(5) 026232 014244          .WORD      ERRO
2069
2070 026234          IS:
2071
2072 026234          ENDSEG          ;**END OF SEGMENT**
(3) 026234          10000$:
(3) 026234 104005          EMT          C$ESEG
2073 026236          ENDTST         ;**END OF TEST**
(3) 026236          L10052:
(3) 026236 104001          EMT          C$ETST
2074
2075          .SBTTL **TEST 22** - CHECK HEADER COMPARE LOGIC
2076 026240          BGNTST         ;**START OF TEST**
2077
2078

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2079 026240
(2)
2080
2081 ;*****
2082 ;CHECK THE HEADER COMPARE LOGIC WORKS. UP TO THIS POINT WE
2083 ;KNOW THAT THE LOGIC FUNCTIONS PROPERLY BUT NOW WE WILL
2084 ;CHECK ALL THE BITS IN THE HEADER WORD FOUR PATTERNS
2085 ;ARE USED A WALKING 1, GROWING 1, WALKING 0, GROWING 0. A SEEK
2086 ;IS ISSUED BEFORE EACH READ TO INSURE WE ARE ON THE PROPER
2087 ;TRACK. ONCE WE ARE ON THE RIGHT TRACK WE LOAD THE RLDA
2088 ;AND ISSUE THE READ. UPON COMPLETION WE WILL CHECK FOR ERRORS
2089 ;WE THEN LOAD THE COMPLIMENT PATTERN INTO THE RLDA
2090 ;EXPECTING A HEADER NOT FOUND TO SET
2091 STARS
2092 ;*****
2093 JSR PC,HDHOME ;HEADS OVER TRACK 0
2094 CNERFG EMT ;HEADS GO HOME OKAY
2095 .WORD L10053-.
2096
2097 BCNSEG ;**START OF SEGMENT**
2098 EMT C$BSEG
2099
2100 SETPRI #PRI07 ;PRIORITY TO 7
2101 MOV #PRI07,R0
2102 EMT C$SPRI
2103 MOV #HDRTAB,R3 ;GET LIST START
2104
2105 BCNSEG ;**START OF SEGMENT**
2106 EMT C$BSEG
2107
2108 JSR RS,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2109 RDHDR ;READ HEADER
2110 JSR R5,WTCRDY ;WAIT FOR CONTROLLRE READY
2111 ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
2112 EMT C$ESCAPE
2113 .WORD 10001$-
2114
2115 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
2116 ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
2117 EMT C$ESCAPE
2118 .WORD 10001$-
2119
2120 JSR E.MP,TMP1 ;READ AND SAVE HEADER
2121 BIC #177,TMP1 ;CLEAR OUT SECTOR AND H.S.
2122 MOV #1,RLDA ;SETUP MARKER FOR SEEK
2123 MOV #R3,TMP2 ;GET HEADER PATTERN
2124 BIC #177,TMP2 ;CLEAR OUT SECTOR AND H.S.
2125 SOB TMP1,TMP2 ;CALCULATE DIFFERENCE TO SEEK
2126 BCS 25 ;BRANCH FOR SEEK OUT
2127 BIS #SIGN,RLDA ;SEEK TOWARDS SPINDLE
2128 BR 35 ;GO PUT IN DIFFERENCE WORD
2129 NEG TMP2 ;WE HAVE TO NEGATE DIFFERENCE
2130 BIS TMP2,RLDA ;SET IN DIFFERENCE WORD
2131 BIT #RHHS,(R3) ;DO WE WANT HEAD SELECT AS 0?
2132 BEQ 45 ;YES, SKIP OVER SETTING H.S.
  
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2123 026416 052777 000020 153622
2124 026424 004537 020456
2125 026430 000006
2126
2127 026432 004537 021276
2128 ESCAPE SEC ;WAIT FOR CONTROLLER READY
2129 EMT C$ESCAPE ;CHECK FOR FL:LOE, ELSE EXIT SEG
2130 .WORD 10001$-
2131
2132 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
2133 ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
2134 EMT C$ESCAPE
2135 .WORD 10001$-
2136
2137 JSR R5,WTDROY ;WAIT FOR DRIVE READY
2138 ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
2139 EMT C$ESCAPE
2140 .WORD 10001$-
2141
2142 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2143 RDHDR ;READ HEADER (VERIFY SEEK)
2144 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
2145 ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
2146 EMT C$ESCAPE
2147 .WORD 10001$-
2148
2149 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
2150 ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
2151 EMT C$ESCAPE
2152 .WORD 10001$-
2153
2154 MOV E.MP,BDDAT ;READ HEADER
2155 BIC SECMSK,BDDAT ;SAVE CYLINDER FOR COMPARE
2156 MOV #R3,GDDAT ;GET EXPECTED HEADER
2157 BIC SECMSK,GDDAT ;SAVE CYLINDER FOR COMPARE
2158 CMP GDDAT,BDDAT ;SEEK END UP OKAY
2159 BEQ 55 ;YES, CONTINUE
2160
2161 ERDF 27,EM11,ERR4 ;SEEK INCORRECT
2162 TRAP T$ERRCODE
2163 .WORD 27
2164 .WORD EM11
2165 .WORD ERR4
2166
2167 ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
2168 EMT C$ESCAPE
2169 .WORD 10001$-
2170
2171 MOV #R3,RLDA ;SET UP DISK ADDRESS
2172 BIC #1,RLDA
2173 MOV #BUF,RLBA ;WORD COUNT
2174 MOV #BUF,RLBA ;BUS ADDRESS
2175
2176 JSR RS,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2177 READ ;READ
2178 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
  
```

ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-54
 CZRLBB.P11 22-NOV-78 15:28 **TEST 22** - CHECK HEADER COMPARE LOGIC SEQ 0084

```

2163 026622 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 026622 EMT C$ESEG
000164 .WORD 10001$-.
2164 026624 104010 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
2165 026625 004537 020214 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 026625 EMT C$ESEG
000154 .WORD 10001$-.
2167 026636 011377 153404 MOV (R3),@RLDA ;SET UP DISK ADDRESS AS
2168 026642 005177 153400 COM @RLDA ;COMPLIMENT TO CAUSE HDR NT FND
2169 026642 012777 177777 MOV #-1,@RLMP ;WORD COUNT
2170 026644 012777 003052 MOV #BUF,@RLBA ;BUS ADDRESS
2171 026654 012777 153362
2173 026662 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2174 026666 000014 READ ;READ
2175 026670 004537 021276 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
2176 026674 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 026674 EMT C$ESEG
000112 .WORD 10001$-.
2177 026700 013737 002226 MOV E,CS,TMPO ;GET CS
2178 026706 042737 001777 BIC #177,TMPO ;SAVE ERROR BITS
2179 026714 022737 112000 CMP #BIT15|BIT12|BIT10,TMPO ;DID HEADER NOT FOUND SET
2180 026722 001402 BEQ 8$ ;YES, CONTINUE
2181 026724 004537 020214 JSR R5,CHERR
2182 026730 104006 8$: CKLOOP
(3) 026730 EMT C$CLP1
2183 026732 022737 112000 002160 CMP #BIT15|BIT12|BIT10,TMPO
2184 026740 001413 BEQ 6$
2186 026742 011337 002166 MOV (R3),GDDAT ;SET UP DATA FOR ERROR
2188 026746 013737 002170 MOV GDDAT,BDDAT ;PRINT OUT
2189 026754 005137 COM BDDAT
2191 026760 28,EM14,ERR4 ;HDR NOT FOUND WOULD NOT SET
(3) 026760 TRAP T$ERRCODE
(5) 026762 000034 .WORD 28
(5) 026764 011137 .WORD EM12
2192 026766 014410 .WORD ERR4
2193 026770 026770 6$: ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 026770 EMT C$ESEG
000016 .WORD 10001$-.
2194 026774 005723 TST (R3)+ ;GET NEXT PATTERN
2196 026776 020457 CMP R3,#HDREND ;AT END?
2197 027002 001402 BEQ 7$ ;YES, EXIT TEST
2198 027004 000137 JMP 1$ ;NO, GO BACK
2199
2200 027010 7$: ENDSEG ;%%END OF SEGMENT%%
2201 027010 10001$: EMT C$ESEG
(3) 027010 104005 EMT C$ESEG
2202 027012 ENDSEG ;%%END OF SEGMENT%%
2203

```

ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-55
 CZRLBB.P11 22-NOV-78 15:28 **TEST 22** - CHECK HEADER COMPARE LOGIC SEQ 0085

```

(3) 027012 10000$: EMT C$ESEG
(3) 027012 104005 ENDTST ;**END OF TEST**
2204 027014 L10053:
(3) 027014 104001 EMT C$ETST
2205
2206 .SBTTL **TEST 23** - CHECK MULTIPLE SECTORS ON READ
2207 BGNTST ;**START OF TEST**
2208 027016
2209
2210 STARS
2211 ;*****
2212 ;VERIFY THAT MULTIPLE SECTORS CAN BE READ, WE WILL CHECK
2213 ;THAT THE RLDA INCREMENTS PROPERLY.
2214 STARS
2215 ;*****
2216 027016 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0
2217 027022 027032 000156 CKERFG ;HEADS GO HOME OKAY
(4) 027030 104032 EMT C$EXIT
(4) 027032 000156 .WORD L10054-.
2218
2219 027034 005037 002160 CLR TMPO ;CLEAR LOCATIONS
2220 027040 005037 002162 CLR TMP1
2221
2222 027044 104004 BGNSEG ;**START OF SEGMENT**
2223 027044 EMT C$BSEG
2224
2225 027046 1$: MOV TMP1,GDDAT ;GET CYLINDER
2226 027046 013737 002162 002166 BIS TMPO,GDDAT ;GET SECTOR
2227 027054 053737 002160 002166 MOV GDDAT,@RLDA ;SET DISK ADDRESS-SECTOR 0
2228 027062 013777 002166 153156 ADD #2,GDDAT ;SET EXPECTED + 2
2229 027070 062737 00002 002166 MOV #BUF,@RLBA ;SET BUS ADDRESS
2230 027076 012777 003052 153140 MOV #-129,@RLMP ;WORD COUNT-SECTOR+1 WORD
2231 027104 012777 177577 153136
2232
2233 027112 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2234 027112 000014 READ ;READ
2235 027120 004537 021276 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY?
2236 027124 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 027124 EMT C$ESEG
000060 .WORD 10000$-.
2237
2238 027130 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
2239 027134 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 027134 EMT C$ESEG
000050 .WORD 10000$-.
2240
2241 027140 013737 002232 002170 MOV E,DA,BDDAT ;READ DISK ADDRESS
2242 027146 013737 002170 002166 CMP BDDAT,GDDAT ;IS DISK ADDRESS CORRECT
2243 027154 001404 BEQ 2$ ;YES, BRANCH NO, REPORT ERROR
2244
2245 027156 29,EM14,ERR4 ;DA DID NOT INCREMENT
(3) 027156 TRAP T$ERRCODE

```



```

(5) 027160 000035 .WORD 29
(5) 027162 011230 .WORD EM14
(5) 027164 014410 .WORD ERR4
2246
2247 027166 104010 2$: ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 027166 000016 EMT C$ESCAPE
(3) 027170 .WORD 10000$-.
2248
2249 027172 005237 002160 INC TMPO ;NEXT SECTOR?
2250 027176 022737 000046 002160 CHP #46, TMPO ;DONE?
2251 027204 001320 BNE I$ ;NO, GO BACK
2252
2253 027206 ENDSEG ;**END OF SEGMENT**
(3) 027206 104005 10000$: EMT C$ESEG
(3) 027210 ENDTST ;**END OF TEST**
(3) 027210 L10054: EMT C$SETST
2256 027212 STARS
;*****
;CHECK THAT WE CAN FORCE A HEADER NOT FOUND AT THE
;END OF A TRACK DOING A MULTIPLE SECTOR READ. WE
;SET UP TO READ TWO SECTORS STARTING AT SECTOR 39
;WE SHOULD TRANSFER 128 WORDS THEN ABORT WITH A
;HEADER NOT FOUND FOR SECTOR 40
STARS
;*****
2263
2264 .SBTTL **TEST 24** - FORCE HDR NT FND AT END OF TRACK
2265
2266 027212 BGNST ;**START OF TEST**
2267
2268
2269
2270 027212 004737 021356 JSR PC, HDHOME ;HEADS OVER TRACK 0
2271 027216 021356 CKERFG ;HEADS GO HOME OKAY
(4) 027224 104032 EMT C$EXIT
(4) 027226 000126 .WORD L10055-.
2273
2274 BGNSEG ;**START OF SEGMENT**
(3) 027230 104004 EMT C$BSEG
2275
2276 027232 012737 000047 002166 MOV #39, GDDAT ;CREATE LAST SECTOR
2277 027240 013777 002166 153000 MOV GDDAT, @RLDA ;LOAD DISK ADDRESS
2278 027246 012777 177577 152774 MOV #129, @RLMP ;WORD COUNT
2279 027254 012777 003052 152762 MOV @BDF, @RLBA ;BUS ADDRESS
2280 027262 004537 020456 JSR R5, LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2281 027266 000014 READ ;READ
2282 027270 004537 021276 JSR R5, WTCRDY ;WAIT FOR CONTROLLER READY
2283 027274 104010 ESCAPE ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 027276 000054 EMT C$ESCAPE
(3) 027276 .WORD 10000$-.
2284
2285 027300 013737 002226 002170 MOV E, CS, BDDAT ;READ CS
027306 042737 001777 002170 BIC #1777, BDDAT ;SAVE ERROR BITS
  
```

```

2286 027314 022737 112000 002170 CHP #112000, BDDAT ;HDR NOT FOUND SET?
2287 027322 001402 BEQ 4$ ;YES, CONTINUE
2288 027324 004537 020214 JSR R5, CHERR
2289 027330 104006 4$: CKLOOP
(3) 027330 EMT C$CLP1
2290
2291 027332 022737 112000 002170 CHP #112000, BDDAT
2292 027340 001404 BEQ I$
2293
2294 027342 ERRDF 30, EM23, ERRO ;HEADER NOT FOUND DID NOT SET
(3) 027342 104462 TRAP T$ERCODE
(3) 027342 000036 .WORD 30
(5) 027350 014244 .WORD EM23
(5) 027350 014244 .WORD ERRO
2295
2296 027352 1$:
2297
2298 027352 ENDSEG ;**END OF SEGMENT**
(3) 027352 104005 10000$: EMT C$ESEG
(3) 027354 ENDTST ;**END OF TEST**
(3) 027354 L10055: EMT C$SETST
2300 027354 104001 STARS
;*****
;FORCE A NON-EXISTANT MEMORY ERROR,
;WE SET THE RLBA TO EQUAL THE
;LAST ADDRESS IN MEMORY AND ISSUE A READ. THE
;READ SHOULD ABORT AFTER ONE WORD TRANSFERRED
STARS
;*****
2307
2308
2309
2310 027356 BGNST ;**START OF TEST**
2311
2312
2313
2314
2315 027356 004737 021356 JSR PC, HDHOME ;HEADS OVER TRACK 0
2316 027362 021356 CKERFG ;HEADS GO HOME OKAY
(4) 027370 104032 EMT C$EXIT
(4) 027372 000076 .WORD L10056-.
2316
2317 027374 104004 BGNSEG ;**START OF SEGMENT**
(3) 027374 EMT C$BSEG
2318
2319
2320
2321
2322 027376 012777 177774 152640 MOV #177774, @RLBA ;LEAD BA
2323 027404 012777 000060 002262 MOV #BA16|BA17, XMEM ;SET EA BIT
2324 027412 005477 152630 CLR @RLDA ;LOAD DISK AVAILABLE
2325 027416 012777 177600 152624 MOV #128, @RLMP ;WORD COUNT
2326 027424 004537 020456 JSR R5, LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2327 027430 000014 READ ;READ
  
```

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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-58
CZRLBB.P11 22-NOV-78 15:28 **TEST 25** - FORCE NON-EXISTANT MEMORY ERROR                               SEQ 0088
2328 027432 004537 021276      JSR    RS,WTCRDY      ;WAIT FOR CONTROLLER
2329 027436          ESCAPE  SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
2330 027436 104010          EMT    C$ESCAPE
2331 027440 000026          .WORD  10000$-.
2332 027442 032737 020000 002226  BIT    #NXM,E.CS      ;DID NXM SET?
2333 027450 001004          BNE    3$            ;YES, CONTINUE
2334 027452          ERRDF  31,EM24,ERRO ;NXM DID NOT SET
2335 027454          TRAP  TSERCODE
2336 027456 011716          .WORD  31
2337 027460 014244          .WORD  EM24
2338 027462          .WORD  ERRO
2339 027462 104010 3$:    ESCAPE  SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
2340 027464 000002          EMT    C$ESCAPE
2341 027464          .WORD  10000$-.
2342 027466          ENDSEG              ;%%END OF SEGMENT%%
2343 027466 104005 10000$: EMT    C$ESEG
2344 027470          ENDTST              ;**END OF TEST**
2345 027470 104001 L10056: EMT    C$ESET
2346 027472          .SBTTL **TEST 26** - FORCE NON-EXISTANT MEMORY ERROR INTERRUPT
2347 027472          BGNST              ;**START OF TEST**
2348          STARS
2349          ;*****
2350 027472          ;CHECK THAT WE CAN FORCE AN INTERRUPT WITH A
2351          ;NON-EXISTANT MEMORY ERROR.
2352          STARS
2353          ;*****
2354 027472 004737 021356      JSR    PC,HDHOME     ;HEADS OVER TRACK 0
2355 027476          CKERFG          ;HEADS GO HOME OKAY
2356 027504 104032          EMT    C$EXIT
2357 027506 000140          .WORD  L10057-.
2358 027510 104004          BGNSEG              ;%%START OF SEGMENT%%
2359 027510          EMT    C$BSEG
2360 027512 005037 002144  CLR    INTFLG        ;CLEAR INTERRUPT OCCURANCE FLAG
2361 027516          SETPRI #PRI00
2362 027516 012700 000000      MOV    #PRI00,R0
2363 027522 104041          EMT    C$SPRI
2364 027524 012774 177774 152512  MOV    #177774,0RLBA ;PRELOAD BA
2365 027532 012737 000060 002262  MOV    #BA16:BA17,XMEM ;SET EA BITS

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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-59
CZRLBB.P11 22-NOV-78 15:28 **TEST 26** - FORCE NON-EXISTANT MEMORY ERROR INTERRUPT                               SEQ 0089
2366 027540 005077 152502      CLR    @RLDA         ;LOAD DA
2367 027544 012777 177777      MOV    #1,0RLMP     ;WORD COUNT
2368 027552 004537 020456      JSR    RS,LDFUNC    ;LOAD THE FUNCTION IN NEXT WORD
2369 027560 000114          READ!INTEN
2370 027564 004537 021276      JSR    RS,WTCRDY    ;WAIT FOR CONTROLLER
2371 027564 012700 000340      SETPRI #PRI07      ;PRIORITY TO 7
2372 027570 104041          MOV    #PRI07,R0
2373 027572          EMT    C$SPRI
2374 027574 104010          ESCAPE  SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
2375 027574 000050          EMT    C$ESCAPE
2376 027574          .WORD  10000$-.
2377 027576 005737 002144      TST    INTFLG        ;INTERRUPT OCCUR?
2378 027602 001004          BNE    4$            ;YES OKAY
2379 027604          ERRDF  32,EM44,ERRO ;NO INTERRUPT W/NXM
2380 027604          TRAP  TSERCODE
2381 027606          .WORD  32
2382 027610          .WORD  EM44
2383 027612          .WORD  ERRO
2384 027614 104010 4$:    ESCAPE  SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
2385 027614 104010          EMT    C$ESCAPE
2386 027616 000026          .WORD  10000$-.
2387 027620 032737 020000 002226  BIT    #NXM,E.CS      ;DID NXM SET?
2388 027626 001004          BNE    3$            ;YES, CONTINUE
2389 027630          ERRDF  33,EM24,ERRO ;NO NXM
2390 027630          TRAP  TSERCODE
2391 027632          .WORD  33
2392 027634          .WORD  EM24
2393 027636          .WORD  ERRO
2394 027640 104010 3$:    ESCAPE  SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
2395 027640          EMT    C$ESCAPE
2396 027642          .WORD  10000$-.
2397 027644          ENDSEG              ;%%END OF SEGMENT%%
2398 027644 104005 10000$: EMT    C$ESEG
2399 027646          ENDTST              ;**END OF TEST**
2400 027646 104001 L10057: EMT    C$ESET
2401 027650          .SBTTL **TEST 27** - CHECK READ WRITE LOOP
2402 027650          BGNST              ;**START OF TEST**
2403          STARS
2404          ;*****
2405          ;VERIFY THAT THE WRITE ACTUALLY WRITES. AT THIS
2406          ;TIME WE KNOW THAT THE WRITE FUNCTION GOES THRU
2407          ;THE MOTIONS BUT WE DON'T KNOW THAT THE DATA
2408          ;ACTUALLY GETS RECORDED ON THE PLATTER.
2409          ;*****

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2400 027650          STARS
2401                ;*****
2402                ;*****
2403 027650 004737 021356      JSR   PC,HDHOME      ;HEADS OVER TRACK 0
2404 027654          CKERFG          ;HEADS GO HOME OKAY
2405 027662 104032          EMT   C$EXIT
2406 027664 000362          .WORD L10060-.
2407                BGNSEC          ;%%START OF SEGMENT%%
2408 027666 104004          EMT   C$BSEG
2409 027670 012700 003052      MOV   #BUF,RO        ;SET UP WRITE BUFFER
2410 027700 012701 000200      MOV   #128,R1        ;128 WORDS/ONE SECTOR
2411 027704 012720 125252      MOV   #125252,(RO)+ ;WRITE PATTERN TO BUFFER
2412 027706 005301          DEC   R1              ;DONE?
2413 027710 001374          BNE  3$              ;NO, BRANCH BACK
2414 027714 152332          CLR   #128,ORLDA    ;DISK ADDRESS
2415 027718 012777 177600      MOV   #128,ORLMP    ;WORD COUNT
2416 027722 012777 003052      MOV   #BUF,ORLBA    ;BUS ADDRESS
2417 027730 004537 020486      JSR   R5,LDFUNC     ;LOAD THE FUNCTION IN NEXT WORD
2418 027734 000012          WRITE          ;WRITE THE PATTERN
2419 027736 004537 021276      JSR   R5,WTCRDY    ;WAIT FOR CONTROLLER READY
2420 027742          ESCAPE        ;CHECK FOR FL:LOE, ELSE EXIT SEG
2421 027744 104010          EMT   C$ESCAPE
2422 027744 000300          .WORD 10000$-.
2423                BGNSEC          ;%%START OF SEGMENT%%
2424 027746 004537 020214      JSR   R5,CHERR     ;CHECK CNTLR FOR ERRORS
2425 027752          ESCAPE        ;CHECK FOR FL:LOE, ELSE EXIT SEG
2426 027754 104010          EMT   C$ESCAPE
2427 027754 000270          .WORD 10000$-.
2428                BGNSEC          ;%%START OF SEGMENT%%
2429 027756 104004          EMT   C$BSEG
2430 027760 012700 003052      MOV   #BUF,RO        ;CLEAR OUT BUFFER BEFORE
2431 027764 012701 000200      MOV   #128,R1        ;READING
2432 027768 012720 125252      MOV   #125252,(RO)+ ;CLEAR BUFFER
2433 027772 005301          DEC   R1              ;DONE?
2434 027774 001375          BNE  4$              ;NO, BRANCH BACK
2435 027776 005077 152244      CLR   #128,ORLDA    ;LOAD DISK ADDRESS
2436 030002 012777 177600      MOV   #128,ORLMP    ;WORD COUNT/ONE SECTION
2437 030004 012777 152240      MOV   #BUF,ORLBA    ;LOAD BUS ADDRESS
2438 030006 004537 020486      JSR   R5,LDFUNC     ;LOAD THE FUNCTION IN NEXT WORD
2439 030022 000014          READ          ;GO READ
2440 030024 004537 021276      JSR   R5,WTCRDY    ;WAIT FOR CONTROLLER READY
2441 030030          ESCAPE        ;CHECK FOR FL:LOE, ELSE EXIT SEG
2442 030030          EMT   C$ESCAPE
2443 030032 104010          .WORD 10001$-.
2444                BGNSEC          ;%%START OF SEGMENT%%
2445 030034 004537 020214      JSR   R5,CHERR     ;CHECK CNTLR FOR ERRORS
2446 030040          TST   T.CRC      ;WAS ERROR A DCK??
2447 030044 005737 002124      BNE  10$             ;YES, SEE IF WE A DUMP
2448 030046          EMT   C$G      ;CHECK FOR FL:LOE, ELSE EXIT SEG
2449 030046          EMT   C$ESCAPE
2450 030050 104010          .WORD 10001$-.
2451 030052 000172          BR    99$           ;SKIP AROUND
    
```

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2443 030054 005737 016774      8$:  TST   T.DMP        ;DO WE STILL WANT TO CHECK IT
2444 030060          BNE  10$             ;NO
2445 030062          CKLOOP          ;YES, CHECK FOR LOOP FIRST
2446 030062 104006          EMT   C$CLP1
2447                BGNSEC          ;%%START OF SEGMENT%%
2448 030064 005037 002130      99$: CLR   CDCNT          ;CLEAR NUMBER WE'RE TO PRINT
2449 030070 005037 002122      CLR   CHECK          ;ALLOW HEADER ON FIRST PRINT
2450 030074 012702 003052      MOV   #BUF,R2        ;COMPARE BUFFER TO CHECK WRITE
2451 030100 012701 000200      MOV   #128,R1        ;128 WORDS
2452 030104 012720 125252      MOV   #125252,GDDAT ;SET UP EXPECTED
2453 030112 011237 002170      MOV   (R2),BDDAT    ;GET DATA
2454 030116 023737 002166      CMP   GDDAT,BDDAT   ;IS DATA OKAY
2455 030122 010237 002162      BGE  6$              ;YES, CONTINUE
2456 030126 010237 002130      MOV   #5,TMP1       ;LOAD BAD MEM LOCATION
2457 030132 023737 016776      CMP   CDCNT,T.LMT   ;CHECKED ENOUGH??
2458 030140          BNE  333$           ;NO
2459 030142          ESCAPE        ;CHECK FOR FL:LOE, ELSE EXIT SEG
2460 030144 104010          EMT   C$ESCAPE
2461 030146 000076          .WORD 1001$-.
2462 030146 005237 002130      333$: INC   CDCNT      ;ACCOUNT FOR IT
2463                BGNSEC          ;%%START OF SEGMENT%%
2464 030152 005737 002122      TST   CHECK          ;HEADER OR JUST DATA
2465 030156 001007          BNE  9$              ;JUST DATA
2466 030160          ERRDF   34,EM25,ERR8 ;BAD DATA
2467 030160          TRAP   T$ERRCODE
2468 030162          .WORD 34
2469 030164          .WORD EM25
2470 030166          .WORD ERR8
2471 030170 005237 002122      INC   CHECK          ;ACCOUNT FOR PRINT OF HEADER
2472 030174 000416          BR    6$
2473                BGNSEC          ;%%START OF SEGMENT%%
2474 030176 013746 002170      9$:  PRINTB #FRMT6,TMP1,GDDAT,BDDAT
2475 030176 013746 002166      MOV   BDDAT,-(SP)
2476 030202 013746 002166      MOV   GDDAT,-(SP)
2477 030206 013746 002162      MOV   TMP1,-(SP)
2478 030212 013746 016042      MOV   #FRMT6,-(SP)
2479 030216 013746 000004      MOV   #4,-(SP)
2480 030222 010600          MOV   SP,RO
2481 030224 104014          EMT   C$PNTB
2482 030226 062706          ADD   #12,SP
2483                BGNSEC          ;%%START OF SEGMENT%%
2484 030232 104006          CKLOOP          ;BUMP BUFFER POINTER
2485 030232          EMT   C$CLP1
2486 030234 005722          TST   (R2)+
2487 030236 005301          DEC   R1              ;DONE?
2488 030240 001324          BNE  5$              ;NO, GO BACK
2489 030242          ENDSEG        ;%%END OF SEGMENT%%
2490 030242 104005          EMT   C$SESEG
2491 030244          ENDSEG        ;%%END OF SEGMENT%%
2492 030244 104005          EMT   C$SESEG
2493 030244          ENDTST
2494 030246 104001          L10060: EMT   C$SETST
    
```

```
.SBTTL **TEST 28** - CHECK SILO LINES
BCNTST                                     ;**START OF TEST**

030250                                     STAGE
;TEST THAT LINES IN / TO SILO ARE GOOD, THAT IS THAT EACH LINE IS
;GOOD AND CAN BE AT EITHER A 1 OR A 0 STATE INDEPENDENTLY OF EACH
;OTHER BIT POSITION THIS IS DONE BY WRITING PATTERNS OF FLOATING 1,
;FLOATING 0, WALKING 0, WALKING 1
;STAGE
;*****

030250 004737 021356        JSR PC,HDRHOME        ;HEADS OVER TRACK 0
030250 014932              CKRFRG                    ;HEADS GO HOME OKAY
030250 008404              ENT C,EXIT              ;
030250 012703              .WORD L10001-.

030266 012703 002662        MOV #DATPAT,R3

030272 164004              BGNSEC                                     ;**START OF SEGMENT**
030272 012700              MOV C,BSEG                                ;WRITE PATTERN INTO MEMORY
030272 011301              MOV #128,R0                               ;128 WORDS
030272 011326              MOV #3,(R0)+                             ;WRITE THE PATTERN
030272 011308              DEC R0                                     ;NO GO BACK
030272 001378              BNE 2$

030272 012777 003052 151724  MOV #BUF,ORLBA        ;SETUP TO WRITE PATTERN ONTO DISK
030272 012777 151724  CLR RLD                    ;LOAD DATA
030272 012777 177800 151716  MOV #128,R1             ;WORD COUNT
030272 008337 020456  JSR R5,LDPUNC          ;LOAD THE FUNCTION IN NEXT WORD
030272 004537 021276  JSR R5,WTCRDY          ;CHECK FOR FL:LOE, ELSE EXIT SEG
030272 104010              ESCAPE SRC                                ;
030272 004539              ENT C,ESCAPE                            ;
030272 004539 020214  .WORD 10001-2-    ;CHECK CTRL FOR ERRORS
030272 004539              ESCAPE SRC,CHERR                        ;CHECK FOR FL:LOE, ELSE EXIT SEG
030272 104010              ENT C,ESCAPE                            ;
030272 000310              .WORD 100003--                          ;**START OF SEGMENT**
030272 104004              BGNSEC                                     ;
030272 012700              MOV C,BSEG                                ;CLEAR MEMORY BEFORE READING IT BACK
030272 011301              MOV #128,R0                               ;128 WORDS
030272 011326              MOV #3,(R0)+                             ;CLEAR
030272 000308              DEC R0                                     ;HOME
030272 001378              BNE 3$

030400 012777 003052 151636  MOV #BUF,ORLBA        ;SETUP TO READ IT BACK
030400 012777 177800 151634  MOV #128,R1             ;128 WORDS
030400 005077 151626  CLR RLD                    ;SECTOR ZERO
```

```
030400 004537 020456        JSR R5,LDPUNC        ;LOAD THE FUNCTION IN NEXT WORD
030400 004537 021276        MOV R5,WTCRDY        ;CHECK FOR FL:LOE, ELSE EXIT SEG
030400 004537              ESCAPE SRC                                ;
030400 004537              ENT C,ESCAPE                            ;
030400 004537 020214  .WORD 10001-2-    ;CHECK CTRL FOR ERRORS
030400 004537              JSR R5,CHERR                        ;WAS ERROR A DCK??
030400 001003              TST SRC                                ;CHECK IF WE A DUMP
030400 001003              ESCAPE SRC                                ;CHECK FOR FL:LOE, ELSE EXIT SEG
030400 104010              ENT C,ESCAPE                            ;
030400 000308              .WORD 100015--                          ;SKIP AROUND
030400 000308 016774  BR 10$                               ;DO WE STILL WANT TO CHECK IT
030400 000308              BR 10$                                   ;YES, CHECK FOR LOOP FIRST
030400 104006              ENT C,SCLP1

030400 005037 002130 99$:   CLR CDCNT                                ;CLEAR NUMBER WE'RE TO PRINT
030400 005037 005142              CHECK R5                                ;ALLOW HEADER ON FIRST PRINT
030400 005037 005142  MOV #R5,GDDAT          ;COMPARE WHAT WE READ BACK
030400 005037 000001  MOV #BUF,TMP2          ;BUFFER START
030400 012737 002164  MOV #1,TMP1          ;START WITH FIRST
030400 012737 002162              .WORD 100015--                          ;
030400 005037 002170 5$:   MOV #TMP2,BDDAT          ;GET DATA
030400 005037 005140  CDR R5,BDDAT          ;GOOD?
030400 005037 005140  BEQ 4$                               ;YES, BRANCH

030400 023737 002130 016774  CMP CDCNT,T.LMT          ;CHECKED ENOUGH??
030400 005037 005142  ESCAPE SRC                                ;NO
030400 005037 005142  ENT C,ESCAPE                            ;CHECK FOR FL:LOE, ELSE EXIT SEG
030400 005037 005237 002130 333$: INC CDCNT                                ;ACCOUNT FOR IT
030400 005037 002122              TST CHECK                                ;HEADER OR JUST DATA
030400 005037 001007              BR 10$                                   ;JUST DATA
030400 005037 104462  TRAP #EM45,ERR10        ;BAD DATA BACK
030400 005037 000043  .WORD 35
030400 005037 013023  .WORD EM45
030400 005037 013023  .WORD ERR10

030572 005237 002122        INC CHECK                                ;ACCOUNT FOR PRINT OF HEADER
030576 000416              BR 4$

030600 013746 002170 9$:   PRINTB #FRMT7,TMP1,GDDAT,BDDAT
030600 013746 002166  MOV BDDAT,-(SP)
030600 013746 002162  MOV GDDAT,-(SP)
030600 013746 002158  MOV TMP1,-(SP)
030600 013746 016117  MOV #FRMT7,-(SP)
030600 013746 000004  MOV #4,-(SP)
030600 010500  MOV #4,R0
030600 006206  ENT C,R0
030600 006206  ADD #12,SP
030600 006206  CRLOOP
030600 000012 4$:
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(3) 030634 104006 EMT C$CLP1
2560 030636 062737 000002 002164 ADD #2,TMP2 ;NEXT LOCATION
2561 030644 005237 002162 INC TMP1 ;NEXT WORD
2562 030650 023727 002162 000201 CMP TMP1,#129. ;DONE
2563 030656 001317 BNE 5$ ;NO, GO BACK
2564 030660 ENDSEG ;**END OF SEGMENT**
2565 030660 104005 10001$: EMT C$ESEG
2566 030662 005723 TST (R3)+ ;DONE ALL PATTERNS
2567 030664 001203 BNE 6$ ;NO, GO BACK
2568 030666 ENDSEG ;**END OF SEGMENT**
2569 030666 104005 10000$: EMT C$ESEG
2570 030670 ENDTST ;**END OF TEST**
2571 030670 104001 L10061: EMT C$ETST
2572 .SBTTL **TEST 29** - CHECK THROUGHPUT OF SILO
2573 BGNTST ;**START OF TEST**
2574 030672
2575
2576
2577
2578
2579
2580 030672 STARS
2581 ;*****
2582 ;TEST THAT THE SILO OPERATES CORRECTLY, WE WILL WRITE A PATTERN THAT CONTAINS
2583 ;A UNIQUE PATTERN IN EACH LOCATION, WE EXPECT IT BACK IN PROPER
2584 ;ORDER, WE DO A ONE SECTOR TRANSFER
2585 STARS
2586 ;*****
2587
2588 030672 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0
2589 030676 000200 CKERFG ;HEADS GO HOME OKAY
2590 030704 104032 EMT C$EXIT
2591 030706 000410 .WORD L10062-.
2592
2593 030710 104004 BGNSEG ;**START OF SEGMENT**
2594 030710 EMT C$BSEG
2595
2596 030712 012700 000001 MOV #1,R0 ;INITIAL 1
2597 030716 012701 000200 MOV #128,R1 ;128 WORDS
2598 030722 012702 003052 MOV #BUF,R2 ;BUFFER
2599 030730 010022 000000 MOV R0,(R2)+ ;WRITE A WORD
2600 030732 005200 INC R0 ;NEXT PATTERN (1-128)
2601 030733 005301 DEC R1 ;DONE
2602 030734 001374 BNE 2$ ;NO
2603
2604 030736 012777 003052 151300 MOV #BUF,@RLBA ;SETUP TO WRITE
2605 030744 012777 177600 151276 MOV #-128,@RLMP ;128 WORDS
2606 030752 005077 151270 CLR @RLDA ;DISK ADDRESS 0
    
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2604 030756 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2605 030762 000012 WRITE
2606 030760 004537 021276 JSR R5,WTCRDY ;CHECK FOR FL:LOE, ELSE EXIT SEG
2607 030764 000000 ESCAPE SEG
2608 (3) 030770 104010 EMT C$ESCAPE
2609 (3) 030772 000322 .WORD 10000$-.
2610
2611 030774 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
2612 031000 000000 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
2613 (3) 031000 104010 EMT C$ESCAPE
2614 (3) 031002 000312 .WORD 10000$-.
2615 031004 BGNSEG ;**START OF SEGMENT**
2616 (3) 031004 104004 EMT C$BSEG
2617 031006 012700 000000 MOV #BUF,R0 ;CLEAR BUFFER
2618 031012 012701 000200 MOV #128,R1 ;128 IN LENGTH
2619 031016 005020 3$: CLR (R0)+ ;CLEAR
2620 031020 005301 DEC R1 ;DOWN COUNT
2621 031022 001375 BNE 3$ ;DONE?
2622
2623 031024 012777 003052 151212 MOV #BUF,@RLBA ;BUS ADDRESS
2624 031032 012777 177600 151210 MOV #-128,@RLMP ;WORD COUNT
2625 031040 005077 151202 CLR @RLDA ;DISK ADDRESS
2626 031044 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2627 031050 000014 READ
2628 031052 004537 021276 JSR R5,WTCRDY ;CHECK FOR FL:LOE, ELSE EXIT SEG
2629 (3) 031056 000000 ESCAPE SEG
2630 (3) 031056 104010 EMT C$ESCAPE
2631 (3) 031060 000232 .WORD 10001$-.
2632
2633 031062 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
2634 031066 005737 002124 TST T,CRC ;WAS ERROR A DCK??
2635 031072 001003 BNE 8$ ;YES,SEE IF WE A DUMP
2636 031074 000000 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
2637 (3) 031074 104010 EMT C$ESCAPE
2638 (3) 031076 000214 .WORD 10001$-.
2639 031100 000404 8$: BR 99$ ;SKIP AROUND
2640 031102 005727 016774 TST T,DMP ;DO WE STILL WANT TO CHECK IT
2641 031106 001772 BEQ 10$ ;NO
2642 031110 104006 CKLOOP ;YES, CHECK FOR LOOP FIRST
2643 (3) 031110 EMT C$CLP1
2644
2645 031112 005037 002130 99$: CLR CDCNT ;CLEAR NUMBER WE'RE TO PRINT
2646 031116 005037 002122 CLR CHECK ;ALLOW HEADER ON FIRST PRINT
2647 031122 012737 000001 002166 MOV #1,GDDAT ;START GOOD AT 1
2648 031130 012737 003052 002164 MOV #BUF,TMP2 ;START OF BUFFER
2649 031136 012737 000001 002162 MOV #1,TMP1 ;FIRST WORD
2650
2651 031144 017737 151014 4$: MOV @TMP2,BDDAT ;GET WORD
2652 031152 023737 002170 002166 CMP BDDAT,CDDAT ;CORRECT?
2653 031160 001440 BEQ 6$ ;YES
2654
2655 031162 023737 002130 016776 CMP CDCNT,T.LMT ;CHECKED ENOUGH??
2656 031170 001002 BNE 333$ ;NO
2657 031172 000000 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
2658 (3) 031174 104010 EMT C$ESCAPE
2659 (3) 031174 000116 .WORD 10001$-.
    
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2648 031176 005237 002130 333$: INC CDCWT ;ACCOUNT FOR IT
2649 031202 005737 002122 TST CHECK ;HEADER OR JUST DATA
2650 031206 001007 BNE 9$ ;JUST DATA
2651 031210 104462 ERRDF 36,EM47,ERR10 ;BAD DATA
2652 031214 006934 TRAP 1,ERRCODE
2653 031218 013056 -WORD 36
2654 031222 014702 -WORD EM47
2655 031224 005237 002122 INC ERR10 ;ACCOUNT FOR PRINT OF HEADER
2656 031224 000416 BR 6$

2657 031226 9$: PRINTB #FRMT7,TMP1,GDDAT,BDDAT
2658 031228 MOV BDDAT,-(SP)
2659 031230 MOV GDDAT,-(SP)
2660 031232 MOV TMP1,-(SP)
2661 031234 MOV #FRMT7,-(SP)
2662 031236 MOV #4,-(SP)
2663 031238 MOV SP,RO
2664 031240 EMT C$PRINTB
2665 031242 ADD #12,SP
2666 031244 6$: CKLOOP
2667 031246 EMT C$CLP1

2668 031264 062737 000002 002164 ADD #2,TMP2 ;NEXT
2669 031272 005237 002164 INC TMP1 ;NEXT
2670 031276 005237 002166 INC GDDAT ;NEXT
2671 031302 023727 002162 000201 CMP TMP1,#129. ;DONE?
2672 031310 001315 BNE 4$

2673 031312 10001$: ENDSEG ;**END OF SEGMENT**
2674 031314 EMT C$ESEG
2675 031316 104005

2676 031314 10000$: ENDSEG ;**END OF SEGMENT**
2677 031316 EMT C$ESEG
2678 031318 104005
2679 031316 ENDYST ;**END OF TEST**
2680 031316 L10062: EMT C$ETST

2681 .SBTTL **TEST 30** - CHECK ZERO FILL ON WRITE
2682 BCNTST ;**START OF TEST**

2683 STARS
2684 *****
2685 }WHEN WRITING PARTIAL SECTORS (LESS THAN 128 WORDS) THE
2686 }CONTROLLER WILL FILL IN THE REMAINING PORTION OF
2687 }THE SECTOR WITH ZERO WORDS. CHECK THIS FEATURE
2688 }WITH WORD COUNTS FROM 1 TO 127
2689 STARS
2690 *****
2691 }
2692 }
2693 }
2694 }
2695 }
2696 }
2697 }
2698 }
2699 }
2700 }
2701 }
2702 }
2703 }
2704 }
2705 }
2706 }
2707 }
2708 }
2709 }
2710 }
2711 }
2712 }
2713 }
2714 }
2715 }
2716 }
2717 }
2718 }
2719 }
2720 }
2721 }
2722 }
2723 }
2724 }
2725 }
2726 }

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2683 031320 004737 021356 JSR PC,HDRHOME ;HEADS OVER TRACK 0
2684 031332 104032 CERFG ;HEADS GO HOME OKAY
2685 031334 000442 EMT C$EXIT
2686 031336 104004 -WORD L10063-.
2687 031336 104004 BGNSEG ;**START OF SEGMENT**
2688 031336 EMT C$BSEG

2689 031340 012737 000001 002162 35$: MOV #1,TMP1 ;START WITH 1 WORD WRITE
2690 031342 012700 003052 MOV #BUF,RO ;WRITE BUFFER WITH 52525, WE'LL
2691 031344 012701 000200 MOV #128,R1 ;WRITE 128 WORDS ALL THOUGH WE'RE
2692 031346 012720 052525 3$: MOV #52525,(RO)+ ;ONLY GOING TO TRANSFER < 128
2693 031348 005391 DEC R1 ;DONE WITH BUFFER?
2694 031350 001374 BNE 3$ ;NO, GO BACK
2695 031352 013700 002162 33$: MOV TMP1,RO ;GET TRANSFER WORD COUNT
2696 031354 005400 NEG RO ;NEGATE FOR RLMP
2697 031356 010077 150650 MOV RO,@RLMP ;STORE WORD COUNT AWAY
2698 031358 003052 150636 150636 MOV #BUF,@RLBA ;SET UP RLBA
2699 031360 005400 CLR RLDA
2700 031362 004537 020456 JSR RS,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2701 031364 000012 WRITE ;WRITE IT
2702 031366 014274 021276 JSR R5,WTCRDY ;WAIT FOR WRITE TO FINISH
2703 031368 004537 ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
2704 031370 104010 EMT C$ESCAPE
2705 031372 000346 -WORD 100005-.

2706 031430 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
2707 031432 004537 ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
2708 031434 104010 EMT C$ESCAPE
2709 031436 000336 -WORD 100005-.

2710 031440 104004 BGNSEG ;**START OF SEGMENT**
2711 031442 012700 003052 EMT C$BSEG
2712 031444 012700 MOV #BUF,RO ;WE'RE GOING TO OVERLAY BUFFER BEFORE
2713 031446 012701 000200 MOV #128,R1 ;READING IT BACK-
2714 031448 012720 125252 18$: MOV #125252,(RO)+ ;OVERLAY IT WITH COMPLIMENT
2715 031450 005400 DEC R1 ;DONE?
2716 031452 001374 BNE 18$ ;NO, KEEP GOING

2717 031462 012777 003052 MOV #BUF,@RLBA ;SET UP TO READ
2718 031464 012777 177600 MOV #128,@RLMP ;128 WORDS TO CHECK ZERO FILL
2719 031466 005077 150544 CLR RLDA ;SECTOR
2720 031468 004537 020456 JSR RS,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2721 031470 004537 021276 JSR R5,WTCRDY ;WAIT TIL WE FINISH THE READ
2722 031472 004537 ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
2723 031474 104010 EMT C$ESCAPE
2724 031476 000234 -WORD 100015-.

2725 031520 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
2726 031522 005737 002124 TST T.CRC ;WAS ERROR A DCK??
2727 031524 001003 BNE 8$ ;YES, SEE IF WE A DUMP
2728 031526 104010 10$: ESCAPE SEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
2729 031528 000216 EMT C$ESCAPE
2730 031530 000404 -WORD 100015-.
2731 031532 000404 BR 9$ ;SKIP AROUND
2732 031534 005737 016774 8$: TST T.DMP ;DO WE STILL WANT TO CHECK IT

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ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-68
CZRLBB.P11 22-NOV-78 15:28 **TEST 30** - CHECK ZERO FILL ON WRITE SEQ 0098

2727 031544 001772 BEQ 10$ ;NO
2728 031546 104006 CKLOOP ;YES, CHECK FOR LOOP FIRST
(3) 031546 104006 EMT C$CLP1
2729 031546 005037 002130 99$: CLR CDCNT ;CLEAR NUMBER WE'RE TO PRINT
2730 031546 005037 002132 CLR CHECK ;ALLOW HEADER ON FIRST PRINT
2731 031560 013702 002162 MOV TMP1,R2 ;WORDS WRITTEN IN R2
2732 031564 012701 000200 MOV #128.,R1 ;CHECK 128 WORDS
2733 031570 012703 003052 MOV #BUF,R3 ;SET UP BUFFER BEGINNING
2734 031574 005037 002164 CLR TMP2 ;ZERO WORD COUNT
2735 031574 012737 052525 002166 4$: MOV #525.GDDAT ;SET UP EXPECTED
2736 031606 011337 002170 (R3),BDDAT ;GET WORD
2737 031612 023737 002170 002166 4$: CMP BDDAT,GDDAT ;IS WORD CORRECT?
2738 031620 001441 BEQ 12$ ;YES, GO CHECK COUNTS AND REPEAT
2739 031622 023737 002130 016776 CMP CDCNT,T.LMT ;CHECKED ENOUGH??
2740 031630 001002 BNE 333$ ;NO
2741 031630 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 031632 104010 EMT C$ESCAPE
(3) 031634 000116 .WORD 100015- ;ACCOUNT FOR IT
2744 031636 005237 002130 333$: INC CDCNT ;ACCOUNT FOR IT
2745 031642 005737 002122 TST CHECK ;HEADER OR JUST DATA
2746 031646 001007 BNE 9$ ;JUST DATA
2747 031650 001007 ERRDF 37,EM27,ERR12
(3) 031650 104462 TRAP 1$ERCODE
(3) 031652 012105 .WORD 3
(5) 031656 015026 .WORD EM27
2749 031660 005237 002122 INC ERR12 ;ACCOUNT FOR PRINT OF HEADER
2750 031664 000417 BR 12$
2751 031666 9$: PRINTB #FRMT9,TMP1,R3,GDDAT,BDDAT
(11) 031666 013746 MOV BDDAT,-(SP)
(10) 031672 013746 MOV GDDAT,-(SP)
(9) 031676 010346 MOV R3,-(SP)
(8) 031700 013746 MOV TMP1,-(SP)
(7) 031704 012746 002162 MOV #FRMT9,-(SP)
(6) 031708 010600 MOV SP,-(SP)
(5) 031714 010600 MOV SP,R0
(4) 031716 104014 EMT C$PNTB
(4) 031720 062706 ADD #14,SP
2753 031724 104006 12$: CKLOOP
2754 031726 005723 002164 6$: EMT C$CLP1
2755 031730 005237 INC (R3)+
2756 031734 005301 TMP2
2757 031736 001405 R1 ;DONE ALL WORDS?
2758 031740 005302 BEQ 7$ ;EXIT TEST
2759 031742 005321 DEC 4$ ;DONE CHECKING NON-ZERO WORDS
2760 031744 005037 002166 BGT 4$ ;NO BRANCH BACK
2761 031750 000716 CLR GDDAT ;YES, SET EXP'D AS ZERO
2762 031752 7$: BR 4$ ;BRANCH BACK
2763 031752 7$: ENDSEG ;EXIT TEST
2764 031752 10001$: ;**END OF SEGMENT**
(3) 031752

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ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-69
CZRLBB.P11 22-NOV-78 15:28 **TEST 30** - CHECK ZERO FILL ON WRITE SEQ 0099

(3) 031752 104005 EMT C$ESEG
2765 031754 005237 002162 000200 INC TMP1
2766 031756 005237 002162 000200 CMP TMP1,#128.
2767 031760 001462 BEQ 3$
2768 031764 001346 JMP 35$
2769 031770 000137 031346 34$:
2770 031774
2771 031774 10000$: ENDSEG ;**END OF SEGMENT**
(3) 031774 EMT C$ESEG
(3) 031774 104005 ENDTST ;**END OF TEST**
2773 031776 L10063: EMT C$ETST
(3) 031776 104001
(3) 031776
2775 032000 .SBTTL **TEST 31** - CHECK SECTOR BITS OF HEADER COMPARE
2776 032000 BGNST ;**START OF TEST**
2777 032000
2778 032000
2779 032000
2780 032000 STARS
2781 032000 ;*****
2782 032000 ;TEST THAT ALL SECTOR BITS OF HEADER WORD CAN COMPARE
2783 032000 ;UNIQUELY. WE TESTED THE HEADER COMPARE LOGIC EARLIER
2784 032000 ;BUT THAT WAS NOT AN EXTENSIVE TEST OF THE SECTOR BITS.
2785 032000 ;THE TEST PROCEDURE IS TO WRITE EACH SECTOR OF TRACK
2786 032000 ;WITH THE SECTOR ADDRESS, THEN GO BACK AND READ
2787 032000 ;EACH SECTOR. IF ANY SECTOR HAS ANY DATA THEN THAT
2788 032000 ;WHICH WAS EXPECTED THEN WE HAVE AN ERROR
2789 032000 ;ERROR PRINT OUT WILL GIVE SECTOR, EXPECTED AND RECEIVED
2790 032000 STARS
2791 032000 ;*****
2792 032000
2793 032000
2794 032000
2795 032000
2796 032000
2797 032000 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0
2798 032004 004032 CKERFG ;HEADS GO HOME OKAY
(4) 032012 000414 EMT C$EXIT
2799 032014 000414 .WORD L10064-.
2800 032016 032016 BGNSEG ;**START OF SEGMENT**
(3) 032016 104004 EMT C$BSEG
2801 032020 005037 002160 1$: CLR TMP0 ;CLEAR
2802 032024 104004 BGNSEG ;**START OF SEGMENT**
(3) 032024 104004 EMT C$BSEG
2805 032026 012702 003052 199$: MOV #BUF,R2 ;WRITE A PATTERN FOR THE WRITE
2806 032032 013701 000200 MOV #128.,R1 ;ONE SECTOR'S WORTH
2807 032036 013722 002160 MOV TMP0,(R2)+ ;WRITE IT
2808 032042 005301 DEC R1 ;DONE,
2809 032042

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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-70
CZRLBB.P11 22-NOV-78 15:28 **TEST 31** - CHECK SECTOR BITS OF HEADER COMPARE                                SEQ 0100

2810 032044 001374          BNE 2$ ;IF NOT, GO BACK
2811 032046 012777 177600 150174      MOV #128, R1LMP ;ONE SECTOR WORD COUNT
2812 032054 012777 003052 150162      MOV #BUF, R1LBA ;WRITE FROM BUF
2813 032060 004537 021828 150156      JSR R5, LDFUNC ;SECTOR
2814 032074 000012          WRITE R5, LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2815 032076 004537          JSR R5, WTCRDY ;WAIT FOR WRITE TO FINISH
2816 032102          ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
2817 032104 104010          EMT C$E$CAPE
2818 032104 000320          .WORD 100015-
2819 032106 005237 002160          INC TMP0 ;NEXT SECTOR
2820 032112 023727 002160 000050      CMP TMP0, #40. ;ALL DONE?
2821 032120 001342          BNE 199$ ;NO GO BACK
2822 032122 005037          CLR TMP0 ;CLEAR
2823 032126 104004          BGNSEG ;%%START OF SEGMENT%%
2824 032126          EMT C$B$SEG
2825 032130 012702 003052 98$: MOV #BUF, R2 ;CLEAR THE BUFFER FIRST
2826 032134 010701 000200          MOV #128, R1 ;128 WORDS
2827 032140 005037          CLR (R2)+
2828 032142 005301          DEC R1
2829 032144 001375          BNE 3$
2830 032146 013777 002160 150072      MOV TMP0, R1LDA ;GET SECTOR
2831 032154 012777 003052 150062      MOV #BUF, R1LBA ;SETUP BUS ADDRESS
2832 032162 012777 177600 150060      MOV #128, R1LMP ;READ A SECTOR
2833 032170 004537 020456      JSR R5, LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2834 032174 000014          READ R5, WTCRDY
2835 032176 004537          JSR R5, WTCRDY ;CHECK FOR FL:LOE, ELSE EXIT SEG
2836 032202          ESCAPE SEG
2837 032202 104010          EMT C$E$CAPE
2838 032204 000216          .WORD 100025-
2839 032206 004537 020214          JSR R5, CHERR ;CHECK CNTLR FOR ERRORS
2840 032212 005037 002124          TST R5, CRC ;WAS ERROR A DCK??
2841 032216 001003          BNE 10$ ;YES, SEE IF WE A DUMP
2842 032220          ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
2843 032222          EMT C$E$CAPE
2844 032222 104010          .WORD 100025-
2845 032224 000404          BR 99$ ;SKIP AROUND
2846 032224 005777 016774          TST DMP ;DO WE STILL WANT TO CHECK IT
2847 032232 001772          BEQ 10$ ;NO
2848 032234          CKLOOP 10$ ;YES, CHECK FOR LOOP FIRST
2849 032234          EMT C$CLP1
2850 ;CHECK NOW TO SEE IF WE READ THE RIGHT SECTOR
2851 032236 005037 002130 99$: CLR CDCNT ;CLEAR NUMBER WE'RE TO PRINT
2852 032242 005037 002122          CLR CHECK ;ALLOW HEADER ON FIRST PRINT
2853 032246 013737 002160 002166      MOV TMP0, GDDAT ;EXPECTED DATA
2854 032254 012702 003052          MOV #BUF, R2 ;BUFFER
2855 032260 012701 002160          MOV #128, R1 ;WORD COUNT
2856 032264 012237 002170          MOV (R2)+, BDDAT ;

```

```

ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-71
CZRLBB.P11 22-NOV-78 15:28 **TEST 31** - CHECK SECTOR BITS OF HEADER COMPARE                                SEQ 0101

2858 032270 023737 002170 002166      CMP BDDAT, GDDAT
2859 032276 001440          BEQ 6$
2860 032300 023737 002130 016776      CMP CDCNT, T.LMT ;CHECKED ENOUGH??
2861 032306 001002          BNE 333$ ;NO
2862 032310          ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
2863 032310 104010          EMT C$E$CAPE
2864 032312 000110          .WORD 100025-
2865 032314 005237 002130 333$: INC CDCNT ;ACCOUNT FOR IT
2866 032320 005737 002122          TST CHECK ;HEADER OR JUST DATA
2867 032324 001007          BNE 9$ ;JUST DATA
2868 032326          ERRDF 38, EM50, ERR11 ;
2869 032326 104462          TRAP $ERRCODE
2870 032330 000046          .WORD 3$
2871 032330 013105          .WORD 3$
2872 032334 014754          .WORD EM50
2873 032336 005237 002122          INC ERR11 ;ACCOUNT FOR PRINT OF HEADER
2874 032342 000416          BR 6$
2875 032344          PRINTB #FRMT8, TMP0, GDDAT, BDDAT
2876 032344 013746 002170          MOV BDDAT, -(SP)
2877 032350 013746 002166          MOV GDDAT, -(SP)
2878 032354 013746 002160          MOV TMP0, -(SP)
2879 032360 012746 016171          MOV #FRMT8, -(SP)
2880 032364 012746 000004          MOV #4, -(SP)
2881 032370 010600          MOV SP, R0
2882 032372 104014          EMT C$ENTB
2883 032374 062706 000012          ADD #12, SP
2884 032400          CKLOOP 6$: EMT C$CLP1
2885 032400 104006          EMT
2886 032404 005301          DEC R1 ;ALL OF SECTOR CHECKED?
2887 032404 001327          BNE 5$ ;GO BACK IF NOT
2888 032406 005237 002160          INC TMP0 ;NEXT SECTOR
2889 032412 023727 002160 000050      CMP TMP0, #40. ;DONE?
2890 032420 001243          BNE 98$ ;NO, GO BACK
2891 032422          ENDSEG ;%%END OF SEGMENT%%
2892 032422          EMT C$E$SEG
2893 032424 104005          ENDSEG ;%%END OF SEGMENT%%
2894 032424          EMT C$E$SEG
2895 032426          ENDSEG ;%%END OF SEGMENT%%
2896 032426 104005          EMT C$E$SEG
2897 032426          ENDTST L10064: EMT ;**END OF TEST**
2898 032430          CSETST
2899 032430 104001          EMT
2900 .SBTTL **TEST 32** - WRITE CHECK NPR INTEGRITY
2901 032432          BGNST ;**START OF TEST**

```



```

2891 032432
(2)
2892
2893
2894 032432
(4)
2895
2896
2897 032432 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0
2898 032436 CKERFG ;HEADS GO HOME OKAY
(4) 104032 EMT
2899 032446 000372 .WORD L10065--
2900 032450 BGNSEG ;**START OF SEGMENT**
(3) 104004 EMT C$BSEG
2901 032452 012700 003052 MOV #BUF,R0 ;SETUP AND WRITE
2902 032456 012701 002200 MOV #128,R1 ;128 WORDS
2903 032462 012720 125252 299$: MOV #125252,(R0)+ ;WRITE
2904 032466 005301 R1 ;DONE??
2905 032470 001374 BNE 299$
2906
2907
2908 032472 012777 003052 147544 MOV #BUF,@RLBA ;LOAD BUS ADDRESS
2909 032500 012777 177600 147542 MOV #-128,@RLMP ;WORD COUNT
2910 032506 005077 147534 CLR @RLDA ;CLEAR DISK ADDRESS
2911 032512 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2912 032516 000114 WRITE R5,WTRCDY ;WAIT FOR CONTROLLER READY
2913 032520 004537 021276 JSR R5,WTRCDY ;CHECK FOR FL:LOE, ELSE EXIT SEG
2914 032524 (3) 104010 ESCAPE SEG
2915 032526 000310 EMT C$ESCAPE
2916 032530 004537 020214 .WORD 10000$- ;CHECK CNTLR FOR ERRORS
2917 032534 104010 JSR R5,CHERR ;CHECK FOR FL:LOE, ELSE EXIT SEG
2918 032536 000300 EMT C$ESCAPE
2919 .WORD 10000$-
2920 ;VERIFY WRITE WITH READ BEFORE WRCHK
2921 032540 005077 147502 CLR @RLDA
2922 032544 012777 003052 147472 MOV #BUF,@RLBA
2923 032552 012777 177600 147470 MOV #-128,@RLMP
2924 032560 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2925 032564 000114 READ R5,WTRCDY
2926 032568 004537 021276 JSR R5,WTRCDY ;CHECK FOR FL:LOE, ELSE EXIT SEG
2927 032572 (3) 104010 ESCAPE SEG
2928 032574 000242 EMT C$ESCAPE
2929 032576 004537 020214 .WORD 10000$- ;CHECK CNTLR FOR ERRORS
2930 032600 104010 JSR R5,CHERR ;CHECK FOR FL:LOE, ELSE EXIT SEG
2931 032604 000232 ESCAPE SEG
2932 .WORD 10000$-
2933 BGNSEG ;**START OF SEGMENT**
(3) EMT C$BSEG
104004

```

```

2933 032610 1$: SETVEC ERRVEC,#TRPHAN,#340 ;SET UP FOR TRAP
(7) 032610 012746 000340 MOV #340,-(SP)
(6) 032614 012746 021350 MOV #TRPHAN,-(SP)
(5) 032618 012746 002132 MOV ERRVEC,-(SP)
(4) 032622 012746 000003 MOV #3,-(SP)
(3) 032630 104037 EMT C$SVEC
(2) 032632 062706 000010 ADD #10,SP
2934 032636 005037 002142 CLR TRPFLG ;CLEAR TRAP OCCURANCE
2935 032640 012777 003052 147374 MOV #BUF,@RLBA ;BUS ADDRESS
2936 032644 012777 147372 CLR @RLDA ;LOAD DISK ADDRESS
2937 032648 012777 177600 147366 MOV #-128,@RLMP ;WORD COUNT OF 128
2938 032652 005037 002166 CLR GDDAT ;SET UP CSR TO LOAD
2939 032656 013737 002134 MOV DRIVE,GDDAT ;SET IN DRIVE
2940 032660 052737 000002 BIS #WRCHK,GDDAT ;SET IN FUNCTION
2941 032664 004537 020764 JSR R5,BEFORE ;LOAD FOR ERROR PRINTOUT
2942 032668 013737 002166 MOV GDDAT,B,CS ;SET IN COMMAND
2943 032672 052737 000201 BIS #201,B,CS ;LOAD CRDY
2944 032676 042737 002000 BIC #OPTI,B,CS ;CLEAR (BIT 10)
2945 032680 013777 002166 147304 MOV GDDAT,@RLCS ;ISSUE WRITE CHECK
2946 032684 012701 000144 MOV #100,R1 ;WAIT FOR CRDY
2947 032688 032777 000200 147272 5$: BIT #CRDY,@RLCS ;NPR DONE
2948 032692 001013 BNE 6$ ;YES 6$
2949 032696 000024 WAITUS #20, ;WAIT A WHILE
(3) 032700 012700 MOV #20,R0
(2) 032704 104027 EMT C$WTU
2950 032708 005301 DEC R1 ;A WHILE UP
2951 032712 001367 RNE 5$ ;NO, GO BACK
2952
2953 032764 004537 021016 JSR R5,AFTER ;CONTROLLER TIMED OUT
2954 032770 104462 ERRDF 0,CRTIM,ERR5 ;TRAP
(3) 032774 000000 TRAP T$ERRCODE ;T$ERRCODE
(2) 032778 000000 .WORD 0
(1) 032782 007172 .WORD CRTIM
2955 033000 6$: CLRVEC ERRVEC ;CLEAR VECTOR
(3) 033004 013700 MOV ERRVEC,R0
(2) 033008 104036 EMT C$SVEC ;CHECK FOR FL:LOE, ELSE EXIT SEG
2956 033012 104010 ESCAPE SEG
(3) 033016 000024 EMT C$ESCAPE
(2) 033020 000024 .WORD 10001$-
2957
2958 033012 005737 002142 TST TRPFLG ;DID TRAP OCCUR?
2959 033016 001406 BEQ 7$ ;NO
2960 033020 004537 021016 JSR R5,AFTER 1,EM57,ERR0 ;TRAP ON WRITE
2961 033024 104461 TRAP T$ERRCODE
(1) 033026 000001 .WORD 1
(5) 033030 013477 .WORD EM57
(5) 033034 014244 .WORD ERRO
2962
2963 7$:
2964
2965 033034 10001$: ENDSEG ;**END OF SEGMENT**
(3) 104005 EMT C$ESEG
2966 033036 ENDSEG ;**END OF SEGMENT**

```

ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-74
 CZRLBB.P11 22-NOV-78 15:28 **TEST 32** - WRITE CHECK NPR INTEGRITY SEQ 0104

```

(3) 033036 104005 10000$: EMT C$ESEG
(3) 033036 104005
2967 033040 104001 ENDTST L10065: **END OF TEST**
(3) 033040 104001 EMT C$ETST
2969 033042 .SBTTL **TEST 33** - WRITE CHECK FUNCTION
2970 033042 BGNSTST **START OF TEST**
2971 033042 STARS
2972 *****
2973 ;CHECK OF WRITE CHECK LOGIC UNDER FLAG MODE
2974 ;WE WILL WRITE CHECK A FULL SECTOR (128 WORDS) FROM
2975 ;MEMORY (BUF). WE CHECK THAT NO ERRORS OCCUR.
2976 STARS
2977 *****
2978 033042 JSR PC,HDHOME ;HEADS OVER TRACK 0
2979 033046 CKERFG ;HEADS GO HOME OKAY
2980 033056 104032 EMT C$EXIT
2981 (4) 033056 000214 .WORD L10066-.
2982 033060 BGNSEG ;**START OF SEGMENT**
2983 033060 104004 EMT C$BSEG
2984 033062 012700 003052 MOV #BUF,RO ;SETUP AND WRITE
2985 033066 012701 000200 MOV #128,R1 ;128 WORDS
2986 033072 012720 125252 299$: MOV #125252,(R0)+ ;WRITE
2987 033076 005301 DEC R1 ;DONE??
2988 033100 001374 BNE 299$
2989 033102 012777 003052 147134 MOV #BUF,@RLBA ;LOAD BUS ADDRESS
2990 033110 012777 177600 147132 MOV #128,@RLMP ;WORD COUNT
2991 033116 005077 147124 CLR @RLDA ;CLEAR DISK ADDRESS
2992 033122 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
2993 033126 000012 WRITE R5,WTCRDY ;WAIT FOR CONTROLLER READY
2994 033130 004537 021276 JSR ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
2995 033134 104010 EMT C$ESCAPE
2996 033136 000132 .WORD 10000$-
2997 033140 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
2998 033144 104010 EMT C$ESCAPE ;CHECK FOR FL:LOE, ELSE EXIT SEG
2999 033146 000122 .WORD 10000$-
3000 033150 104004 BGNSEG ;**START OF SEGMENT**
3001 033150 104004 EMT C$BSEG
3002 ;VERIFY WRITE WITH READ BEFORE WRCHK
3003 033152 005077 147070 CLR @RLDA
3004 033156 012777 003052 147060 MOV #BUF,@RLBA
3005 033164 012777 177600 147056 MOV #128,@RLMP
3006 033172 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
  
```

ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-75
 CZRLBB.P11 22-NOV-78 15:28 **TEST 33** - WRITE CHECK FUNCTION SEQ 0105

```

3009 033176 000014 READ
3010 033200 004537 021276 JSR R5,WTCRDY
3011 033204 004537 021276 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3012 033204 104010 EMT C$ESCAPE
3013 033210 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
3014 033214 104010 EMT C$ESCAPE ;CHECK FOR FL:LOE, ELSE EXIT SEG
3015 033216 000050 .WORD 10001$-
3016 033220 104004 BGNSEG ;**START OF SEGMENT**
3017 033220 104004 EMT C$BSEG
3018 033222 095077 147020 3$: CLR @RLDA
3019 033222 012777 177600 147014 MOV #128,@RLMP ;WORD COUNT
3020 033224 012777 003052 147002 MOV #BUF,@RLBA ;BUS ADDRESS
3021 033242 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
3022 033246 000002 WRCHK ;WRITE CHECK
3023 033250 004537 021276 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
3024 033254 004537 021276 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3025 033254 104010 EMT C$ESCAPE
3026 033256 000006 .WORD 10002$-
3027 033260 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
3028 033264 004537 020214 ENDSEG ;**END OF SEGMENT**
3029 033264 104005 10002$: EMT C$ESEG
3030 033266 104005 10001$: EMT C$ESEG ;**END OF SEGMENT**
3031 033270 104005 10000$: EMT C$ESEG ;**END OF SEGMENT**
3032 033270 104005 ENDTST L10066: **END OF TEST**
3033 033272 104001 EMT C$ETST
3034 .SBTTL **TEST 34** - WRITE CHECK FUNCTION INTERRUPT
3035 033274 BGNSTST **START OF TEST**
3036 033274 STARS
3037 *****
3038 ;CHECK OF WRITE CHECK LOGIC UNDER INTERRUPT MODE
3039 ;WE WILL WRITE CHECK A FULL SECTOR (128 WORDS) FROM MEMORY (BUF).
3040 ;WE CHECK THAT NO ERRORS OCCUR. WE DO NOT CHECK RLDA OR RLBA
3041 ;INCREMENT AT THIS TIME.
3042 STARS
3043 *****
3044 033274 JSR PC,HDHOME ;HEADS OVER TRACK 0
3045 033274 004737 021356
3046
3047
  
```

```

3048 033300          CKERFG          )HEADS GO HOME OKAY
(4) 033306          ENT          C$EXIT
(4) 033310          .WORD          L10067-.
3049
3050 033312          BGNSEG          ;**START OF SEGMENT**
(3) 033312          ENT          C$BSEG
3051 104004
3052 033314          MOV          #BUF,R0          ;SETUP AND WRITE
3053 033320          MOV          #128,R1          ;128 WORDS
3054 033330          MOV          #1252,(R0)+       ;WRITE
3055 033332          DEC          R1          ;DONE??
3056 033332          DEC          299$
3057
3058 033334          MOV          @RLBA, @RLBA      ;LOAD BUS ADDRESS
(3) 033342          CLR          #128, @RLMP      ;WORD COUNT
(3) 033342          CLR          @RLDA, @RLMP      ;CLEAR DISK ADDRESS
3059 033342          JSR          R5, LDFUNC      ;LOAD THE FUNCTION IN NEXT WORD
3060 033344          WRITE
3061 033344          JSR          R5, WTCRDY      ;WAIT FOR CONTROLLER READY
3062 033360          ESCAPE          SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 033362          ENT          C$ESCAPE
(3) 033362          .WORD          10000S-
3063 033362          JSR          R5, CHERR          ;CHECK CNTLR FOR ERRORS
3064 033366          ESCAPE          SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 033366          ENT          C$ESCAPE
(3) 033370          .WORD          10000S-
3065 033372          JSR          R5, CHERR          ;CHECK CNTLR FOR ERRORS
3066 033376          ESCAPE          SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 033376          ENT          C$ESCAPE
(3) 033400          .WORD          10000S-
3067          ;VERIFY WRITE WITH READ BEFORE WRCHK
3068
3069 033402          CLR          @RLDA
3070 033406          MOV          #BUF, @RLBA      ;LOAD THE FUNCTION IN NEXT WORD
3071 033414          MOV          #128, @RLMP
3072 033422          JSR          R5, LDFUNC
3073          READ
3074 033430          JSR          R5, WTCRDY
3075 033434          ESCAPE          SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 033434          ENT          C$ESCAPE
(3) 033436          .WORD          10000S-
3076 033440          JSR          R5, CHERR          ;CHECK CNTLR FOR ERRORS
3077 033444          ESCAPE          SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 033444          ENT          C$ESCAPE
(3) 033446          .WORD          10000S-
3078
3079 033450          BGNSEG          ;**START OF SEGMENT**
(3) 033450          ENT          C$BSEG
3080
3081
3082 033452          CLR          INTFLG          ;CLEAR INTERRUPT OCCURANCE FLAG
3083 033456          CLR          @RLDA
3084 033460          MOV          #128, @RLMP      ;SET UP WORD COUNT
3085 033470          MOV          #BUF, @RLBA      ;SET UP BUS ADDRESS
3086
3087 033476          SETPRI          #PRI00          ;PRIORITY TO 0
(3) 033476          MOV          #PRI00, R0
(3) 033502          ENT          C$SPRI
3088 033504          JSR          R5, LDFUNC      ;LOAD THE FUNCTION IN NEXT WORD
3089 033510          WRCHKIINTEN          ;WRITE CHECK UNDER INTERRUPT
  
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3090 033512          JSR          R5, WTCRDY      ;WAIT FOR INTERRUPT
3091 033516          ESCAPE          SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 033516          ENT          C$ESCAPE
(3) 033520          .WORD          10001S-
3092
3093 033522          SETPRI          #PRI07          ;SET PRIORITY TO 7
(3) 033522          MOV          #PRI07, R0
3094 033526          ENT          C$SPRI
3095 033534          SET          INTFLG
3096 033534          BNE          2$
3097 033536          ERRDF          4, EM60, ERRO      ;WRITE DID NOT INTERRUPT
(3) 033536          TRAP          4, T$ERCODE
(3) 033540          .WORD          4
(3) 033540          .WORD          03537
(3) 033544          .WORD          EM60
3098 033546          ESCAPE          SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 033546          ENT          C$ESCAPE
(3) 033550          .WORD          10001S-
3099
3100 033552          JSR          R5, CHERR          ;CHECK CNTLR FOR ERRORS
3101
3102 033556          ENDSEG          ;**END OF SEGMENT**
(3) 033556          ENT          C$ESEG
3103 033560          ENDSEG          ;**END OF SEGMENT**
(3) 033560          ENT          C$ESEG
3104 033562          ENDTST          ;**END OF TEST**
(3) 033562          L10067: ENT          C$TST
3105
3106          .SBTTL          **TEST 35** - PROPER INCREMENT OF RLBA ON WRITE CHECK
3107          BGNST
3108 033564
3109
3110
3111          STARS
3112          ;*****
3113          ;CHECK THAT THE RLBA WILL INCREMENT PROPERLY AFTER THE
3114          ;WRITE CHECK WAS FINISHED THE RLBA SHOULD BE 128 WORDS (256 BYTES)
3115          ;CREATED. STARTING RLBA IS "BUF" ENDING SHOULD BE "BUF + 256."
3116          ;WE WILL MONITOR ALL ERRORS AND REPORT THEM ACCORDINGLY
3117          ;*****
3118
3119 033564          JSR          PC, HDHOME          ;HEADS OVER TRACK 0
(4) 033570          CKERFG          ;HEADS GO HOME OKAY
(4) 033576          ENT          C$EXIT
(4) 033600          .WORD          L10070-.
3121
3122 033602          BGNSEG          ;**START OF SEGMENT**
(3) 033602          ENT          C$BSEG
3123
3124 033604          MOV          #BUF, R0          ;SETUP AND WRITE
  
```

```

3125 033610 012701 000200      MOV     #128, R1      ;128 WORDS
3126 033614 012720 125252      MOV     #125252, (R0)+ ;WRITE
3127 033620 005301          DEC     R1            ;DONE??
3128 033622 001374          BNE     299$
3129 033624          ;
3130 033624 012777 003052 146112      MOV     #BUF, @RLBA  ;LOAD BUS ADDRESS
3131 033634 012777 177600 146410      MOV     #128, @RLMP  ;WORD COUNT
3132 033640 005077 146402      CLR     @RLDA        ;CLEAR DISK ADDRESS
3133 033644 004537 020456      JSR     R5, LDFUNC   ;LOAD THE FUNCTION IN NEXT WORD
3134 033650 000012          WRITE
3135 033652 004537 021276      JSR     R5, WTCRDY   ;WAIT FOR CONTROLLER READY
3136 033656 000012          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3137 033660 104010          EMT     C$ESCAPE
3138 033662 000174          .WORD 10000$-
3139 033662 004537 020214      JSR     R5, CHERR    ;CHECK CNTLR FOR ERRORS
3140 033666 000012          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3141 033666 104010          EMT     C$ESCAPE
3142 033670 000164          .WORD 10000$-
3143 033672          ;VERIFY WRITE WITH READ BEFORE WRCHK
3144 033672 005077 146350      CLR     @RLDA
3145 033676 012777 003052 146340      MOV     #BUF, @RLBA  ;LOAD THE FUNCTION IN NEXT WORD
3146 033704 012777 177600 146336      MOV     #128, @RLMP
3147 033712 004537 020456      JSR     R5, LDFUNC
3148 033716 000014          READ
3149 033720 004537 021276      JSR     R5, WTCRDY
3150 033724 000012          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3151 033724 104010          EMT     C$ESCAPE
3152 033724 000174          .WORD 10000$-
3153 033730 004537 020214      JSR     R5, CHERR    ;CHECK CNTLR FOR ERRORS
3154 033734 000012          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3155 033734 104010          EMT     C$ESCAPE
3156 033736 000116          .WORD 10000$-
3157 033740          BGNSEG
3158 033740 104004          EMT     C$BSEG      ;**START OF SEGMENT**
3159 033742          ;
3160 033742 005077 146300      CLR     @RLDA
3161 033746 012777 003052 146270      MOV     #BUF, @RLBA  ;SET UP BUS ADDRESS
3162 033750 012777 177600 146266      MOV     #128, @RLMP  ;WORD COUNT
3163 033754 012737 003052 002166      MOV     #BUF, @DDAT  ;FORM EXPECTED BUS ADDRESS
3164 033758 062737 000400 002166      ADD     #256, @DDAT  ;AFTER WRITE
3165 033760          ;
3166 033776 004537 020456      JSR     R5, LDFUNC   ;LOAD THE FUNCTION IN NEXT WORD
3167 034004 004537 021276      JSR     R5, WTCRDY   ;WRITE CHECK
3168 034004 004537 021276      JSR     R5, WTCRDY   ;WAIT FOR CONTROLLER READY
3169 034010 000012          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3170 034010 104010          EMT     C$ESCAPE
3171 034012 000040          .WORD 10001$-
3172 034014          ;
3173 034020 004537 020214      JSR     R5, CHERR    ;CHECK CNTLR FOR ERRORS
3174 034020 000012          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3175 034020 104010          EMT     C$ESCAPE
3176 034022 000030          .WORD 10001$-
3177 034024 017737 146214 002170      MOV     @RLBA, @DDAT ;READ "RLBA" FOR PRESENT ADDRESS
  
```

```

3168 034032 023737 002170 002166      CMP     @DDAT, @DDAT ;DID "BA" INCREMENT PROPERLY?
3169 034040 001404          BEQ     2$          ;YES, CONTINUE
3170 034042          ;
3171 034042 104462          ERRDF  5, EM61, ERR4 ;BA DID NOT INCREMENT
3172 034044 000174          TRAP  15, ERRCODE
3173 034046 000174          .WORD 5
3174 034050 014410          .WORD EM61
3175 034052          .WORD ERR4
3176 034052          ;
3177 034052          2$:
3178 034052          ENDSEG          ;**END OF SEGMENT**
3179 034052 10001$          EMT     C$ESEG
3180 034054          ENDSEG          ;**END OF SEGMENT**
3181 034054 10000$          EMT     C$ESEG
3182 034056 104005          ENDTST
3183 034056 104001          L10070: EMT     C$ETST
3184 034060          .SBTTL **TEST 36** - PROPER INCREMENT OF RLDA ON WRITE CHECK
3185 034060          BGNST          ;**START OF TEST**
3186 034060          STARS
3187 034060          ;*****
3188 034060          ;CHECK THAT THE SECTOR INCREMENTS AFTER THE WRITE CHECK WAS FINISHED.
3189 034060          ;A FULL SECTOR WRITE CHECK THE RLDA SHOULD REFLECT AN INCREMENT
3190 034060          ;OF THE SECTOR. "GDDAT" WAS THE EXPECTED RLDA.
3191 034060          STARS
3192 034060          ;*****
3193 034060 004737 021356      JSR     PC, HDHOME   ;HEADS OVER TRACK 0
3194 034064 000254          CKERFG          ;HEADS GO HOME OKAY
3195 034072 104032          EMT     C$EXIT
3196 034074 000254          .WORD L10071-
3197 034076          ;
3198 034076          BGNSEG          ;**START OF SEGMENT**
3199 034076 104004          EMT     C$BSEG
3200 034100 012700 003052 146116      MOV     #BUF, R0      ;SETUP AND WRITE
3201 034104 012701 000200 146114      MOV     #128, R1      ;128 WORDS
3202 034108 012720 125252      MOV     #125252, (R0)+ ;WRITE
3203 034110 005077 020456      DEC     R1            ;DONE??
3204 034112 001374          BNE     299$
3205 034120          ;
3206 034120 012777 003052 146116      MOV     #BUF, @RLBA  ;LOAD BUS ADDRESS
3207 034126 012777 177600 146114      MOV     #128, @RLMP  ;WORD COUNT
3208 034134 005077 020456      CLR     @RLDA        ;CLEAR DISK ADDRESS
3209 034140 000012          JSR     R5, LDFUNC   ;LOAD THE FUNCTION IN NEXT WORD
3210 034144 000012          WRITE
3211 034146 004537 021276      JSR     R5, WTCRDY   ;WAIT FOR CONTROLLER READY
3212 034152 104010          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3213 034152 000012          EMT     C$ESCAPE
  
```

```

(3) 034154 000172 .WORD 10000$-
3208 034156 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
3209 034162 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 034164 000162 EMT C$ESCAPE
3210 034164 000162 .WORD 10000$-
;VERIFY WRITE WITH READ BEFORE WRCHK
3211 CLR @RLDA
3212 MOV #BUF,@RLBA
3213 MOV #126,@RLMP
3214 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
3215 034206 004537 020456 JSR R5,LDFUNC
3216 034212 000014 READ
3217 JSR R5,WTCRDY ;CHECK FOR FL:LOE, ELSE EXIT SEG
3218 034214 004537 021276 ESCAPE SEG
3219 034220 104010 EMT C$ESCAPE
3220 034222 000124 .WORD 10000$-
3221 034224 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
3222 034230 004537 020214 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3223 034232 104010 EMT C$ESCAPE
3224 034234 000114 .WORD 10000$-
3225 034234 104004 BGNSEC ;**START OF SEGMENT**
3226 034234 104004 EMT C$BSEGC
3227 3$: 034236 CLR GDDAT
3228 034238 005037 002166 MOV GDDAT,@RLDA ;SETUP DISK ADDRESS
3229 034242 013777 002166 145776 INC GDDAT ;CREATE EXPECTED SECTOR
3230 034250 005237 002166 MOV #128,@RLMP ;WORD COUNT
3231 034254 012777 177600 145766 MOV #BUF,@RLBA ;SETUP BUS ADDRESS
3232 034262 012777 003052 145754 MOV #BUF,@RLBA
3233 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
3234 034270 004537 020456 WRCHK
3235 034274 000002 ;WRITE CHECK
3236 034276 004537 021276 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
3237 034302 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3238 034304 104010 EMT C$ESCAPE
3239 034304 000040 .WORD 10001$-
3240 034306 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
3241 034312 004312 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3242 034314 104010 EMT C$ESCAPE
3243 034314 000030 .WORD 10001$-
3244 034316 013737 002232 002170 MOV E.DA,BDDAT ;READ DISK ADDRESS
3245 034324 023737 002166 002170 CMP GDDAT,BDDAT ;DID SECTOR INCREMENT PROPERLY
3246 034332 001404 BEQ 2$ ;YES, BRANCH NO, REPORT ERROR
3247 034334 ERROF 6.,EM62,ERR4 ;DA DID NOT INCREMENT
3248 034334 104462 TRAP T$ERRCODE
3249 034336 000006 .WORD 6
3250 034340 013652 .WORD EM62
3251 034342 014410 .WORD ERR4
3252 034344 2$:
3253 034344 ENDSEC ;**END OF SEGMENT**
    
```

```

(3) 034344 10001$:
3248 034346 104005 EMT ENDSEC C$ESEC ;**END OF SEGMENT**
(3) 034346 10000$: EMT C$ESEC ;**END OF TEST**
3249 034350 ENDTST L10071:
(3) 034350 104001 EMT C$SETST
3250 034350 104001 .SBTTL **TEST 37** - MULTIPLE SECTOR WRITE CHECK
3251 BGNSTST ;**START OF TEST**
3252 034352 STARS
3253 *****
3254 ;CHECK FOR MULTIPLE SECTOR WRITE CHECK. THIS TEST CHECKS
3255 ;THAT TWO SECTORS CAN BE SUCCESSFULLY CHECKED. WE LOAD
3256 ;A WORD COUNT OF 129 WORDS (ONE SECTOR + 1 WORD) STARTING AT
3257 ;SECTOR 0 THRU SECTOR 37 AND VERIFY THAT THE RLDA DOES
3258 ;A DOUBLE INCREMENT EACH TIME.
3259 STARS
3260 *****
3261 034352 JSR PC,HDHOME ;HEADS OVER TRACK 0
3262 034356 CKERFG ;HEADS GO HOME OKAY
3263 034364 104032 EMT C$EXIT
3264 034366 000354 .WORD L10072-.
3265 034370 BGNSEC ;**START OF SEGMENT**
3266 034370 104004 EMT C$BSEGC
3267 034372 012737 000000 002160 MOV #0,THPO
3268 034400 012737 000000 002162 MOV #0,THP1
3269 034406 012700 003052 MOV #BUF,R0 ;SETUP AND WRITE
3270 034412 012701 000201 MOV #129,R1 ;129 WORDS
3271 034416 012720 125252 299$: MOV #125252,(R0)+ ;WRITE
3272 034422 005301 DEC R1 ;DONE??
3273 034424 001374 BNE 299$
3274 034426 012777 145610 1$: MOV #BUF,@RLBA ;LOAD BUS ADDRESS
3275 034434 012777 177577 145606 MOV #126,@RLMP ;WORD COUNT
3276 034442 013737 002162 002166 MOV THP1,GDDAT
3277 034450 053737 002160 002166 BIS THPO,GDDAT
3278 034456 013777 002166 145662 MOV GDDAT,@RLDA
3279 034464 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
3280 034470 000012 WRITE
3281 034472 004537 021276 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
3282 034476 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3283 034476 104010 EMT C$ESCAPE
3284 034500 000240 .WORD 10000$-
3285 034502 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
3286 034506 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3287 034506 104010 EMT C$ESCAPE
    
```

```

(3) 034510 000230          .WORD 10000$-
3300          ;VERIFY WRITE WITH READ BEFORE WRCHK
3301          MOV TMP1,GDDAT      ;TMP1,GDDAT
3302          BIS TMP0,GDDAT      ;TMP0,GDDAT
3303          MOV GDDA,@RLDA      ;GDDA,@RLDA
3304          MOV #BUF,@RLBA      ;#BUF,@RLBA
3305          MOV #129,@RLMP      ;#129,@RLMP
3306          JSR R5,LDFUNC        ;R5,LDFUNC
3307          READ                 ;LOAD THE FUNCTION IN NEXT WORD
3308          JSR R5,WTCRDY        ;R5,WTCRDY
3309          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3310          EMT C$ESCAPE
3311          .WORD 10000$-
3312          JSR R5,CHERR        ;CHECK CNTLR FOR ERRORS
3313          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3314          EMT C$ESCAPE
3315          .WORD 10000$-
3316          BGNSEG             ;**START OF SEGMENT**
3317          EMT CSBSEG
3318
3319          MOV TMP1,GDDAT      ;GET CYLINDER
3320          BIS TMP0,GDDAT      ;GET SECTOR
3321          MOV GDDA,@RLDA      ;SET DISK ADDRESS-SECTOR 0
3322          ADD #2,GDDAT        ;SET EXPECTED + 2
3323          MOV #BUF,@RLBA      ;SET BUS ADDRESS
3324          MOV #129,@RLMP      ;WORD COUNT-SECTOR+1 WORD
3325          JSR R5,LDFUNC        ;LOAD THE FUNCTION IN NEXT WORD
3326          JSR WRCHK           ;WRITE CHECK
3327          JSR R5,WTCRDY        ;WAIT FOR CONTROLLER READY?
3328          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3329          EMT C$ESCAPE
3330          .WORD 10001$-
3331          JSR R5,CHERR        ;CHECK CNTLR FOR ERRORS
3332          ESCAPE SEG          ;CHECK FOR FL:LOE, ELSE EXIT SEG
3333          EMT C$ESCAPE
3334          .WORD 10001$-
3335          MOV E,DA,BDDAT      ;READ DISK ADDRESS
3336          CMP BDDA1,GDDAT     ;IS DISK ADDRESS CORRECT
3337          BEQ                 ;YES, BRANCH NO, REPORT ERROR
3338          ERRDF 7,EM63,ERR4   ;DISK ADDRESS NOT CORRECT
3339          TRAP 7,ERRCODE
3340          .WORD 7
3341          .WORD EM63
3342          .WORD ERR4
3343          2$: CKLOOP
3344          EMT CSCLP1
    
```

```

3331 034722          ENDSEG          ;**END OF SEGMENT**
3332 (3) 034722          10001$: EMT C$ESEG
3333 (3) 034722 104005
3334 (3) 034724 034724 002160 002160      ;NEXT SECTOR
3335 (3) 034730 022737 000046 002160      ;AT END?
3336 (3) 034736 001233          INC TMP0          ;NO, GO BACK
3337 (3) 034740 104005          ENDSEG          ;**END OF SEGMENT**
3338 (3) 034742          10000$: EMT C$ESEG
3339 (3) 034742          ENDTST L10072: ;**END OF TEST**
3340 (3) 034742 104001          .SBTTL EMT C$SETST
3341 (3) 034744          ;**TEST 38** - FORCE DCK WITH WRITE CHECK
3342 (3) 034744          BGNST          ;**START OF TEST**
3343 (2) STARS
3344 ;*****
3345 ;FORCE A DCK WITH WRITE CHECK. THIS IS DONE BY WRITING
3346 ;A SECTOR AND CHANGING A WORD IN MEMORY BEFORE WRITE CHECK
3347 ;IS ISSUED..
3348 STARS
3349 ;*****
3350 (3) 034744 004737 021356          JSR PC,HDHOME    ;HEADS OVER TRACK 0
3351 (3) 034750          CKERFG          ;HEADS GO HOME OKAY
3352 (3) 034756 104032          EMT C$EXIT
3353 (3) 034760 000262          .WORD L10073-.
3354 (3) 034762          BGNSEG             ;**START OF SEGMENT**
3355 (3) 034762 104004          EMT CSBSEG
3356 (3) 034764 012700 003052          MOV #BUF,R0      ;SETUP AND WRITE
3357 (3) 034770 012701 000200          MOV #128,R1      ;128 WORDS
3358 (3) 034774 012720 125252          MOV #125252,(R0)+ ;WRITE
3359 (3) 035000 005301          DEC R1           ;DONE??
3360 (3) 035002 001374          BNE 299$
3361 (3) 035004 012777 003052 145232          MOV #BUF,@RLBA  ;LOAD BUS ADDRESS
3362 (3) 035012 012777 177600 145230          MOV #128,@RLMP  ;WORD COUNT
3363 (3) 035020 005077 145222          CLR @RLDA       ;CLEAR DISK ADDRESS
3364 (3) 035024 004537 020456          JSR R5,LDFUNC   ;LOAD THE FUNCTION IN NEXT WORD
3365 (3) 035032 004537 021276          JSR R5,WTCRDY   ;WAIT FOR CONTROLLER READY
3366 (3) 035036 104010          ESCAPE SEG      ;CHECK FOR FL:LOE, ELSE EXIT SEG
3367 (3) 035040 000200          EMT C$ESCAPE
3368 (3) 035042 004537 020214          .WORD 10000$-
3369 (3) 035046 104010          JSR R5,CHERR    ;CHECK CNTLR FOR ERRORS
3370 (3) 035050 000170          ESCAPE SEG      ;CHECK FOR FL:LOE, ELSE EXIT SEG
3371 (3) 035052 005077 145170          EMT C$ESCAPE
3372 (3) 035056 012777 003052 145160          .WORD 10000$-
3373          ;VERIFY WRITE WITH READ BEFORE WRCHK
3374          CLR @RLDA
3375          MOV #BUF,@RLBA
    
```

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ASSEMBLY ROUTINES      MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-84
CZRLBB.P11            22-NOV-78 15:28      **TEST 38** - FORCE DCK WITH WRITE CHECK      SEQ 0114
3372 035064 012777 177600 145156      MOV    #128,@RLMP
3373 035072 004537 020456      JSR    R5,LDFUNC          ;LOAD THE FUNCTION IN NEXT WORD
3374 035076 000014      READ
3375 035100 004537 021276      JSR    R5,WTCRDY
3376 035104      ESCAPE SEG                ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 035104      EMT    C$ESCAPE
(3) 035104      .WORD 10000$-
3377 035110 004537 020214      JSR    R5,CHERR          ;CHECK CNTLR FOR ERRORS
3378 035114      ESCAPE SEG                ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 035114      EMT    C$ESCAPE
(3) 035116 104010      .WORD 10000$-
3379 035116 000122
3380 035120      BGNSEG                    ;**START OF SEGMENT**
(3) 035120 104004      EMT    C$BSEG
3381 035122
3382 035122 005037 003052      CLR    BUF
3383 035126 005077 145114      CLR    @RLDA
3384 035132 012777 003052 145104      MOV    #BUF,@RLBA      ;SETTING SECTOR 40 OF CYL. ADDR.
3385 035132 012777 177600 145102      MOV    #128,@RLMP      ;WORD COUNT
3386 035140 004537 020456      JSR    R5,LDFUNC
3387 035146 000002      WRCHK R5,WTCRDY        ;LOAD THE FUNCTION IN NEXT WORD
3388 035152 004537 021276      JSR    R5,WTCRDY        ;WRITE CHECK
3389 035154 004537 021276      JSR    R5,WTCRDY        ;WAIT FOR CONTROLLER READY
3390 035160      ESCAPE SEG                ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 035160      EMT    C$ESCAPE
(3) 035162 104010      .WORD 10001$-
3393 035164 013737 002226 002160      MOV    E,C$TMP0         ;GET RLCS
3394 035172 042737 001777 002160      BIC    #1777,TMP0       ;SAVE ERROR BITS
3395 035200 022737 104000 002160      CMP    #BIT15BIT11,TMP0 ;DCK SET.
3396 035206 001402      BEQ    IS                ;YES, CONTINUE
3397 035210 004537 020214      JSR    R5,CHERR
3398 035214      CKLOOP
3399 035214 104006      EMT    C$CLP1
3400 035216 022737 104000 002160      CMP    #BIT15BIT11,TMP0
3401 035224 001404      BEQ    2$
3402 035226      ERRDF 23,EM65,ERR0
3403 035226      TRAP  T$ECCODE
(3) 035230 000027      .WORD 23
(3) 035232 014105      .WORD EM65
(3) 035234 014244      .WORD ERR0
3404 035236      2$:
3405 035236      ENDSEG                    ;**END OF SEGMENT**
3406 035236 104005      10001$: EMT    C$ESEG
3407 035240      ENDSEG                    ;**END OF SEGMENT**
3408 035240 104005      10000$: EMT    C$ESEG
3409 035242      ENDTST                    ;**END OF TEST**
(3) 035242 104001      L10073: EMT    C$ETST
(3) 035242

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ASSEMBLY ROUTINES      MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-85
CZRLBB.P11            22-NOV-78 15:28      **TEST 38** - FORCE DCK WITH WRITE CHECK      SEQ 0115
3410
3411      .SBTTL **TEST 39** - FORCE DCK WITH WRITE CHECK INTERRUPT
3412
3413 035244      BGNST                    ;**START OF TEST**
3414
3415 035244      STARS
(2) *****
3416      ; FORCE A DCK IN INTERRUPT MODE
3417      STARS *****
3418      ;
3419
3420
3421 035244 004737 021356      JSR    PC,HDDHOME       ;HEADS OVER TRACK 0
3422 035250      CKERFG                  ;HEADS GO HOME OKAY
(4) 035256 104032      EMT    C$EXIT
(4) 035260 000322      .WORD L10074-
3424 035262      BGNSEG                    ;**START OF SEGMENT**
(3) 035262 104004      EMT    C$BSEG
3425 035264 012700 003052      MOV    #BUF,R0          ;SETUP AND WRITE
3426 035270 012701 000200      MOV    #128,R1          ;128 WORDS
3427 035274 012700 125252      MOV    #125252,(R0)+    ;WRITE
3428 035300 005301      DEC    R1                ;DONE??
3429 035302 001374      BNE    299$
3430
3431 035304 012777 003052 144732      MOV    #BUF,@RLBA      ;LOAD BUS ADDRESS
3432 035312 012777 177600 144730      MOV    #128,@RLMP      ;WORD COUNT
3433 035320 005077 144722      CLR    @RLDA            ;CLEAR DISK ADDRESS
3434 035324 004537 020456      JSR    R5,LDFUNC        ;LOAD THE FUNCTION IN NEXT WORD
3435 035330 000012      WRITE
3436 035332 004537 021276      JSR    R5,WTCRDY        ;WAIT FOR CONTROLLER READY
3437 035336      ESCAPE SEG                ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 035336      EMT    C$ESCAPE
(3) 035340 104010      .WORD 10000$-
3439 035342 004537 020214      JSR    R5,CHERR          ;CHECK CNTLR FOR ERRORS
3440 035346      ESCAPE SEG                ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 035346      EMT    C$ESCAPE
(3) 035350 104010      .WORD 10000$-
3441 035350 000230      ;VERIFY WRITE WITH READ BEFORE WRCHK
3442
3443 035352 005077 144670      CLR    @RLDA
3444 035356 012777 003052 144660      MOV    #BUF,@RLBA      ;LOAD THE FUNCTION IN NEXT WORD
3445 035364 012777 177600 144656      MOV    #128,@RLMP
3446 035372 004537 020456      JSR    R5,LDFUNC
3447 035376 000014      READ
3448 035400 004537 021276      JSR    R5,WTCRDY
3449 035404      ESCAPE SEG                ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 035404      EMT    C$ESCAPE
(3) 035406 104010      .WORD 10000$-
3450 035410 004537 020214      JSR    R5,CHERR          ;CHECK CNTLR FOR ERRORS
3451 035414      ESCAPE SEG                ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 035414      EMT    C$ESCAPE
(3) 035416 000162      .WORD 10000$-
3452

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```

3453 035420 104004 BGNSEG ;**START OF SEGMENT**
3454 035420 EMT C$BSEG
3455 035422 SETPRI #PRI00
3456 035422 MOV #PRI00,R0
3457 035422 EMT C$SPRI
3458 035422 CLR INTFLG ;CLEAR INTERRUPT OCCURANCE FLAG
3459 035444 MOV #BUF,@RLBA ;SETTING SECTOR 40 OF CYL. ADDR.
3460 035452 MOV #128,@RLMP ;WORD COUNT
3461 035460 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
3462 035464 JSR R5,WTCRDY ;WRITE CHECK
3463 035466 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
3464 035472 EMT C$CLP1
3465 035472 SETPRI #PRI07
3466 035474 MOV #PRI07,R0
3467 035500 EMT C$SPRI
3468 035502 TST INTFLG ;DID INTERRUPT OCCUR
3469 035506 BNE 2$ ;YES OKAY
3470 035510 ERRDF 24,EM66,ERR0 ;NO INTERRUPT FROM DCK
3471 035510 TRAP T$ERRCODE
3472 035512 .WORD 24
3473 035514 .WORD EM66
3474 035516 .WORD ERR0
3475 035520 2$: ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3476 035520 EMT C$ESCAPE
3477 035522 .WORD 10001$-
3478 035524 MOV E,CS,TMPO ;SET RLCS
3479 035532 BIC #1777,TMPO ;SAVE ERROR BITS
3480 035540 CMP #BIT15:BIT11,TMPO ;DCK SET
3481 035546 BEQ 1$ ;YES, CONTINUE
3482 035550 JSR R5,CHERR
3483 035554 EMT C$CLP1
3484 035556 CMP #BIT15:BIT11,TMPO
3485 035564 BEQ 3$
3486 035566 ERRDF 24,EM65,ERR0
3487 035570 TRAP T$ERRCODE
3488 035572 .WORD 24
3489 035574 .WORD EM65
3490 035576 .WORD ERR0 ;WHEN FORCED
3491 035576 3$:
3492 035576 ENDSEG ;**END OF SEGMENT**
3493 035576 (3) 10001$:
    
```

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3491 035576 104005 EMT C$ESEG ;**END OF SEGMENT**
3492 035600 104005 EMT C$ESEG ;**END OF TEST**
3493 035602 104001 ENDTST L10074: EMT C$SETST
3494 035604 .SBTTL **TEST 40** - CHECK ZERO FILL ON WRITE WITH WRITE CHECK
3495 035604 BGNTST ;**START OF TEST**
3496 035604 STARS
3497 ;*****
3498 ;WHEN WRITING PARTIAL SECTORS (LESS THAN 128 WORDS) THE
3499 ;CONTROLLER WILL FILL IN THE REMAINING PORTION OF
3500 ;THE SECTOR WITH ZERO WORDS. CHECK THIS FEATURE CAN BE WRITE CHECKED
3501 ;WITH WORD COUNTS FROM 1 TO 127
3502 STARS
3503 ;*****
3504 035604 JSR PC,HDHOME ;HEADS OVER TRACK 0
3505 035610 CKERFG ;HEADS GO HOME OKAY
3506 035616 EMT C$EXIT
3507 035620 .WORD L10075-
3508 035622 104004 BGNSEG ;**START OF SEGMENT**
3509 035622 EMT C$BSEG
3510 035624 MOV #1,TMP1 ;START WITH 1 WORD WRITE
3511 035632 MOV #BUF,R0 ;WRITE BUFFER WITH 52525 WE'LL
3512 035636 MOV #128,R1 ;WRITE 128 WORDS ALL THOUGH WE'RE
3513 035642 MOV #52525,(R0)+ ;ONLY GOING TO TRANSFER < 128
3514 035646 DEC R1 ;DONE WITH BUFFER?
3515 035650 BNE 3$ ;NO, GO BACK
3516 035652 MOV TMP1,R0 ;GET TRANSFER WORD COUNT
3517 035656 NEG R0 ;NEGATE FOR RLMP
3518 035660 MOV R0,@RLMP ;STORE WORD COUNT AWAY
3519 035664 MOV #BUF,@RLBA ;SET UP RLBA
3520 035668 CLR @RLDA
3521 035672 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
3522 035676 JSR R5,WTCRDY ;WRITE IT
3523 035702 WRITE R5,WTCRDY ;WAIT FOR WRITE TO FINISH
3524 035704 JSR R5,WTCRDY ;CHECK FOR FL:LOE, ELSE EXIT SEG
3525 035710 EMT C$ESCAPE
3526 035712 .WORD 10000$-
3527 035714 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
3528 035720 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3529 035720 EMT C$ESCAPE
3530 035722 .WORD 10000$-
3531 ;VERIFY WRITE WITH READ BEFORE WRCHK
3532
    
```



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ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-88
CZRLBB.P11 22-NOV-78 15:28 **TEST 40** - CHECK ZERO FILL ON WRITE WITH WRITE CHECK SEQ 0118

3533 035724 005077 144316 CLR @RLDA
3534 035730 005077 144316 MOV #BUF,@RLBA
3535 035736 014700 002182 MOV TMP1,R0
3536 035742 005400 NEG R0
3537 035744 010077 144300 MOV R0,@RLMP
3538 035750 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
3539 035754 000014 READ
3540 035762 004537 021276 JSR R5,WTCRDY
3541 (3) 035762 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3542 (3) 035764 000126 EMT C$ESCAPE
3543 (3) 035766 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
3544 (3) 035772 004537 021276 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3545 (3) 035774 104010 EMT C$ESCAPE
3546 (3) 035774 000116 .WORD 10000$-.
3547 BGNSEG ;%%START OF SEGMENT%%
3548 035776 104004 EMT C$BSEG
3549 (3) 036000 012777 003052 144236 MOV #BUF,@RLBA ;SET UP TO READ
3550 (3) 036006 013700 002182 MOV TMP1,R0
3551 036012 005400 NEG R0
3552 036014 010077 144230 MOV R0,@RLMP
3553 036020 005077 144222 CLR @RLDA ;SECTOR
3554 036024 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
3555 036030 000022 WRCHK
3556 036032 004537 021276 JSR R5,WTCRDY ;WAIT TIL WE FINISH THE WRCHK
3557 (3) 036036 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3558 (3) 036040 104010 EMT C$ESCAPE
3559 (3) 036042 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
3560 (3) 036046 005737 002124 TST T.CRC ;WAS ERROR A DCK??
3561 036052 001003 BNE R5 ;YES, GIVE MOR INFO
3562 (3) 036054 104010 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3563 (3) 036054 000016 EMT C$ESCAPE
3564 (3) 036060 000405 .WORD 10001$-.
3565 (3) 036062 104006 BR 99$ ;SKIP AROUND
3566 (3) 036064 004006 CKLOOP ;YES, CHECK FOR LOOP FIRST
3567 (3) 036064 104462 EMT C$CLP1
3568 (3) 036066 000045 ERDF 37,EM64,ERR14
3569 (3) 036070 014031 TRAP 37,ERRCODE
3570 (3) 036072 015150 .WORD EM64
3571 (3) 036074 000000 .WORD ERR14
3572 (3) 036074 104005 99$: ENDSEG ;EXIT TEST
3573 (3) 036074 10001$: EMT C$ESEG ;%%END OF SEGMENT%%
3574 (3) 036074 104005 EMT C$ESEG
3575 036076 005237 002182 INC TMP1
3576 036102 023727 000200 CMP TMP1,#128.
3577 036110 001250 BNE 33$
3578 036112 000000 ENDSEG ;%%END OF SEGMENT%%
3579 (3) 036112 104005 EMT C$ESEG

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ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-89
CZRLBB.P11 22-NOV-78 15:28 **TEST 40** - CHECK ZERO FILL ON WRITE WITH WRITE CHECK SEQ 0119

3571 (3) 036114 ENDTST ;**END OF TEST**
3572 (3) 036114 L10075:
3573 (3) 036114 104001 .SBTTL *TEST 41** - EXTENDED CHECK OF WRITE CHECK FUNCTION
3574 036116 BGNST ;**START OF TEST**
3575 036116 STARS
3576 (2) ;*****
3577 ;CHECK OF WRITE CHECK LOGIC UNDER FLAG MODE
3578 ;THIS TEST IS DONE WITH ALL BIT PATTERNS
3579 ;WE WILL WRITE CHECK A FULL SECTOR (128 WORDS) FROM
3580 ;MEMORY (BUF). WE CHECK THAT NO ERRORS OCCUR.
3581 036116 STARS
3582 ;*****
3583 (3) 036116 004737 021356 JSR PC,HDRHOME ;HEADS OVER TRACK 0
3584 (3) 036120 004737 021356 CKRFRG ;HEADS GO HOME OKAY
3585 (4) 036130 104032 EMT C$EXIT
3586 (4) 036132 000246 .WORD L10076-.
3587 036134 012703 002504 MOV #HDRTAB,R3
3588 (3) 036140 BGNSEG ;%%START OF SEGMENT%%
3589 (3) 036140 104004 EMT C$BSEG
3590 036142 012700 003052 298$: MOV #BUF,R0 ;SETUP AND WRITE
3591 (3) 036146 012701 000200 MOV #128,R1 ;128 WORDS
3592 (3) 036152 011302 MOV (R1),R2
3593 036154 010220 299$: MOV R2,(R0)+ ;WRITE
3594 036156 005301 DEC R1 ;DONE??
3595 036160 001375 BNE 299$
3596 036162 012777 003052 144054 MOV #BUF,@RLBA ;LOAD BUS ADDRESS
3597 (3) 036170 013777 177600 144052 MOV #128,@RLMP ;WORD COUNT
3598 036176 005077 144044 CLR @RLDA ;CLEAR DISK ADDRESS
3599 036202 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
3600 036206 000012 WRITE
3601 036210 004537 021276 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
3602 (3) 036214 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3603 (3) 036216 104010 EMT C$ESCAPE
3604 (3) 036216 000160 .WORD 10000$-.
3605 036220 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
3606 036224 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3607 (3) 036224 104010 EMT C$ESCAPE
3608 (3) 036230 000150 .WORD 10000$-.
3609 (3) 036230 104004 BGNSEG ;%%START OF SEGMENT%%
3610 (3) 036230 000000 EMT C$BSEG
3611 ;VERIFY WRITE WITH READ BEFORE WRCHK
3612 036232 005077 144010 CLR @RLDA
3613 036236 012777 003052 144000 MOV #BUF,@RLBA
3614 036244 012777 177600 143776 MOV #128,@RLMP
3615 036252 004537 020456 JSR R5,LDFUNC ;LOAD THE FUNCTION IN NEXT WORD

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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-90
CZRLBB.P11 22-NOV-78 15:28 **TEST 41** - EXTENDED CHECK OF WRITE CHECK FUNCTION                               SEQ 0120

3615 036256 000014          READ
3616 036260 004537          JSR
3617 036264          ESCAPE. SEG
3618 036264          EMT
3619 036266 104010          C$ESEG
3620 036270 000076          .WORD 10001$-
3621 036270 004537          JSR R5,CHERR
3622 036274 104010          ESCAPE SEG
3623 036276 000066          EMT
3624 036300          .WORD C$ESEG-
3625 036300 104004          BGNSEG
3626 036300          EMT
3627 036302          C$BSEG
3628 036302          ;**START OF SEGMENT**
3629 036302          3$:
3630 036302 005077 143740          CLR @RLDA
3631 036306 012777 177600          MOV #-128,@RLMP
3632 036314 012777 003052          MOV #BUF,@RLBA
3633 036322 004537 020456          JSR R5,LDFUNC
3634 036326 000002          WRCHK
3635 036330 004537 021276          JSR R5,WTCRDY
3636 036334 104010          ESCAPE SEG
3637 036336 000024          EMT
3638 036336          .WORD C$ESEG-
3639 036340 004537 020214          JSR R5,CHERR
3640 036344 005737 002124          TST T,CRC
3641 036350 001404          BEQ
3642 036352          ERRHRD 410,ERR15,EM70
3643 036352          TRAP T$ERRCODE
3644 036352          .WORD 410
3645 036352          .WORD ERR15
3646 036360          .WORD EM70
3647 036362          4$:
3648 036362          ENDSEG
3649 036362          10002$:
3650 036362          EMT
3651 036362          C$ESEG
3652 036364          ENDSEG
3653 036364          10001$:
3654 036364          EMT
3655 036364          C$ESEG
3656 036366 005723          TST (R3)+
3657 036370 020327          CMP R3,#HDREND
3658 036374 001262          BNE
3659 036376          ENDSEG
3660 036376          10000$:
3661 036376          EMT
3662 036376          C$ESEG
3663 036400          ENDTST
3664 036400          L10076:
3665 036400          EMT
3666 036400          C$ESET
3667 036400          104005
3668 036400          104005
3669 036400          104005
3670 036400          104005
3671 036400          104005
3672 036400          104005
3673 036400          104005
3674 036400          104005
3675 036400          104005
3676 036400          104005
3677 036400          104005
3678 036400          104005
3679 036400          104005
3680 036400          104005
3681 036400          104005
3682 036400          104005
3683 036400          104005
3684 036400          104005
3685 036400          104005
3686 036400          104005
3687 036400          104005
3688 036400          104005
3689 036400          104005
3690 036400          104005
3691 036400          104005
3692 036400          104005
3693 036400          104005
3694 036400          104005
3695 036400          104005
3696 036400          104005
3697 036400          104005

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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-91
CZRLBB.P11 22-NOV-78 15:28 **TEST 41** - EXTENDED CHECK OF WRITE CHECK FUNCTION                               SEQ 0121

3652          .SBTTL **TEST 42** - EXTENDED CHECK OF WRITE CHECK FUNCTION
3653          BGNST
3654          ;**START OF TEST**
3655 036402
3656 036402
3657          STARS
3658          ;*****
3659          ;CHECK OF WRITE CHECK LOGIC UNDER FLAG MODE
3660          ;TEST IS DONE WITH ALL BIT PATTERNS
3661          ;WE WILL WRITE CHECK A FULL SECTOR (128 WORDS) FROM
3662          ;MEMORY (BUF). WE CHECK THAT NO ERRORS OCCUR.
3663          STARS
3664          ;*****
3665 036402 004737 021356          JSR PC,HDHOME
3666 036406 104032          CRERFG
3667 036414 000252          EMT
3668 036416          .WORD C$EXIT
3669 036420 012703 002504          MOV #HDRTAB,R3
3670 036424 104004          BGNSEG
3671 036424          EMT
3672 036426 012700 003052          MOV #BUF,R0
3673 036430 012701 000200          MOV #128,R1
3674 036436 011302          MOV (R1),R2
3675 036440 052702 100000          BIS #BIT15,R2
3676 036444 010220          MOV R2,(R0)+
3677 036446 005301          DEC R1
3678 036450 001375          BNE
3679          298$:
3680 036452 012777 003052 143564          MOV #BUF,@RLBA
3681 036460 012777 177600 143562          MOV #-128,@RLMP
3682 036466 005077 143554          CLR @RLDA
3683 036472 004537 020456          JSR R5,LDFUNC
3684 036476 000012          WRITE
3685 036500 004537 021276          JSR R5,WTCRDY
3686 036504          ESCAPE SEG
3687 036506          EMT
3688 036510 104010          C$ESEG
3689 036514 000160          .WORD 10000$-
3690 036516 004537 020214          JSR R5,CHERR
3691 036518          ESCAPE SEG
3692 036518          EMT
3693 036518          .WORD C$ESEG-
3694 036520 104004          BGNSEG
3695 036520          EMT
3696 036520          C$BSEG
3697          ;**START OF SEGMENT**
3698          ;VERIFY WRITE WITH READ BEFORE WRCHK
3699 036522 005077 143520          CLR @RLDA
3700 036526 012777 003052          MOV #BUF,@RLBA
3701 036534 012777 177600          MOV #-128,@RLMP
3702 036542 004537 020456          JSR R5,LDFUNC
3703 036546 000014          READ

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ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-92
CZRLBB-P11 22-NOV-78 15:28 **TEST 42** - EXTENDED CHECK OF WRITE CHECK FUNCTION SEQ 0122

3698 036550 004537 021276 JSR R5,WTCRDY
3699 036554 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 036554 104010 EMT C$ESEG
3700 036556 000076 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
3701 036564 004537 020214 JSR R5,CHERR ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 036564 104010 EMT C$ESEG
(3) 036566 000066 .WORD 10001$-

3703 036570 BGNSEG ;**START OF SEGMENT**
(3) 036570 104004 EMT C$BSEG

3705 036572 3S:
3706 036572 005077 143450 CLR @RLDA ;WORD COUNT
3707 036576 012777 177600 MOV #-128,@RLBA ;BUS ADDRESS
3708 036574 012777 003052 JSR R5,LDOPUNC ;LOAD THE FUNCTION IN NEXT WORD
3709 036612 004537 020456 JSR WRCHK ;WRITE CHECK
(3) 036616 000002

3711 036620 004537 021276 JSR R5,WTCRDY ;WAIT FOR CONTROLLER READY
3712 036624 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 036624 104010 EMT C$ESEG
(3) 036626 000024 .WORD 10002$-

3715 036630 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
3716 036634 005737 002124 TST T,CRC
3718 036640 001404 BEQ 4$

3720 036642 ERRHRD 410,ERR15,EM70
(3) 036642 104463 TRAP T,ERRCODE
(5) 036644 000632 .WORD 410
(5) 036646 015216 .WORD ERR15
(5) 036650 014220 .WORD EM70

3721 036652 4S:
3722
3723
3724
3725 036652 ENDSEG ;**END OF SEGMENT**
(3) 036652 104005 EMT C$ESEG
3726 036654 10001$: ENDSEG ;**END OF SEGMENT**
(3) 036654 104005 EMT C$ESEG
3727
3728 036656 005723 TST (R3)+
3729 036660 020327 CMP R3,#HDREND
3730 036664 001260 BNE 298$

3732 036666 ENDSEG ;**END OF SEGMENT**
(3) 036666 104005 EMT C$ESEG
3733 036670 ENDTST ;**END OF TEST**
(3) L10077:
3734 036670 104001 EMT C$SETST
.SBTTL **TEST 43** - READ WITHOUT HEADER COMPARE FUNCTION

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ASSEMBLY ROUTINES MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-93
CZRLBB-P11 22-NOV-78 15:28 **TEST 43** - READ WITHOUT HEADER COMPARE FUNCTION SEQ 0123

3735 036672 STARS
3736 ;*****
3737 ;TEST THAT READ WITHOUT HEADER VERIFICATION WORKS. THIS FUNCTION SHOULD
3738 ;READ AT THE NEXT SECTOR ENCOUNTERED. SET THE RLDA TO 0
3739 ;AND ISSUE THE FUNCTION IN FLAG MODE. UPON COMPLETION CHECK
3740 ;FOR ERRORS
3741 036672 STARS
3742 ;*****
3743 BGNST ;**START OF TEST**
3744
3745 036672 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0
3746 036676 CKERFG EMT ;HEADS GO HOME OKAY
(4) 036670 104032 EMT C$EXIT
(4) 036706 000052 .WORD L10100-

3747 036710 BGNSEG ;**START OF SEGMENT**
(3) 036710 104004 EMT C$BSEG

3750 036712 012777 177600 MOV #-128,@RLMP ;SET UP WORD COUNT
3752 036720 012777 003052 MOV #0,@RLBA ;SETUP BUS ADDRESS
3753 036726 012777 177777 MOV #-1,@RLDA ;HEADER SHOULDNT MATTER
3754 036734 004537 020456 JSR R5,LDOPUNC ;LOAD THE FUNCTION IN NEXT WORD
3755 036740 000016 RDNRD ;READ DATA WITHOUT HEADER VERIFY
3756 036742 004537 021276 JSR R5,WTCRDY ;WAIT FOR IT TO FINISH
(3) 036746 ESCAPE SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
(3) 036746 104010 EMT C$ESEG
(3) 036750 000006 .WORD 10000$-

3759 036752 004537 020214 JSR R5,CHERR ;CHECK CNTLR FOR ERRORS
3760
3761 036756 ENDSEG ;**END OF SEGMENT**
(3) 036756 104005 EMT C$ESEG
3762 036760 ENDTST ;**END OF TEST**
(3) L10100:
(3) 036760 104001 EMT C$SETST

3763 .SBTTL **TEST 44** - READ WITHOUT HEADER COMPARE FUNCTION INTERRUPT
3764 BGNST ;**START OF TEST**
3765
3766 STARS
3767 ;*****
3768 ;TEST THAT READ WITHOUT HEADER VERIFICATION WORKS IN
3769 ;INTERRUPT MODE.
3770 036762 STARS
3771 ;*****
3772 ;
3773 036762 004737 021356 JSR PC,HDHOME ;HEADS OVER TRACK 0
3774 036766 CKERFG EMT ;HEADS GO HOME OKAY
(4) 036774 104032 EMT C$EXIT
(4) 036776 000114 .WORD L10101-

3775

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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-94
CZRLBB.P11 22-NOV-78 15:28 **TEST 44** - READ WITHOUT HEADER COMPARE FUNCTION INTERRUPT          SEQ 0124

3776 037000 104004          BGNSEG          ;**START OF SEGMENT**
3777 037000          EMT          C$BSEG
3778 037002 005037 002144          CLR          INTFLG ;CLEAR INTERRUPT OCCURANCE FLAG
3779 037006 012777 177600          MOV          #128, @RLMP ;SET UP WORD COUNT FOR ONE SECTOR
3780 037014 012777 003052 143234          MOV          #BUF, @RLBA ;SETUP BUFFER ADDRESS
3781 037022 012777 177777 143222          MOV          #1, @RLDA ;LOAD BUFFER ADDRESS IS A DON'T CARE
3782 037030 012700 000000          SETPRI          #PRI00
3783 037034 104041          MOV          #PRI00, R0
3784 037036 004537 020456          EMT          C$SPRI
3785 037042 000118          JSR          R5, LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
3786 037044 004537 021276          RDNHDIIN          SEG ;INTERRUPT ENABLED
3787 037050          SETPRI          #PRI07 ;WAIT FOR INTERRUPT
3788 037054 104041          MOV          #PRI07, R0
3789 037056          EMT          C$SPRI
3790 037060 000030          ESCAPE          SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3791 037062 005737 002144          EMT          C$ESCAPE
3792 037066 001004          .WORD          10000$-
3793 037070          TST          INTFLG ;DID IT INTERRUPT
3794 037072 104462          BNE          IS ;IF INTERRUPT GO TO IS
3795 037074 000050          ERRDF          40, @M40, ERRO ;NO INTERRUPT
3796 037076 012547          TRAP          T$ERCODE
3797 037078 014244          .WORD          40
3798 037100 104010          .WORD          EM40
3799 037102 000006          .WORD          ERRO ;CHECK FOR FL:LOE, ELSE EXIT SEG
3800 037104 004537 020214          JSR          R5, CHERR
3801 037110          EMT          C$ESCAPE
3802 037112 104005          .WORD          10000$- ;CHECK CNTLR FOR ERRORS
3803 037114          ENDSeg          ;**END OF SEGMENT**
3804 037116          EMT          C$ESEG
3805 037118          ENDTST          ;**END OF TEST**
3806 037120          L10101          EMT          C$SETST
3807 037122 104001          .WORD          10000$-
3808          .SBTTL          **TEST 45** - CHECK RD W/O HDR CMP ACTUALLY READS
3809          BGNST          ;**START OF TEST**
3810 037114          STARS
3811          ;*****
3812          ;CHECK THAT THE READ W/O HDR CMP FUNCTION ACTUALLY READS (INTO MEMORY)
3813          ;WE WILL WRITE A PATTERN INTO MEMORY AND THEN ISSUE
3814          ;A READ TO OVERLAY THAT PATTERN. AFTER THE READ
3815          ;WE CHECK TO SEE IF THE WRITTEN PATTERN HAS CHANGED.
3816          ;IF NOT WE ISSUE IT AGAIN AT THE SAME SECTION AFTER
3817          ;HAVING MODIFIED OUR PATTERN IN MEMORY (SINCE THERE IS
3818          ;ONE CHANCE THAT THE DISK COULD HAVE OUR PATTERN). AFTER
3819          ;THE SECOND READ WE CHECK THE BUFFER AGAIN. IF IT'S
3820          ;NOT CHANGED WE REPORT AN ERROR

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ASSEMBLY ROUTINES          MACY11 30A(1052) 22-NOV-78 15:35 PAGE 1-95
CZRLBB.P11 22-NOV-78 15:28 **TEST 45** - CHECK RD W/O HDR CMP ACTUALLY READS          SEQ 0125

3814 037114          STARS
3815          ;*****
3816          ;HEADS OVER TRACK 0
3817          ;HEADS GO HOME OKAY
3818          JSR          PC, HDHOME
3819          EMT          C$EXIT
3820          .WORD          L10102-
3821          BGNSEG          ;**START OF SEGMENT**
3822          EMT          C$BSEG
3823 037134 012737 024350 002160          MOV          #24350, TMP0 ;SET PATTERN TO WRITE
3824 037142 005037 002162          CLR          TMP1 ;CLEAR PASS INDICATOR
3825 037146 012700 003052          1$: MOV          #BUF, R0 ;SET UP BUFFER BEGINNING
3826 037152 012701 000200          MOV          #128, R1
3827 037156 013720 002160          2$: MOV          TMP0, (R0)+ ;WRITE BUFFER
3828 037162 005301          DEC          R1 ;DONE??
3829 037164 001374          BNE          2$ ;NO, GO BACK
3830 037166 012777 000050 143052          MOV          #40, @RLDA ;LOAD DISK ADDRESS TO NONSENSE
3831 037174 012777 177600 143046          MOV          #128, @RLMP ;SET WORD COUNT
3832 037202 012777 003052 143034          MOV          #BUF, @RLBA ;LOAD BUS ADDRESS
3833 037210 012737 003052 002166          MOV          #BUF, @DAT ;FOR ERROR PRINT
3834 037216 004537 020456          JSR          R5, LDFUNC ;LOAD THE FUNCTION IN NEXT WORD
3835 037222 000016          RDNHDIIN          SEG ;READ W/O HDR CMP
3836 037224 004537 021276          JSR          R5, WTCRDY ;WAIT FOR CONTROLLER READY
3837 037230          EMT          C$ESCAPE ;CHECK FOR FL:LOE, ELSE EXIT SEG
3838 037232 104010          .WORD          10000$-
3839 037234 004537 020214          JSR          R5, CHERR ;CHECK CNTLR FOR ERRORS
3840 037240          ESCAPE          SEG ;CHECK FOR FL:LOE, ELSE EXIT SEG
3841 037242 104010          EMT          C$ESCAPE
3842 037244 000044          .WORD          10000$-
3843 037244 012702 003052          4$: MOV          #BUF, R2 ;SET TO START COMPARING DATA
3844 037250 022237 002160          CMP          (R2)+, TMP0 ;DID DATA CHANGE?
3845 037254 001014          BNE          6$ ;YES, CHECK FOR END
3846          ;
3847          ;DATA DIDN'T CHANGE, CHECK
3848          ;IF 1ST OR 2ND TIME?
3849          ;2ND-REPORT 1ST-TRY AGAIN
3850 037256 005737 002162          TST          TMP1
3851 037262 001005          BNE          5$
3852 037264 005237 002162          INC          TMP1 ;INC PASS COUNT
3853 037270 005137 002160          COM          TMP0 ;COMPLIMENT PATTERN
3854 037274 000724          BR          IS ;GO DO IT AGAIN
3855 037276          5$: ERRDF          20, @M55, ERR9
3856 037278          TRAP          T$ERCODE
3857 037300          .WORD          20
3858 037302          .WORD          EM55
3859 037304          .WORD          ERR9
3860 037306          6$:

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3858      037306      104005      104001
3859      037306
3860      037310
3861      037310
3862      037310
3863      037312
3864      037312
3865      037312
3866      037312
3867      037312
3868      037312
3869      037312
3870      037312
3871      037312 004737 021356
3872      037312 004737 021356
3873      037312 004737 021356
3874      037316 004737 021356
3875      037326 000120 000120
3876      037330 004004 004004
3877      037330 004004 004004
3878      037332 012777 000050 142706
3879      037340 012777 003052 142676
3880      037346 012777 177600 142674
3881      037354 012777 003052 002166
3882      037362 062737 000400 002166
3883      037370 004537 020456
3884      037374 000016 000016
3885      037374 000016 021276
3886      037376 004537 021276
3887      037402 104010 000040
3888      037402 000040 000040
3889      037406 004537 020214
3890      037412 104010 000030
3891      037416 013737 002230 002170
3892      037424 023737 002170 002166
3893      037432 001404 001404
3894      037434 104462 000025
3895      037436 000025 013310
3896      037440 013310 014410
3897      037442 014410 014410
  
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3897      037444      104005      104001
3898      037444
3899      037444
3900      037444
3901      037446
3902      037446
3903      037446
3904      037446
3905      037446
3906      037446
3907      037446
3908      037446
3909      037450
3910      037450
3911      037450
3912      037450
3913      037450
3914      037450
3915      037450
3916      037450
3917      037450 004737 021356
3918      037450 004737 021356
3919      037454 004032 000116
3920      037462 004032 000116
3921      037466 004004 004004
3922      037466 004004 004004
3923      037470 012737 000050 002166
3924      037476 013777 002166 142542
3925      037504 005237 002166
3926      037510 012777 177600 142532
3927      037516 012777 003052 142520
3928      037524 004537 020456
3929      037530 000016 000016
3930      037532 004537 021276
3931      037536 104010 000040
3932      037540 000040 000040
3933      037542 004537 020214
3934      037546 104010 000030
3935      037550 000030 000030
3936      037552 013737 002232 002170
3937      037556 023737 002166 002170
  
```

```

3940 037566 001404 BEQ 1$ ;YES, BRANCH NO, REPORT ERROR
3941 037570 ERPRD 22, EM54,ERR4
3942 037570 TRAP 22,ERRCODE
(5) 037572 000026 .WORD 22
(5) 037574 013400 .WORD EM54
(5) 037576 014410 .WORD ERR4
3944 037600 1$:
3945 037600 ENDSEG ;%%END OF SEGMENT%%
(3) 037600 10000$:
3946 037600 104005 EMT C$ESEG
(3) 037602 ENDTST L10104: ;**END OF TEST**
(3) 037602 104001 EMT C$ETST
3948
3949
3950
3951
3952 037604 BGNMOD HRDPRM
3953
3954 037604 BGNHRD
(3) 037604 000025 .WORD L10105-L$HARD/2
3955
3956 037606 GPRML CNTYPE,CNT,1,YES
(4) 037606 004130 .WORD T$CODE
(4) 037610 037660 .WORD CNTYPE
(4) 037612 000001 .WORD 1
3957 037614 GPRMA CSRMSG,CSR,0,160000,177776,YES
(4) 037614 000031 .WORD T$CODE
(4) 037616 037665 .WORD CSRMSG
(4) 037620 160000 .WORD T$LOLIM
(4) 037622 177776 .WORD T$HILIM
3958 037624 GPRMA VECMSG,VECT,0,0,776,YES
(4) 037624 001031 .WORD T$CODE
(4) 037626 037717 .WORD VECMSG
(4) 037630 000000 .WORD T$LOLIM
(4) 037632 000776 .WORD T$HILIM
3959 037634 GPRMD BRMSG,PRIOR,0,340,0,7,YES
(4) 037634 002032 .WORD T$CODE
(4) 037636 037701 .WORD BRMSG
(4) 037640 000340 .WORD 340
(4) 037642 000000 .WORD T$LOLIM
(4) 037644 000007 .WORD T$HILIM
3960 037646 GPRMD DRMSG,DRBT,0,03400,0,7,YES
(4) 037646 003032 .WORD T$CODE
(4) 037650 037721 .WORD DRMSG
(4) 037652 003400 .WORD 3400
(4) 037654 000000 .WORD T$LOLIM
(4) 037656 000007 .WORD T$HILIM
3962 037660 ENDRD
(2) 037660 L10105: .EVEN
3963
  
```

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3964 037660 046122 030461 000 CNTYPE: .ASCIZ /RL11/
3965 037665 102 051525 040440 CSRMSG: .ASCIZ /BUS ADDRESS/
(4) 037672 042104 042522 051523
3966 037700 000
(4) 037701 102 020122 042514 BRMSG: .ASCIZ /BR LEVEL/
(4) 037706 042526 000114
(4) 037710 042526 052103 051117 VECMSG: .ASCIZ /VECTOR/
3967 037720 000
(4) 037721 104 044522 042526 DRMSG: .ASCIZ /DRIVE/
(4) 037726 000
3969 037730 .EVEN
3970
3971 037730 ENDMOD
3972
3973
3974 037730 BGNMOD SFTPRM
3975
3976 037730 BGNSFT
(3) 037730 000025 .WORD L10106-L$SOFT/2
3977
3978 037732 GPRML DMSG,DLT,1,YES
(4) 037732 000130 .WORD T$CODE
(4) 037734 040004 .WORD DMSG
(4) 037736 000001 .WORD 1
3979 037740 XFERF 1$
(5) 037740 006044 .WORD T$CODE
3980 037742 GPRMD EMSC,ELT,0,177777,0,177777,YES
(4) 037742 001052 .WORD T$CODE
(4) 037744 040152 .WORD EMSC
(4) 037746 177777 .WORD 177777
(4) 037750 000000 .WORD T$LOLIM
(4) 037752 177777 .WORD T$HILIM
3981 037754 GPRML SMSG,SIZE,1,YES
(4) 037754 002130 .WORD T$CODE
(4) 037756 040030 .WORD SMSG
(4) 037760 000001 .WORD 1
3982 037762 GPRML CMSG,DMPCK,1,YES
(4) 037762 003130 .WORD T$CODE
(4) 037764 040041 .WORD CMSG
(4) 037766 000001 .WORD 1
3983 037770 XFERF 2$
(5) 037770 006044 .WORD T$CODE
3984 037772 GPRMD LMSG,DLMT,0,177777,1,128.,YES
(4) 037772 004052 .WORD T$CODE
(4) 037774 040065 .WORD LMSG
(4) 037776 177777 .WORD 177777
(4) 040000 000001 .WORD T$LOLIM
(4) 040002 000200 .WORD T$HILIM
3985 040004 2$:
3986
3987
3988 040004 ENDSFT
(2) 040004 L10106: .EVEN
(3) 040004
3989
3990 040004 051104 050117 047440 DMSG: .ASCIZ /DROP ON ERROR LIMIT/
  
```

```

040012 020116 051195 047522
040013 020122 044514 044515
040014 060154
3991 040030 052501 047524 044523 SMSG: .ASCIZ /AUTOSIZE/
040036 042532 000
3992 040041 042522 046517 040520 CMSC: .ASCIZ /COMPARE DATA ON DCK/
040048 020101 042040 052101
040052 045503 047117 042040
3993 040065 043 047440 020106 LMSG: .ASCIZ /# OF WORDS IN ERROR REPORTED/
040072 047527 042122 020123
040100 047114 042440 051105
040106 051117 051040 050105
040114 051117 042524 000104
3994 040122 051105 047522 020122 EMSG: .ASCIZ /ERROR LIMIT/
040130 044514 044515 000124
3995
3996 040136 ENDMOD
3997
3998
3999
4000 040514 .=40514
4001 ;AREA RESERVED AS PATCH AREA FOR DIAGNOSTICS.
4002 ;.=40514 WAS SELECTED AS "LASTAD" TO PROVIDE APT TO LSI-11 COMPATIBILITY.
4003 ;BIT 7 OF "LASTAD" MUST BE CLEARED TO ACHIEVE A VALID MAILBOX ADDRESS
4004 ;WHEN RUNNING ON THE LSI-11 UNDER APT.
4005
4006 040514 LASTAD
4007 (7) 040514 L$LAST: .EVEN
(4)
4008
4009
4010
4011
4012
4013
  
```

```

4015 .SBTTL DIAGNOSTIC SUPERVISOR -- LOW CORE SET UP
14886 071310 000000 .WORD 0 ;SPACE FOR USER POOL POINTER
14887 071312 000000 .WORD 0 ;SIZE
14888 071314 000000 .WORD 0 ;CHECKSUM (NOT CURRENTLY USED)
14889 071316 000000 .WORD 0 ;SIZE OF H.W. PTAB. ALLOCATION
14890 071322 END.SUPV.=+2
14891 000200 .END 200
  
```

ABOFLA	041040	G	BIT9	=	001000	G	CONTIN	=	017226		CSPOIN	=	000040	DSAAL	057572
ABOPAS	040756	G	BLD.HW	=	046202	G	CRDY	=	000200		CSQIO	=	000377	DSAAAM	057602
ABO.FM	043320		BLOCK	=	063614		CRIF	=	060172		CSRDBU	=	000007	EF.COM	000036
AFREG	007451		BPRIDR	=	007456		CRTIM	=	007100		CSRFPG	=	000050	EF.NEM	000035
AFSTG	040546	G	BMSG	=	037456		CSR	=	000100		CSRFOT	=	000045	EF.PW	010014
AFZER	021016		BUF	=	003052		CSRMSG	=	037665		CSRESH	=	000033	EF.RES	000037
ALLOC	061460		BVEC	=	002254		CURR.S	=	040522	G	CSREVT	=	000002	EF.STA	000040
APT.ER	042450		B\$AAB	=	047604		CURR.T	=	040524	G	CSRRPT	=	000025	EF01	000001
ARLBA	007106		B\$AAF	=	047516		CYLM\$K	=	002202		CSSEFG	=	000047	EF02	000004
ARLCS	007101		B.BA	=	007220		C\$AAD	=	000062		C\$AAB	=	000041	EF03	000004
ARLDA	007024		B.DA	=	002222		C\$AAE	=	023074		C\$AVE	=	000037	EF04	000004
ARLMP	000712		B.MP	=	002222		C\$AAK	=	054072		C\$TPRT	=	000013	EF05	000005
ASSEMB	=	0000010	CALBCC	=	002156	G	C\$AAL	=	054236		C\$UNBU	=	000031	EF06	000006
ASAAV	045316		CALBPC	=	000022	G	C\$ABRT	=	000021		C\$WTM	=	000026	EF07	000007
ASAAW	045332		CALPCS	=	000024		C\$ADR	=	000020		C\$WTU	=	000027	EF08	000010
ASAAZ	045352		CALCSP	=	000026		C\$AE	=	000054		DAMS	=	000020	EF09	000011
ASAAZ	045366		CALCSP	=	000030		C\$BRK	=	000022		DATPAT	=	002662	EF10	000012
ASABA	045376		CALCTC	=	000030		C\$BSEG	=	000004		DCKMES	=	007277	EF11	000013
BA16	=	000020	CAL.CI	=	066202	G	C\$BSUB	=	000002		DECM\$G	=	060004	EF12	000014
BA17	000040		CAL.TI	=	066240	G	C\$BUFF	=	000030		DEM\$S	=	007245	EF13	000015
BCCFBK	002154		CDCNT	=	002130		C\$CEFG	=	000046		DEPLG	=	002310	EF14	000016
BCSR	002252		CHECK	=	002132		C\$CLEA	=	000012		DERR	=	040000	EF15	000017
BDDAT	002170		CHERR	=	020214		C\$CLPI	=	000006		DEV.CO	=	040526	EF16	000020
BEFORE	002764		CHKLUP	=	047620		C\$CVCC	=	000036		DIAGM	=	000000	ELT	000002
BERFG	007130		CHKSTR	=	062022		C\$DCLN	=	000044		DIAG.T	=	041046	EM	040122
BGN.SU	=	040714	CHKTTY	=	060110		C\$DDBU	=	000053		DLMT	=	060010	EM.TR	041044
BGNMSG	=	040714	CHKNA	=	066610		C\$DRPT	=	000001		DLT	=	000000	EM10	010352
BIT0	000001	G	CHK.PC	=	053110		C\$DU	=	000055		DLT	=	000000	EM10	011043
BIT00	000001	G	CHK.SW	=	042150		C\$EDIT	=	000002		DLMES	=	007304	EM100	010417
BIT01	000002	G	CHRCNT	=	061342		C\$ERRF	=	000002		DMPCK	=	000006	EM11	011110
BIT02	000002	G	CH.PLA	=	045466		C\$ERRH	=	000003		DM\$G	=	040004	EM11	011110
BIT03	000003	G	CHK.PS	=	020126		C\$ERRP	=	000001		DPDWD	=	070456	EM12	011137
BIT04	000002	G	CLEAR	=	047102		C\$ERRS	=	000004		DPHUL	=	000020	EM13	011176
BIT05	000004	G	CLKACC	=	040754	G	C\$ESCA	=	000010		DRBT	=	000006	EM14	011230
BIT06	000010	G	CLKKBP	=	066204		C\$ESGC	=	000005		DRDY	=	000001	EM16	011312
BIT07	000020	G	CLKKCN	=	040752		C\$ESUB	=	000003		DRIVE	=	002134	EM17	011351
BIT08	000001	G	CLKJUM	=	066610		C\$EXST	=	000032		DRMSG	=	037721	EM20	011411
BIT09	000000	G	CLKKRES	=	067612		C\$EXTP	=	000032		DRPCOD	=	010766	EM22	011568
BIT1	000002	G	CLKSER	=	067746	G	C\$GMAN	=	000043		DRST	=	000010	EM23	011636
BIT10	002000	G	CLKSDN	=	041012	G	C\$GPHR	=	000042		DRTIM	=	007217	EM24	011716
BIT11	004000	G	CLK.SE	=	045566	G	C\$GPRT	=	000040		DRSPCOD	=	017000	EM25	011774
BIT12	000001	G	CLNCDD	=	060640	G	C\$GTIM	=	000052		DSO	=	000000	EM26	012041
BIT13	020000	G	CL.NA	=	046036	G	C\$HINT	=	000011		DSL	=	000000	EM26	012041
BIT14	040000	G	CM\$G	=	040041		C\$INLP	=	000020		DS2	=	001000	EM30	014165
BIT15	100000	G	CNT	=	000010		C\$KWP	=	000035		DS3	=	001400	EM31	012225
BIT2	000004	G	CNTYPE	=	037660		C\$KWON	=	000034		DUNIT	=	040762	EM32	012270
BIT3	000001	G	CNVT	=	040560		C\$LDUP	=	000100		DVC.FT	=	054042	EM33	012335
BIT4	000000	G	COM\$M	=	040564	G	C\$MNH	=	000011		DWORG	=	054040	EM34	014476
BIT5	000040	G	COMMAN	=	064074		C\$MNH	=	000023		D\$ARC	=	054746	EM35	014544
BIT6	000010	G	COMP	=	007316		C\$PNTB	=	000014		DSAAH	=	054764	EM36	012506
BIT7	000020	G	CONT	=	017354		C\$PNTF	=	000017		DSAAJ	=	057532	EM4	010607
BIT8	000040	G	CONTCL	=	067672	G	C\$PNTS	=	000016		DSAAJ	=	057536	EM40	012547
							C\$PNTX	=	000015		DSAAK	=	057554	EM41	012612

EM42	012654		ERR5	01445	G	F\$MOD	=	000000		HDREND	002660	LPRFR	040622	G	
EM43	012723		ERR9	01455	G	F\$MS	=	000001		HDRES	002667	LPCSD	040620	G	
EM44	013024		ERR7	01456	G	F\$PWR	=	000017		HDRTAB	002504	LPT.AD	045140	G	
EM45	013056		ERR8	01456	G	F\$RPT	=	000012		HERTZ	045126	LPT.RE	045140	G	
EM47	013056		ERR9	014636	G	F\$SEGC	=	000003		HNFMES	007272	L\$1.RE	045134	G	
EM5	010646		ESC.PC	053106		F\$SERV	=	000005		HOLDSP	=	000020	LUP	066106	G
EM50	013105		EW.COU	040520	G	F\$SRV	=	000000		HPFCOD	016750	LUP.AD	053112	G	
EM52	013117		E.BA	002220		F\$SUB	=	000010		HRPRM	040550	L\$ADP	002074	G	
EM53	013310		E.CS	002222		F\$SW	=	000014		HW.ADP	040550	L\$AUP	002074	G	
EM54	013400		E.DA	002232		F\$TEST	=	000001		HSAB	064606	L\$CCP	002106	G	
EM55	013230		E.MP1	002234		GARBAG	=	066134		ININIT	040772	L\$SCLEA	020010	G	
EM55	013456		E.MP2	002236		GDAT	=	002166		INITCO	017140	L\$SCD	002032	G	
EM56	013477		E.MP3	002240		GETCHR	=	060050		INITIA	060020	L\$SDP	002011	G	
EM57	010712		FILL.C	000204	G	GETCWN	=	063434		INITM	046104	L\$SDC	002142	G	
EM60	013537		FIRST	002200	G	GETPAR	=	055126		INIT.R	040606	L\$SDVP	002064	G	
EM61	013575		FIX	020752		GETSWI	=	062430		INPUTA	060746	L\$SDISP	017002	G	
EM62	013652		FLAGS	040560	G	GET.TW	=	062200		INTEN	=	000100	L\$SDR	002112	G
EM63	013377		FLAGS1	040562	G	GLBDAT	=	002122	G	INTFLG	002144	L\$SDRCT	002070	G	
EM64	014031		FLAGT	064016	G	GLBEQA	=	002122	G	INTFOR	054244	L\$SDRS	002072	G	
EM65	014105		FLAG.T	045546		GLBERR	=	014244	G	INTSRV	020010	L\$SDRT	002072	G	
EM66	014146		FLA.SE	063760		GLBSUB	=	020110	G	INVAL	045052	L\$SDST	002040	G	
EM7	010766		FLG.NA	045506		GLBXT	=	007052	G	INVINT	054102	L\$SDU	020104	G	
EM70	014220		FNDFNC	002260		GDDRVYR	=	000202	G	INV.SW	042104	L\$SDUT	002076	G	
END.OF	017070		FORM.T	054410		G\$BIT	=	000002		IN.SUF	047054	L\$SDVTY	002114	G	
END.SU	=	071322	FREE	061716		G\$TIM	=	010006		ISAU	=	000041	L\$SEFV	002034	G
ENVIRO	040566	G	FRMT1	015606		G\$TIM	=	000747		ISCLN	=	000041	L\$SEFLG	002034	G
EP.CH	067770	G	FRMT10	016415		G\$TIM	=	000747		ISDU	=	000041	L\$SEXP1	002042	G
EP.FM	043334	G	FRMT11	016550		G\$EXCP	=	000400		ISHRD	=	000041	L\$SEXP2	002044	G
EP.IN	045500	G	FRMT13	016641		G\$HIL	=	000002		ISINIT	=	000041	L\$SEXP3	002046	G
ERRCOUN	002314		FRMT14	016723		G\$LDL	=	000001		ISMOD	=	000041	L\$SHARD	037606	G
ERRFLG	002266		FRMT15	016723		G\$MOD	=	000000		HS\$C	=	000041	L\$SHPCD	002016	G
ERRPOIN	=	0000000	FRMT2	015645		G\$OFFS	=	000400		ISPMR	=	000041	L\$SHPT	002022	G
ERRROR	054314		FRMT2A	015664		G\$OF\$1	=	000376		ISRPT	=	000041	L\$SHW	016752	G
ERRROR	054314		FRMT2B	015677		G\$PRMA	=	000001		ISSEG	=	000041	L\$SICP	002104	G
ERRROR	054314		FRMT3	015726		G\$PRMD	=	000002		ISSFT	=	000041	L\$SINT	017140	G
ERRROR	054314		FRMT4	015733		G\$PRML	=	000000		ISSRV	=	000041	L\$SADP	002026	G
ERRROR	054314		FRMT5	015771		G\$RADD	=	000140		ISSUB	=	000041	L\$LAST	040544	G
ERRROR	054314		FRMT6	016042		G\$RADD	=	000040		ISTST	=	000041	L\$SMREV	002050	G
ERRROR	054314		FRMT7	016117		G\$RADD	=	000020		JSJMP	=	000167	L\$SNAME	002000	G
ERRROR	054314		FRMT8	016117		G\$RADD	=	000120		KBPT	=	040624	L\$SEPP	002066	G
ERRROR	054314		FRMT9	016310		G\$RADD	=	000020		KBUF	=	040626	L\$SERV	002010	G
ERRROR	054314		FRMT98	016615		G\$RADD	=	000000		LDCSR	=	002146	L\$SFT	037732	G
ERRROR	054314		FRMT99	016667		G\$RADD	=	000100		LDFUNC	=	020456	L\$SPC	002022	G
ERRROR	054314		F\$AU	=	000015	G\$XFER	=	000004		LF	=	007311	L\$SPCP	002020	G
ERRROR	054314		F\$B												

L10000	014260	L10065	033040	NUM.LA	054602	READ.P	066210	G	SWCHAN	045300
L10001	014392	L10066	033272	NUM.MD	040558	RECBAC	070700	G	SWTICH	064152
L10002	014334	L10067	033562	NUM.UN	041164	RECSAV	070664	G	SW.ADR	040554
L10003	014406	L10070	034056	NUMITS	047572	REQM.P	040570	G	SW.PTA	045264
L10004	014454	L10071	034350	NXM	020000	REQM.T	045464	G	SYS.FT	054032
L10005	014524	L10073	035242	NXMMES	017432	RESE	020440	G	SEMP3	010000
L10006	014562	L10074	035602	NXTFOR	064252	RE.SET	042252	G	TEST.M	002174
L10010	014634	L10075	036114	OCTMSG	057776	RHDINT	007623	G	TEMP3	002174
L10011	014700	L10076	036400	OPI	003000	RHDMES	007563	G	TEMP4	002176
L10013	014752	L10077	036670	OPIERR	007324	RHHS	000100	G	TERMI	066176
L10014	015024	L10100	040094	OPIHRS	002400	RICA	007444	G	TERMLI	064000
L10015	015100	L10101	039150	OPIHRS	002400	RCLS	002542	G	TEST.M	045420
L10015	015146	L10102	037310	OPIHRS	002400	RLDA	002246	G	TIM.FL	040750
L10016	015214	L10103	037446	OSAPTS	000000	RLMP	002250	G	TIMSRV	020116
L10017	015254	L10104	037602	OSAU	000000	RSTACK	070140	G	TIM.CD	040602
L10020	015764	L10105	037650	OSBCHP	000000	SAVEDD	042450	G	TIM.OP	054406
L10021	017000	L10106	040094	OSBCHP	000000	SEARCH	062146	G	TMPO	002160
L10022	020006	MAJ.IN	040576	OSDU	000001	SECMSK	002150	G	TMP1	002162
L10023	020102	MAJ.LD	066206	OSGNSW	000001	SEEK	000006	G	TMP2	002164
L10024	020106	MAJ.US	040600	OSPDIN	000001	SEGSTA	041014	G	TOD.MA	057742
L10025	020114	MAN.TI	001244	OSRSES	063502	SEKINT	007715	G	TRPLG	067742
L10026	022344	HAPT6	070744	PAR.LA	057474	SEKMS	007664	G	TRPHN	021350
L10027	022374	MASK.B	047616	PASS.C	040530	SET.MA	045672	G	TRYPNC	002264
L10030	022530	MASK.W	047614	PRINTC	061320	SFTPRM	037730	G	TST.AB	047730
L10031	022530	MAXCYL	002212	PRINTF	064626	SHIFT	070776	G	TST.TO	042132
L10032	022662	MAXSEC	002206	PRIOR	000000	SIGN	000004	G	TYPEC	060336
L10033	023026	MDHEDR	002000	PRI00	000000	SINBCC	021064	G	TYPC	054232
L10034	023026	NEM.SI	045000	PRI01	000040	SIZE	000004	G	TYVFLA	063624
L10035	023520	NERLMT	016770	PRI02	000000	SKHOME	010303	G	TYPLIN	060234
L10036	023710	MIN.IN	040572	PRI03	000140	SMSG	040030	G	TYPNUM	057616
L10037	024106	MIN.US	040574	PRI04	000200	SPEC.U	045406	G	TYPSTR	060254
L10040	024260	MK	000001	PRI05	000340	SPFCOD	018784	G	TYPER	054062
L10041	024456	MODR	070256	PRI06	000300	SPY.SE	000400	G	TYUN	047074
L10042	024456	MSCRFL	007313	PRI07	000340	START	017244	G	TSARGC	000004
L10043	025026	MSG.AD	040540	PRNTST	061210	STARTC	067666	G	TSCODE	004052
L10044	025130	MSG.TY	040514	PRO.CM	045460	STARTI	017206	G	TSCQ	000062
L10045	025254	MUL	070212	PUB.S	041000	STHS	000100	G	TERRR	000026
L10046	025704	MXSEC1	007377	PUBCHR	060024	STRCHR	060700	G	TERRR	000040
L10047	025704	NEWPT	062446	PWR.FR	002304	STRTR	045464	G	TFLAG	000040
L10050	025736	NEXTAR	064176	PWR.FL	071150	ST.SET	042316	G	TSHLI	000020
L10051	026056	NOOPO	000000	PWR.PL	040604	SUNIT	045470	G	TSLI	000001
L10052	026236	NOPIHT	007430	PWR.SA	071276	SUPERV	043352	G	TSLSYM	010000
L10053	027014	NOPMES	007377	PWR.SB	063772	SUPLA	040762	G	TSLV	000000
L10054	027100	NORPR	017166	PWR.UP	071374	SUPV.T	040762	G	TSS	000000
L10055	027354	NORRY	007070	P.CLK	045120	SUP.PR	042070	G	TSSK1	000005
L10056	027470	NORES	007052	RDDINT	010076	SVCGBL	000000	G	TSSK2	000003
L10057	027646	NO.CLK	045102	RDDMES	010045	SVCHAN	050006	G	TSSK3	000003
L10060	030246	NO.FLA	063772	RDHDR	000010	SVCLNS	000000	G	TSSAVL	177777
L10062	030670	NO.PT	045306	RDNHD	010247	SVCSUB	000000	G	TSSAVL	177777
L10063	031776	NO.PTA	045306	RDNINT	010247	SVCTAG	000000	G	TSSAVL	177777
L10064	032430	NR	000000	RDNMES	010213	SVCTST	177777	G	TSSSKO	010000
		NUMBIN	054434	READ	000014	SVHD	002214	G	TSSSK1	010001
									TSSSK2	010002

TSSUBN	= 000000	T11	024110	G	T34	033274	G	USER.P	040774	G	XEQ.OP	047310
TSTAGI	= 177777	T12	024262	G	T35	033564	G	USER.T	040776	G	XEQ.PR	042510
TSTAGN	= 010107	T13	024460	G	T36	034060	G	UIT	002136	G	XEQ.TE	047354
TSTEMP	= 000000	T14	024650	G	T37	034324	G	VALID	041234	G	XMER	002152
TSTEST	= 000057	T15	024840	G	T38	034724	G	VALD	042054	G	XPDLY	002152
TSTEST	= 177777	T16	025132	G	T39	035244	G	VAL.SW	045520	G	XTIME	066676
TSTST	= 000001	T17	025256	G	T4	022376	G	VECMSC	037712	G	XTIME	067522
TSSCL	= 010023	T18	025452	G	T40	025604	G	VECT	000002	G	XTIMST	066726
TSDU	= 010024	T19	025606	G	T41	036116	G	WCKINT	007522	G	XXOP.D	045064
TSHAR	= 010105	T20	025740	G	T42	036406	G	WCKMES	007462	G	XXX	017266
TSSIN	= 010022	T21	026060	G	T43	036672	G	WHY	002126	G	XSLWA	000000
TSSMS	= 010017	T22	026240	G	T44	036762	G	WIDTH	055002	G	XSPALS	000040
TSSSEC	= 010000	T23	027016	G	T45	037114	G	WRCHK	000002	G	XSPFFS	000040
TSSSD	= 010106	T24	027166	G	T46	037312	G	WRITE	000012	G	XSTRUE	000020
TSSSR	= 010025	T25	027362	G	T47	037452	G	WLOCK	010331	G	XSTRG	045560
TSSSM	= 010021	T26	027472	G	T6	022664	G	WRINT	010161	G	XSTRAD	067776
TSSSTE	= 010104	T27	027650	G	T7	023022	G	WRTRDY	021276	G	SSAV2	071042
T.CNTL	002306	T28	030250	G	T8	023220	G	WTRDY	021236	G	SSAV3	071074
T.CRC	002124	T29	030672	G	T9	023522	G	XEQDIA	070024	G	SSAV4	071074
T.DMP	016774	T30	032246	G	UNITST	002140	G	XEQSUB	070012	G	SSAV5	071114
T.DWT	016772	T31	032300	G	UNIT.D	040532	G	XEQ.CL	047534	G	SSAV5	071120
T.SIZE	016772	T32	032000	G	UNI.HA	045410	G	XEQ.CH	045044	G		
T1	021632	T33	032432	G	UOPIHN	002276	G	XEQ.IN	047216	G		
T10	023712		033042	G	UOPIHM	002274	G	XEQ.LA	043306	G		

. ABS. 071320 000

ERRORS DETECTED: 0

DSKZ: CZRLBB.DSKZ: CZRLBB-CZRLBB/ML,CZRLBB.P11,CZRLBB.SUP
 RUN-TIME: 66 62 1 SECONDS
 RUN-TIME RATIO: 323/130=2.4
 CORE USED: 16K (31 PAGES)