

B 9489

MINI - DISK

**MAINTENANCE
TEST
ROUTINES**

Burroughs 
FIELD ENGINEERING

FIELD ENGINEERING PROPRIETARY DATA

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COMPASS

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ENTRY	INSTRUCTION	AGREE	DISGREE		
	<p>SEQUENTIAL USE OF THIS DIAGNOSTIC IS RECOMMENDED SINCE EACH DECISION MAY BE BASED ON A PREVIOUS RESULT. WHERE A FAULT CONDITION MAY BE CAUSED BY MORE THAN ONE COMPONENT, THEY ARE LISTED IN THE ORDER OF MAXIMUM PROBABILITY. BEFORE COMMENCING THIS ROUTINE A VISUAL CHECK SHOULD BE MADE TO ENSURE THERE ARE NO OBVIOUS FAULT CONDITIONS SUCH AS BROKEN OR LOOSE WIRING ETC.</p> <p>THE MAJORITY OF LOGIC LEVELS ARE STANDARD TTL LEVELS AS LISTED BELOW:</p> <p>HIGH LEVEL IS + 2.5V to + 5.5 V</p> <p>LOW LEVEL IS 0V TO 0.7V.</p> <p>EXCEPTIONS TO THE ABOVE VOLTAGES WILL BE FOUND IN THE READ CHANNEL WHERE SIGNALS MAY HAVE A DC BIAS OF +2.5V TO +3.5V AND AN AC PEAK TO PEAK VOLTAGE OF 2.7V TO 3.7V SUPERIMPOSED UPON THIS DC LEVEL.</p> <p>THE 1ST ROUTINE ASSUMES THAT THE FOLLOWING VOLTAGES ARE PRESENT AND WITHIN $\pm 10\%$ OF DC NOMINAL VALUES SHOWN BELOW:</p> <p>+ 24V DC + 12 V DC - 12V DC + 5V DC</p> <p>+ 24V GND + 12 V GND - 12 V GND + 5 V GND</p> <p>UNLESS OTHERWISE INSTRUCTED, REMOVE ALL JUMPERS</p> <p>AFTER COMPLETING EACH SECTION.</p>				

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ENTRY	INSTRUCTION	AGREE	DISGREE		
V00010	<p>REFERENCE TO ADJUSTMENT GUIDE SHOULD BE MADE IF ANY DISAGREEMENTS ARE OBSERVED</p> <p><u>SECTION V00</u></p> <p><u>P.C.B. MOUNTING</u></p> <p>THE P.C.B. SHALL BE LOCATED IN THE SLOTS PROVIDED IN THE REAR OF THE FACIA&SECURED BY FOUR SCREWS TO THE BASE CASTING. THE P.C.B CLAMPING HOLE UNDER THE MAINS CONNECTOR BLOCK IS USED AS AN EARTHING POINT FOR THE P.C.B. TO THE CASTING.</p>		V01010		

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ENTRY	INSTRUCTION VISUAL INSPECTION OF DRIVE	AGREE	DISGREE	
	REFERENCE TO ADJUSTMENT GUIDE SHOULD BE MADE IF ANY DISAGREEMENTS ARE OBSERVED			
	<u>SECTION V01</u>			
V01010	<u>PLUG CONNECTIONS</u>	V01020		
V01020	POLARIZING KEYS ARE REQUIRED ON THE FOLLOWING SOCKET TERMINALS:-			
	a) LOWER HEAD (HD1) - POS ⁿ 2 BETWEEN R & GR WIRES			
	b) UPPER HEAD (HDO) - POS ⁿ 4 BETWEEN GR & BL WIRES			
	c) STEP MOTOR - POS ⁿ 4 BETWEEN OR & BL WIRES	V01030		
V01030	SOCKETS SHALL BE CONNECTED TO PCB AS FOLLOWS:-			
	a) RIBBON CABLE (OUTSIDE WIRES ARE BR (PIN 1) & BL (PIN 20) TO PCB CONNECTOR 4.			
	b) STEPPING MOTOR TO PCB CONN. 5			
	c) LOWER (HD1) HEAD TO PCB CONN. 6			
	d) UPPER (HDO) HEAD TO PCB CONN 7	V02010		

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ENTRY	INSTRUCTION VISUAL INSPECTION OF DRIVE	AGREE	DISGREE	
	REFERENCE TO ADJUSTMENT GUIDE SHOULD BE MADE IF ANY DISAGREEMENTS ARE OBSERVED			
	<u>SECTION V02</u>			
V02010	<u>SOLENOIDS</u>			
	UPPER (LOWER) SOLENOID			
	ENSURE THE UPPER (LOWER) SOLENOID IS SECURELY MOUNTED TO THE CARTRIDGE RECEIVER (BASE CASTING) & THE LIFT ARM SECURELY ATTACHED TO THE SOLENOID	V02020		
V02020	WITH THE UNIT DOOR CLOSED CHECK OVER THE FULL RANGE OF CARRIAGE MOVEMENT & ENSURE CLEARANCE EXISTS BETWEEN PIN ON PAD ARM & LIFT ARM WHEN SOLENOID IS IN THE ENERGISED POSITION.			
	THE PIN SHOULD NOT STRIKE THE BACK FACE OF THE LIFT ARM IN EITHER THE ENERGISED OR UNENERGISED POSITIONS	V02030		
V02030	THE PRESSURE PAD IS LIFTED CLEAR OF THE LOWER (UPPER) HEAD WHEN THE SOLENOID IS UNENERGISED.	V02040		
V02040	MAKE SURE THE TWO WIRES ARE PUSHED FIRMLY ONTO THE SOLENOID TERMINALS (WHICH SHOULD BE PARALLEL TO THE SOLENOID MTG)	V02050		
V02050	ENSURE THAT PIN ON THE PAD ARM EXTENDS AT LEAST HALFWAY ACROSS THE LIFTING FACE OF THE LIFT ARM WHEN THE SOLENOID IS UNENERGISED	V03010		

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ENTRY	INSTRUCTION VISUAL INSPECTION OF DRIVE	AGREE	DISGREE		
	REFERENCE TO ADJUSTMENT GUIDE SHOULD BE MADE IF ANY DISAGREEMENTS ARE OBSERVED				
	<u>SECTION VO3</u>				
VO3010	<u>POSITIONER</u> THERE SHOULD BE A CLEARANCE OF 0.030 INCH MINIMUM, 0.120 INCH MAXIMUM BETWEEN THE BACKLASH NUT AND THE REAR OF THE CARRIAGE BLOCK BUSH.	VO3020			
VO3020	THE BACKLASH NUT LOCKING PLATE SHOULD NOT TOUCH THE BOTTOM OF THE NOTCH IN THE NUT. THERE SHOULD BE CLEARANCE (.020 MIN) BETWEEN NUT & REAR BUSH OF CARRIAGE	VO3030			
VO3030	OPENING & CLOSING THE DOOR & CART REC ^T SHOULD RAISE & LOWER THE UPPER HEAD ARM. CHECK THAT THE UPPER HEAD ARM ALWAYS REGISTERS BACK ONTO CARRIAGE BLOCK WHEN THE DOOR IS CLOSED (NO STICTION)	VO3040			
VO3040	ENSURE THE TM.00 VANE DOES NOT STRIKE THE TRACK 00 TRANSDUCER	VO3050			
VO3050	PRESS. PAD LOADS 12 -16 GM	VO4010			

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ENTRY	INSTRUCTION VISUAL INSPECTION OF DRIVE	AGREE	DISGREE		
	REFERENCE TO ADJUSTMENT GUIDE SHOULD BE MADE IF ANY DISAGREEMENTS ARE OBSERVED				
	<u>SECTION VO4</u>				
VO4010	<u>CARTRIDGE REC^T</u> ENSURE THE CART REC ^R IS PUSHED FULLY ONTO THE PIVOT DOWELS FITTED TO CASTING	VO4020			
VO4020	FOAM PADS MUST BE FITTED TO REC ^T	VO4030			
VO4030	REC ^T SHOULD NOT "BOTTOM OUT" ON CASTING AT FRONT OF UNIT (NEAR DOOR)	VO4040			
VO4040	BOTH INDEX/SECTOR & W.I.H. TRANSDUCERS MUST BE SECURELY FITTED TO REC ^T	VO4050			
VO4050	DISK PRESENT MICROSWITCH. OBSERVE THAT THE BUTTON OF THE MICROSWITCH IS IN THE DOWN(CLOSED)POSITION) WITH THE DOOR CLOSED & NO DISK FITTED	VO4060			
VO4060	OPEN DOOR, INSERT A DISK, OBSERVE THAT THE DISK DOES NOT HIT THE TOP HEAD (ADJUST DISK GUIDE IF IT DOES) OR THE LOWER PRESSURE PAD (WRONG ADJUSTMENT ON LOWER SOLENOID ASSY)	VO5010			

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ENTRY	INSTRUCTION	VISUAL INSPECTION OF DRIVE	AGREE	DISGREE
VO5010	REFERENCE TO ADJUSTMENT GUIDE SHOULD BE MADE IF ANY DISAGREEMENTS ARE OBSERVED <u>SECTION VO5</u> <u>BELT & PULLEY</u> VERIFY THAT BELT IS FITTED CORRECTLY I.E. RIBBED SIDE AWAY FROM PULLEY (WILL HAVE PART NO MARKED ON THAT SIDE) AND ENSURE BELT IS NOT TWISTED BETWEEN THE MOTOR & SPINDLE PULLEYS. <u>NOTE:</u> PULLEY CONFIGURATION ON MOTOR 60 HZ SUPPLY :- SMALL PULLEY 50 HZ SUPPLY :- LARGE PULLEY			
			NO0010	

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ENTRY	INSTRUCTION	CONFIDENCE TEST PATH.	AGREE	DISGREE
NO0001	<u>SECTION OO INPUT INTERFACE</u> SELECT DRIVE TO BE TESTED. INSERT DISK CLOSE DOOR CHECK THAT 'FILE OPERATIONAL' LAMP IS LIT.			
NO0002	CHECK AT I/C 59 PIN 6 FOR A LOGIC '0'		NO0002	EO0001
NO0003	CHECK TP 8/7 FOR A LOGIC '1'		NO0003	NO1000
NO0004	CHECK AT I/C 59 PIN 8 FOR A LOGIC '0'		NO0004	EO0003
			NO0010	NO1000

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ENTRY	INSTRUCTION	CONFIDENCE	TEST PATH	AGREE	DISAGREE
	<u>SECTION 00 INPUT INTERFACE</u>				
N00010	(UNIT SELECT)				
	SELECT MASTER DRIVE				
	CHECK THAT MASTER DRIVE IS SELECTED BY THE PRESENCE OF A LOGIC '1' AT I/C 18 PIN 5 ON MASTER BOARD.	N00011	E00010		
N00011	CHECK FOR A LOGIC '0' AT I/C 63 PIN 11, I/C 67 PIN 15 AND I/C 65 PIN 15	N00012	E00014		
N00012	REMOVE CONNECTOR FROM CONNECTOR 2 ON MASTER BOARD				
	CHECK FOR A LOGIC '0' AT I/C 63 PIN 11, I/C 67 PIN 15 AND I/C 65 PIN 15.	N00013	E00014		
	RECONNECT CONNECTOR 2.				
N00013	SELECT SLAVE DRIVE				
	CHECK THAT SLAVE DRIVE IS SELECTED BY THE PRESENCE OF A LOGIC '1' AT I/C 18 PIN 5 ON SLAVE BOARD	N00014	E00010		
N00014	CHECK FOR A LOGIC '0' AT I/C 63 PIN 11, I/C 67 PIN 15 AND I/C 65 PIN 15 ON MASTER BOARD	N00015	E00014		
N00015	REMOVE CONNECTOR FROM CONNECTOR 2 ON MASTER BOARD				
	CHECK FOR A LOGIC '1' AT I/C 63 PIN 11, I/C 67 PIN 15 AND I/C 65 PIN 15 ON MASTER BOARD		E00014		
	RECONNECT CONNECTOR 2	N00020			
N00020	(CAB DE-SELECT)				
	CONNECT I/C 66 PIN 3 TO GROUND POTENTIAL				
	CHECK ALL THE FOLLOWING SIGNALS ARE AT LOGIC '1'				

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ENTRY	INSTRUCTION	CONFIDENCE	TEST PATH	AGREE	DISAGREE
	ILL ADD/		JI-37 (I/C 63 PIN 3)		
	FILE OP/		JI-40 (I/C 59 PIN 6)		
	WIH/		JI-34 (I/C 59 PIN 11)		
	POSN SETT/		JI-39 (I/C 59 PIN 8)		
	SECTOR/		JI-35 (I/C 64 PIN 3)		
	INDEX/		JI-36 (I/C 64 PIN 6)		
	DATA CLK/		JI-2 (I/C 64 PIN 11)		
	RD DATA/		JI-4 (I/C 64 PIN 8)		
	REMOVE GND FROM I/C 66 PIN 3	N00030		E00020	
N00030	(SEEK)				
	SELECT MASTER DRIVE				
	ISSUE CONTINUOUS SEEK PULSES.				

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ENTRY	INSTRUCTION	AGREE	DISGREP	
	<u>SECTION 00 (CONT'D)</u>			
	MEASURE NEGATIVE PULSE WIDTHS OF SIGNAL AT I/C 60 PIN 8			
	MEASURE POSITIVE PULSE WIDTHS OF SIGNAL AT I/C 18 PIN 1			
N00031	ARE THE PULSE WIDTHS BETWEEN 0.5 us TO 5 us WIDE SELECT SLAVE DRIVE ISSUE CONTINUOUS SEEK PULSES	N00031	E00030	
	MEASURE NEGATIVE PULSE WIDTH OF SIGNAL AT I/C 60 PIN 8			
	MEASURE POSITIVE PULSE WIDTH OF SIGNAL AT I/C 18 PIN 1			
N00040	ARE THE PULSE WIDTHS EQUAL HEAD SELECT SELECT MASTER DRIVE SELECT HEAD '0'	N00040	E00031	
N00041	CHECK FOR A LOGIC '0' AT I/C 32 PIN 12 SELECT HEAD '1'	N00041	E00040	
N00042	CHECK FOR A LOGIC '1' AT I/C 32 PIN 12 SELECT SLAVE DRIVE SELECT HEAD '0'	N00042	E00040	
N00043	CHECK FOR A LOGIC '0' AT I/C 32 PIN 12 SELECT HEAD '1'	N00043	E00042	
N00050	CHECK FOR A LOGIC '1' AT I/C 32 PIN 12 WRITE ENABLE SELECT MASTER DRIVE SELECT HEAD '0'	N00050	E00042	
	SET TO WRITE DISABLE MODE (IE WRT EN/IS LOGIC '1') CONDITION MAY BE OBTAINED BY REMOVING CONNECTOR 1.			
N00051	CHECK THAT I/C 44 PIN 11 IS AT LOGIC '0' SET TO WRITE ENABLE MODE (I.E. WRT EN/IS AT LOGIC '0') CONDITION MAY BE OBTAINED BY CONNECTING GND TO CONN 1 PIN 46	N00051	E00050	
	CHECK THAT I/C 44 PIN 11 IS AT LOGIC '1'	N00052	E00050	

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ENTRY	INSTRUCTION	AGREE	DISGREP	
N00052	SELECT SLAVE DRIVE SELECT HEAD '0' SET TO READ MODE CHECK THAT I/C 44 PIN 11 IS AT LOGIC '0'	N00053	E00052	

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ENTRY	INSTRUCTION	CONFIDENCE TEST PATH	AGREE	DISGREE	
	SECTION 00 (CONT'D)				
N00053	SET TO WRITE MODE				
	CHECK THAT I/C 44 PIN 11 IS AT LOGIC 'I'		N00060	E00052	
N00060	WRITE DATA				
	SELECT MASTER DRIVE				
	SELECT HEAD '0' AND TRACK '86'				
	WRITE AN ALL 'I's PATTERN AT EVERY SECTOR EACH REVOLUTION				
	CHECK AT TP 9/3 (a) FOR A LOGIC 'I' WHENEVER WRT.EN/ IS AT LOGIC 'O' (b) FOR A LOGIC 'O' WHENEVER WRT.EN/ IS AT LOGIC 'I'		N00061	E00060	
N00061	SELECT SLAVE DRIVE				
	SELECT HEAD '0' AND TRACK '86'				
	WRITE AN ALL 'I's PATTERN AT EVERY SECTOR EACH REV.				
	CHECK AT T.P. 9/3 (a) FOR A LOGIC 'I' WHENEVER WRT.EN/ IS AT LOGIC 'O' (b) FOR A LOGIC 'O' WHENEVER WRT.EN/ IS AT LOGIC 'I'		N00070	E00062	
N00070	READ ENABLE				
	SELECT MASTER DRIVE. SET TO READ MODE				
	CHECK THAT DURING READ MODE I/C 17 PIN 3 ON MASTER DRIVE IS AT LOGIC 'O'		N00071	E00070	
N00071	CHECK THAT I/C 17 PIN 3 ON SLAVE DRIVE IS AT LOGIC '1' CONTINUOUSLY		N00072	E00070	
N00072	SELECT SLAVE DRIVE. SET TO READ MODE				
	CHECK THAT DURING READ MODE I/C 17 PIN 3 ON SLAVE DRIVE IS AT LOGIC 'O'		N00073	E00074	
N00073	CHECK THAT I/C 17 PIN 3 ON MASTER DRIVE IS AT LOGIC '1' CONTINUOUSLY		N00074	E00070	
N00074	SELECT MASTER DRIVE. SET TO READ MODE				
	CHECK THAT DURING READ MODE T.P. 10/5 IS AT A LOGIC 'I'		N00075	E00070	
N00075	CHECK THAT DURING READ MODE I/C 47 PIN 4 IS AT A LOGIC 'O'		N00076	E00070	
N00076	SET TO READ DISABLE MODE (I.E. READ EN/ IS AT LOGIC 'I')				

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ENTRY	INSTRUCTION	CONFIDENCE TEST PATH	AGREE	DISGREE	
	SECTION 00 (CONT'D)				
	CHECK THAT T.P. 10/5 IS AT LOGIC 'O'		N00077	E00070	
N00077	CHECK THAT I/C 47 PIN 4 IS AT LOGIC '1'		N00080	E00070	
N00080	SELECT MASTER DRIVE				
	SELECT HEAD '0'				
	SELECT TRACK '00'				
	CHECK THE FOLLOWING POINTS FOR A LOGIC 'O' :-				
	I/C 46 PIN 4			E00080	
	I/C 46 PIN 6			E00090	
	I/C 46 PIN 8			E00100	
	I/C 46 PIN 10			E00110	
	I/C 46 PIN 16			E00120	
	I/C 46 PIN 18			E00130	
	I/C 46 PIN 20		N00090	E00140	
N00090	SELECT TRACK '63'				
	CHECK THE FOLLOWING POINTS FOR A LOGIC '1'				
	I/C 46 PIN 4			E00080	
	I/C 46 PIN 6			E00090	
	I/C 46 PIN 8			E00100	
	I/C 46 PIN 10			E00110	
	I/C 46 PIN 16			E00120	
	I/C 46 PIN 18		N00091	E00130	
N00091	CHECK AT I/C 46 PIN 20 FOR A LOGIC 'O'		N00100	E00140	
N00100	SELECT TRACK 64				
	CHECK THE FOLLOWING POINTS FOR A LOGIC 'O'				
	I/C 46 PIN 4			E00080	
	I/C 46 PIN 6			E00090	
	I/C 46 PIN 8			E00100	
	I/C 46 PIN 10			E00110	
	I/C 46 PIN 16			E00120	
	I/C 46 PIN 18		N00101	E00130	
N00101	CHECK AT I/C 46 PIN 20 FOR A LOGIC '1'		N00110	E00140	
N00110	SELECT SLAVE DRIVE. REPEAT AS FROM N00080		N00120		
N00120	INPUT INTERFACE IS FUNCTIONING CORRECTLY		N01000		

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ENTRY	INSTRUCTION	CONFIDENCE TEST PATH	AGREE	DISGREE	
	SECTION 01 OUTPUT INTERFACE				
NO1000	ILLEGAL ADDRESS:- SELECT DRIVE TO BE TESTED				
	SELECT TRACK '88'				
	CHECK AT I/C 63 PIN 3 FOR A LOGIC '0'		NO1010	E01010	
NO1010	FILE OPERATIONAL:-				
	SELECT DRIVE TO BE TESTED				
	CHECK AT I/C 59 PIN 6 FOR A LOGIC '0'		NO1020	E01020	
NO1020	WRITE INHIBIT:-				
	SELECT DRIVE TO BE TESTED. ENSURE A WRITE ENABLED DISK HAS BEEN INSERTED.				
	CHECK AT I/C 59 PIN 11 FOR A LOGIC '1'		NO1030	E01030	
NO1030	POSITION SETTLED:-				
	SELECT DRIVE TO BE TESTED				
	SELECT HEAD '0'. SELECT TRACK '00'				
	CHECK AT I/C 59 PIN 8 FOR A LOGIC '0'		NO1040	E01040	
NO1040	SECTOR:-				
	SELECT DRIVE TO BE TESTED:				
	CHECK AT I/C 64 PIN 3 FOR POSITIVE PULSES OF WIDTH				
	5MS \pm 0.15MS		NO1050	E01050	
NO1050	INDEX:-				
	SELECT DRIVE TO BE TESTED				
	CHECK AT I/C 64 PIN 6 FOR POSITIVE PULSES OF WIDTH				
	160 MS \pm 5MS'		NO1060	E01060	
NO1060	DATA CLOCK				
	SELECT DRIVE TO BE TESTED. SELECT HD '0' SELECT TRACK '01'				
	WRITE AN ALL 'IOIO' PATTERN CONTINUOUSLY ONTO DISK I.E. AT EVERY SECTOR EACH REV.				
	CHECK AT I/C 64 PIN 11 FOR A FREQUENCY OF 375 \pm 1 KHZ		NO1061	E01070	
NO1061	READ CONTINUOUSLY FROM DISK I.E. AT EVERY SECTOR EACH REV.				
	CHECK AT I/C 64 PIN 11 FOR A FREQUENCY OF 375 \pm 1KHZ		NO1070	E01071	
NO1070	READ DATA:-				
	SELECT DRIVE TO BE TESTED				

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ENTRY	INSTRUCTION	CONFIDENCE TEST PATH	AGREE	DISGREE	
	SECTION 01 (CONT'D)				
	SELECT HD '0' SELECT TRACK '01'				
	WRITE AN ALL 'IOIO' PATTERN ONTO DISK AT EVERY SECTOR				
	READ BACK FROM DISK I.E. AT EVERY SECTOR EACH REV.				
	CHECK AT I/C 64 PIN 8 FOR A FREQUENCY OF 187.5 \pm 1KHZ		NO1080	E01080	
NO1080	THE O/P INTERFACE CIRCUITRY IS FAILURE FREE		NO2010		

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ENTRY	INSTRUCTION	CONFIDENCE TEST PATH	AGREE	DISGREE	
N02010	SECTION 02 HD. LD. CONTROL SELECT DRIVE TO BE TESTED SELECT HEAD '1' DO NOT SELECT EITHER READ OR WRITE MODE				
N02020	CHECK AT T/P 5/5 FOR A LOGIC '1' CHECK THAT A SINGLE NEGATIVE GOING PULSE OF WIDTH 115 ± 15 MS IS PRESENT AT T.P. 5/5 UPON INITIALLY SELECTING EITHER READ OR WRITE MODE		N02020	E02010	
N02021	SELECT HEAD '0' DO NOT SELECT EITHER READ OR WRITE MODE		N02021	E02010	
N02022	CHECK AT T.P. 5/5 FOR A LOGIC '1' CHECK THAT A SINGLE NEGATIVE GOING PULSE OF WIDTH 115 ± 15 MS IS PRESENT AT T.P. 5/5 UPON INITIALLY SELECTING EITHER READ OR WRITE MODE ON BRDS WITH ASSY NOS 18807495, 18807487, 18812040, 18812057		N02022	E02010	
N02023	CHECK THAT A SINGLE NEGATIVE GOING PULSE OF WIDTH 85 ± 15 MS IS PRESENT AT T.P. 5/5 UPON INITIALLY SELECTING HEAD OR WRITE MODE ON BRDS, WITH ASSY NOS 18813840 OR 18813857		N02023	E02010	
N02030	CHECK THAT DURING READ OR WRITE MODE COLLECTOR OF Q13 IS AT LOGIC '0' AND COLLECTOR OF Q12 IS AT + 24 VOLTS		N02030	E02010	
N02040	SELECT HEAD '1' CHECK THAT DURING READ OR WRITE MODE THAT COLLECTOR OF Q13 IS AT +24 VOLTS AND COLLECTOR OF Q12 IS AT LOGIC '0'.		N02040	E02040	
N02050	THE HEAD LOAD CONTROL CCT IS FAILURE FREE		N02050 N03010	E02040	

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ENTRY	INSTRUCTION	CONFIDENCE TEST PATH	AGREE	DISGREE	
	SECTION 03 WRT. DATA ENCODER				
N03010	SELECT MASTER DRIVE. SET TO READ MODE. CONNECT T.P. 10/5 TO GRND				
N03020	CHECK AT T.P. 10/2 FOR A FREQUENCY OF 750 ± 1KHZ CONNECT T.P. 9/3 TO GROUND	N03020		E03010	
N03030	CHECK THAT THE INTERVAL BETWEEN THE POSITIVE EDGE OF THE SIGNAL AT T.P. 9/2 AND THE POSITIVE EDGE OF THE SIGNAL AT T.P. 9/4 IS 0.65 ± 0.1 US. REMOVE GND FROM T.P. 9/3 AND CONNECT GND TO I/C 66 PIN 5.	N03030		E03020	
N03040	CHECK THAT THE INTERVAL BETWEEN THE POSITIVE EDGE OF THE SIGNAL AT T.P. 9/2 AND THE POSITIVE EDGE OF THE SIGNAL AT T.P. 9/4 IS 1.95 ± 0.1 US.	N03040		E03020	
N04010	THE WRITE DATA ENCODER IS FAILURE FREE.	N04010			

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ENTRY	INSTRUCTION	CONFIDENCE TEST PATH	AGREE	DISAGREE	
NO4010	SECTION 04' RD/WRT CHANNEL SELECT DRIVE TO BE TESTED SELECT HEAD '1' AND TRACK 86 CONNECT TP 10/5 AND TP 9/3 TO GROUND CONNECT I/C 1 PIN 9 TO GROUND CHECK AT TP4 ,AND TP12, FOR A FREQUENCY OF 187.5 ± 1 KHZ		NO4020	EO4010	
NO4020	CHECK AT TP 21 FOR A FREQUENCY OF 375 ± 1 KHZ		NO4021	EO4250	
NO4021	FOR BOARDS WITH ASSEMBLY NOS 18807495 (SLV ASSY) 18807487 (MST ASSY) GO TO NO4030 FOR BOARD WITH ASSEMBLY NOS 18813840 (MST ASSY) 18813857 (SLV ASSY); 18812040 (MSTASSY), 18812057 (SLV ASSY) GO TO NO4022				
NO4022	SELECT TRACK '00'				
	CHECK THAT I/C 20 PINS 11 AND 8 ARE AT LOGIC '0'.		NO4023	EO4290	
NO4023	SELECT TRACK '32'				
	CHECK THAT I/C 20 PINS 11 AND 8 ARE AT A LOGIC '1'		NO4024	EO4300	
NO4024	SELECT TRACK '64'				
	CHECK THAT I/C 20 PINS 11 AND 8 ARE AT A LOGIC '1'		NO4030	EO4310	
NO4030	SELECT HEAD '0' AND TRACK 86 CHECK AT TP4 AND TP12 FOR A FREQUENCY OF 187.5 ± 1 KHZ		NO4040	EO4010	
NO4040	CHECK AT TP21 FOR A FREQUENCY OF 375 ± 1 KHZ		NO4040A	EO4250	
NO4040A	DETERMINE THE REVISION LEVEL OF THE P.C.B. FOR P.C.B.'s UP TO AND INCLUDING REV J GO TO NO4043 FOR P.C.B.'s FROM REV K AND ONWARDS GO TO NO4041				
NO4041	CHECK AT I/C 1 PIN 9 FOR A LOGIC '1' AND AT TP 1 FOR A D.C. LEVEL OF + 12 VOLTS ± 1.2 VOLTS		NO4042	EO4131	
NO4042	CHECK THAT T.P.I. GOES TO A D.C. LEVEL OF + 6 VOLTS ± 0.6 VOLTS 400 ± 40 μS AFTER CONNECTING I/C 1 PIN 9 TO GND		NO4050	EO413F	

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FORM C.R. 012

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ENTRY	INSTRUCTION	CONFIDENCE TEST PATH	AGREE	DISAGREE	
NO4043	CHECK AT I/C 1 PIN 9 FOR A LOGIC '1' AND AT TP1 FOR A DC LEVEL OF + 12 VOLTS ± 1.2 VOLTS.		NO4044	EO4138	
NO4044	CHECK THAT TP1 GOES TO A DC LEVEL OF + 6 VOLTS ± 0.6 VOLTS 290 - 840 μS AFTER CONNECTING I/C 1 PIN 9 TO GND		NO4050	EO4138	
NO4050	THE READ CHANNEL WRITE DRIVER ERASE GAP DELAY AND MAGNETIC HEADS ARE FAILURE FREE.		NO5010		

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FORM C.R. 012

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ENTRY	INSTRUCTION	CONFIDENCE TEST PATH	AGREE	DISAGREE	
	<u>SECTION 05 DATA DECODER</u>				
N05010	(a) DISCONNECT MASTER/SLAVE INTERCONNECT CABLE (CONN 2) (b) CONNECT PIN 11 OF CONN 2 TO OV (c) SELECT SLAVE ON CONTROL DEVICE (d) CONNECT TP 10/2 TO PIN 16 OF CONN 2				
	CHECK DUTY CYCLE OBTAINED AT IC 57 PIN 6 FOR DUTY CYCLE 20% - 30% (CALIBRATION MAY BE PERFORMED ON MEASURING INST. BY USING TP 9/4 AS AN ACCURATE 25% DUTY CYCLE SOURCE)	N05020	E05010		
N05020	SET CONTROLLER TO CONTINUOUS 'READ' MODE AND RUN MASTER DRIVE CHECK INTERVAL BETWEEN POS EDGE OF TP10/5 AND POSITIVE EDGE OF TP11/4 FOR 34-52µ sec	N05030	E05011		
N05030	SELECT MASTER DRIVE AND CONTINUOUS READ ON CONTROLLER. RUN DRIVE WITH WRITTEN DISK. SET COUNTER TO 'START COUNT' ON POS EDGE OF TP 10/5 AND 'STOP COUNT' ON NEG EDGE OF TP 10/5				
	CHECK FOR COUNT OF 5-50 ON TP 10/3 (NEG EDGES)	N05040	E05012		
N05040	RESELECT 'SLAVE DRIVE' AND DESELECT 'READ'				
	CHECK TP 10/4 FOR 750KHZ ± 2KHZ	N05050	E05070		
N05050	CONNECT TP 10/3 TO OV				
	CHECK IC 54 PIN 6 FOR LOGIC '0'	N05060	F05250		
	DISCONNECT TP 10/3 FROM OV				
N05060	CHECK TP 11/2 FOR DUTY CYCLE 46.88% ± 0.1% AND FREQUENCY 375KHz ± 2KHz (CALIBRATION MAY BE PERFORMED ON MEASURING INST. BY USING TP 9/2 FOR ACCURATE 50.0% DUTY CYCLE)	N05070	E05040		
N05070	CHECK TP 11/3 FOR DUTY CYCLE 26.63% ± 0.1% (CALIBRATION MAY BE PERFORMED ON MEASURING INST. BY USING TP 9/4 FOR ACCURATE 25.0% DUTY CYCLE)	N05080	E05110		
N05080	CONNECT TP 10/1 TO OV CONNECT TP 9/5 TO OV CHECK TP 11/2 FOR FREQUENCY 325-425 KHZ REMOVE ABOVE CONNECTIONS	N05090	E05130		

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ENTRY	INSTRUCTION	CONFIDENCE TEST PATH	AGREE	DISAGREE	
	<u>SECTION 05 (CONT)</u>				
N05090	CHECK IC 60 PIN 13 FOR FREQUENCY OF 375KHZ ± 2 KHZ	N05100	F05170		
N05100	CHECK IC 60 PIN 13 FOR DUTY CYCLE OF 25% (SEE N05010 FOR CALIBRATION OF DUTY CYCLE METER)	N05110	F05170		
N05110	CHECK TP 10/6 FOR LOGIC '1'	N05120	F05180		
N05120	REMOVE CONNECTION FROM TP 10/2 TO PIN 16 OF CONN 2. CHECK TP 10/6 FOR LOGIC '0'	N06010	F05180		

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ENTRY	INSTRUCTION	CONFIDENCE TEST PATH	AGREE	DISGREE	
	<u>SECTION 06 INDEX/SECTOR, UP-TO-SPEED</u>				
NO6010	INSERT DISK & CLOSE DOOR				
	CHECK TP 8/4 FOR POSITIVE GOING PULSES OF WIDTH 5 msec \pm 0.15 msec.		NO6015	EO6010	
NO6015	CHECK THAT DISK ROTATES CORRECTLY AND CORRECT SIDE OF PULLEY IS IN OPERATION FOR MAINS FREQ. PRESENT		NO6020	FO6400	
	(LARGE DIA 50 HZ)				
	(SMALL DIA 60 HZ)				
	CHECK BELT IS FITTED CORRECTLY WITH SOFT-SIDE TOWARDS PULLEY SURFACE.				
NO6020	CHECK TP 8/5 FOR POSITIVE GOING PULSES OF WIDTH 160 msec \pm 5 msec		NO6030	EO6050	
NO6030	CHECK IC 26 PIN 6 FOR NEGATIVE GOING PULSES OF WIDTH 160 msec \pm 5 msec		NO6040	FO5060	
NO6040	OPEN DOOR, CHECK TP 5/3 FOR LOGIC '0'		NO6050	EO6110	
NO6050	CHECK IC 22 PIN 8 FOR LOGIC '1'		NO6060	FO6130	
NO6060	INSERT DISK AND CLOSE DOOR, CHECK TP 5/3 FOR LOGIC '1'		NO6070	EO6130	
NO6070	CHECK FOR LOGIC '0' ON IC 22 PIN 8		NO7010	FO6130	

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ENTRY	INSTRUCTION	CONFIDENCE TEST PATH	AGREE	DISGREE	
	<u>SECTION 07 POSITIONER CLOCK</u>				
NO7010	REMOVE STEPPER MOTOR CONNECTOR (CONN5)				
	MOVE CARRIAGE BLOCK TOWARDS SPINDLE				
	INSERT DISK AND CLOSE DOOR				
	CONNECT TP 6/3 TO OV				
	CHECK TP 6/2 IS AT LOGIC 1		NO702C	FO7010	
NO7020	DETERMINE THE REVISION LEVEL OF THE P.C.B.				
	FOR P.C.B.'s UP TO AND INCLUDING REV J GO TO NO7030				
	FOR P.C.B.'s FROM REV K AND ONWARDS GO TO NO7021				
NO7021	CHECK AT I/C 1 PIN 9 FOR A LOGIC '1' AND A T.P.1				
	FOR A D.C. LEVEL OF + 12 VOLTS \pm 1.2 VOLTS		NO7022	EO4131	
NO7022	CHECK AT I/C 39 PIN 3 FOR A SINGLE NEGATIVE GOING PULSE OF LENGTH 400 \pm 40 US WHEN I/C1 PIN 5 IS CONNECTED TO GND.		NO7023	EO4131	
NO7023	CHECK TP 6/5 FOR POSITIVE PULSES OF FREQUENCY 190 - 246 HZ.		NO7024	EO7010	
NO7024	CHECK TP 6/5 FOR PULSE WIDTH 100 nS - 190 nS		NO7025	FO7080	
NO7025	CONNECT I/C 39 PIN 3 TO GND				
	CHECK TP 6/5 FOR A LOGIC '0'		NO7040	FO7030	
NO7030	CHECK TP 6/5 FOR POSITIVE PULSES OF FREQUENCY 190 - 246 HZ.		NO7031	EO7010	
NO7031	CHECK TP 6/5 FOR PULSE WIDTH 290 - 840 US		NO7040	FO7080	
NO7040	OPEN DOOR. CHECK TP 6/5 FOR A FREQUENCY OF 117-177 Hz.		NO7050	EO7080	
NO7050	CONNECT TP 6/2 TO OV				
	CHECK TP 6/5 FOR LOGIC '0'		NO7055	FO7030	
	REMOVE LINK ON TP 6/2				
NO7055	REPLACE STEPPER MOTOR PLUG (CONN 5)				
	REMOVE LINK IN NO7010, CLOSE DOOR				
	CHECK TP 6/3 FOR LOGIC '1'		NO7060	FO7130	
NO7060	CHECK IC 42 PIN 12 FOR LOGIC '0'		NO8010	FO7060	

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ENTRY	INSTRUCTION	CONFIDENCE TEST PATH	AGREE	DISG FEE	
	<u>SECTION 08 WRITE INHIBIT, SEEK GATING, ILLEGAL ADD AND SETTLING TIME DELAY.</u>				
N08010	REMOVE DISK AND CLOSE DOOR				
	CHECK THAT RED INDICATOR LAMP ON FRONT PANEL IS NOT LIT		N08020	E08010	
N08020	INSERT WRITE ENABLED DISK (WITHOUT HOLE PUNCHED IN CORNER) AND CHECK THAT RED LAMP IS LIT.		N08030	E08050	
	(SEEK GATING)				
N08030	SELECT DRIVE, DO NOT SET 'WRITE EN'				
	CHECK TP 8/2 FOR NEGATIVE PULSE OF DURATION 500nsec - 1/2nsec ON OCCURANCE OF EACH 'SEEK' PULSE ISSUED		N08031	E08100	
N08031	SELECT 'WRITE ENABLE'				
	CHECK THAT NO PULSES OCCUR ON TP 8/2 WHEN SEEK PULSES ARE APPLIED. DESELECT 'WRITE ENABLE'		N08032	E08291	
N08032	DESELECT DRIVE UNDER TEST				
	CHECK THAT NO PULSES OCCUR ON TP8/2 WHEN SEEK PULSES ARE APPLIED		N08040	F08340	
N08040	SEEK TRACK 1, CHECK IC 26 PIN 3 FOR LOGIC '1'		N08041	E08500	
N08041	SEEK TRACK 2, CHECK IC 26 PIN 3 FOR LOGIC '1'		N08042	E08540	
N08042	SEEK TRACK 4, CHECK IC 26 PIN 3 FOR LOGIC '1'		N08043	E08550	
N08043	SEEK TRACK 8, CHECK IC 26 PIN 3 FOR LOGIC '1'		N08044	E08570	
N08044	SEEK TRACK 16, CHECK IC 26 PIN 3 FOR LOGIC '1'		N08045	E08580	
N08045	SEEK TRACK 32, CHECK IC 26 PIN 3 FOR LOGIC '1'		N08046	E08600	
N08046	SEEK TRACK 64, CHECK IC 26 PIN 3 FOR LOGIC '1'		N08050	E08150	
N08050	SELECT HEAD 1 AND ISSUE 'SEEK'				
	CHECK IC 32 PIN 9 FOR LOGIC '1'		N08070	F08150	
N08070	SELECT HEAD '0' DO NOT ISSUE SEEK				
	CHECK IC 32 PIN 9 FOR LOGIC '1'		N08080	F08150	
N08080	ISSUE SEEK				
	CHECK IC 32 PIN 9 FOR LOGIC '0'		N08090	F08150	
N08090	SELECT TRACK 00, DO NOT ISSUE SEEK		N08100	F08360	
	CHECK IC 26 PIN 3 FOR LOGIC '1'				
N08100	ISSUE SEEK		N08120	F08170	
	CHECK IC 26 PIN 3 FOR LOGIC '0'				


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ENTRY	INSTRUCTION	CONFIDENCE TEST PATH	AGREE	DISG FEE	
	<u>SECTION 08 (CONT'D)</u>				
	<u>(ILLEGAL ADD)</u>				
N08120	SELECT TRACK ADDRESS 88, CHECK IC 16 PIN 5 FOR LOGIC '0'		N08130	F08300	
N08130	ISSUE SEEK, CHECK IC 16 PIN 5 FOR LOGIC '1'		N08140	E08300	
N08140	SELECT ADD 96, CHECK IC 16 PIN 5 FOR LOGIC '1'		N08150	E08300	
N08150	IS OPERATION OF ILL ADD DET. THOUGHT TO BE CORRECT.		N08170	E08300	
	<u>(SETTLING TIME DELAY)</u>				
N08170	CONNECT TP 6/2 TO OV				
	CHECK TP 5/6 FOR LOGIC '1'		E08180	F07100	
N08 30	ISSUE A SEEK PULSE				
	CHECK AT I/C 33 PIN 6 FOR A NEGATIVE PULSE OF WIDTH 100 - 160 msec FOR BRDS WITH ASSY NOS 18807495 (SLV), 18807487 (MST), 18812040 (MST) 18812057 (SLV), OR A PULSE OF WIDTH 47 - 60 msec FOR BRDS WITH ASSY NOS 18813840 (MST), 18813857 (SLV)		N08190	E07100	
N08190	REMOVE LINK IN N07070				
	CONNECT TP 5/6 TO OV				
	CHECK THAT IC 33 PIN 6 IS AT A LOGIC '1' WITH SEEK PULSES APPLIED TO DRIVE				
	REMOVE LINK FROM TP 5/6		N09010	F07140	


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ENTRY	INSTRUCTION	CONFIDENCE TEST PATH	AGREE	DISGREE	
	<u>SECTION 09 POSITIONER (SPEED FAIL MOVEMENT)</u>				
N09010	OPEN DOOR				
	CHECK STEPPER MOTOR CAN BE ROTATED BY HAND TOWARDS SPINDLE WITH NO ENERGISATION OF STEPPER MOTOR		N09020	E09040	
N09020	ROTATE STEPPER MOTOR				
	SHAFT UNTIL HEADS ARE APPROX IN CENTRE OF DISK				
	CHECK 'FILE OP' INDICATOR 15 NOT LIT		N09030	E09010	
N09030	REMOVE STEPPER MOTOR PLUG				
	ROTATE STEPPER MOTOR SHAFT UNTIL CARRIAGE BLOCK TOUCHES CASTING.				
	CHECK TP 7/5, 7/4, 7/3 FOR FREQUENCY 39-59Hz		N09040	E09070	
N09040	CHECK DUTY CYCLE OF ABOVE TO BE 23-43%		N09050	E09070	
N09050	CHECK THAT INTERVAL BETWEEN POSITIVE EDGE ON TP 7/4 AND POSITIVE EDGE ON TP 7/5 IS 5.6-8.5msec		N09060	E09070	
N09060	RECONNECT STEPPER MOTOR PLUG WHILE CHECKING FOR SMALL MOVEMENT OF CARRIAGE TOWARDS SPINDLE		N10010	E09560	


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ENTRY	INSTRUCTION	CONFIDENCE TEST PATH	AGREE	DISGREE	
	<u>SECTION 10 POSITIONER INITIALIZATION</u>				
N10010	INSERT DISK ROTATE STEPPER MOTOR UNTIL HEADS ARE POSITIONED NEAR SPINDLE				
	CLOSE UNIT DOOR WHILST OBSERVING CARRIAGE BLOCK.				
	DOES CARRIAGE BLOCK GO TOWARDS CASTING?		N10020	E10010	
N10020	DOES CARRIAGE STOP, LEAVING GAP BETWEEN CASTING AND REAR OF CARRIAGE?		N10030	E10200	
N10030	CHECK FILE OPERATIONAL LAMP IS LIT		N11010	E10300	


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ENTRY	INSTRUCTION	CONFIDENCE TEST PATH	AGREE	DISGFEE	
N11010	SECTION 11 WRITE EN. GATING INSERT WRITE ENABLED DISK AND CLOSE DOOR SET WRITE ENABLE ON CONTROLLER SELECT DRIVE TO BE TESTED CONNECT IC 12 PIN 6 TO OV CHECK IC 28 PIN 8 FOR LOGIC '0'		N11020	E11010	
N11020	REMOVE LINK FROM IC 12 PIN 6 CONNECT TP 8/7 TO OV CHECK IC 28 PIN 8 FOR LOGIC '1'		N11030	F11020	
N11030	CONNECT IC 12 PIN 6 TO OV AFTER EACH OPERATION SHOWN BELOW (*) CHECK IC 28 PIN 8 FOR LOGIC '1' *Deselect DRIVE SELECT DRIVE *Deselect 'WRITE ENABLE' ON CONTROLLER SELECT WRITE ENABLE *REMOVE DISK AND CLOSE DOOR REMOVE JUMPERS		N12010	F11020	

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ENTRY	INSTRUCTION	CONFIDENCE TEST PATH	AGREE	DISGFEE	
N12010	SECTION 12 POSITIONER NORMAL OPERATION INSERT DISK, CLOSE DOOR SEEK THE TRACKS BELOW IN THE ORDER SPECIFIED WHILST COUNTING THE NO. OF POSITIVE PULSES OCCURRING ON TP 6/5 (A) AS SHOWN BELOW. CHECK THE STATUS OF IC 35 (B) AS SHOWN BELOW.				
	<u>TRACK</u>	<u>TOTAL</u> <u>COUNT (A)</u>	<u>IC 35</u> <u>PIN 9 (B)</u>	<u>PIN 5 (B)</u>	
	0	0	LOGIC '0'	LOGIC '0'	A&B E12010 B F12040
	20	20	LOGIC '0'	LOGIC '1'	A&B E12010 B F12040
	85	85	LOGIC '1'	LOGIC '0'	A&B E12010 B F12040
	47	123	LOGIC '0'	LOGIC '1'	A & B E12020 B F12040
	19	151	LOGIC '1'	LOGIC '0'	A&B E12020 B F12040
	6	164	LOGIC '0'	LOGIC '0'	A&B E12020 B F12040
	88	164	LOGIC '0'	LOGIC '0'	A&B E12100 B F12040
	96	164	LOGIC '0'	LOGIC '0'	N12020 A&B E12100 B F 12040
N12011	SEEK TRACK 10 LOCATE HEADS AROUND TRACK 86 BY ROTATING STEPPER MOTOR SHAFT SEEK TO TRACK 0				
	ARE HEADS POSITIONED IN VICINITY OF TRACK 0?		N12020	E12025	
N12020	KEEP ONE HEAD LOADED AND ALTERNATE BETWEEN TK01 AND TK 02				
	CHECK TP 8/7 FOR NEGATIVE PULSE WIDTH 115-145 msec		N12030	E12110	

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ENTRY	INSTRUCTION <u>CONFIDENCE TEST PATH</u>	AGREE	DISGREE		
N12030	ISSUE 'SEEK' THEN 'READ' STOP READING FOR TIME GREATER THAN 5 SECONDS. ISSUE READ SIGNAL CHECKING TP 8/7 FOR NEGATIVE PULSE WITH 100-150 msec.	N12040	E12110		
N12040	ISSUE SEEK TO TRACK ZERO FROM TRACKS 1,2,3,6,12,86. DOES POSITION OF CARRIAGE BLOCK AT TRACK ZERO VARY?	N12050	F12200		
N12050	IS HEAD TRAVEL OVER DISK SURFACE FROM TRACK 0 TO 86 OVER CORRECT PORTION OF DISK?	N12060	F12200		
N12060	NO FAILURE FOUND BY THIS DIAGNOSTIC.				


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ENTRY	INSTRUCTION <u>FAILURE INVESTIGATION PATH</u>	AGREE	DISGREE		
	<u>SECTION 00</u>				
E00001	CHECK AT I/C 27 PIN 10 FOR A LOGIC '1'	F00220	E00002		
E00002	CONNECT I/C 59 PIN 6 TO GND	N00003			
E00003	CONNECT I/C 59 PIN 8 TO GND	N00010			


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ENTRY	INSTRUCTION	FAILURE INVESTIGATION PATH	AGREE	DISGREE	
	<u>SECTION 00</u>				
E00010	SELECT MASTER DRIVE		E00011	F00010	
	CHECK FOR A LOGIC '1' AT I/C 66 PIN 1		E00012	F00020	
E00011	CHECK FOR A LOGIC '0' AT I/C 66 PIN 2		E00013	F00010	
E00012	SELECT SLAVE DRIVE		F00030	F00020	
	CHECK FOR A LOGIC '0' AT I/C 66 PIN 1		E00015	F00021	
E00013	CHECK FOR A LOGIC '1' AT I/C 66 PIN 2		E00016	F00020	
E00014	SELECT SLAVE DRIVE. REMOVE CONNECTOR 2 ON MASTER BRD. CONNECT I/C 63 PIN 12 AND 13 TO GND. CHECK FOR A LOGIC '1' AT I/C 63 PIN 11, I/C 67 PIN 15 AND I/C 65 PIN 16		F00020	F00021	
E00015	REMOVE GROUND FROM I/C 63 PIN 12 AND 13 CHECK FOR A LOGIC '1' AT I/C 63 PIN 11		E00021	F00040	
E00016	SELECT MASTER DRIVE. CHECK FOR A LOGIC '0' AT I/C 63 PIN 11, I/C 67 PIN 15 AND I/C 65 PIN 16. REPLACE CONNECTOR 2.		E00022	F00050	
E00020	CONNECT I/C 66 PIN 3 TO GROUND POTENTIAL CHECK FOR A LOGIC '1' AT I/C 66 PIN 4		E00023	F00040	
E00021	CHECK FOR A LOGIC '0' AT I/C 66 PIN 10		F00060	F00050	
E00022	REMOVE GROUND CONNECTION FROM I/C 66 PIN 3 CHECK FOR A LOGIC '0' AT I/C 66 PIN 4		F00080	F00070	
E00023	CHECK FOR A LOGIC '1' AT I/C 66 PIN 10		F00080	F00070	
E00030	SELECT MASTER DRIVE ISSUE CONTINUOUS SEEK PULSES CHECK FOR NEGATIVE PULSES OF WIDTH 0.5US to 1.5US AT I/C 60 PIN 8		E00041	F00100	
E00031	SELECT SLAVE DRIVE; ISSUE CONTINUOUS SEEK PULSES CHECK FOR POSITIVE PULSES OF WIDTH 0.5US to 5US AT I/C 18 PIN 1 ON SLAVE BRD.				
E00040	SELECT MASTER DRIVE SELECT HEAD '0' CONNECT I/C 66 PIN 13 TO GND. POTENTIAL CHECK AT I/C 66 PIN 12 FOR A LOGIC '1'				

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ENTRY	INSTRUCTION	FAILURE INVESTIGATION PATH	AGREE	DISGREE	
	<u>SECTION 00 CONT'D</u>				
E00041	REMOVE GND. FROM I/C 66 PIN 13 CHECK AT I/C 66 PIN 12 FOR A LOGIC '0'		F00090	F00100	
E00042	SELECT SLAVE DRIVE SELECT HEAD '0' CONNECT I/C 66 PIN 13 TO GND. POTENTIAL CHECK AT I/C 66 PIN 12 FOR A LOGIC '1'		E00043	F00100	
E00043	REMOVE GND. FROM I/C 66 PIN 13 CHECK AT I/C 66 PIN 12 FOR A LOGIC '0'		F00090	F00100	
E00050	SELECT MASTER DRIVE SELECT HEAD '0' SET TO WRITE DISABLE MODE (I.E. WRT.EN/IS AT LOGIC '1')		E00051	F00120	
	CONNECT I/C 60 PIN 6 TO GND. POTENTIAL CHECK AT I/C 60 PIN 4 FOR A LOGIC '1'		F00110	F00120	
E00051	REMOVE GND. FROM I/C 60 PIN 6 CHECK AT I/C 60 PIN 4 FOR A LOGIC '0' REMOVE ANY GND CONNECTIONS AND REPLACE CONNECTOR.		F00110	F00120	
E00052	SELECT SLAVE DRIVE SELECT HEAD '0' SET TO WRITE DISABLE MODE (I.E. WRT.EN/IS AT LOGIC '1')		E00053	F00120	
	CONNECT I/C 60 PIN 6 TO GND. POTENTIAL CHECK AT I/C 60 PIN 4 FOR A LOGIC '1'		F00110	F00120	
E00053	REMOVE GND. FROM I/C 60 PIN 6 CHECK AT I/C 60 PIN 4 FOR A LOGIC '0'		F00110	F00120	
E00060	SELECT MASTER DRIVE SELECT HEAD '0' SET TO READ MODE CONNECT I/C 66 PIN 5 TO GND. POTENTIAL		E00061	F00140	
	CHECK AT I/C 66 PIN 6 FOR A LOGIC '1'		F00130	F00140	
E00061	REMOVE GND. FROM I/C 66 PIN 5 CHECK AT I/C 66 PIN 6 FOR A LOGIC '0'				
E00062	SELECT SLAVE DRIVE SELECT HEAD '0' SET TO READ MODE CONNECT I/C 66 PIN 5 TO GND. POTENTIAL		E00063	F00140	
	CHECK AT I/C 66 PIN 6 FOR A LOGIC '1'				

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ENTRY	INSTRUCTION	FAILURE INVESTIGATION PATH	AGREE	DISGREE	
	SECTION 00 CONT'D				
E00063	REMOVE GND. FROM I/C 66 PIN 5		FO0130	FO0140	
FO0070	CHECK AT I/C 66 PIN 6 FOR A LOGIC '0'				
	SELECT MASTER DRIVE				
	SET TO READ DISABLE MODE (I.E. READ EN/IS AT LOGIC '1')				
	CONNECT I/C 60 PIN 3 TO GND POTENTIAL				
	CHECK AT T.P. 10/5 FOR A LOGIC '1'		E00071	FO0160	
E00071	CHECK AT I/C 47 PIN 4 FOR A LOGIC '0'		E00072	FO0170	
E00072	REMOVE GND. FROM I/C 60 PIN 3				
	CHECK AT I/C 47 PIN 4 FOR A LOGIC '1'		E00073	FO0170	
E00073	CHECK AT T.P. 10/5 FOR A LOGIC '0'		FO0150	FO0160	
E00074	SELECT SLAVE DRIVE				
	SET TO READ DISABLE MODE (I.E. READ EN/IS AT LOGIC '1')				
	CONNECT I/C 60 PIN 3 TO GND. POTENTIAL				
	CHECK AT T.P. 10/5 FOR A LOGIC '1'		E00075	FO0160	
E00075	REMOVE GND. FROM I/C 60 PIN 3				
	CHECK AT T.P. 10/5 FOR A LOGIC '0'		E00076	FO0160	
E00076	CHECK AT I/C 47 PIN 4 (SLAVE BRD) FOR A LOGIC '1'		FO0150	E00170	
E00080	CONNECT I/C 68 PIN 9 TO GND				
	CHECK AT I/C 46 PIN 4 FOR A LOGIC '1'		E00081	FO0180	
E00081	REMOVE GND. FROM I/C 68 PIN 9				
	CHECK AT I/C 46 PIN 4 FOR A LOGIC '0'		FO0190	FO0180	
E00090	CONNECT I/C 68 PIN 1 TO GND				
	CHECK AT I/C 46 PIN 6 FOR A LOGIC '1'		E00091	FO0180	
E00091	REMOVE GND FROM I/C 68 PIN 1				
	CHECK AT I/C 46 PIN 6 FOR A LOGIC '0'		FO0190	FO0180	
E00100	CONNECT I/C 68 PIN 13 TO GND				
	CHECK AT I/C 46 PIN 8 FOR A LOGIC '1'		E00101	FO0180	
E00101	REMOVE GND FROM I/C 68 PIN 13				
	CHECK AT I/C 46 PIN 8 FOR A LOGIC '0'		FO0190	FO0180	
E00110	CONNECT I/C 68 PIN 11 TO GND				
	CHECK AT I/C 46 PIN 10 FOR A LOGIC '1'		E00111	FO0180	
E00111	REMOVE GND FROM I/C 68 PIN 11				
	CHECK AT I/C 46 PIN 10 FOR A LOGIC '0'		FO0190	FO0180	


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ENTRY	INSTRUCTION	FAILURE INVESTIGATION PATH	AGREE	DISGREE	
	SECTION 00 CONT'D				
E00120	CONNECT I/C 68 PIN 3 TO GND				
	CHECK AT I/C 46 PIN 16 FOR A LOGIC '1'		E00121	FO0180	
E00121	REMOVE GND FROM I/C 68 PIN 3				
	CHECK AT I/C 46 PIN 16 FOR A LOGIC '0'		FO0190	FO0180	
E00130	CONNECT I/C 68 PIN 5 TO GND				
	CHECK AT I/C 46 PIN 18 FOR A LOGIC '1'		E00131	FO0180	
E00131	REMOVE GND FROM I/C 68 PIN 5				
	CHECK AT I/C 46 PIN 18 FOR A LOGIC '0'		FO0190	FO0180	
E00140	CONNECT I/C 66 PIN 5 TO GND				
	CHECK AT I/C 46 PIN 20 FOR A LOGIC '1'		E00141	FO0210	
E00141	REMOVE GND. FROM I/C 66 PIN 5				
	CHECK AT I/C 46 PIN 20 FOR A LOGIC '0'		FO0200	FO0210	


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ENTRY	INSTRUCTION FAILURE INVESTIGATION PATH	AGREE	DISAGREE		
	<u>SECTION 01</u>				
E01010	SELECT DRIVE TO BE TESTED SELECT TRACK 88				
	CHECK AT I/C 16 PIN 5 FOR A LOGIC '1'	F01010	F01020		
E01020	SELECT DRIVE TO BE TESTED CHECK AT I/C 13 PIN 6 FOR A LOGIC '1'	F01030	F01040		
E01030	SELECT DRIVE TO BE TESTED ENSURE A WRITE ENABLED DISK HAS BEEN INSERTED				
	CHECK AT T.P. 8/6 FOR A LOGIC '0'	F01030	F01050		
E01040	SELECT DRIVE TO BE TESTED SELECT HEAD '0' SELECT TRACK '00'				
	CHECK AT T.P. 8/7 FOR A LOGIC '1'	F01030	F01060		
E01050	SELECT DRIVE TO BE TESTED CHECK AT I/C 65 PIN 7 FOR POSITIVE PULSES				
	OF WIDTH 5 MS ± 0.15 MS.	F01070	F01080		
E01060	SELECT DRIVE TO BE TESTED CHECK AT I/C 65 PIN 9 FOR POSITIVE PULSES OF WIDTH				
	160 MS ± 5 MS	F01070	F01080		
E01070	SELECT DRIVE TO BE TESTED SELECT HD '0' SELECT TRACK '01'				
	WRITE AN ALL '1010' PATTERN ONTO DISK I.E. AT EVERY SECTOR EACH REVOLUTION				
	CHECK AT T.P. 10/7 FOR A FREQUENCY OF 375 ± 1 KHZ	F01090	F01100		
E01071	READ FROM DISK AT EVERY SECTOR EACH REV.				
	CHECK AT T.P. 10/7 FOR A FREQUENCY OF 375 ± 1KHZ	F01090	F01110		
E01080	SELECT DRIVE TO BE TESTED SELECT HD. '0' SELECT TRACK '01'				
	WRITE AN ALL '1010' PATTERN ONTO DISK AT EVERY SECTOR				
	READ BACK FROM DISK AT EVERY SECTOR EACH REV.				
	CHECK AT T.P. 10/6 FOR A FREQUENCY OF 187.5 ± 1 KHZ	F01090	F01120		

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ENTRY	INSTRUCTION FAILURE INVESTIGATION PATH	AGREE	DISAGREE		
	<u>SECTION 02</u>				
E02010	SELECT HD. '0' SET TO READ MODE				
	CHECK TP 7/2 FOR A LOGIC '1'				
	CHECK TP 7/1 FOR A LOGIC '0'			E02030	E02020
E02020	CHECK AT I/C 22 PIN 6 FOR A LOGIC '1'				F02010
	CHECK AT I/C 11 PIN 6 FOR A LOGIC '1'				F02020
	CHECK AT I/C 14 PIN 10 FOR A LOGIC '0'				F02030
	CHECK AT TP 22 FOR A LOGIC '0'				F02040
	CHECK AT I/C 14 PIN 6 AND I/C 14 PIN 8 FOR A LOGIC '1'	F02060	F02050		
E02030	CHECK AT I/C 14 PIN 2 FOR A LOGIC '0' AND AT I/C 14 PIN 4 FOR A LOGIC '1'				F02070
	CHECK AT I/C 15 PIN 3 FOR A LOGIC '1' AND AT I/C 15 PIN 8 FOR A LOGIC '0'				F02080
	CHECK AT I/C 15 PIN 11 FOR A LOGIC '0'	F02100	F02090		
E02040	SELECT HEAD '0' SET TO READ MODE				
	CHECK THAT I/C 42 PIN 2 IS AT A LOGIC '1'				F02110
	CHECK THAT COLLECTOR OF Q13 IS AT A LOGIC '0'				F02120
	CHECK THAT I/C 42 PIN 4 IS AT LOGIC '0'				F02130
	CHECK THAT COLLECTOR OF Q12 IS AT + 24 VOLTS	E02050	F02140		
E02050	SELECT HEAD '1' SET TO READ MODE				
	CHECK THAT I/C 42 PIN 2 IS AT A LOGIC '0'				F02110
	CHECK THAT COLLECTOR OF Q13 IS AT + 24 VOLTS				F02120
	CHECK THAT I/C 42 PIN 4 IS AT A LOGIC '1'				F02130
	CHECK THAT COLLECTOR OF Q12 IS AT A LOGIC '0'				F02140

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ENTRY	INSTRUCTION	FAILURE INVESTIGATION PATH	AGREE	DISGREE	
	<u>SECTION 03</u>				
EO3010	CHECK FOR A FREQUENCY OF 1.5 MHZ \pm 500HZ AT I/C 51 PIN 6		FO3010	FO3020	
	REMOVE GROUND FROM T/P 10/5				
EO3020	CONNECT TP's 9/3 AND 10/5 TO GROUND				
	CHECK AT I/C 49 PINS 6 AND 8 AND AT I/C 48 PINS 2 AND 4 FOR A LOGIC '1'			FO3040	
	CHECK AT I/C 49 PINS 5 AND 12 AND AT TP 9/1 AND I/C 48 PIN 9 FOR A LOGIC '0'		EO3030	FO3040	
EO3030	CHECK AT TP 9/2, I/C 54 PINS 8 AND 9				
	TP 9/2, I/C 48 PINS 10 AND 5, I/C 49 PINS 3 AND 11 FOR A FREQUENCY OF 375 \pm 1KHZ		EO3040	FO3030	
EO3040	CHECK AT I/C 53 PIN 11, I/C 48 PINS 13 AND 1 FOR A FREQUENCY OF 750 \pm 1KHZ		EO3050	FO3060	
EO3050	CHECK THAT THE INTERVAL BETWEEN THE POSITIVE EDGE OF THE SIGNAL AT TP 9/2 AND THE NEGATIVE EDGE OF THE SIGNAL AT I/C 48 PIN 6 IS 0.65 \pm 0.1US		EO3060	FO3070	
EO3060	REMOVE THE GROUND CONNECTION FROM T.P. 9/3 AND CONNECT I/C 66 PIN 5 TO GROUND				
	CHECK AT I/C 49 PINS 6 AND 8 AND AT I/C 48 PINS 2 AND 4 FOR A LOGIC '0'		EO3070	FO3040	
EO3070	CHECK AT I/C 49 PINS 5 AND 12, TP 9/1 AND I/C 48 PIN 9 FOR A LOGIC '1'		EO3080	FO3050;	
EO3080	CHECK THAT THE INTERVAL BETWEEN THE POSITIVE EDGE OF THE SIGNAL AT T.P. 9/2 AND THE NEGATIVE EDGE OF THE SIGNAL AT I/C 48 PIN 8 IS 1.95 \pm 0.1US.		FO3060	FO3070	
	REMOVE GRND FROM T.P. 10/5				

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ENTRY	INSTRUCTION	FAILURE INVESTIGATION PATH	AGREE	DISGREE	
	<u>SECTION 04</u>				
EO4010	CHECK AT T.P. 18 AND I/C 2 PIN 3 FOR A FREQUENCY OF 375 \pm 1 KHZ			EO4020	FO4060
EO4020	DISCONNECT ALL LEADS FROM CONN 6, CONN 7, TP 10/5 TP 9/3 AND I/C 1 PIN 9				
	SET TO READ MODE				
	SELECT HEAD '1'				
	CHECK THAT COLLECTOR OF Q2 IS AT LOGIC '0'				
	CHECK THAT COLLECTOR OF Q1 IS AT + 12 VOLTS			EO4030	EO4140
EO4030	SELECT HEAD '0'				
	CHECK THAT COLLECTOR OF Q1 IS AT LOGIC '0'				
	CHECK THAT COLLECTOR OF Q2 IS AT + 12 VOLTS			EO4040	EO4160
EO4040	SELECT HEAD '1'				
	CONNECT A DUMMY HEAD TO CONNECTOR 6 (I.E. HEAD '1')				
	CONNECT TP 10/5 AND TP 9/3 TO GROUND				
	CONNECT I/C 1 PIN 9 TO GROUND				
	CHECK AT TP4 AND TP 12 FOR A FREQUENCY OF 187.5 \pm 1 KHZ			FO4080	EO4050
EO4050	SELECT HEAD '0'				
	REMOVE DUMMY HEAD FROM CONNECTOR 6 AND CONNECT IT TO CONNECTOR 7 (I.E. HEAD '0')				
	CHECK AT TP 4 AND TP 12 FOR A FREQUENCY OF 187.5 \pm 1 KHZ			FO4070	EO4060
EO4060	CHECK THAT TP 13, IS AT A D.C. LEVEL OF BETWEEN 8.5 VOLTS AND 9.0 VOLTS			EO4070	EO4200
EO4070	REMOVE GROUND FROM I/C 1 PIN 9				
	CHECK THAT T.P. 13 IS AT A D.C. LEVEL OF BETWEEN 11.0 VOLTS AND 11.5 VOLTS			EO4080	EO4200
EO4080	CONNECT I/C 1 PIN 9 TO GND				
	CHECK AT I/C 3 PIN 6 FOR A FREQUENCY OF 187.5 \pm 1 KHZ			EO4090	EO4180
EO4090	CHECK AT I/C 3 PIN 8 FOR A FREQUENCY OF 187.5 \pm 1 KHZ			EO4100	EO4180
EO4100	CONNECT I/C 1 PIN 9 TO GND				
	CHECK THAT COLLECTOR OF Q3 IS AT A D.C. LEVEL OF BETWEEN 1.0 AND 1.5 VOLTS.			EO4110	EO4130

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ENTRY	INSTRUCTION <u>FAILURE INVESTIGATION PATH</u>	AGREE	DISAGREE		
	<u>SECTION 04 CONT</u>				
E04110	REMOVE GND FROM I/C 1 PIN 9				
	CHECK THAT COLLECTOR OF Q3 IS 0.7 VOLTS OR LESS	E04131	E04120		
E04120	CHECK AT TP1 FOR A D.C. VOLTAGE OF + 12 VOLTS ± 1.2 VOLTS.	F04090	E04131		
E04130	CHECK AT T.P. 1 FOR A D.C. VOLTAGE OF + 6 VOLTS ± 0.6 V.	F04090	E04131		
E04131	AT THIS POINT THE REVISION LEVEL OF THE P.C.B. SHOULD BE DETERMINED.				
	FOR P.C.B.'S UP TO AND INCLUDING REV J GO TO E04138				
	FOR P.C.B.'S FROM REV K AND ONWARDS GO TO E04132				
E04132	CONNECT I/C 1 PIN 9 TO GND				
	CHECK AT I/C 1 PIN 6 FOR A LOGIC '1'	E04133	F04230		
E04133	CHECK AT I/C 15 PIN 6 FOR A LOGIC '1'	E04134	F04240		
E04134	REMOVE GND FROM I/C 1 PIN 9				
	CHECK AT I/C 1 PIN 6 FOR A LOGIC '0'	E04135	F04230		
E04135	CHECK AT I/C 15 PIN 6 FOR A LOGIC '0'	E04136	F04250		
E04136	CHECK AT I/C 21 PIN 9 FOR A SINGLE POSITIVE GOING PULSE OF LENGTH 400 ± 40US WHEN I/C 1 PIN 9 IS CONNECTED TO GND	E04137	F04260		
E04137	REMOVE GND FROM I/C 1 PIN 9				
	CHECK AT COLLECTOR OF Q18 FOR A SINGLE NEGATIVE GOING PULSE OF LENGTH 400 ± 40US WHEN I/C 1 PIN 5 IS CONNECTED TO GND	F04100	F04270		
E04138	CONNECT I/C 1 PIN 9 TO GND				
	CHECK THAT I/C 1 PIN 6 IS AT A LOGIC '1'	E04139	F04190		
E04139	REMOVE GND FROM I/C 1 PIN 9				
	CHECK THAT I/C 1 PIN 6 IS AT A LOGIC '0'	F04100	F04190		
E04140	CHECK THAT I/C 1 PIN 1 IS AT LOGIC '1'	E04150	F04110		
E04150	CHECK THAT I/C 1 PIN 2 IS AT LOGIC '0'	F04120	F04130		
E04160	CHECK THAT I/C 1 PIN 1 IS AT LOGIC '0'	E04170	F04110		
E04170	CHECK THAT I/C 1 PIN 2 IS AT LOGIC '1'	E04171	F04130		
E04171	CHECK THAT I/C 1 PIN 4 IS AT LOGIC '0'	F04140	F04130		
E04180	CHECK THAT I/C 1 PIN 8 IS AT LOGIC '1'	E04190	F04150		
E04190	CHECK THAT I/C 2 PIN 5 AND 6 ARE AT 187.5 ± 1 KHZ	F04160	F04170		
	SELECT TRACK '00'				

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ENTRY	INSTRUCTION <u>FAILURE INVESTIGATION PATH</u>	AGREE	DISAGREE		
	<u>SECTION 04 CONT'D</u>				
	CHECK THAT I/C 1 PINS 11 AND 13 ARE AT LOGIC '0'	E04201	F04180		
E04201	CHECK THAT I/C 3 PIN 11 IS AT LOGIC '0'	E04210	F04190		
E04210	SELECT TRACK '32'				
	CHECK THAT I/C 1 PIN 13 IS AT LOGIC '1' AND THAT I/C 1 PIN 11 IS AT A LOGIC '0'	E04211	F04180		
E04211	CHECK THAT I/C 3 PIN 11 IS AT LOGIC '1'	E04220	F04190		
E04220	SELECT TRACK '00'				
	CHECK THAT I/C 3 PIN 11 IS AT LOGIC '0'	E04230	F04190		
E04230	SELECT TRACK '64'				
	CHECK THAT I/C 1 PIN 11 IS AT LOGIC '1' AND THAT I/C 1 PIN 13 IS AT A LOGIC '0'	E04231	F04180		
E04231	CHECK THAT I/C 3 PIN 11 IS AT A LOGIC '1'	E04240	F04190		
E04240	CHECK THAT COLLECTOR OF Q4 IS AT A VOLTAGE BETWEEN 9.8 VOLTS AND 10.2 VOLTS				
	REPLACE HEADS AND REMOVE ALL OTHER LINKS PREVIOUSLY ADDED.				F04200
E04250	CHECK AT T.P. 16 FOR A FREQUENCY WHICH IS SOME MULTIPLE OF 187.5 KHZ.				
	I.E. 187.5 KHZ, 375 KHZ, 562.5 KHZ, or 750 KHZ ALL ± 1 KHZ	E04260	E04280		
E04260	CHECK AT T.P. 20 FOR A FREQUENCY THE SAME AS THAT DESCRIBED IN E04250	E04270	F04010		
E04270	CHECK AT COLLECTOR OF Q15 FOR A FREQUENCY OF 187.5 ± 1 KHZ	F04020	F04030		
E04280	CHECK AT T.P. 14 FOR A FREQUENCY THE SAME AS THAT DESCRIBED IN E04250	F04040	F04050		
E04290	SELECT TRACK '00'				
	CHECK AT I/C 20 PINS 9, 10, 12 AND 13 FOR A LOGIC '1'	F04280	E04200		
E04300	SELECT TRACK '32'				
	CHECK AT I/C 20 PINS 10 AND 12, FOR A LOGIC '0'	F04280	E04200		
E04310	SELECT TRACK '64'				
	CHECK AT I/C 20 PINS 9 AND 13, FOR A LOGIC '0'	F04280	E04200		

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ENTRY	INSTRUCTION	FAILURE INVESTIGATION PATH	AGREE	DISGEE	
	<u>SECTION 05</u>				
EO5010	IS A FREQUENCY OF 375K ± 1K PRESENT AT IC 57 PIN 6	F05010	EO5020		
EO5020	IS ANY FREQUENCY PRESENT AT IC 57 PIN 6	F05020	EO5030		
EO5030	CHECK TP 10/1 FOR 375K ± 2 K	F05040	F05030		
EO5040	CHECK TP 11/2 FOR A FREQUENCY OF 375K ± 2 K	F05050	EO5050		
EO5050	CHECK IC 54 PINS 5 & 6 FOR A FREQUENCY OF 375K ± 2K	F05060	EO5060		
EO5060	CHECK TP 10/4 FOR A FREQUENCY OF 750 K ± 4K	F05070	EO5070		
EO5070	CHECK IC 61 PIN 12 FOR 375K ± 2K	EO5080	F05120		
EO5080	CHECK TP 11/6 FOR + 9.1V ± 0.5 V	EO5090	F05080		
EO5090	CHECK TP 11/5 FOR - 9.1V ± 0.5V	EO5100	F05090		
EO5100	CHECK WIPER OF RV4 FOR 0 TO - 9.6 V	F05100	F05110		
EO5110	CHECK TP 11/3 FOR FREQUENCY 375K ± 2K	F05130	EO5120		
EO5120	CHECK IC50 PIN 11 FOR FREQUENCY 375K ± 2K	F05150	F05140		
EO5130	IS ANY FREQUENCY OBTAINED?	F05160	F05110		
EO5011	CHECK WIDTH OF ACTIVE LOW SIGNAL ON IC 57 PIN 9 FOR 34-52 u SEC.	F05220	F05210		
EO5012	CHECK IC 57 PIN 10 FOR POSITIVE PULSE WIDTH 34-52 u SEC	F05240	F05230		

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ENTRY	INSTRUCTION	FAILURE INVESTIGATION PATH	AGREE	DISGEE	
	<u>SECTOR FAIL</u> (SECTION 06)				
EO6010	CHECK IC 45 PIN 6 FOR A FREQUENCY OF 194 Hz - 412Hz	EO6011	EO6300		
EO6011	CHECK THE REVISION LEVEL OF THE P.C.B. FOR BRDS UP TO AND INCLUDING REV J GO TO EO6020 FOR BRDS FROM REV K AND ONWARDS GO TO E.06012				
EO6012	CHECK I/C 44 PIN 6 FOR A FREQUENCY OF 194-412 Hz	EO6013	F06410		
EO6013	CHECK I/C 21 PIN 5 FOR A FREQUENCY OF 194-412 Hz	EO6030	F06020		
EO6020	Check IC 21 PIN 8 FOR A FREQUENCY OF 194-412 Hz	EO6030	F06020		
EO6030	CHECK IC 10 PIN 10 FOR A FREQUENCY OF 194 - 412 Hz WITH POS PULSE WIDTH 1.6 - 2.0 u Sec	EO6040	F06030		
EO6040	CHECK IC 10 PIN 7 FOR A FREQUENCY OF 194 - 206 Hz WITH NEGATIVE PULSE WIDTH 3.4-4.6 m SEC	F06051	F06040		
	<u>INDEX FAIL</u>				
EO6050	CHECK IC 45 PIN 6 FOR A FREQUENCY OF 194-412 Hz	EO6060	F06010		
EO6051	CHECK THE REVISION LEVEL OF THE P.C.B. FOR BRDS UP TO AND INCLUDING REV J GO TO EO6060 FOR BRDS FROM REV K AND ONWARDS GO TO E.06052				
EO6052	CHECK I/C 44 PIN 6 FOR A FREQUENCY OF 194-412 Hz	EO6053	F06410		
EO6053	CHECK I/C 21 PIN 5 FOR A FREQUENCY OF 194-412 Hz	EO6060	F06020		
EO6060	CHECK IC 21 PIN 8 FOR A FREQUENCY OF 194-412 Hz	EO6070	F06020		
EO6070	CHECK IC 10 PIN 10 FOR A FREQUENCY OF 194-412 Hz WITH POS PULSE WIDTH 1.6 - 2.0 u SEC	EO6080	F06030		
EO6080	CHECK TP 5/2 FOR A FREQUENCY OF 194 - 206 Hz WITH POSITIVE PULSE WIDTH 3.4 - 4.6 M SEC	F06052	F06040		
	<u>UP TO SPEED FAIL</u>				
EO6110	CHECK IC 22 PIN 13 FOR LOGIC '0'	F06110	EO6120		
EO6120	CHECK IC 31 PIN 8 FOR LOGIC '0'	EO6150	F06120		
EO6130	CHECK IC 22 PIN 13 FOR CONTINUOUS LOGIC '1'	F06110	EO6140		
EO6140	CHECK IC 31 PIN 8 FOR A VOLTAGE GREATER THAN 1V	EO6150	F06120		
EO6150	CHECK IC 45 PIN 9 FOR VOLTAGE > 2.4 V	EO6160	F06150		
EO6160	CHECK IC 45 PIN 8 FOR LOGIC '0'	EO6180	F06160		
EO6180	CHECK IC 20 PIN 6 FOR LOGIC '1'	F06180	F06190		
EO6190	CHECK IC 20 PIN 4 FOR VOLTAGE 0 TO - 0.7 V	F06190	F06200		
EO6300	CHECK IC 45 PIN 4 FOR FREQUENCY 194 - 412 Hz	F06010	EO6310		
EO6310	CHECK ACROSS INDEX/SECTOR LED FOR 0.9 TO 1.3 V	F06310	EO6300		

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ENTRY	INSTRUCTION <u>FAILURE INVESTIGATION PATH</u>	AGREE	DISAGREE		
	<u>SECTION 07</u>				
E07010	IS A FREQUENCY PRESENT AT TP 6/5?	E07050	E07030		
E07030	CHECK TP 6/5 FOR LOGIC '1'	F07030	E07040		
E07040	CHECK IC 40 PIN 8 FOR LOGIC '1'	F07040	F07050		
E07050	CHECK TP 6/5 FOR A FREQUENCY OF 117-177 Hz	E07060	F07020		
E07060	CHECK IC 42 PIN 10 FOR VOLTAGE LEVEL OF 4.5-5.5 V	F07060	F07070		
E07100	CHECK IC 13 PIN 2 FOR POSITIVE PULSES WIDTH 0.5-1.5 u SEC	F07120	F07110		
E07080	CHECK IC 42 PIN 10 FOR VOLTAGE LEVEL <1V	F07060	F07070		


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ENTRY	INSTRUCTION <u>FAILURE INVESTIGATION PATH</u>	AGREE	DISAGREE		
	<u>SECTION 08</u>				
E08010	CHECK IC 45 PIN 1 FOR VOLTAGE LESS THAN 0.8V	E08030	E08020		
E08020	CHECK FOR VOLTAGE OF 1.0 - 1.3V ACROSS W/H LED	F08020	F08010		
E08030	CHECK IC 45 PIN 3 FOR LOGIC '1'	E08040	F08030		
E08040	CHECK IC 44 PIN 8 FOR LOGIC '0'	F08050	F08040		
E08050	CHECK IC 45 PIN 1 FOR VOLTAGE GREATER THAN 2 V	E08060	F08060		
E08060	CHECK IC 45 PIN 3 FOR LOGIC '0'	E08070	F08030		
E08070	CHECK IC 44 PIN 8 FOR LOGIC '1'	F08050	F08040		
E08100	CHECK IC 44 PIN 10 FOR LOGIC '1'	E08110	F08100		
E08110	CHECK IC 18 PIN 3 FOR NEG PULSE AS IN N08030	E08120	F08110		
E08120	CHECK IC 13 PIN 12 FOR POS PULSE AS IN N08030	F08130	F08120		
E08150	CHECK IC 46 PIN 21 FOR LOGIC '1'	E08160	F08160		
E08160	CHECK IC 36 PIN 4 FOR LOGIC '0'	F08180	F08170		
E08170	CHECK IC 46 PINS 5,7,9,11 FOR LOGIC '0'	E08180	F08190		
E08180	CHECK IC 46 PINS 17, 19, 21, FOR LOGIC '0'	E08190	F08200		
E08190	CHECK IC 36 PIN 13 FOR LOGIC 1	E08200	F08210		
E08200	CHECK IC 36 PIN 10 FOR LOGIC '1'	E08210	F08210		
E08210	CHECK IC 36 PIN 1 FOR LOGIC 1	E08220	F08210		
E08220	CHECK IC 41 PIN 12 FOR LOGIC '0'	E08230	F08220		
E08230	CHECK IC 36 PIN 4 FOR LOGIC '1'	F08180	F08170		
E08291	CHECK IC 44 PIN 10 FOR LOGIC '0'	F08340	F08100		

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ENTRY	INSTRUCTION FAILURE INVESTIGATION PATH	AGREE	DISGREE		
	<u>SECTION 08 CONTINUED</u>				
E08300	TRACK NO. CHECK IC 16 PIN 5 FOR				
	SELECT ADDRESS 80 '0' LOGIC		E08310		
	ISSUING A 'SEEK' 72 '0' "		E08310		
	AFTER EACH 24 '0' "		E08310		
	SELECTION 64 '0' "		E08330		
	32 '0' "		E08330		
	88 '1' "		E08340		
	96 '1' "		E08360		
E0831C	CHECK IC 28 PIN 6 FOR LOGIC '1'	E09010	E08310		
E08320	CHECK IC 18 PIN 8 FOR LOGIC '0'	F08320	F08310		
E08330	CHECK IC 18 PIN 11 FOR LOGIC '1'	F08340	F08320		
E08340	CHECK IC 28 PIN 6 FOR LOGIC '0'	E08320	F08340		
E08350	CHECK IC 18 PIN 8 FOR LOGIC '1'	E08350	F08310		
E08360	CHECK IC 18 PIN 11 FOR LOGIC '0'	F08330	F08320		
E08500	CHECK IC 36 PIN 11 FOR LOGIC '1'	E08350	F08340		
E08510	CHECK IC 36 PIN 13 FOR LOGIC '0'	E08510	F08190		
E08520	CHECK IC 41 PIN 12 FOR LOGIC '1'	E08520	F08250		
E08530	CHECK IC 36 PIN 4 FOR LOGIC '0'	E08530	F08220		
E08540	CHECK IC 36 PIN 12 FOR LOGIC '1'	F08180	F08170		
E08550	CHECK IC 36 PIN 8 FOR LOGIC '1'	F08250	F08190		
E08560	CHECK IC 36 PIN 10 FOR LOGIC '0'	E08560	F08190		
E08570	CHECK IC 36 PIN 9 FOR LOGIC '1'	F08220	F08250		
E08580	CHECK IC 36 PIN 3 FOR LOGIC '1'	F08210	F08190		
E08590	CHECK IC 36 PIN 1 FOR LOGIC '0'	E08590	F08190		
E08600	CHECK IC 36 PIN 2 FOR LOGIC '1'	F08220	F08250		
		F08250	F08160		

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ENTRY	INSTRUCTION FAILURE INVESTIGATION PATH	AGREE	DISGREE		
	<u>SECTION 09</u>				
E09010	CHECK IC 25 PIN 3 FOR LOGIC '1'	E09020	F09010		
E09020	CHECK IC 13 PIN 6 FOR LOGIC '0'	F09040	F09020		
E09040	CHECK FOR LOGIC '1' ON IC 33 PIN 6	E09050	E09600		
E09700	CHECK TP 7/5 FOR LOGIC '0'	E09530	F09050		
E09710	CHECK TP 7/4 FOR LOGIC '0'	E09540	F09060		
E09720	CHECK TP 7/3 FOR LOGIC '0'	E09550	F09070		
E09070	CHECK IC 25 PIN 11 FOR LOGIC '0'	E09160	E09130		
E09100	CHECK IC 45 PIN 12 FOR LOGIC '0'	E09110	E09120		
E09120	CHECK TKO LED FOR A VOLTAGE OF 0.9 - 1.3V	F09130	F09120		
E09110	CHECK TP 6/6 FOR LOGIC '1'	F09150	F09140		
E09130	CHECK IC 45 PIN 12 FOR LOGIC '1'	E09140	F09130		
E09140	CHECK TP 6/6 FOR LOGIC '0'	E09150	F09140		
E09150	CHECK IC 25 PIN 6 FOR LOGIC '1'	F09160	F09150		
E09160	CHECK TP 6/2 FOR LOGIC '1'	E09170	F09170		
E09170	CHECK IC 24 PIN 6 FOR LOGIC '1'	E09180	F09180		
E09180	CHECK TP 5/6 FOR POSITIVE PULSES	E09190	F09190		
E09190	CHECK IC 11 PIN 8 FOR LOGIC '1'	E09300	E09200		
E09200	CHECK IC 13 PIN 4 FOR '1'	E09210	F09200		
E09210	CHECK TP 8/1 FOR LOGIC '0'	F09210	F09220		
E09050	CHECK TP 7/8 FOR 21 - 27V	E09060	E09700		
E09060	CHECK TP 7/7 FOR 21 - 27V	E09061	E09710		
E09061	CHECK TP 7/6 FOR 21 - 27V	F09340	E09720		
E09300	CHECK TP 8/3 FOR LOGIC '1'	E09310	F09230		
E09310	CHECK IC 24 PIN 8 FOR LOGIC '0'	E09400	E09320		
E09320	CHECK IC 13 PIN 10 FOR LOGIC '1'	F09240	F09250		
E09400	CHECK IC 35 PIN 9 IC 35 PIN 5 FOR DUTY CYCLE 23-43% AT A FREQUENCY 39 - 59 Hz	E09410	F09300		
E09410	CHECK IC 26 PIN 11 FOR FREQUENCY 39-59 Hz DUTY CYCLE 23-77% A	E09430	F09300		
E09430	CHECK IC 20 PIN 3 FOR LOGIC '1'	E09440	F09310		
E09440	CHECK TP 7/5 FOR FREQUENCY 39-59 Hz DUTY CYCLE 23-43%	E09530	F09320		

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ENTRY	INSTRUCTION FAILURE INVESTIGATION PATH	AGREE	DISGREE		
	<u>SECTION 09</u> Continued				
E09450	CHECK TP 7/4 FOR FREQUENCY 39-59 Hz DUTY CYCLE 23-43%	E09540	F09320		
E09460	CHECK TP 7/3 FOR FREQUENCY 39-59 Hz DUTY CYCLE 23-43%	E09550	F09320		
E09530	REMOVE STEPPER MOTOR PLUG IF NOT ALREADY REMOVED.				
	CHECK CONN 5 PLUG, SOCKET 3-5 FOR RESISTANCE 2-20 OHM, 3- CHASSIS OPEN CIRCUIT	F09330	F09340		
E09540	CHECK CONN 5 PLUG, SOCKETS 2-5 FOR RESISTANCE 2-20 OHM 2- CHASSIS OPEN CIRCUIT	F09350	F09340		
E09550	CHECK CONN 5 PLUG, SOCKETS 1-5 FOR RESISTANCE 2-20 OHM 1-CHASSIS OPEN CIRCUIT	F09360	F09340		
E09560	CHECK THAT RESISTANCES IN E09530/40/50 ARE CORRECT AND WITHIN 2% OF EACH OTHER	E09070	F09370		
E09600	REMOVE STEPPER MOTOR PLUG: POSITION CARRIAGE BLOCK AS N09030. CHECK TP 5/6 FOR LOGIC '1'	F09500	E09800		
E09800	CHECK IC 24 PIN 6 FOR LOGIC '0'	F09150	E09810		
E09810	CHECK IC 25 PIN 6 FOR LOGIC '0'	E09820	E09180		
E09820	CHECK IC 25 PIN 11 FOR LOGIC '1'	E09830	F09150		
E09830	CHECK IC 25 PIN 8 FOR LOGIC '1'	F09180	F09400		

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ENTRY	INSTRUCTION FAILURE INVESTIGATION PATH	AGREE	DISGREE		
	<u>SECTION 10</u>				
E10010	REMOVE STEPPER MOTOR PLUG, POSITION HEADS TO CENTRE OF DISK OPEN AND CLOSE DOOR				
	CHECK IC 25 PIN 3 FOR LOGIC '1'	E10030	F10020		
E10030	CHECK IC 25 PIN 6 FOR LOGIC '0'	E10040	F10030		
E10040	CHECK TP 6/2 FOR LOGIC '1'	E10050	F10040		
E10050	CHECK IC 25 PIN 8 FOR LOGIC '0'	E10060	F10050		
E10060	CHECK IC 24 PIN 6 FOR LOGIC '1'	E10070	F10060		
E10070	CHECK TP 5/6 FOR NEGATIVE PULSES (DIRECTION CONTROL)	E10080	F10070		
E10080	CHECK IC 24 PIN 8 FOR LOGIC '1'	E10090	F10080		
E10090	CHECK IC 37 PIN 15 FOR LOGIC '0'	E10100	F10090		
E10100	CHECK IC 13 PIN 8 FOR LOGIC '1'	E10110	F10100		
E10110	CHECK IC 24 PIN 11 FOR LOGIC '0'	F10110	F10080		
E10200	REMOVE STEPPER MOTOR PLUG, POSITION HEADS TO CENTRE OF DISK. OPEN AND CLOSE DOOR. BY ROTATING STEPPER MOTOR SPINDLE, MOVE CARRIAGE TOWARDS CASTING UNTIL CONTACT IS MADE WITH CASTING.				
	CHECK IC 25 PIN 3 FOR LOGIC '0'	E10210	F10120		
E10210	CHECK IC 25 PIN 6 FOR LOGIC '1'	E10220	F10130		
E10220	CHECK IC 25 PIN 11 FOR LOGIC '1'	E10230	F10130		
E10230	CHECK TP 6/2 FOR LOGIC '0'	F10070	E10240		
E10240	CHECK TP 6/3 FOR LOGIC '1'	F10040	E10250		
E10250	CHECK IC 30, 29 PINS 4,5,9,10 FOR LOGIC '0'	F10140	E10260		
E10260	CHECK IC 37 PIN 2 FOR LOGIC '1'	F10160	F10150		
E10300	CHECK IC 13 PIN 6 FOR '1'	F10210	F10200		

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ENTRY	INSTRUCTION	FAILURE INVESTIGATION PATH	AGREE	DISAGREE	
	<u>SECTION 11</u>				
E11010	CHECK TP 8/7 FOR LOGIC '1'		F11020	F11010	

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ENTRY	INSTRUCTION	FAILURE INVESTIGATION PATH	AGREE	DISAGREE	
	<u>SECTION 12</u>				
E12010	OPEN & CLOSE DOOR. ROTATE STEPPER MOTOR SHAFT UNTIL TRACK 00 TRANSDUCER IS UNCOVERED. REMOVE STEPPER MOTOR CONNECTOR CONNECT TP 6/3 TO OV CONNECT IC13 PIN 8 TO OV SEEK TRACK 42				
	CHECK TP 8/3 FOR LOGIC '1'		E12020	F12020	
E12020	CHECK IC 24 PIN 3 FOR FREQUENCY 190-246 HZ		E12021	E12023	
E12021	CHECK IC 13 PIN 4 FOR LOGIC '0'		E12022	F12310	
E12022	CHECK IC 11 PIN 8 FOR LOGIC '1'		E12030	F12320	
E12023	DETERMINE THE REVISION LEVEL OF THE P.C.B. FOR P.C.B'S UP TO AND INCLUDING REV J GO TO F12010 FOR P.C.B'S FROM REV K AND ONWARDS GO TO E12024				
E12024	CHECK I/C 32 PIN 5 FOR LOGIC '1'		F12010	F12024	
E12030	CHECK PULSE WIDTHS AT FOLLOWING POINTS (NEG & POS)				
	IC 30 PIN 4 3.6 - 5.2 MSEC			F12030	
	5 7.2 - 10.4 MSEC			F12030	
	9 14.4 - 20.8 MSEC			F12030	
	10 28.8 - 41.6 MSEC			F12030	
	IC 29 PIN 4 57.6 - 83.2 MSEC			F12040	
	5 115.2 - 166.4 MSEC			F12040	
	9 230.4 - 332.8 MSEC		E12040	F12040	
E12040	CHECK INTERVAL BETWEEN POSITIVE EDGE OF IC 30 PIN 10 AND IC 30 PIN 3 FOR 44-64 MSEC		E12041	F12050	
E12041	CHECK IC 38 PINS 2 & 15 FOR PERIOD OF 57 - 93 MSEC		E12042	F12100	
E12042	CHECK TP 6/1 FOR PERIOD OF 460 - 664 MSEC		E12043	F12200	
E12043	CHECK IC 13 PIN 10 FOR PERIOD 460 - 664 MSEC		E12050	E12044	
E12044	CHECK TP 8/1 FOR PERIOD OF 460 - 664 MSEC		F12120	F12110	
E12050	CHECK INTERVAL BETWEEN POSITIVE EDGE OF IC 29 PIN 10 AND IC 29 PIN 3 FOR 690 - 1038 M SEC		E12051	F12060	
E12051	WIND STEPPER MOTOR BACK UNTIL CARRIAGE TOUCHES CASTING.				
	CHECK IC 11 PIN 8 FOR ANY FREQUENCY		E12060	E12052	
E12052	CHECK IC 13 PIN 4 FOR LOGIC '1'		F12330	F12310	

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ENTRY	INSTRUCTION <u>FAILURE INVESTIGATION PATH</u>	AGREE	DISAGREE		
	<u>SECTION 12 CONT'd</u>				
E12060	ROTATE STEPPER MOTOR UNTIL TK00 TRANSDUCER IS UNCOVERED. REMOVE LINK ON IC 13 PIN 3 CONNECT TP 6/1 TO OV CHECK TP 8/3 FOR LOGIC '0'	E12062	F12120		
E12062	CHECK PULSE WIDTHS AS IN E12030	E12070	AS IN E12030		
E12070	CHECK INTERVAL BETWEEN NEG. EDGE OF IC 30 PIN 10 AND POS. EDGE OF IC 30 PIN 3 FOR 44 - 64 MSEC	E12080	F12050		
E12080	CHECK INTERVAL BETWEEN NEG. EDGE IC 29 PIN 10 POS. EDGE IC 29 PIN 3 FOR 690 - 1038 MSEC	F12070	F12060		
E12100	CHECK IC 26 PIN 8 FOR LOGIC '0'	F12150	F12140		
E12110	CHECK IC 12 PIN 6 FOR NEGATIVE PULSES	F12170	F12160		

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ENTRY	INSTRUCTION <u>FAILURE DIRECTORY</u>	AGREE	DISAGREE		
	<u>SECTION 00</u>				
FO0010	FAILURE: INCORRECT SIGNAL FROM CONTROLLER I/C 66				
FO0020	FAILURE: I/C 66 I/C 63 I/C 62 I/C 65, I/C 67 I/C 28, I/C 18				
FO0021	FAILURE: I/C 63 I/C 65, I/C 67				
FO0030	FAILURE: I/C 63 I/C 62 I/C 28, I/C 18				
FO0040	FAILURE: I/C 66 I/C 60				
FO0050	FAILURE: I/C 66 I/C 64 I/C 63 I/C 59				
FO0060	FAILURE: I/C 64 I/C 63 I/C 59				
FO0070	FAILURE: INCORRECT SIGNAL FROM CONTROLLER I/C 60				
FO0080	FAILURE: I/C 60 I/C 18				
FO0090	FAILURE: INCORRECT SIGNAL FROM CONTROLLER				
FO0100	FAILURE: I/C 66 I/C 32				
FO0110	FAILURE: INCORRECT SIGNAL FROM CONTROLLER I/C 60				
FO0120	FAILURE: I/C 60 I/C 44, I/C 28				
FO0130	FAILURE: INCORRECT SIGNAL FROM CONTROLLER I/C 66				

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ENTRY	INSTRUCTION	FAILURE DIRECTORY	AGREE	DISGREE	
	<u>SECTION 00</u>				
FO0140	FAILURE:	I/C 66 I/C 49			
FO0150	FAILURE:	INCORRECT SIGNAL FROM CONTROLLER I/C 62			
FO0160	FAILURE	I/C 60 I/C 52, I/C 62, I/C 57, I/C 50			
FO0170	FAILURE:	I/C 52 I/C 47, I/C 51			
FO0180	FAILURE:	I/C 68 I/C 46			
FO0190	FAILURE:	INCORRECT SIGNAL FROM CONTROLLER I/C 68			
FO0200	FAILURE:	INCORRECT SIGNAL FROM CONTROLLER I/C 66			
FO0210	FAILURE:	I/C 66; I/C 46			
FO0220	FAILURE:	FILE OP LAMP I/C 27 R85, LAMP HARNESS.			

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ENTRY	INSTRUCTION	FAILURE DIRECTORY	AGREE	DISGREE	
	<u>SECTION 01</u>				
FO1010	FAILURE:	I/C 67 I/C 63			
FO1020	FAILURE:	ILLEGAL ADDRESS CCT I/C 67			
FO1030	FAILURE:	I/C 67, I/C 59			
FO1040	FAILURE:	POSITIONER INITIALIZATION CCT I/C 67			
FO1050	FAILURE:	WRITE INHIBIT CCT I/C 67			
FO1060	FAILURE:	POSITION SETTLED CCT I/C 67			
FO1070	FAILURE:	I/C 58 I/C 64			
FO1080	FAILURE:	INDEX/SECTOR CCT I/C 65			
FO1090	FAILURE:	I/C 64			
FO1100	FAILURE:	WRITE DATA ENCODER CCT I/C 52			
FO1110	FAILURE:	HEAD DATA DECODE CCT I/C 52			
FO1120	FAILURE:	HEAD DATA DECODE CCT			

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ENTRY	INSTRUCTION	FAILURE DIRECTORY	AGREE	DISGREE	
	<u>SECTION 02</u>				
FO2010	FAILURE:	I/C 22 I/C 17 I/C 14			
FO2020	FAILURE:	I/C 11 I/C 14			
FO2030	FAILURE:	I/C 14 I/C 19;			
FO2040	FAILURE:	I/C 19 I/C 14			
FO2050	FAILURE:	I/C 14 I/C 17			
FO2060	FAILURE:	I/C 17 I/C 14			
FO2070	FAILURE:	I/C 14 I/C 15			
FO2080	FAILURE:	I/C 15			
FO2090	FAILURE:	I/C 15 I/C 23			
FO2100	FAILURE:	I/C 23			
FO2110	FAILURE:	I/C 42 Q13			
FO2120	FAILURE:	Q13, HEAD SOLENOID D19, D20, C112, C113			
FO2130	FAILURE:	I/C 42 Q12			
FO2140	FAILURE:	Q12, HEAD SOLENOID D19, D20, C112, C113.			

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ENTRY	INSTRUCTION	FAILURE DIRECTORY	AGREE	DISGREE	
	<u>SECTION 03</u>				
FO3010	FAILURE:	I/C 47 I/C 54 I/C 53			
FO3020	FAILURE:	I/C 51 CRYSTAL			
FO3030	FAILURE:	I/C 54 I/C 48 I/C 52 I/C 49			
FO3040	FAILURE:	I/C 49 I/C 48			
FO3050	FAILURE:	I/C 49 I/C 48			
FO3060	FAILURE:	I/C 53 I/C 48			
FO3070	FAILURE:	I/C 48 I/C 53			
FO3080	FAILURE:	I/C 53 I/C 62			
FO3090	FAILURE:	I/C 48 I/C 53			

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ENTRY	INSTRUCTION	FAILURE DIRECTORY	AGREE	DISAGREE	
		<u>SECTION 04</u>			
FO4010	FAILURE:	I/C 8			
		I/C 6			
FO4020	FAILURE:	Q17			
		I/C 5			
		I/C 65			
FO4030	FAILURE:	Q16, Q15			
		I/C 6			
FO4040	FAILURE:	I/C 7			
		LOW-PASS FILTER COMPONENTS			
		DELAY LINE			
FO4050	FAILURE:	I/C 4			
		D11, D12			
		D4, D5, D6, D7			
		D9, D10			
FO4060	FAILURE:	I/C 2, I/C 62			
		WRITE DATA ENCODER			
		INTERFACE LOGIC			
FO4070	FAILURE:	HEAD '0'			
FO4080	FAILURE:	HEAD '1'			
FO4090	FAILURE:	Q3			
		D2, D3			
		CONN 6 / CONN 7			
FO4100	FAILURE:	Q3			
		D1			
		I/C 3			
FO4110	FAILURE:	HEAD ADDRESS CIRCUIT			
FO4120	FAILURE:	Q2			
		Q1			
FO4130	FAILURE:	I/C 1			
		Q1, Q2			
FO4140	FAILURE:	Q1			
		Q2			
FO4150	FAILURE:	I/C 1			
		I/C 2, I/C 3.			

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ENTRY	INSTRUCTION	FAILURE DIRECTORY	AGREE	DISAGREE	
		<u>SECTION 04 CONT'd</u>			
FO4160	FAILURE:	I/C 3			
		Q6			
		Q7			
FO4170	FAILURE:	I/C 2			
		I/C 3			
FO4180	FAILURE:	TRACK ADDRESS CIRCUIT			
FO4190	FAILURE:	I/C 3			
		I/C 1			
FO4200	FAILURE:	Q4, Q5			
		Q6, Q7			
FO4210	FAILURE:	I/C 21			
		Q18			
FO4220	FAILURE:	Q18,			
		I/C 15, I/C 39			
FO4230	FAILURE:	I/C 1			
		I/C 21			
FO4240	FAILURE:	I/C 15			
		I/C 3			
FO4250	FAILURE:	I/C 15, Q18			
		I/C 3			
FO4260	FAILURE:	I/C 21			
		Q18			
FO4270	FAILURE:	Q18			
		I/C 15			
		I/C 21			
FO4280	FAILURE:	I/C 26			
		C106, C107			
		I/C 8			

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ENTRY	INSTRUCTION <u>FAILURE DIRECTORY</u>	AGREE	DISAGREE	
	<u>SECTION 05</u>			
FO5010	FAILURE: IC 57, R99, C70 OUT OF SPEC			
FO5020	FAILURE: IC 57, R111, C78			
FO6030	FAILURE: IC 53, IC 57			
FO5040	FAILURE: IC 57, R99, C70, IC50, IC55, R111			
FO5050	FAILURE: RV2 OUT OF ADJUSTMENT, ADJUST TO CONDITIONS IN NO5060. IF THIS IS NOT POSSIBLE RV2, R98 C58, IC55 OUT OF SPEC.			
FO5060	FAILURE: IC 55			
FO5070	FAILURE: IC 54, IC 60, IC 55			
FO5080	FAILURE: D23, R114, C87			
FO5090	FAILURE: D22, R116, C85			
FO5100	FAILURE: RV4			
FO5110	FAILURE: IC 61, Q14 & PASSIVE COMPONENTS ASSOCIATED WITH PLL.			
FO5120	FAILURE: R111, C78			
FO5130	FAILURE: RV3 OUT OF ADJUSTMENT SET UP AS IN NO5070			
FO5140	FAILURE: IC50, IC 56			
FO5150	FAILURE: IC56			
FO5160	FAILURE: VR4 IN NEED OF ADJUSTMENT, SEE ADJUSTMENT GUIDE (PLL)			
FO5170	FAILURE: IC 60, IC 52			
FO5180	FAILURE: IC 56			
FO5210	FAILURE: IC 57, C71, R93 (IC 50)			
FO5220	FAILURE: IC 50, C64, (IC 56)			
FO5230	FAILURE: IC 57, (IC 50)			
FO5240	FAILURE: IC 50, (IC 54)			
FO5250	FAILURE: IC 54, (IC 60, IC 55)			


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ENTRY	INSTRUCTION <u>FAILURE DIRECTORY</u>	AGREE	DISAGREE	
	<u>SECTION 06</u>			
FO6010	FAILURE: IC 45, DISK SPEED INCORRECT			
FO6020	FAILURE: IC 21 (IC 10)			
FO6030	FAILURE: IC 10			
FO6040	FAILURE: IC 10 (IC 11)			
FO6051	FAILURE: IC 11 (IC 65)			
FO6060	FAILURE: IC 26 (IC 31, IC 22, IC 19)			
FO6052	FAILURE: IC 11 (IC 26, IC 65)			
FO6110	FAILURE: IC 22 (IC 46, 29, 25)			
FO6120	FAILURE: IC 31 (R73, C46)			
FO6130	FAILURE: IC 22 (IC 42, IC 19)			
FO6150	FAILURE: IC 45, C48, D14			
FO6160	FAILURE: IC 45, IC 42			
FO6170	FAILURE: D14, C48, IC 45			
FO6180	FAILURE: IC 45, 42			
FO6190	FAILURE: IC 20			
FO6200	FAILURE: R150, R68, J4 CONNECTOR			
FO6300	FAILURE: R88 or J4 HARNESS FAULTY			
FO6310	FAILURE: INDEX/SECTOR TRANSUCER FAULTY, J4 HARNESS FAULTY.			
FO6400	FAILURE: MICROSWITCH FAULTY, CHECK MOTOR WIRING AS SHOWN CIRCUIT DIAGRAM. DRIVE BELT OFF.			
FO6410	FAILURE: IC 44 IC 21			


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ENTRY	INSTRUCTION FAILURE DIRECTORY	AGREE	DISGREE	
	<u>SECTION 07</u>			
F07010	FAILURE: IC 12 (IC 39)			
F07020	FAILURE: R76, C51, R75			
F07030	FAILURE: IC 39			
F07040	FAILURE: IC 40			
F07050	FAILURE: IC 39, IC 40			
F07060	FAILURE: IC 42, R72			
F07070	FAILURE: Q8, R71, R69, R77			
F07080	FAILURE: R74, C49, IC 39			
F07100	FAILURE: IC 12, 24, 33, 35			
F07110	FAILURE: IC 13, 33			
F07120	FAILURE: IC 33, R70, C101, C99, D15, IC 43, IC 12			
F07130	FAILURE: POSITIONER FAULT, CARRY ON WITH FOLLOWING NO FAULT PATHS.			
F07140	FAILURE: IC 33.			

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ENTRY	INSTRUCTION FAILURE DIRECTORY	AGREE	DISGREE	
	<u>SECTION 08</u>			
F08010	FAILURE: FAULTY WIH LED OR HARNESS CONNECTION			
F08020	FAILURE: FAULTY WIH RECEIVER, HARNESS CONNECTION R81, IC 45, TRANSDUCERS NOT ALIGNED			
F08030	FAILURE: IC 45, IC 44			
F08040	FAILURE: IC 44, IC 27, IC 28			
F08050	FAILURE: IC 27, WIH LAMP, R86			
F08060	FAILURE: WIH RECEIVER, HARNESS CONNECTION, IC 45			
F08100	FAILURE: IC 44, IC 18			
F08110	FAILURE: IC 18, IC 13			
F08120	FAILURE: IC 13, IC 18			
F08130	FAILURE: IC 18, IC 32, IC 46, IC 13, IC 22			
F08150	FAILURE: IC 32, IC 1, IC 14, IC 17			
F08160	FAILURE: IC 46, IC 36, IC 37, IC 18, IC 1			
F08170	FAILURE: IC 36, IC 26,			
F08180	FAILURE: IC 26, IC 24			
F08190	FAILURE: IC 46, IC 36, IC 38, IC 28			
F08200	FAILURE: IC 46, IC 36, IC 37, IC 38, IC 28, IC 18, IC 1			
F08210	FAILURE: IC 36, IC 41			
F08220	FAILURE: IC 41, IC 36			
F08250	FAILURE: IC 36, 41			
F08260	FAILURE: IC 41, IC 36			
F08270	FAILURE: IC 36			
F08300	FAILURE: IC 16			
F08310	FAILURE: IC 28, 18			
F08320	FAILURE: IC 18, 16, 26			
F08330	FAILURE: IC 16, 67			
F08340	FAILURE: IC 18			
F08350	FAILURE: IC 32			
F08360	FAILURE: IC 46			

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ENTRY	INSTRUCTION	FAILURE DIRECTORY	AGREE	DISAGREE	
	<u>SECTION 09</u>				
F09010	FAILURE:	IC 25, IC 13, IC 38, IC 37			
F09020	FAILURE:	IC 13, IC 26, IC 16, IC 12, IC 27, IC 67			
F09030	FAILURE:	IC 25, 12, 24			
F09040	FAILURE:	IC 27, R85, HARNESS, FILE OF INDICATOR LAMP			
F09050	FAILURE:	IC 43, R84, Q9			
F09060	FAILURE:	IC 43, R83, Q10			
F09070	FAILURE:	IC 43, R82, Q11			
F09080	FAILURE:	Q9			
F09090	FAILURE:	Q10 STEPPER MOTOR / HARNESS CONNECTION			
F09100	FAILURE:	Q11			
F09120	TRACK 00	TRANSDUCER LED FAULTY, / HARNESS FAULTY, R87			
F09130	TRACK 00	RECEIVER, / HARNESS FAULTY, / IC 45			
F09140		IC 45, 25, 13			
F09150		IC 25, IC 12, IC 24, 29, 30			
F09160		IC 25, IC 12, IC 24			
F09170		IC 12, IC 39			
F09180		IC 24, IC 12			
F09190		IC 12, IC 24, IC 35			
F09200		IC 13, IC 11			
F09210		IC 11, C100, IC 29, IC 30, IC 35			
F09220		IC 37			
F09230		IC 24, IC 29, IC 30, IC 41, 34			
F09240		IC 24, IC 34, IC 41			
F09250		IC 13, 24			
F09300		IC 35, IC 41, IC 34, IC 26, IC 43			
F09310		IC 20, R152			
F09320		IC 43			


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ENTRY	INSTRUCTION	FAILURE DIRECTORY	AGREE	DISAGREE	
	<u>SECTION 09</u> CONTINUED				
F09330	FAILURE:	Q9			
F09340	FAILURE:	STEPPER MOTOR FAULT / HARNESS			
F09350	FAILURE:	Q10			
F09360	FAILURE:	Q11			
F09370	FAILURE:	STEPPER MOTOR OUT OF SPEC			
F09400	FAILURE:	IC 25, 24			
F09500	FAILURE:	SEEKS ARE BEING APPLIED FROM CONTROLLER. DO NOT APPLY SEEKS UNLESS INSTRUCTED.			

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ENTRY	INSTRUCTION FAILURE DIRECTORY	AGREE	DISGREE		
<u>SECTION 10</u>					
F10010	FAILURE: IC 46				
F10020	FAILURE: IC 25, INTERMITTANT TKOO FAILURE INCLUDING HARNESS etc. IC 13, 37, 38				
F10030	FAILURE: IC 25, 12, 24, 29, 30, INTERMITTANT TKOO FAILURE INCLUDING HARNESS etc.				
F10040	FAILURE: IC 12, IC 39				
F10050	FAILURE: IC 25, 24				
F10060	FAILURE: IC 24, IC 12				
F10070	FAILURE: IC 12, IC 24				
F10080	FAILURE: IC 24, IC 34, 41				
F10090	FAILURE: IC 37				
F10100	FAILURE: IC 13, 24				
F10110	FAILURE: IC 35, 41, 34, 26, 43				
F10120	FAILURE: IC 25, 13, 37, 38				
F10130	FAILURE: IC 25				
F10140	FAILURE: IC 37, 38				
F10150	FAILURE: IC 37, 38, 29, 30				
F10160	FAILURE: IC 11, IC 29, 30, 35				
F10200	FAILURE: IC 13, IC 27, IC 67				
F10210	FAILURE: FILE OF INDICATOR LAMP, IC 27, R85				

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ENTRY	INSTRUCTION FAILURE DIRECTORY	AGREE	DISGREE		
<u>SECTION 11</u>					
F11010	FAILURE: IC 44, 67				
F11020	FAILURE: IC 28, IC 1, IC 17				

 GLENFORTHES, SCOTLAND UK		PREP S. Barnes D. Hall CHKD	PROJ APPD	A- 1881 330J PAGE 69 of 69	REV A
PROPRIETARY TO SURROUGH'S CORPORATION - NOT TO BE REPRODUCED OR USED FOR MANUFACTURING PURPOSES EXCEPT ON SURROUGH'S ORDER OR PRIOR WRITTEN CONSENT.			MINI-DISK CONFIDENCE/FAILURE LOCATION ROUTINE.		
ENTRY	INSTRUCTION <u>FAILURE DIRECTORY</u>	AGREE	DISGREE		
	<u>SECTION 12</u>				
F12010	FAILURE: IC 24, 30, 29				
F12020	FAILURE: IC 24, 30, 29, 41, 34				
F12030	FAILURE: IC 30, IC 38				
F12040	FAILURE: IC 29, 37				
F12050	FAILURE: IC 30				
F12060	FAILURE: IC 29				
F12070	FAILURE: IC 37 and 38 EQUALLY SUSPECT				
F12080	FAILURE: RESET OPERATION OF IC 35 SUSPECT, IC11 - C100				
F12090	FAILURE: IC 13, IC 11				
F12100	FAILURE: IC 38				
F12110	FAILURE: IC 37, 13, 11				
F12120	FAILURE: IC 13, 24				
F12140	FAILURE: IC 26, 25				
F12150	FAILURE: IC 25				
F12160	FAILURE: IC 12, 44				
F12170	FAILURE: IC 44, 28, 67				
F12200	FAILURE: POSITION OF TRACK ZERO TRANSDUCER SHOULD BE ADJUSTED (ADJUSTMENT GUIDE)				
F12210	FAILURE: TRACK ZERO ALIGNMENT, RADIAL ALIGNMENT (ADJUSTMENT GUIDE)				
F12300	FAILURE: IC 37, 13				
F12310	FAILURE: IC 13, 11				
F12320	FAILURE: IC 29, 30, 35, C100				
F12330	FAILURE: IC 11, 29, 30, 35				
F12400	FAILURE: IC 35, 34, 41, 24				
F12024	FAILURE: IC 32, 24.				