Microprocessor programming has never been easier. An all-in-one development station lets you link assembly-language, PL/M and Fortran programs. ROM-based software includes an assembler, a text editor, and all peripheral interfaces. In-circuit emulation provides hardware and software debugging and more. See p. 117.
A new space saver from Bourns.

Now there's a new dimension in space savings... the Model 20 Trimpot® SIP Cermet Trimmer... a standard SIP, designed to meet your high density PC board needs.

With no sacrifice in performance, the Model 20 trimmer occupies only 25% of the precious board space used by comparable DIP configurations and only 50% of that used by conventional 3/16" rectangular trimmers. Featuring .100-inch spacing and a lower board profile... only .185" inches off the board... it's priced at a modest 75¢* in 1,000 to 4,999 quantities. And, it's available in 18 standard resistance values ranging from 10 ohms to 5 megohms.

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TRIMPOT PRODUCTS DIVISION, BOURNS, INC., 1200 Columbia Avenue, Riverside, CA 92507. Phone: 714 761-5050 — TWX: 910 332-1252.

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*Domestic U.S.A. price only.
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Compare the prices against those of available competitive units.

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66 N. First Ave, P.O. Box 190
Beech Grove, Indiana 46107
Telephone: (317) 783-3221
TWX 810-341-3226

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Need RF Components In a Hurry?

Mini-Circuits' distributors now stock Mixers, Amplifiers, Power Splitters/Combiners, Directional Couplers, RF Transformers, Limiters and Frequency Doubler for quick off-the-shelf delivery...everywhere.

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For complete specifications, performance curves and application information, please refer to '77-'78 MicroWaves' Product Data Directory (page 291—page 482), '77-'78 Gold Book (page 817—page 1008) and '77-'78 EEM (page 3006—page 3055). For your own personal 100-page RF COMPONENTS DESIGNERS' GUIDE, circle Reader Service Number 3.
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Cover: Photo by Rob Janoff, courtesy of Intel Corp.
Advanced Micro Devices announces an advanced course in microprogrammable microprocessing.

Step by step, function by function, month by month, we'll build a fast, powerful, microprogrammed machine.

And on December 31, 1978, you'll know what we know. As it turns out, that's quite a lot.

BUILD A MICROCOMPUTER THIS YEAR.
CHAPTER ONE: COMPUTER ARCHITECTURE.

Modern digital processors are built using one of two techniques: A fixed-instruction MOS processor, such as the 8080A or 8085, or a microprogrammed TTL design. Because of the extremely low cost and small size of the microcomputer built around a fixed-instruction microprocessor, this approach is dominant.

But, not all problems can be solved with an 8080A or 8085. They may not be fast enough. And, applications requiring more than 8 bits of precision, substantial amounts of arithmetic processing, adherence to a pre-defined instruction set or blazing speed need something more than MOS has to offer. You need microprogramming capability. You need bipolar LSI.

During the year, you'll be meeting several new members of the Am2900 Family, a series of low-power Schottky LSI devices specifically designed for microprogrammed machines.

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Specialists in Power Conversion Equipment
Across the desk

The hazards of JEDEC

The Focus on Power Transistors and Thyristors (ED No. 23, Nov. 8, 1977, p. 52) contains much good information. Especially pertinent is the forthright advice offered on the hazards that lie in wait for the designer who puts too much faith in JEDEC numbers, and not enough reliance on an understanding of the uncontrolled parameters that can so easily change without warning.

Design engineers should note that the “H” suffix, as applied in RCA’s (and others’) 2N3055H, represents a marketing game, not a JEDEC-recognized and authorized suffix. The same part may emerge soon as the 2N3055A, but meanwhile designers need to be careful in assuming JEDEC control in a situation where manufacturers have chosen to disregard well-established JEDEC rules.

Perhaps less clear is your Focus’s discussion of temperature derating. The author would have been more to the point to say: “If you want to dissipate 115 W and permit the junction temperature to go to 200 °C, then the case temperature for a device with a thermal resistance of 1.5 °C/W has to be held at 

\[(200 - 1.5 \times 115) = 27.5 °C.\]

The designer would then face two important questions: How realistic is it to try keeping the case that cool in his product’s operating environment? And how close to the rated maximum of 200 °C dare he go in light of his overall reliability constraints?

Furthermore, the author doesn’t even mention the use of heats sinks or heat radiators. Certainly it goes without saying that every 115-W device is intended to be cooled by a heat sink or radiator.

Lawrence W. Johnson
Hewlett-Packard
3500 Deer Creek Rd.
Palo Alto, CA 94304

Misplaced Caption Dept.

Hang on a moment. He’s coming out of the meeting. I’ll see if he can talk to you now.


Our good data sheets could be better

I read your Focus article on Power Transistors and Thyristors (ED No. 23, Nov. 8, 1977, p. 52) with keen interest. You correctly point out many of the wrongs and rights that the semiconductor industry does in attempting to specify these power products properly.

However, some clarifying points are in order. The attempt by RCA to differentiate the single-diffused from the epitaxial-base 2N3055 was done by adding a suffix “H” for the Hometaxial single-diffused structure, not for the...

(continued on page 8)
epitaxial one. JEDEC has now ruled RCA as well as Motorola and others that followed RCA, out of order, and the “H” will not be used.

Motorola, prior to the use of the “H” suffix by RCA, had requested an “A” suffix in compliance with JEDEC rules and believe it will be granted.

The JEDEC rules, rightly or wrongly, do not identify process information. JEDEC registration merely tries to provide a standard for electrical, mechanical and thermal ratings and specs, which, if met, should allow a semiconductor device to work in the same application despite other differences. But interchangeability problems will still persist, because there are subtle differences even with processes that appear identical. To help prevent such problems, Motorola has a standard operating procedure, whereby users are notified of all critical process changes early enough for trial and acceptance prior to product shipment.

Your Focus states that safe-operation-area (SOA) curves are included because of JEDEC, implying that without JEDEC they might not be provided. But what is JEDEC? The power-transistor committee consists of 11 representatives from the semiconductor industry—Delco, Fairchild, GE, GSI, IR, Motorola, RCA, Solitron, STC, TRW and Westinghouse. IBM supplies the only “user” representative. And the National Bureau of Standards has observers at most meetings. So, in reality, power-transistor companies developed the SOA concept and included it willingly on data sheets. Motorola used the SOA concept back in the early germanium days. And RCA has been a pioneer for a self-imposed “everything-including-the-warts” concept.

Also, the arbitrary use of a 25-C case temperature is not as ridiculous as it seems. We know of several applications at very high currents that use water cooling to keep the case temperatures at no more than 30 C. So where does the supplier draw the line? Moreover, both the supplier and the user can easily measure case temperature.

Nevertheless, your criticism of the lack of high-temperature specifications is valid. Still Motorola, especially for its Switchmode series, gives seven limit specifications at 100-C and one at 150-C case temperatures, and the allowable power dissipation both at 25-C and 100-C case temperatures. Also, the reverse-bias-safe-operating-area (RBOSA) limit curves are good at 100 C, and the forward-bias SOA have derating curves. These curves are “specified ratings,” not typical design information.

Your comments on Es / b are very good. We agree, that the RBOSA curve, which is a clamped voltage-current limit, is a more useful concept, since it avoids the single-circuit fallacy of Es / b, and reflects what most users find very practical. Of course, power transistors with high Es / b capability would permit snubbers and clamps, to be eliminated, but so far this is beyond the state of the art. However, your statement that Es / b depends on several variables should not have included the use of a clamp, since Es / b, by definition is an unplamped capability.

Despite these minor clarifications, we feel that the over-all effect of your article is good—to warn the user to study the data sheets in detail, which we welcome. We feel Motorola's data sheets are very good, now. Can they be improved? You bet! And we are trying to do so with each new issue.

Ralph Greenburg
Manager
Power Products Planning
Motorola Inc.
5005 E. McDowell Rd.
Phoenix, AZ 85008

A diode is missing, and ‘8’ should be ‘B’

Some typographical errors have crept into my Idea for Design, “Buffer Circuit for Line Driver Protects Against Shorts and ±325-V Surges” (ED No. 23, Nov. 8, 1977, p. 102). Fig. 3 should have a 1N4004 rectifier in the line between the 2N5416 collector terminal and the junction with the two 51-kΩ resistors that lead to the 0.01-µF output capacitor. The anode of the 1N4004 should be connected to the collector. Also, all zener diodes shown are 1N5363Bs, not 1N53638s.

Roxton Baker
Design Engineer
C-E Power Systems
Combustion Engineering Inc.
100 Prospect Hill Rd.
Windsor, CT 06095

Me an editor?

If you'd like to be among the first to know (and write about) what's going on in the electronics industry, you might enjoy being an editor.

We have openings at our office in Rochelle Park, NJ. Call Ralph Dobriner at (201) 843-0550.

A matter of clarity

Our article, “Exploit Existing NOVA Software by Designing Computer Systems Around the MicroNOVA...” (ED No. 19, Sept. 13, 1977, p. 54) contained several inaccuracies.

The last two sentences of the paragraph before the subhead, “Other circuits help the processor” (p. 56), should read: “During a Refresh operation, the CPU specifies a group equivalent to 1/64 of all the memory locations to be refreshed, but transfers no data. The refresh address is selected by a 6-bit refresh-address register placed on the lower six address lines.”

Daniel Falkoff
Design Engineer
Natalio Kerllenevich
Design Engineer
Philip M. Kreiker
Design Engineer

Data General
Route 9
Westboro, MA 01581

'Science' of name changing

It's a funny thing. Everybody knows who heals people (doctors do that), and everybody knows who defends people in court (lawyers do that). But nobody really knows who sends our men into space and builds our computers. The news media always use the term “scientists.”

Is it any wonder that the engineering community does not have the status of doctors and lawyers? Nobody knows who we are or what we do. It wasn't until I was a sophomore in college that I discovered that what I wanted to be was an "engineer." I'd thought I wanted to be a scientist.

Maybe we should change our name. "Electrical and Electronic Scientists" has a very good ring—and would be recognized instantly by the public.

Richard Walbaum
President
Flow Master
2900 Baylor St.
Bakersfield, CA 93305

ELECTRONIC DESIGN 4, February 15, 1978
Whether used on the bench or combined with a desktop computer for automatic measurements, this 1500 MHz spectrum analyzer brings new power to frequency-domain measurements.

Microprocessor-based 1.5 GHz spectrum analyzer offers state-of-the-art performance and usability

Ten-hertz resolution at 1500 MHz! That’s just one of the major contributions of the new HP 8568A Spectrum Analyzer. This 100 Hz-to-1500 MHz analyzer’s measurement range is $-137$ to $+30$ dBm, it measures signal frequencies to counter accuracy, it can measure 50-Hz sidebands that are 60 dB down, and its spurious-free dynamic range is $\geq 85$ dB.

As you can see from the illustration, the analyzer’s functions and operating state are keyboard selected. The instrument’s internal microcomputer administers controls, calculates and manipulates data (including correcting for hardware inaccuracies), and provides new and useful operating features. Among these are tunable markers that greatly speed measurements, automatic peak search, automatic signal track, and complete CRT labeling of all pertinent operating conditions.

(continued on third page)
Catch those elusive errors with HP’s new 150 Mb/s error detection system

At a time when high speed digital transmission is beginning to be commercially exploited, Hewlett-Packard introduces a new 150 Mb/s bit error rate measurement system. The 3762A Data Generator and 3763A Error Detector are specifically designed for field evaluation, commissioning and maintenance of digital line and radio equipment. The measurements performed by the system are:

- binary bit-by-bit error detection on binary and coded signals
- clock frequency offset generation and measurement

The 3762A/3763A system strikes a fine balance between dedication and flexibility. Thanks to a wide variety of options available, it can be configured to meet the specialized requirements of existing systems such as cable and radio. At the same time, flexibility is retained to meet the developing needs of new systems such as optical fiber. Choices of internal clock frequencies, data formats, interface levels and impedance are available.

Key new features of the 3762A/3763A include a \(2^{23} - 1\) PRBS test pattern and new interface code for high speed systems, input equalization for interconnecting cable loss, and zero block injection to check the pattern dependence of systems. Burst gating inputs allow the 3762A/3763A to operate in burst mode for TDMA satellite applications. To extend the capability further, outputs from the 3763A to an external counter, printer and pen recorder allow unattended long term measurements and error distribution analyses.

For complete details, check B on the HP Reply Card.

Hewlett-Packard’s new 3762A/3763A being used to check a 120 Mb/s coaxial line system.

Programmable IC Tester cuts production costs

HP’s IC tester is easy for your operator to use and its capability of generating the needed test programs gives you full control of testing schedules.

More and more people are finding that pretesting ICs cuts production costs significantly. For example, finding the bad ICs before they get loaded into your PC board can save you up to $10 each.

HP’s cost effective solution to IC testing is the new 5046A Digital IC Test System. It is easy for your operator to use and you retain full control of the test programs. Choose from any of the standard tests in our growing library of 1200 programs, write your own programs, or buy ours and modify them to suit your needs.

The HP 9825A computing controller comes with the system, and you can use it for other tasks when it’s not needed for writing test programs. If you already have a 9825A, order the 5046A without it and save money.

With the 5046A you get HP’s versatile 5045A Digital IC Tester which is capable of testing all major logic families including ECL, CMOS, TTL, and DTL. The programming system gives you easy access to the tester’s unique versatility. Any IC pin can act as input, output, or clock. Currents and voltages can be set up individually for each IC pin and 16 separate tests can be made on each IC.

Interface to a wide range of automatic IC handlers—even many of the lower cost ones—is simple, reliable, and economical with standard options.

For more information, check C on the HP Reply Card.

New SLMS makes major contribution to high-density FDM system management

HP’s new 3747A/B Selective Level Measuring Sets, extended frequency versions of the current 3745A/B SLMS’s, are designed to make fast, accurate selective level measurements on frequency-division multiplexed (FDM) baseband signals. A built-in frequency synthesizer gives accurate and stable tuning to the precise frequency at which the measurement is to be made, thus simplifying the tuning of the SLMS. Tuning is to a 10 Hz resolution over the frequency range 10 kHz to 90 MHz.

Three basic measurement filters are provided: 1) a 22 Hz Pilot filter; 2) either a 3.1 kHz or 2.5 kHz Channel filter (for 4 kHz or 3 kHz channel spacings); and 3) a 48 kHz Group filter. Weighted and notch filters for noise measurements are available as options.

The SLMS is internally controlled by a microprocessor, which provides several ease-of-use and time-saving features. As well as tuning exactly to an entered frequency, the SLMS can refer to the BELL or CCITT multiplex frequency plans in its memory and automatically tune to the correct frequency at any level in the multiplex. (Other frequency plans can be installed by special order.) Thus, 250 pilot measurements could be made in about 2 minutes, or 2700 channel powers or carrier leaks could be measured in about 15 minutes.

For more details on this new SLMS check D on the HP Reply Card.

Fully programmable via the HP Interface Bus, the SLMS’s can form the basis for powerful, fully-automatic surveillance of multichannel communications systems.

MEASUREMENT-COMPUTATION NEWS
Application note tells how to calibrate accelerometers automatically

A new, three-page application note, "Automatic Accelerometer Calibration", from Hewlett-Packard describes how HP's Data Acquisition System is being used to calibrate accelerometers. In tests made by the U.S. Army, accelerometers are used to measure the ground impact force on the cargo and its container package in order to relate the cargo's survivability to the force.

For your complimentary copy of this Application Note 204-1, check W on the HP Reply Card.

Powerful new spectrum analyzer

(continued from first page)

Two-tone test using 8568A shows 3rd-order intermodulation products > 85 dB down.

All of the keyboard functions are remotely programmable via the HP Interface Bus such that the 8568A analyzer and HP 9625A desktop computer combine to form a friendly yet powerful automatic system with tremendous measurement capabilities. Challenging applications that can benefit from the spectrum analyzer-computing controller combination include electromagnetic interference (EMI) testing and spectrum surveillance.

To learn more about this revolutionary advancement in spectrum analysis, check A on the HP Reply Card.

Make timing measurements easily and accurately with two new high-frequency scopes

Now you can choose from two new HP delta time oscilloscopes which make timing measurements easily, with greater repeatability and one percent accuracy— the 1715A with 200 MHz bandwidth, or the 1725A with 275 MHz bandwidth. Both offer an optional, built-in DMM for direct delta time readout, plus autoranging AC/DC volts, amps, and ohms.

A large 8×10-centimeter CRT provides a dual, bright, crisp display on which timing measurements can be made conveniently and accurately, using the Hewlett-Packard developed delta time technique. This technique makes timing measurements such as transition times, propagation delay, clock phasing, and other high-speed digital timing measurements faster and with more repeatability than was previously possible with standard delayed sweep oscilloscopes. For easier percentage measurements, reference lines of 0 and 100% are 5 divisions apart so that each division represents 20% of the reference amplitude. Auto focus and intensity control circuits reduce the need for frequent intensity and focus adjustments, as well as improving CRT life.

Measurement capability is further enhanced by the logically arranged, easy-to-use front panel controls which speed operator familiarization, reduce the possibility of measurement errors and thus improve accuracy. Selectable 1 MΩ and 50 Ω input impedance make it easy to select the best input impedance for your measurement. Sweep speeds from 10 ns/div to 0.5 s/div allow you to expand your signals for maximum resolution and a ×10 magnifier provides one ns/div sweep speed for critical timing measurements.

In addition to faster and more accurate delta time measurements, both new oscilloscopes offer you a selection of channel A or B as the starting point for delta time measurements. This often eliminates the need to move probes and simplifies trace overlap for zeroing. But you can select conventional delayed sweep with the flip of a switch, for simple trace expansion.

The optional autoranging 3½-digit DMM can be factory installed. Or, for easy field installation, a kit is available. Another option, HP's "Gold Button", gives you pushbutton selection of either time domain or data domain when the 1715A or 1725A is used with HP's 1607A Logic State Analyzer.

For more details, check E on the HP Reply Card.
Eleven software libraries for HP's new System 45 save costly programming time

System 45, the newest member of the Series 9800 Desktop Computers, combines with our 11 software libraries to save you costly programming time. Depending on your application, System 45 with its CRT and our software provide either a total solution or convenient building blocks for developing your specialized programs. This is what's available:

- **Utility Library**-consisting of two dozen useful, general purpose programs, free with each System 45.
- **Numerical Analysis**-50 powerful routines to handle fourier analysis and differential equations.
- **Regression Analysis**-a comprehensive package of programs for linear, stepwise and polynomial regression.
- **Basic Statistics & Data Manipulation**-programs to perform a wide variety of operations from means and standard deviations to complicated correlation coefficient calculations.
- **Waveform Analysis**-a unique group of programs to analyze large volumes of data based on fast fourier transform routines.
- **Management Science**-four libraries for text processing, linear programming, forecasting and graphics, and network analysis.
- **Business Administration**-libraries for payroll and inventory control.

For more information on System 45, check G on the HP Reply Card. For additional details on each of the software libraries, check H for Numerical Analysis; I for Regression Analysis; J for Basic Statistics & Data Manipulation; K for Waveform Analysis; L for Management Science; and M for Business Administration.

New meter makes fiber optic power measurements with ease and accuracy

A new power sensor/power meter combination designed specifically for measurements of signal power in single optical fibers is now available from Hewlett-Packard. By using the well-known detection principle of a thermistor bridge, absolute power is indicated on a meter.

The HP 84801A Power Sensor contains the thermistor elements, one of which is optically coupled to a single fiber pigtail, one meter long with a core diameter of 200 µm (Dupont PFX-5120R, plastic-clad). This large diameter relative to commonly-used single fibers permits low loss couplings, Numerical aperture is 0.4, nominal.

The HP 432A Power Meter operates the thermistor sensor in a balanced bridge. Since the thermistor is virtually a black body, it efficiently converts the optical power to heat. This tends to unbalance the thermistor bridge and the power necessary to rebalance is metered. Thus, direct, absolute power measurement is obtained with high confidence and convenience.

Absolute accuracies ranging from 7% to 14% are specified over a dynamic power range of 1 µW to 10 mW. The full spectral range is 600 nm to 1200 nm, with four calibration points traceable to the National Bureau of Standards at 650, 820, 1050 and 1150 nm.

If you'd like more information on this first Hewlett-Packard entry into the fiber optic communications field, check F on the HP Reply Card.

Optical power in single fibers can now be measured with HP's 84801A/432A system.

New microwave synthesizer application note

Application Note 218-2, *Obtaining Millihertz Resolution from the 8671A and 8672A*, the second from the Microwave Synthesizer Series, is now available.

The note describes how the HP 8671A and 8672A Microwave Synthesizers can be used in combination with other HP synthesizers to obtain resolution as fine as 1-3 mHz across the 2-18 GHz band. Some sample calculator sub-routines are given to aid programming of the system.

For your complimentary copy of Application Note 218-2, please check X on the HP Reply Card.
Advances in time interval instrumentation

With HP's 5370A Universal Time Counter you can measure time intervals with a resolution of 20 picoseconds—that's five times better than counters did before. It also automatically computes statistical data. And the new HP 5359A Time Synthesizer generates pulses whose time delay and width are adjustable in 50 picosecond steps—that's 20 times better than delay generators did before.

Uses for these advancements are in semiconductor testing, radar and laser ranging, digital communications, computer testing, nuclear studies, and calibration. The 5359A can also generate precise delayed sweeps for oscilloscopes and very accurately time-position the external gates of frequency counters.

Using the 5370A's keyboard, you can quickly set up to compute pulse jitter, minimum and maximum time interval, mean time, and standard deviation. The 5370A is also a full capability universal counter, measuring period and frequency from 0 to 100 MHz with up to eleven digits of information in one second measurement time.

Major specifications for the 5359A Time Synthesizer include: delay range, 0 to 160 ms; pulse width, 5 ns to 160 ms; amplitude adjustable 0.5 to 5 V into 50 ohms; offset adjustable ±1 V. All are controllable by the 5359A front panel keyboard or system commands. Calibration is automatic.

For details on the 5359A, check N on the HP Reply Card. For the 5370A, check O.

Programmable, 4-color plotters output computer data with fidelity and speed

Two new HP plotters, the 7221A and the 9872A, prepare high quality, four-color plots with unprecedented ease. Controlling these plotters is an HP designed microprocessor which enables dramatically fast, precise, and convenient plotting not available in earlier plotters. Although similar in appearance, the two plotters were designed to fulfill two different needs: 1) direct connection to desktop computers, or other controllers using a standard parallel interface, and 2) connection in a serial communication link to a host computer or terminal.

Both plotters feature user-initiated confidence test to verify their overall mechanical and electrical operation. A built-in self-test allows service personnel to perform a series of tests without the use of external test equipment.

Graphic Plotter for Local Operation

The 9872A interfaces to your controller through the HP-IB interface. Using an easily understood, two-letter mnemonic graphics language, the HP-GL, you can start plotting with only a minimum of programming experience. With 38 graphic instructions, you can select any of four pens, designate any one of five resident character sets, and define any one of seven line types. To enhance the graph, you can change the slant, size, and direction of the characters, program the pen speed, draw arcs with tick marks, and identify traces with symbols. Window plotting permits error-free off-scale data handling.

For further details, check Q on the HP Reply Card.

Remote Terminal Graphic Plotter

The 7221A, with an RS-232C/CCITT V.24 interface, features three modes of operation for efficient use of expensive computer time. These three modes are: direct communication with the terminal, standby, and on-line to the host computer. In addition to sharing many of the graphic functions of the 9872A, the 7221A further reduces communication time through resident arc and circle generation, programmable macroinstructions, definable dashed lines, internal characters, and built-in buffer.

Check P on the HP Reply Card for details.

Convert HP-PLOT/21 for use on your system

A further enhancement of the 7221A is provided by the HP-PLOT/21 software package. Consisting of 86 FORTRAN IV subroutines for HP 3000 Series II, GE MARK III timeshare and TYMESHARE X systems, HP-PLOT/21 makes the advanced capabilities of the 7221A easily accessible. For use with other systems, our Applications Note 229-1 provides information to help the system programmers determine the feasibility of converting the HP-PLOT/21 to operate on their systems.

For your complimentary copy of AN 229-1, check Y on the HP Reply Card.

Microprocessors make both models easy-to-use and versatile in benchtop or systems use, as in the above VCO tester.
Up to 1.8 million bytes of fault-control semiconductor memory—at 5¢ a byte*

Hewlett-Packard's high-capacity memory packs one megabyte of "fault-control memory" in a compact 131 cm (12¼ in) high HP 21MX mainframe—all for only 5 cents a byte.

Hewlett-Packard made an early commitment to semiconductor memory. Since 1974, we have been able to offer memory to our customers at price decreases averaging 30% a year, in part the result of increased memory density.

The newest module, HP 12747, quadruples capacity from 32k to 128k bytes utilizing 16k-bit, N-Channel, MOS/RAM memory chips—the first in use by a major manufacturer of small computers.

Memory for 21MX K, M, and E series computers and HP 1000 computer systems can now be expanded to a maximum of 1.8 megabytes. Upgrading from your present 21MX system to larger memories is also possible.

A new fault-control memory system provides for detection and correction of all single bit memory errors using the standard 21-bit Hamming code. But, because there is always the chance of a double bit error, we added a 22nd parity bit to the Hamming code, enabling us to detect all double errors. Programs will continue running and data will be protected even if a memory chip malfunctions.

Fault control memory is available as an optional controller with its associated check bit arrays.

Density and reliability are not the only advantages of semiconductor memory. Cost savings is another.

*Domestic U.S.A. only

For more information, check R on the HP Reply Card.

New DC power supply catalog from HP

Choosing the right power supply for your application is easy with HP's new DC Power Supply Catalog. This 128-page catalog contains product descriptions, photographs, outline drawings, specification, and prices for HP's complete line of power supplies covering the range from 10 W to 11 kW. Products include:

- General-purpose lab and system power supplies
- Precision voltage and current sources
- Digitally programmable power sources

Included is a section detailing several methods to control DC power supplies using the HP Interface Bus. In addition, another section covers power supply ac and load connections.

For your free copy, check Z on the HP Reply Card.
New packaged GaAs FET offers low noise and moderate power

HP increases distribution outlets for semiconductor components

HP's Schottky diode—world's first in a DO-35 package

Linear output power of new 1-micron GaAs FET provides the design engineer with a superior device usable over the broad range of frequencies, 2 to 12 GHz.

HP's new packaged GaAs FET, the HFET-1101, is a cost-effective and rugged transistor which is easy to work with and yields superior performance. It is characterized for low noise and moderate power requirements in the 2 to 12 GHz range, with typical noise figure of 1.6 dB (2.2 dB max) and 11 dB typical associated gain at 4 GHz.

When tuned for maximum output power at +5 dBm input, linear output (1 dB compression) is typically 35 mW at 4 GHz and 25 mW at 8 GHz. The HFET-1101 is useful as a low noise second stage or output stage in the 2 to 8 GHz range for broad and narrow band applications such as land and satellite communications and radar.

The HFET-1101 is packaged in the HPAC-100A, a rugged, hermetic 2.5 mm (0.1 in) square metal/ceramic package. High reliability tested versions of this device can be specified. A chip version, the HFET-1000 is also available.

For performance specifications on both the package and chip versions, check S on the HP Reply Card.

With a low forward voltage of 410 mV, the HSCH-1001 (1N6263) is a functional replacement for many germanium diodes.

It offers switching speed in the picosecond range, a breakdown voltage of 60V, a temperature rating of -65°C to +200°C and is in a hermetic-glass package rugged enough for automatic insertion. Applications include waveform clipping, clamping and sampling, transistor speed-up, RF signal detection, and power monitoring.

As the world's first Schottky barrier diode in the industry standard DO-35 package, the HSCH-1001 gives you a reliable, high performance alternative for your general purpose switching needs.

For details, check T on the HP Reply Card.

The HSCH-1001 is offered in the low-cost DO-35 hermetic package. It is rugged enough for automatic insertion equipment and can be supplied in tape or reel.
New HP-IB digital multimeter cuts cost of data gathering

Hewlett-Packard's new 3438A DMM has a built-in HP-IB interface for automatic low cost data collection. This 3½-digit multimeter has five full functions with volt-ohm autoranging. Ten milliohm and 100 µV AC and DC sensitivities make the 3438A excellent for gathering data from various types of transducers.

The 3438A may be used in a talk-only mode with a companion HP 5150A printer to record data on tape. The 5150A recorder includes a clock pacer to control untended data gathering. Transducer outputs can be normalized or linearized while measurements are made, using a controlling computer such as the HP 9825A. Data may also be stored in the HP 9825A tape cassette for later use.

Whether you use HP's 3438A in manual or autoranging mode, in a system or on the bench, it will always indicate with lighted annunciators, the most commonly understood engineering units. If an improper selection of function and range is chosen, an overload indication on the display informs the operator that a measurement error was made.

For additional information on this item, check V on the HP Reply Card.

Now you can have a 3½-digit multimeter for use in low-cost data acquisition. Used with a controlling computer and scanner, data may be recorded on cassette tape for later use, or analyzed as it is received for process control.
Want mass terminations for I/O interconnecting? We have the widest choice.

Now Scotchflex brand DELTA Connectors bring the proved labor-savings of 3M's mass termination system to subminiature connections. DELTA series components include pin and socket connectors, junction shells, 25-conductor flat cable and strain relief clips. These system assemblies interface directly with all other industry standard "D" series subminiature connectors. They're also compatible with all connectors in our complete Scotchflex line.

Our broad line of Scotchflex socket connectors includes a variety of 12 different sizes and center spacings to fit standard wrap panels and custom configurations. Also offered are Scotchflex card-edge connectors in sizes for 20 to 50 conductors.

Only 3M offers you so wide a choice of mass terminating flat cable and system components for fast, economical assembly of I/O interconnections between modules or sub-assemblies in your equipment designs. Plus off-the-shelf availability from experienced distributors, and the unmatched experience of the people who pioneered electronic mass terminations.

For more information on Scotchflex products call 612-733-3350.

Scotchflex systems from 3M. The source.

A family of Scotchflex male plug connectors is now available in sizes from 10 to 50 contacts to mate with Scotchflex socket connectors for T-tap or mid-span connections or rack and panel applications.

"Scotchflex" is a registered trademark of 3M Co.

See our catalog in EEM, page 1056
Fluke Counters.

What's in a name?

CIRCLE 7 FOR 1900 SERIES LITERATURE
A glance at our counter guide shows how broad a selection you have when you choose the Fluke name.

Pick resolution from six to nine digits, and top-end frequencies from 80-1250 MHz. Notice that all Fluke counters are multi-function, from the frequency/period/totalize capability of the 1900A to the six-function 1953A Universal Counter-Timer. All models have input signal conditioning for reliable readings in the presence of noise, distortion, and ringing, and most have attenuators for increased dynamic range. So for R&D, GENERAL BENCH or PRODUCTION LINE applications, buy exactly what you need over an affordable, performance-effective price range.

For FIELD MAINTENANCE AND SERVICE, most of the line is available with an optional rechargeable battery pack installed inside the compact, portable case. Let autoranging keep the display full at all times for "hands-off" convenience, and rely upon autoreset to eliminate erroneous partial-readings. In the Fluke tradition, they'll take a real field beating, too.

COMMUNICATIONS people find the 1911A through 1925A attractive for VHF/UHF measurements. They're so sensitive that a simple optional whip on the 50-Ohm fuse-protected input makes transmitter checks quick and easy. With automatic clean dropout, you'll always be right because the reading goes to zero with a fading signal. They're RFI-shielded, and TCXO timebases are available for the kind of high accuracy you might need over environmental extremes. And if you choose the 1920A, you can get a resolution multiplier for high-resolution audio measurements.

If you have an ADVANCED BENCH or SYSTEMS application, the 1953A was designed for you. With fully programmable ranges and functions (including trigger level), the 1953A is available with IEEE-488 or BCD parallel options, at a price more than $1,000 less than the competition's similarly-equipped models.

CALL (800) 426-0361, TOLL FREE and let us show you how the Fluke name is as meaningful in counters as it has been for other fine test and measurement instrumentation for the last 30 years. It's your assurance you've bought the best, backed by more than 32 service centers in 18 countries, worldwide.

John Fluke Mfg. Co., Inc., P.O. Box 43210, Mountlake Terrace, WA 98043. In Europe, contact Fluke (Nederland) B.V., P.O. Box 5053, Tilburg, The Netherlands. Tel. (013) 673973. Telex: 52237.

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CIRCLE 8 FOR 1953A LITERATURE ONLY

Electronic Design 4, February 15, 1978
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MEPCO/ELECTRA CR Series resistors can broaden your design capabilities quickly.

Choose from 1/8, 1/4, or 1/2-watt power ratings. In tolerances of 2 or 5 percent. And in the widest resistance range ever offered in a high-performance low-cost film resistor.

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The CR Series carbon film resistors feature a low negative temperature coefficient, lower power and voltage coefficients, plus higher initial accuracy, long-term stability and much greater moisture resistance than carbon comps.

The industry's going to carbon film resistors. Come on over to the "Most Wanted" Comp Killers from Mepco/Electra. Write or CALL M/E for technical data and samples. MEPCO/ELECTRA, INC., Columbia Road, Morristown, N.J. 07960 (201) 539-2000.
News scope

FEBRUARY 15, 1978

Aircraft weather radar puts radome on the wing

The first practical weather radar system for single-engine, general-aviation aircraft combines a small, lightweight radar with a high-efficiency radome that slips over the leading edge of the wing.

This combination succeeds where other experimental radar systems have failed, mainly because previous radome systems were too bulky and heavy. And a dipole array that would sit on a plane wing's leading edge has not yet proven practical. The radome, from Norton Co. (Akron, OH) weighs just 10 lb.

The radar, a Bendix Avionics RDR-160, is a 6-kW, X-band system with a 10-in. dish antenna specifically designed for single-engine planes. The Bendix Ft. Lauderdale Division has made the radar about 8 lb lighter than a standard weather radar by reducing the packages in the radar system from three to two.

One 12-lb section, sitting on the wing, integrates the receive-transmit electronics with the antenna system. Weight and space are saved here by using, for the first time, a positive-pulse magnetron, instead of the usual strapped-vane.

Space requirements are tightened further by eliminating a driving mechanism used in a standard weather-radar antenna to tilt the entire dish assembly up or down. All this has been replaced by a flexible feed for the tilt function. Like a standard antenna, the Bendix unit scans left and right in azimuth.

The other radar package is a 7-lb panel-mounted scope indicator unit that presents a picture of storms as far away as 160 nautical miles. The presentation is digital, and the electronics required to process the analog returns from the radar and convert them to digital levels on the scope is contained in the panel package.

The Norton radome has an average transmission efficiency of 94% and can accommodate either the Bendix 10-in. circular-dish antenna or a 12-in. para-bolic antenna. Sitting over the leading edge of the wings of several Beechcraft Bonanza models (for which it is approved by the FAA) the radome doesn't require the aircraft structure to be modified in any way. Moreover, it doesn't alter the basic flight characteristics noticeably.

The combined radar-radome system costs $11,000 installed.

Mini handles more users simultaneously, efficiently

Minicomputers generally lose efficiency when handling over 15 or 20 users in multitask, data-service applications. But with new architectural and operating-system features, Data General's M/600 Input/Output Management System can service up to 64 concurrent users efficiently. In fact, this recently announced system performs well enough to compete with middle-range "mainframe" computers. Its 1- Mbyte semiconductor memory makes it the largest of Data General's Eclipse line.

Other key features include:

- A three-level I/O management system, to provide efficient transfer of data for different classes of peripherals.
- A demand-paged memory-management facility that uses an interleaved store instead of the conventional cache-type store.
- On-line diagnostic techniques that run continuously to detect potential problems before breakdown occurs.

The M600's three-level hierarchy consists of a high-speed burst multiplexer channel, a standard data channel and an independent input/output processor.

The burst multiplexer provides a high speed direct-communications path between the main memory and high-performance peripherals like fixed-head-disc subsystems and 96 or 100-Mbyte disc-storage subsystems. Data can be transferred up to 10 Mbytes/s.

Up to eight high-speed controllers can connect to the M/600 burst multiplexer, for a potential 6-billion bytes of storage.

The standard data channel handles communications between the system's job processor—the main processing unit—and medium-speed peripheral equipment such as magnetic-tape and cartridge-disc drives. The channel also works as a 2.5-Mbyte/s interface between the job processor and the third-level I/O processor, which handles up to 64 low-speed display and and printer terminals.

The M/600's demand-paged memory facility increases the effective memory capacity far above the one actually present by dividing each user's memory space into pages, and keeping only the active ones in the main memory. The other pages are stored on fast-access devices like fixed-head discs. To reduce the memory-swapping overhead, the operating system executive has an adaptive page-replacement algorithm to ensure that the user's working set of pages are in the main memory at all times.

For programming-language support, Data General has Fortran IV, optimizing Fortran V, and extended Basic. P1/I and DG-L are also available. RJE80 and HASP II package options enable the M/600 system to communicate with other Data General computers or with IBM-compatible systems.

Price is from $160,000 to $395,000.

CIRCLE NO. 430

Computer programs cut IC design time in half

By combining computer programs that simulate the functions and timing of digital integrated circuits with programs that generate test procedures for the devices, IC designers at Bell Labs have halved the time it takes IC manufacturers to put a newly conceived device into production.

The computer programs also help cut
3M has less than 0.03%, and Mitsubishi especially at high signal levels. Where digital recorders is very good. From 30 to 24 kHz, another tape does not degrade the analog machines reach a few percent, about 68 dB, while 3M gives better than 85 dB. Distortion is also much better, performance far superior to the best available from analog machines. The frequency response of both machines is ±0.3 dB; from 30 Hz to 15 kHz, 3M's is ±0.3 dB, and Mitsubishi better than 85 dB. Distortion is also much better, especially at high signal levels. Where analog machines reach a few percent, 3M has less than 0.03%, and Mitsubishi less than 0.01%.

The frequency response of both digital recorders is very good. From 30 Hz to 15 kHz, 3M's is ±0.3 dB, from dc to 20 kHz, Mitsubishi's is ±0.5 dB. Another advantage is that copying to another tape does not degrade the audio at all, provided it stays in digital form.

Digital audio recording is done by sampling the incoming audio, converting it to digital form, and rerecording the digital signal on the tape. In playback, the digital signal is buffered and retimed to eliminate wow and flutter. The retimed signal is then converted back to analog audio.

The professional system from 3M (St. Paul, MN), developed jointly with the British Broadcasting Corporation, actually consists of two machines, one a 32-channel, the other a 4-channel. (The user has a choice of recording on two or all four channels.) A recording is first made on the 32-channel machine. It is then played back, mixed down in a conventional console, and rerecorded on the other machine. The tape, specially developed for the 3M machines, is 1 in. wide for the 32-channel, and 1/4 in. wide for the 4-channel. Tape speed for both machines is 45 in/s, with a running time of more than 30 minutes for a 12 1/2-in. reel, and 45 minutes for a 14-in. reel.

The 3M digital audio is PCM-encoded as 16-bit words, which are protected from bit errors by an error-correcting scheme developed by the BBC. The words are grouped into blocks, which are given cyclic-redundancy and parity checks. If these checks show errors, the bad audio is reconstructed.

The digitized audio is recorded on one tape track per audio channel, with a density of about 28,000 bits/in.

The professional machine developed by Mitsubishi (Tokyo) has two audio channels, which are recorded as two tracks on 1/4-in. tape running at 15 in/s. The audio is PCM-encoded in this machine as well.

User-terminal protocol standard ready for review

A proposed standard for computer user-terminal protocols would apply anytime a Federal government user seeks access to or exit from one or more computer services.

User-terminal protocols permit a user at a keyboard terminal to send and receive standardized messages that permit access to computer services available at that terminal from one or more computer systems over any type of communications facility. But right now the protocols from computer-service suppliers aren't standardized, which makes it difficult for users to select at any one time the one service that is most suitable.

The standard protocol would standardize user, system and error messages, user and system signals and message sequences, and provide a list of definitions.

The provisions of the standard would be mandatory and would go into effect a year after it is issued.

The bureau is seeking comments on the proposed standard from all interested parties. Comments and questions should be addressed to the Associate Director for ADP Standards, Institute for Computer Sciences and Technology, National Bureau of Standards, Washington, DC 20234. Comments must be received on or before March 13, 1978. Single copies of the Federal Register notice may be obtained from the above address.

Disposable keys open μP-controlled locks

Paper or plastic keys encoded with perforations, magnetic stripes, bar codes, or any other combination can open doors secured with microprocessor-controlled locks. The locks automatically adjust to a new combination and cancel any previously assigned keys.

Under development at Arthur D. Little Inc., Cambridge, MA, the locks contain nonvolatile programmable read-only memories that can be reprogrammed as keys succeed one another. A central keymaking device, called a KeyMint, has a supply of uncoded keys that can be encoded with the old and new combinations so that only an authorized key can change a combination. Applications for the devices include industrial security and hotels.

One example of industrial security is imprinting new codes on pay slips or check stubs of authorized employees, so that keys are valid only between salary dates. Keys of former employees are automatically rendered unusable.

In a hotel, the guest keys can be changed with each room rental, while the keys of hotel employees remain unchanged. Further, a maid's key can be programmed not to open a lock while the privacy switch is engaged, but a supervisor's key can override this setting.

The project is in the prototype stage. An operating door lock has been built, and a preproduction version designed, with an NEC 4-bit microprocessor and a General Instrument's 1400-bit electrically- alterable ROM.
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CIRCLE NUMBER 10
Analog chips are becoming systems: New LSI merges analog and digital

Many of the integrated circuits to be described at this week's International Solid-State Circuits Conference in San Francisco are complex enough to be considered "systems on a chip." Indeed, several of these "systems" even contain analog and digital circuits on the same chip. They include:

- A single chip that contains both a/d and d/a converters, as well as counters, comparators, op amps, and random logic.
- A single chip that combines linear CMOS/SOS (silicon-on-sapphire) with digital circuitry.
- A single chip that contains a complete 10-bit a/d converter, with reference and clock.

But analog-digital chips won't be the only chip systems highlighted. Other developments reported include:

- Precision audio-frequency filters with the capacitors right on the chip.
- A sample-and-hold IC that uses no FETs, yet has low droop with a reasonable value of hold capacitor.
- A chip that combines a photodiode with signal-processing circuitry.
- MOS voltage references.

Analog-digital LSI isn't really new, according to Paul Brokaw, Director of Product Planning at Analog Devices Semiconductor, Wilmington, MA: "Suddenly we wake up and realize it's been around for a while." A data-acquisition system on a chip, a single-chip DMM circuit and a single-chip TV chroma circuit are examples of existing analog-digital LSI circuits.

**Chip size or complexity?**

But what constitutes analog-digital LSI? Some industry leaders point to chip size, stressing a minimum 10,000 to 20,000 mil², while others use complexity as the criterion. But the latter is much harder to measure than for purely digital LSI. The "equivalent gate count" used for purely digital LSI doesn't have an analog counterpart.

For example, there's no such thing as an analog chip containing 24 general-purpose uncommitted op amps.

One possible way of measuring complexity is by counting what Brokaw calls "irreducible elements," such as op amps, comparators, and analog switches. However, analog circuitry cannot be completely organized into such neat categories. Those who define analog-digital LSI by complexity agree that anything as complex as a complete 10-bit a/d converter on a chip qualifies.

This dual LSI technology has been made possible by steady, if unspectacular improvements in process technology. Specific developments include higher-resolution masking, ion implantation, dry processing and trimming at the wafer stage. The process technologies are basically digital, with adaptations to accommodate analog circuitry. The technologies used are linear-compatible PL, mixed MOS and bipolar, NMOS, and in one notable instance, linear CMOS/SOS.

For example, a chip that uses linear-
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Performance when it counts. When there's no room for a second chance or second best. For more than a decade, Hybrid Systems has been building quality products that take systems designers where they want to go. Data converters. Thin film resistors. Hybrid IC's.

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When the chips are down...

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compatible PL to combine a variety of analog and digital functions for automotive speed control will be described in a paper by R.B. Jarrett, who is a design engineer in the Automotive and Special Products Group, Linear Integrated Circuits, of Motorola Semiconductor, Phoenix, AZ.

This chip is part of a developmental auto speed-control system. One of several things the system can do is hold the car to a desired speed. Inputs to the chip come from the gas pedal, a variable-reluctance speed pickup, and four control buttons on the steering wheel.

When the system is used to hold the car to a desired speed, the driver pushes one of the buttons, which causes the car's speed to be stored digitally.

**Throttle control**

The desired speed is converted to analog form by a 9-bit d/a converter, then compared by an analog circuit with the actual speed, which is obtained from the variable-reluctance pickup. The result of this comparison drives two solenoid valves controlling the engine throttle. The throttle is air-actuated, so one valve opens the throttle, and the other closes it.

The commands from the four push-buttons are multiplexed (to save three wires) by encoding them with a simple 4-bit d/a converter at the steering wheel. The commands are demultiplexed by a 4-bit a/d converter on the chip, which also contains op amps, counters, and random logic. One of the counters is probably used for tachometer-type speed measurement.

Another analog-digital LSI chip to be described at the ISSCC contains three op amps. While this may not seem novel, there is a new twist. The op amps are linear SOS (silicon-on-sapphire) circuits, and CMOS at that. Apparently, this chip and one other not described at the ISSCC contain the only linear SOS circuits being made in the U.S.

The IC, reported on by Ross M. Orndorff and Daryl T. Butcher of Rockwell International Corp. (Anaheim, CA), is used in switching-regulated power supplies. It regulates the supply's output, drives the power switches (off-chip) in the regulator circuit, senses current overload, and sequences the supply in multi-output power systems.

The specs of the CMOS op amps in the chip are quite respectable. Gain bandwidth is 50 MHz, which may seem high for such functions as comparing the supply's output with the reference. But the chip can modulate at 1 MHz, so it looks capable of driving very fast power switches. Drive outputs are 400-ohm CMOS switches.

Other op-amp specs include more than 80-dB open-loop gain, CMRR of 85 dB, a slew rate of 30 V/μs, saturation recovery time of 1.5 μs max, and offset voltage of 10 mV.

Regulation is done by pulse-width modulation, using a ramp-type circuit on the chip. Current overload is sensed by an off-chip shunt. A signal is sent from the shunt to a dedicated op amp and logic on the chip, which turn off the power switch in the regulator immediately. The switch is turned back on with the next clock pulse.

While pulse-width modulation may prove handy for power switches, pulse-code modulation is proving essential to converting telephone systems to digital transmission. The next few years will see a great many digital channels installed, and PCM become the standard.

**Analog-digital functions on chips described at the ISSCC.**

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*100 quantities.
for digital transmission. But this standard will depend upon having low-cost coder-decoders (Codecs) to convert both ways between analog and PCM digital signals.

**Enter the analog-digital chip**

A complete conversion from analog to digital requires low-pass filtering, sampling, holding, and nonlinear conversion to digital form. Conversion the other way is also nonlinear, followed by low-pass filtering. If possible, one IC should do all of this.

Encouraging progress toward this goal will be reported in a session on PCM Codecs, and one of the papers, from Intel, will describe a single-chip design NMOS Codec. Not only that, but a two-chip design will be unveiled by James B. Cecil, Edwin M. W. Chow, John A. Flink, James E. Solomon of National Semiconductor, Santa Clara, CA; Tommy Svensson of Ellettel, Stockholm, Sweden, and C. Gunnar Svala or North Electric Co. in Columbus, OH.

One chip, made with bipolar technology, contains a sample-and-hold, a comparator, and a stable reference. The other chip contains a diffused nonlinear ladder, a 128-to-1 analog switch, a CMOS successive-approximation register, and a CMOS high-speed (up to 2 Mbits/s) serial input buffer for incoming PCM data.

In this two-chip Codec, the nonlinear d/a converter is time-shared between decoding and providing feedback for encoding. This converter includes a modification of the standard R-2R ladder network to make a direct nonlinear conversion from PCM digital data to quantized analog voltage.

In this ladder network, the resistors of value R are in series between the reference voltage and ground. The nodes between the R's are loaded by resistors of value 2R going to ground. It turns out that the voltages at these nodes correspond to the chord endpoints in the μ and A encoding laws. To interpolate between these endpoints, each R is subdivided to provide 16 taps for each chord. Since there are eight chords, there are 128 taps in all. The PCM digital data are decoded directly by the 128-to-1 analog switch to select one of these taps.

Still, the big IC story is really one-chip systems. Indeed, a complete 10-bit a/d converter on a chip is expected to be announced soon by Analog Devices. According to preliminary information from the company, this converter can be used without any other components, even though it has no sample-and-hold, and a full-scale trimmer is optional. The clock and reference are on the chip. Inputs are the voltage to be converted, and a convert-command line which can also float the three-state digital outputs. An output line tells when the data are ready.

This PL chip contains a d/a converter, voltage reference, comparator, successive-approximation register, clock, and three-state output buffers. The d/a's ladder network is made of thin-film resistors, which are laser-trimmed at the wafer stage.

The input signal range can be 0 to +10 or -5 to +5 V and is selected by floating or grounding a pin. Outputs will sink 3.2 mA.

The analog-input resistance is about 5 kΩ. It has been made slightly more sensitive than nominal full scale, so that a 50-Ω trimmer can be used to adjust for exact full scale. With no trimmer, about three counts are lost from the top of the digital output.

The convert-command line is labeled BLANK & CONVERT CONTROL. When this line is high, the data outputs float; when it goes low, conversion starts. The outputs remain floating until conversion is complete. When conversion is complete, the DATA READY line, which is an open-collector output, goes low.

In summary, the outlook for analog-digital LSI chips is very encouraging. But there are other combinations of analog circuits being put on chips as well.

**On-chip caps for audio filters**

For example, precision, high-order audio-frequency filters, essential to telecommunications, instrumentation, and PCM systems, have always needed discrete, "off-chip" precision resistors and capacitors.

In a paper, to be revealed by Kheng-Sang Tan and Paul R. Gray, of the University of California at Berkeley, a low-pass filter is made from IC capacitors with precisely controlled capacitance ratios, analog differential integrators, and a phase-locked loop (PLL). The filter has 0.1-dB passband ripple, with fifth-order Chebyshev response. Cutoff frequency is 8 kHz. In experimental form, the filter consists of several 90 by 100-mil chips.

The filter contains five differential integrators in an active-ladder configuration. Each integrator, in turn,
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We keep you out of trouble.

*CIRCLE NUMBER 13*
A new high-voltage, high-current integrable switch is based on double-diffused (DMOS) technology and is the first high-power switching device with a MOS gate. As a result, the power gain of the TRIMOS (for "MOS-controlled triac") is high—a few picowatts control tens or even hundreds of watts. And despite the high output currents and voltages, the excellent isolation of the insulated MOS gate requires only low-power control circuitry, which may be fabricated on the same chip. The MOS gate needs only a few volts at 10 or 20 picoperampers.

This regenerative device may be integrated with other MOS components for a host of new applications in cross-
New Portable Spectrum Analyzer for electrical, vibration and acoustic testing.

GenRad's 2512 spectrum analyzer gives you the power, speed and accuracy you need for studying sound and vibration problems—all neatly packaged in a lightweight portable instrument.

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The front panel has a minimal number of pushbutton controls. The operator deals with only one function at a time due to the sophisticated interaction between the display and controls. Operation of similar equipment often requires a thorough understanding of a maze of switches, knobs, and buttons.

And it's ideal for lab or field applications
Lightweight at approximately 38 pounds, the 2512 is compact enough to carry conveniently to the most remote sites.

Request complete information from GenRad,
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point switching, output stages, and power control.

A wide range of switching currents has been demonstrated as well as a MOS gate control to turn off the triac. Typical turn-on and turn-off times are about 200 ns, and single-pulse slew capability exceeds 1000 V/μs.

Area for area, the new device provides lower on resistance than a DMOS transistor. If a 0.5-mm-square area of a chip is used, a 200-V DMOS transistor provides an on resistance of 50 to 100 Ω. But the 200-V TRIMOS provides an on resistance of only 5 to 10 Ω, according to its developers, James Plummer and Brad Scharf of the Stanford University Integrated Circuits Lab.

For ultrasonic uses

Scharf and Plummer who will describe the structure and operation of the new TRIMOS at the ISSCC in San Francisco this week, developed the device to suit an ultrasound application at Stanford University. The ultrasonic imaging system under development requires analog multiplexers for 5-MHz transducer drive currents of a peak 0.5 A at about 200 V.

The DMOS transistors originally used handled the voltage and currents adequately. But an intrinsic diode between the drain and source made it difficult to tie two transistors to the same transducer.

“We were seeking a symmetric device, with no diode, so that our analog multiplexer could be used more flexibly,” explains Dr. Plummer, associate director of the laboratory. “In merging two high-voltage DMOS transistors around the same drain, we got the symmetry, and the regeneration feature as well.” Plummer feels the TRIMOS should find applications in many areas now served by current-controlled pnpn switches, which offer poor input isolation.

A cross-sectional drawing of the TRIMOS and a photomicrograph show the dual DMOS transistors and their common drain. Contact is made to the source and diffused channel of each DMOS, to form symmetrical anode and cathode contacts, and to the shared gate metal to form the control electrode.

With the cathode grounded and the gate held below the positive DMOS

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This photomicrograph shows simple geometry of the TRIMOS. Other MOS devices can be produced on the same chip, adjacent to TRIMOS devices.

threshold voltage, the pn junction at the cathode end blocks any applied positive anode voltage, which holds the switch off up to its breakdown voltage (200 V at present).

For gate potentials above threshold, there are three distinct regions of operation. In the low-level realm, anode potentials of less than about 1.5 V allow both DMOS channels to become inverted. Both transistors are in their linear regions and all the anode-to-cathode current is carried by electrons at the surface. The device exhibits the low-on-resistance I-V characteristics of two short-channel (2.5 μ) DMOS transistors in series.

The intermediate level of operation occurs for increasing anode bias, which causes the p-n anode junction to become forward-biased and to serve as the emitter of a wide-base pnp lateral transistor. The junction's injected holes drift and diffuse to the cathode p region, where they are collected to contribute an added component to the device current. The result is an increase in transconductance.

As the pnp collector current increases with anode or gate potential, its flow through the pinched resistor \( R_p \) raises the potential of the cathode's p region beneath the gate and begins to turn on the vertical npn transistor inherent in the DMOS structure. This npn and the pnp form a four-layer diode that regeneratively switches when the alphas of the pnp and npn transistors add up to one. In its "on" state, TRIMOS exhibits a dynamic resistance of less than 10 Ω and can pass currents of several amps.

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**NEW EAX solid state AC relay.** Thyristor controlled and isolated by pulse transformer circuit. Can be driven directly by logic circuits such as TTL, MOS and HTL. Terminals for 0.1" grid printed circuit board mounting. Rated 1.2 amps, 120V AC.
The first IC accelerometer developed for biomedical research is less than one-tenth the size and weight of the smallest commercially available miniature accelerometers. With such a small, implantable transducer available, new kinds of heart and fetus research become feasible.

Weighting less than 0.02 g, in a $2 \times 3 \times 0.6$ mm package, the accelerometer is small enough for a matrix of several accelerometers to be sutured to the heart muscle to measure the motion of the heart wall over the cardiac cycle. This technique may, in time, signal the early phases of coronary occlusion, a prelude to a heart attack.

In another proposed application, the IC transducer may be used to measure the motion of a fetus within the uterus, to provide information about fetal heart output, and to indicate that a fetus is in trouble, in time to prevent serious injury or death.

Only a few implanted accelerometer studies have ever been done, mainly because researchers have been hampered by transducer size. Small size and mass prevent mechanical loading of the organ being measured, and allow several accelerometers to be used within a small region of the body. One-mil platinum wires are used with the new IC accelerometer to provide flexibility and minimize any of the loading effects.

The accelerometer will be described in a paper at ISSCC this week by its developer, Dr. Lynn Roylance. She researched, designed, fabricated and tested it at Stanford University's Integrated Circuits Laboratory, in Palo Alto, CA, under the supervision of James B. Angell, Professor of Electrical Engineering and co-author of the paper.

The accelerometer's active element, sealed within a glass-silicon-glass sandwich, is a very thin (15 $\mu$m) cantilevered beam of silicon. A silicon or gold mass is mounted at the free end of the beam. A 200-$\mu$m-thick silicon supporting rim surrounding the beam and the mass provides mounting for...
The active element, a 7500-Ω diffused resistor on a thin beam (just left of the mass), changes resistance in proportion to acceleration.

the cantilever and space for contacts.

A resistor diffused into the top surface of the beam changes value with acceleration caused by the stress induced in the beam. A second resistor, placed in an unstressed region, is used for temperature compensation.

Several hundred accelerometers can be fabricated per silicon wafer with standard IC photolithographic and diffusion techniques, plus anisotropic etching to shape the silicon. The 55° slant surfaces shown on the accelerometer are principal crystallographic planes of the silicon, exposed by the nonuniform action of the potassium hydroxide etchant, which attacks some planes of silicon about 100 times faster than other planes.

The accelerometer detects one-axis accelerations down to 0.01 g over a 100-Hz bandwidth, with an upper acceleration limit of 50 g. But the versatile beam-geometry design allows the sensitivity to be varied readily over several orders of magnitude, yet remain tightly controlled.

Increasing the sensitivity has two tradeoff effects. The maximum acceleration the transducer can withstand decreases proportionally, and the resonant frequency drops, limiting the useful bandwidth.

The frequency response is essentially that of an ideal two-pole system, with the resonance typically between 500 and 2000 Hz. With air in the cavity, the damping factor is 0.005, but with a five-centipoise fluid, such as a light silicone oil, 0.7 critical damping can be achieved.

Accelerometers have been made with sensitivities ranging from 0.005% to 0.2% resistance change per g; corresponding operating ranges are ±200 g to ±30 g. Accelerations less than 0.001 g can be detected.

The miniature accelerometer’s performance compares very well with that of the small strain-gauge accelerometers available commercially.
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CIRCLE NUMBER 19
Industry will gain slightly from Carter budget

Congress should find little to quarrel with in those areas concerning the electronics industry—defense and space—in the $500.2-billion federal budget submitted by President Carter Jan. 23. Lacking such controversial items as the B-1 bomber, which fueled Congressional debate last year, the new budget reflects President Carter's desire to stress a steady buildup in the development and production of previously approved weapons, particularly those needed to strengthen NATO forces.

The Administration sidestepped such potentially explosive issues as a new MX mobile intercontinental missile and new aircraft carriers for the Defense Department, and a fifth Space Shuttle reusable orbiting spacecraft for the National Aeronautics and Space Administration.

At any rate, there will be a few more defense and space dollars for the electronics industry. All the main ingredients of the defense budget available to industry are slated to grow during fiscal year 1979, which begins Oct. 1. Operations and maintenance will grow from $35.1-billion to $38.1-billion, procurement from $30.3-billion to $32-billion, and research, development, test and evaluation from $11.4-billion to $12.5-billion. This accounts for two-thirds of the $126-billion in spending authority being sought for the Pentagon. The NASA budget, almost all of which is available to industry, is due to grow from $4.06-billion this year to $4.37-billion in fiscal 1979.

There won't be many new programs

However, the only new defense programs of any substance to be approved for the fiscal 1979 budget are the Navy's F-18 naval strike fighter and the Air Force's Advanced Tanker/Cargo Aircraft (AT/CA), both to be built by McDonnell Douglas. The Navy will order the first five fighters (out of the planned total procurement of 800 aircraft) under an $864.8-million request. The Air Force will order the first two AT/CAs, which are militarized versions of the DC-10 jumbo jet.

Cruise missiles continue to receive high priority. The proposed budget includes $416.1-million for continued development and additional procurement of the Air Force's Air Launched Cruise Missile (ALCM) being built by Boeing; $152.1-million for continued development of the Navy's Tomahawk cruise missile by General Dynamics, and initial development of a Ground Launched Cruise Missile (GLCM) for the Air Force based on the Tomahawk design. A competition between the ALCM and Tomahawk is planned for 1979, and only one is expected to go into full-scale production.

At NASA, the largest share of the budget is committed to the Space Shuttle, which, having tested successfully at Edwards (California) Air Force Base last year, is due to make its first orbital flight in June, 1979. The Shuttle is slated to get $1.4-billion in the new budget ($985-million for continued development and $454-million for production).

In announcing plans for the Shuttle, NASA Administrator Robert A. Frosch disclosed that the President had approved only four Orbiter spacecraft. NASA
will keep two, one will go to the Air Force, and a fourth will probably be shared by the two organizations. Another Orbiter had been sought for the Air Force, but Frosch said that decision could be postponed until the budget for fiscal 1981 is submitted.

One indicator of the impact the defense and space budgets will have on industry is the government-employment estimates for the coming year. Defense-industry employment is projected to grow from 1,930,000 to 2,050,000, which will more than offset an anticipated decline in military and civilian government employment from 3,080,000 to 3,057,000. NASA estimates that its contractor workforce will grow slightly from 102,800 to 104,300. Government employment will be frozen at 23,237.

More dollars—but not much more

However, with the inflation rate estimated by defense planners to continue at 6%, real growth in defense spending should be only about 3.5% in fiscal 1979. Although the Defense Department is asking for $126-billion in spending authority, it’s expected to spend just $115.2-billion. For the current fiscal year, Congress appropriated $116.8-billion, and spending is estimated to reach $105.3-billion by the end of the year.

This growth is substantially less than projected by former president Ford, who had planned to ask Congress for $134.4-billion in defense appropriations for fiscal 1979 and $165.9-billion by fiscal 1982. President Carter, who promised to cut defense spending by $5-billion to $7-billion during his 1976 presidential campaign, is now projecting continued growth in defense budgets to $160.5-billion by fiscal 1982.

Besides the decision to halt production of the B-1 and keep it in the R&D category, major cuts in next year’s shipbuilding programs helped prevent the defense budget from getting any higher than it did, according to Defense Secretary Harold Brown. Shipbuilding expenditures will be down $1.1-billion below this year’s level, and no aircraft carriers will be funded. In fact, the entire shipbuilding program is being reviewed by the White House, and findings are expected to be conveyed to Congress in March.

The Air Force had high hopes of accelerating development of the MX to replace the canceled B-1, but Secretary Brown cut fiscal 1979’s request for this program to $158.2-million—only slightly more than this year’s $134.4-million and well below the $400-million planned for fiscal 1979 by the Ford administration. MX is expected to be a $30-billion program, bigger even than the B-1 ($24.8-billion)—if it ever enters production.

Advanced programs will have to wait

Other advanced development programs wiped out by Brown include the Navy’s high-speed Surface Effects Ship (SES), for which $333-million had been appropriated in previous years; a new Air Force jet-powered cargo aircraft known as the Advance Medium STOL (short take-off & landing) transport, for which McDonnell Douglas and Boeing had built prototypes; and a new air-defense fighter aircraft, the Follow On Interceptor (FOI), which was expected to be a longer-flying version of the McDonnell Douglas F-15 Air Force fighter.

NASA, meanwhile, has postponed asking for new unmanned spacecraft for making observations of gamma rays in space and for orbiting the moon to make additional surveys of the lunar surface.

Over-all, federal support of research and development is due to rise for the ninth consecutive year—from $26.3-billion in the last budget to $27.9-billion in the new one. An additional $1.2-billion will be committed for new R&D facilities, but that’s down from this year’s $1.7-billion.
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If you checked "All of the above, plus______,” nice going. You're close. But what did you put in The blank? An analog multiplier? Relay circuit? Switches for signal routing? Test oscillators at pre-set frequencies? Digital logic circuits? Converters? Special processors? To be completely correct, your answer should include any one of these, or some other non-standard item, because TM 500 configurability not only allows you to choose from over 30 ready-to-go, compact plug-ins for testing and measuring, but the mainframe also makes room for compatible custom plug-ins you assemble yourself with a TM 500 custom Plug-in Kit.

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EXECUTIVE DIRECTOR'S REPORT

ELECTRONIC DESIGN 4, February 15, 1978

CIRCLE NUMBER 25

45
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visual superiority

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Measures Signals At Rates Up To 5 Million Times / Second

Microwave signals in most systems are modulated in such a manner that measurement of their frequencies at any given instant is extremely difficult by conventional means such as counting. Typical microwave signals include:
- 100% amplitude modulation with pulse widths from 100 nanoseconds to CW
- Bi-phase or quadrature phase modulated carriers
- Frequency modulation within a pulse (chirp)
- Interpulse frequency modulation (frequency agile)
- Frequency modulation during settling of fast voltage controlled oscillators (post tuning drift)

The Anaren line of Digital Frequency Discriminators (DFD's) can measure the frequency of these types of signals at rates up to five million times per second. The DFD is basically a microwave interferometer and is very similar to the optical interferometer (See Figure 1).

Light from a point source is collected by the first lens and is transformed from a spherical to a plane wave front. The wave passes through a prism and is skewed, as shown, at an angle that is a function of the prism geometry and the wavelength of the light signal. A second lens focuses the signal to a point in the image plane that is unique to the wavelength (and therefore, the frequency of the signal).

In the DFD (see Figure 2) an eight-way equal phase power divider simulates the function of the first lens. Four tapered delay lines and their reference lines perform the function of the prism, and four correlators (phase detectors) provide video outputs which, when quantized, provide a digital word that uniquely describes the phase front, and therefore, the frequency of the microwave signal.

If the RF signals are pulsed, then a threshold detector, triggered by each pulse, provides the READ strobe. Prior knowledge of time-of-arrival is not required. The latches can also be controlled by an external input so that CW or FM signals can be monitored at any desired instant up to a maximum rate of 5 million strobes per second.

Frequency accuracy varies from approximately 0.5 MHz in L-band (1-2 GHz) to 5 MHz in Ku-band (12-18 GHz). Instantaneous bandwidth can be traded for accuracy by down converting to a lower band. For example, any GHz band in Ku-band can be down-converted to the 1-2 GHz frequency range where 0.5 MHz accuracy can be obtained.

Operating Frequency (GHz)         Mean Frequency Resolution (MHz)         RMS Frequency Accuracy (MHz)         Input Power Levels (dBm)         RF Pulse Width (ns)         Max. Instantaneous FM Rate (MHz/µsec)
Model No.     182105     182135     182106     182136     182107     182137     182108     182138     182109     182139

Contact your local Anaren Technical Representative for a copy of the DFD Technical Manual, Pub. M1804-77, or write/phone below. Delivery 12 weeks ARO.

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CIRCLE NUMBER 29
The systems man

Jack was a systems man—in every fiber of his body. He believed fervently that all human activity—and certainly business and engineering activity—should be systematized. If things are not run by systems, he reasoned, they're run by accident. And then, of course, why bother?

So it came as no surprise, when Jack was brought in as VP Engineering, that he started setting up systems. He began with reliability. He wanted a system for gathering information, then acting on it methodically, to improve reliability.

On his first interview with Charlie, the chief engineer, he challenged: “How many of your instruments come back for repair?”

“That depends on the instrument,” Charlie told him, then showed, as examples, that lots of Model 23s were coming back for repair these days, but hardly any Model 85s. When Charlie added that nothing was being done to improve the 23 because the returns-for-repair problem would soon go away, Jack almost flew into a rage.

If a problem’s going to go away by itself, he fumed, we don’t need engineers. And why had nothing been done to improve the 23? And were there frequency-of-repair records to guide design modifications and to help set spare-parts inventories? And did records show if failures were due to component, design or manufacturing faults? And don’t we have records to identify poor vendors and poor components? And, by golly, we’re going to get some systems to monitor all of this so that we can take intelligent action.

“Oh, we’ve got all that,” Charlie told him. “It’s just that we decided not to modify the 23 because it’s almost 20 years old. It hasn’t been in the catalog for 10 years. Eventually, we expect Model 23 customers to buy the Model 85 instead of repairing the old box.”

When Jack simmered down, his enthusiasm for systems was in no way lessened, but he was slightly older and a good bit wiser. He learned that he was not alone in appreciating the value in systematic approaches to things. And he had learned, too, that it’s often wise to know the facts before trying to cure them.

GEORGE ROSTKY
Editor-in-Chief
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We serve special interests—yours!
Unlighted indicator assemblies look like so many gum drops. Only when they are lighted can you discover considerable differences in brightness, visibility range and longevity among units. The lamp inside the assembly makes most of the difference. Yet brightness specs, and to a lesser extent life specs, are confusing. And limited LED viewing angles are often downplayed in catalogs and spec sheets.

Indicator lamps—incandescent, LED and neon—

Morris Grossman
Associate Editor

Incandescent indicator lamps come in many sizes, shapes and basing arrangements for almost any application, as illustrated by this collection from Sylvania's comprehensive line of miniature incandescent lamps.
Offshore sources provide stiff competition to domestic lamp manufacturers. This family portrait of incandescent lamps from Shogyo International is only part of the product line available from the company.

Confused with the light measurement units? Don’t feel too bad. Photometric units are generally unfamiliar to electronics designers (see box). And lamp manufacturers add to the confusion by lack of uniformity in terms and by not clearly defining the units they use.

If lamp makers would standardize on the latest International System of Metric Units (SI units), comparison could be easy. But each technology continues to cling to its traditional approach. For example, “candlepower,” an obsolete word for light intensity, is not a photometric unit. In GE’s miniature and subminiature-lamp catalogs, candlepower is apparently measured in candelas, which is a recognized unit. But GE never really comes out and says so.

However, you could reach this conclusion if you know what a lumen is. “To convert mean spherical candlepower to lumens, multiply by 12.57 (4π),” the GE catalogs say. Apparently, you are expected to know that 1 lumen/steradian equals 1 candela. In addition, the term “mean spherical” is not defined by GE, but of course, the implication is that the value is the total light flux output in lumens divided by 4π steradians.

GE is not alone in loosely defining terms. Sylvania defines “mean spherical” as an “average of the luminous intensity in all directions” in its catalog. But Sylvania at least describes intensity in MSCD (mean spherical candelas). Since GE and Sylvania seem to list the same intensity values for equivalent lamps, you can assume that MSCP and MSCD are really the same photometric units and that both companies are “averaging” in the same way. Note: The word “average” is a generic term. There are many kinds of averages. A so-called weighted average in statistical analysis is called the arithmetic mean, or simply “mean.”

But what is really disturbing is the mixing of old English and SI photometric units for two different characteristics of a particular lamp. In addition to...
LED solid-state indicator lamps, such as these Jupiter units made by IEE, come in red, green, yellow and amber. Dome-shaped housings include sizes from T-1 through T-1-3/4. Fresnel-lens wide viewing-angle units in size T-2 are available with round or square tops.

Because they are small, LEDs can be arranged easily into arrays. Dialight's 555 lamps are packaged on a rectangular plastic block with built-in resistors and leads for mounting on PC boards. Ten LED units, side by side, need only one inch of space on a board.

mean spherical intensity, tubular T-2 lamps have an additional photometric spec called "end foot-candles,"—a measure of the portion of light that passes through a 0.25-in. aperture at the lamp's end. Both GE and Sylvania use the same unit here. Candelas used for light intensity are valid SI metric units, but foot-candles are obsolete English units for illuminance—the amount of light that falls on a surface from an outside source. To be consistent, the SI unit for illuminance—lux, or lumens per square meter—should be used.

If you make the same loose assumptions for neon lamps as is done for incandescents—averaging the total light flux around the $4\pi$ steradians of a whole sphere—then a neon's "intensity" range becomes 0.06 to 0.15 lm divided by 12.6, or 4.8 to 11.9 mcd per milliampere (normally rated at 0.25 to 2.5 mA) compared to the incandescent's range of 6 to 50,000 mcd. LED intensities range between 1 and 40 mcd.

The real world isn't so shiny

Although imprecise about lamp-intensity units, incandescent-lamp makers are most careful to explain that their rated lamp lives are based on "averages" of lamps tested on stationary racks, under closely controlled laboratory conditions and energized with regulated ac power. Perhaps this great care results from the frequently large discrepancy between the rated and actual lives. Actual use seldom duplicates the benign conditions of lamp life tests.

Incandescent-lamp life is very sensitive to voltage, shock and vibration and the frequency of on/off cycling. A 5% rise of a lamp's voltage above its design level can reduce life by 50%—life varies inversely as the twelfth power of voltage. Furthermore, on/off
cycling applies thermal, hence, mechanical stresses to an incandescent's filament, which also shortens life.

In addition, the mechanical resonant frequency of the filament, its support and the lead wires strongly influence a lamp's resistance to mechanical shock and vibration. Thus, catalogs correctly recommend low-voltage filaments, which are short and rigid and have high resonant frequencies. High-voltage units, however, more readily respond to shock and vibration and eventually their long filaments break under the abuse. But the catalogs give little data to guide you in such a selection. One catalog, merely says that "some lamps, such as 6.3-V panel units, incorporate mounting arrangements specially tuned to resonant frequencies that protect the filament against shock and vibration." But no quantitative data are provided, nor are the lamps in question clearly identified.

Unfortunately, except for idealized life-vs-voltage curves, catalogs and spec sheets don't provide much quantitative data that would allow a design engineer to estimate for himself the effect of these various factors on lamp life. And even the life-vs-voltage curves have limited value. They usually come with a warning that they aren't accurate beyond 95 to 110% of the design voltage, which is only a tiny portion of the over-all curve (see the incandescent-lamp life-output curves).

The major lamp manufacturers imply in their literature that they have stacks of data on these life factors. So why must you ask for them?

**Dc shortens lamp life**

Moreover, unless you diligently read footnotes, you'll miss the important point that operating an incandescent lamp on dc greatly reduces its life. Dc operation is doubly significant today because of the increasing use of lamps with dc in solid-state circuitry.

One reason life is shortened with dc stems from lighting a lamp through a series resistor or semiconductor device to obtain the correct lamp voltage. Unfortunately, an incandescent's resistance increases with age; consequently, so does the voltage across the lamp increase as it ages. The result is a lamp life that is about half of what it would be when the lamp operates from a low-impedance, constant-voltage source, such as ac from a well regulated transformer.

Another, even more deadly life reducer—filament notching, or the uneven evaporation of the filament—also stems from dc operation. This factor is particularly important in small, bright lamps, whose filaments operate in the usual 1700-to-2300-K range. For reasons not fully understood, ac appears to cause much less notching than dc. Thus ac-operated lamps can last from two to 10 times as long as dc-operated lamps.

Filament evaporation is the basic mechanism limiting an incandescent lamp's life. Unfortunately, evaporation is not uniform because of unavoidable material impurities, localized effects of cold working, and nonuniform temperature distribution. In small lamps, thin filaments—often only 0.001 in. in diameter—are more rapidly cut through by notching than thick ones, because the rate of notching is independent of the thickness of the filament. Fortunately, "long-lived" lamps, those with design lives of 10,000 to 100,000 h, operate below 1700 K and consequently aren't significantly affected by notching.

Another way to reduce an incandescent's life is to operate it in a flashing mode.

(continued on page 58)
Measuring luminance

To work effectively in the optoelectric field requires familiarity with terms like lumen, candela, and lambert, and a “feel” for the values that common sources and surfaces radiate and reflect (see chart).

A lumen has the dimensions of power. One watt is equivalent to 680 lumens (Im) at the peak of human-eye sensitivity (green, 555 nm). The lumen is often referred to as light flux that can be spread out to illuminate a surface. It can come from a concentrated source or extended sources of various shapes. When the light energy, or flux (F) in lumens, comes from a point source, the intensity (I) of the source is measured in lumens/steradian (lm/sr). A unit of intensity is called the candela (cd) and equals 1 lm/sr.

A steradian is a unit of solid angle. There are 4π steradians in a sphere. This is analogous to the radian in two-dimension geometry, in which there are 2π radians per circle (360°). Because there are 4π ≈ 12.6 steradians in a sphere, the total flux that one candela generates is 12.6 lumens, or 4π lm for a point source of intensity, I. Of course, we have assumed that a point source radiates uniformly in all directions. For a small area, A, that is at a right angle to a radius from a point source, the amount of flux striking the area is approximately

\[ F(\text{lumens}) = A(I/r^2), \]

where r is the distance from source to surface. Flux radiating in all directions spreads out as the square of the distance from a point source.

While many light sources, like LEDs, approximate point sources, many bar and panel-type lights are better described as area sources. The intensity of point-source LEDs is generally specified in candelas, or milliarcandlas (md), but displays and indicators that are area-like need another kind of measure that accounts for the different geometry. Photometric specialists use the lambert to describe the brightness of area-type sources. The lambert is related to the candela by a constant and an area factor to take care of the geometry difference—at least in theory—making the lambert and candela more comparable on a radiated-energy basis. Note that the word intensity is used to describe point sources, and brightness is reserved for area sources.

Theoretically, a small area is assumed to radiate (or reflect) on one side only. Therefore only half as much flux comes out of the area compared with a point source. Another assumption is that the surface is perfectly diffuse and radiates or reflects according to Lambert’s law of cosines—the emission brightness varies as the cosine of the angle from a normal to the area. In practice this is only approximated, but the factor is needed to relate point and area-source intensity and brightness units.

Because of Lambert’s law, an area source emits half again as much flux as a point source. Only 1/4 (4 π)

**Light-level brightness values of common objects**
steradians of total flux can theoretically come out of a small area source. Therefore,

\[ 1 \text{ lambert} = 1 \text{ lumen/(sr·cm}^2) = \frac{1}{(\pi)} \text{ (cd/cm}^2) \]

\[ = (10,000/\pi) \text{ (cd/m}^2) = 3183 \text{ cd/m}^2. \]

The cm\(^2\), or m\(^2\), dimension is needed to spread the given flux over the source's area.

From here on, photometric units become a semantic nightmare. The distinction between intensity and brightness is subtle enough, but photometric specialists have "refined" these terms and replaced them with "luminous intensity" and luminance, respectively. Many other confusing terms have been introduced, and now supposedly obsolete terminology exists side by side with newer—and not much clearer—words, like "illuminance" and "irradiance" for illumination (surface brightness from an external source), and obsolete "candlepower," which is replaced by the terms luminous intensity and luminance.

The number of equivalent units that exists to describe each quality is beyond belief (see abridged conversion tables). Units like hefners, nox, careel units, the English sperm candle and candlepower abound, but have been left off the tables.

Fortunately, by relating all units back to lumens or watts, you can usually work your way out of any difficulty. If manufacturers would stick to the so-called radiometric system of units for specifying optoelectronic devices—watts/m\(^2\), watts/steradian, etc.—the confusion would be very much reduced.

### Converting radiometric and photometric units

<table>
<thead>
<tr>
<th>Radiometric and photometric equations and units</th>
<th>Radiometric</th>
<th>Photometric</th>
</tr>
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<tbody>
<tr>
<td><strong>Definition</strong></td>
<td><strong>Name</strong></td>
<td><strong>Unit (SI)</strong></td>
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<tr>
<td>Energy</td>
<td>radiant energy</td>
<td>joule</td>
</tr>
<tr>
<td>Energy per unit time = power = flux</td>
<td>radiant flux</td>
<td>watt</td>
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<tr>
<td>Power input per unit area</td>
<td>irradiance</td>
<td>W/m(^2)</td>
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<tr>
<td>Power per unit area</td>
<td>radiant exitance</td>
<td>W/m(^2)</td>
</tr>
<tr>
<td>Power per unit solid angle</td>
<td>radiant intensity</td>
<td>W/steradian</td>
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<tr>
<td>Power per unit solid angle per unit projected</td>
<td>radiance</td>
<td>W/steradian</td>
</tr>
</tbody>
</table>

#### Illumination conversion factors

- 1 lumen = 1/680 light watt (at 555 nm)
- 1 lumen · hour = 60 lumen · minutes
- 1 footcandle = 1 lumen / ft\(^2\)
- 1 lux = 1 lumen / m\(^2\)

<table>
<thead>
<tr>
<th>Footcandles</th>
<th>Lux</th>
<th>Phot</th>
<th>Milliphots</th>
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<tr>
<td>1</td>
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<tr>
<td>Milliphots</td>
<td></td>
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</tr>
</tbody>
</table>

#### Luminance conversion factors

- 1 nit = 1 candela/m\(^2\)*
- 1 stibl = 1 candela/cm\(^2\)
- 1 apostilb (international) = 0.1 millilambert = 1 blondel
- 1 lambert = 1,000 millilamberts

<table>
<thead>
<tr>
<th>Footlamberts</th>
<th>Candelas/m(^2)</th>
<th>Candelas/CM(^2)</th>
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*International system of metric units—recommended standard

But flashing lamps are better attention grabbers for a given intensity of light than steady burners. Sylvania says that a 1-ms flash is about five times more effective than a steady source; 100-ms flashes are only two to three times more effective. And GE claims that its subminiature lamps can be operated as flashers with "generally no sacrifice in life as long as the light output as a flasher doesn't exceed that of steady burning."

Careful! Does GE mean that the flasher's peak intensity—not total light output—should be the same as the rating? With short duty cycles, very high bursts of energy to the filament would be required to produce light output equivalent to the energy content of steady burning. Such high peak voltage and temperatures will damage incandescent filaments.

What's more, the flashing rate shouldn't excite the filament's mechanical resonant frequency—a factor rarely mentioned. At resonance, thermal energy can couple into the mechanical system to produce mechanical vibrations (a singing filament). The flash rate need only be a subharmonic of the resonant frequency to excite the filament. Singing filaments often occur with dimmer (time modulation) controls on 60-Hz power.

Lamps operating at 10 to 28 V have lower over-all resonance frequencies than low-voltage, 6.3 and 5-V lamps. High-voltage lamps usually have long, finely coiled filaments with many resonant points. The coiled filament turns short easily when vibrating, putting excess voltage on the rest of the filament. Such generalized information is freely given in the catalogs. But getting specific resonance data on particular lamps is tough. It's not on the spec sheets, and even if a manufacturer has it for a specific lamp, it's often difficult to pry it out of him.

However, you don't have to flash a lamp continuously to affect its life. When you merely turn a lamp on and off frequently, the cooling and heating cycling fatigues filaments and supports. Interestingly, a tungsten filament is more fragile at room temperature than at high temperatures. In cooling—between 350 and 250 C—the filament passes through a ductile/brittle region below which breakage is most likely.

Inrush current becomes important, too, at low on/off frequencies. Inrush current with a lamp starting at room temperature can be as high as 12 times normal and last for 20 to 40 ms. Thus several lamps on the same circuit can cause the circuit breaker to trip when they are turned on cold.

**Geometry makes a difference**

The geometry of a LED's construction and its lens, if any, tremendously affects the amount of radiation that is finally emitted after the unit's internal quantum efficiency takes its toll. A major problem in LED construction—and a source of loss—is how to affix the front electrical contact so that the contact resistance is low—without, at the same time, blocking any significant amount of radiation.

Many arrangements have been devised for extracting the maximum amount of radiation. The most common structure is flat, with the only usable light emitted from the top of the chip. Several types use a miniature parabola that collects the edge emission and directs it forward along with the top surface emission. Some edge-emitter types use large top contacts to improve electrical efficiency and depend mainly on the edge emission for the light output.

Though a reflector can significantly improve the performance of a LED, even better results can be obtained when the structure allows more of the radiation generated within the chip to have access to the outside. A substance like gallium arsenide has a high index of refraction (about 3.6) so that radiation that arrives at a flat surface with an angle greater than 16° from the normal is reflected back into the chip. If the chip's active material is shaped like a dome, the light arrives at this surface almost normally everywhere, and very little is reflected back. Even though the material is far from transparent, efficiency is improved 10 times over a typical flat structure. If the material were perfectly transparent, the improvement would be closer to 25 times.

Plastic or glass lenses allow the light that does emerge to be concentrated or distributed. A shallow or Fresnel-type lens allows a broad emission angle and an acute lens, much thicker in the middle than at its edge, provides a narrower beam.

A diffuser-type lens is sometimes advantageous. A good diffuser lens will lose not more than 10% of the light, but it can greatly enhance the contrast of the emitted light against ambient light and spread the LED's light over a wide viewing angle. A colored lens can also help avoid ambient washout. A clear lens, however, will allow the reflection of ambient light, which can often wash out the LED's light output. And since the light from a LED originates from a relatively small area and is very intense, viewing through a clear lens can be uncomfortable.
Of course, with rapid flashing, the lamp filament doesn’t cool to room temperature between flashes, and inrush current is much less. Also, you can reduce inrush current by maintaining a low value of preheating voltage (about 1% of normal) when the lamp is off.

The rate of decay of inrush current depends upon the heat capacity of the filament. For tungsten, about 350 joules/gram are needed to reach 2500 K. Thus, a popular midget 28-V lamp, the 327, requires only about 0.045 J of energy and roughly 1 ms to heat, and it takes several milliseconds to get rid of this heat. But data on these properties, too, are not found in catalogs or are difficult to obtain. It’s probably easier to dig up your own data experimentally from a representative sampling of lamps.

### Keep cool to live long

Getting rid of incandescent-lamp heat is another important factor largely ignored in catalogs. A cool bulb temperature is necessary for a long lamp life. In general, miniature vacuum lamps should be operated with the hottest bulb spot no higher than 100°C. Higher temperatures greatly accelerate bulb darkening and eventual filament failure.

Since the lamp’s vacuum is almost a perfect insulator to the glass bulb, most of the heat conduction to the ambient is via the filament supports and through the lamp’s base, though some heat reaches the bulb via infrared radiation from the filament. But the newer all-glass wedge-base lamps have no metal or ceramic base, so heat dissipation depends totally on the bulb. Seldom are you advised to use heat-sink grease to improve the thermal coupling of such lamps to their holders. Of course, the holder then must be able to get rid of the heat.

Clearly, it’s best to mount the lamp to encourage convection—the free passage of air across the bulb to the atmosphere, especially from below. And if many lamps are used in a display, you should be careful to prevent their heat from disturbing other heat-sensitive components.

But note: Halogen lamps operate at higher temperatures than vacuum types. The wall temperatures of halogen-cycle should be kept above 250°C. And hot spots on bulb walls can go as high as 700°C in normal operation without harming the lamps. But watch out for the surroundings. Special care must be taken with housings for such lamps. The kindling point of many materials is less than 700°C. And though, 700°C doesn’t harm the bulb, lamp-base temperatures should not exceed 350°C, because the lamp’s lead wires may deteriorate and base cement may loosen at higher temperatures.

High temperatures are necessary for the halogen-cycle effect. When vaporized, a halogen such as iodine sealed into a high-temperature glass envelope, combines with tungsten evaporated from the filament and redeposits tungsten back on the filament, instead of on the bulb. Thus the filament lasts longer and the bulb doesn’t blacken. Halogen lamps maintain 85 to 95% of their light output for 70% of their lives—a 50% improvement compared to vacuum-type incandescent lamps, according to GE. Furthermore, for a given light output, a halogen lamp is about 1/6 the size of a vacuum type.

### LEDs produce less heat

Where indicators are closely packed into clusters and rows, as in master test panels for telephone systems, and myriads of lamps are on constantly, incandescents can give off enough heat to damage jack cords hanging down in front of such panels. By contrast, you can hold a glowing LED comfortably. Moreover, well made LEDs have half lives in excess of 200,000 h—about 23 yrs of continuous use. It never really burns out, but simply grows dimmer.

"Before, when we used incandescents, we had to test every lamp routinely to make sure it was working and was indicating a real circuit malfunction," reports Henry M. Bradley, Senior Engineer with Southern New England Telephone Co. "The T-2s we used might last anywhere from 20 to 5000 hours, so you never knew when they might go out. With LEDs, frequent testing isn’t necessary."

Chicago Miniature Lamp’s CM4-9031 LED indicators in standard telephone slide-base mountings fit T-2 sockets. But, though many LED styles are packaged to “replace” incandescents, LEDs and incandescents aren’t completely interchangeable. Furthermore, LEDs shouldn’t be mixed with incandescents in closely spaced groups, because heat from the incandescents can destroy the LEDs.

LEDs are usually rated at a 25°C ambient, which is unrealistic in many equipments. But a more-realistic 80°C temperature causes light intensity to drop to about 75% of rated. And continuous operation at 80°C or higher makes the LED grow dim at an accelerated rate. At low temperatures, however, LEDs become very efficient. At −50°C, their intensity may climb as high as 200% above rated. But even though incandescents can safely operate in much higher ambient than LEDs, a low temperature like −50°C can often crack the glass envelope or seal because when the lamps are turned off, cooling is too rapid. Also drive requirements are very different for LEDs and incandescents. A LED always needs a series ballast resistor to limit its current, whereas an incandescent is best operated from a low-impedance source. And if the power is an ac source, a LED will probably need a series diode in addition to the series resistor. Although LEDs are diodes, their reverse breakdown voltages are low—usually 3 to 6 V.

Furthermore, a LED’s light distribution is highly directional (see box), unlike incandescents, which are usually more evenly distributed. Most of a LED’s light is radiated straight ahead, with very little to the sides. And since beam patterns vary greatly among different LED types, manufacturers often provide special light distribution curves. The so-called LED viewing angle
—often called the half-intensity viewing angle—is the angle between points where the light intensity is at least 50% of the peak. Narrow-viewing angle ratings run around 28°; wide angles, about 65°.

However, low-cost lenses, such as Visual Communications' Cliplite lenses, which feature striated lines and Fresnel rings, can provide a 180° viewing angle from so-called point-source, T-1-3/4 LEDs. And LED indicator assemblies, such as Data Display Products' LEDy Bugs, are viewable "with considerable brightness" over 180°, also because of a Fresnel-lens.

**Beware of typical specs**

However, the peak, or midpoint, light intensity usually listed in LED spec sheets as typical is meaningless without a **guaranteed minimum**. A given LED manufacturing run provides a wide range of intensities. Finished LEDs must then be sorted into intensity groups. Those with low light outputs are weeded out, hopefully, and dumped at bargain prices. The brighter ones, above the guaranteed minimum, are sold to more demanding customers at higher prices.

Another so-called typical spec—forward-voltage—needs a **maximum value** to be useful to the design engineer. But this maximum voltage is not a safety-limit value, as its name might imply. Maximum forward voltage is the level that ensures that every LED in a selected group of LEDs lights and draws at least a specified forward current. A common specified test current for indicator LEDs is 20 mA.

While you're at it, watch those typical wavelength specs. The human eye may not notice small changes at the red end of the visible spectrum, but green, yellow and orange LEDs should be right on the nose, or else you'll see the difference. This specification is especially important when purchasing "equivalents" from second sources.

But even if the LEDs meet all the minimum and maximum specs in *initial tests*, you still can get many with weak semiconductor junctions or with excess stresses in their protective epoxy coatings. Failures will show up too late—after the LEDs are soldered into PC boards, or worse, in the field when heated and cooled during normal operation.

For reliable operation, you're strongly advised to specify a series of "torture" tests, to be performed by the supplier, if you trust him, or to be included as part of your incoming inspection. Manufacturers report that the most common user abuse is excess temperature cycling. But can poor quality control by the manufacturer be part of this problem?

In defense of LED suppliers, however, most suppliers specify a maximum soldering temperature of 260 C for only 5 s, which isn't always heeded. Typical wave-soldering machines have no problem operating economically within this limit. But when throughput is pushed by zealous board assemblers, the wavesoldering machine is likely to be operated at a higher temperature. Although temperatures above 260 C might be tolerated by most silicon devices, LEDs will suffer. Also, uncontrolled hand-soldering temperatures often cause overheating problems for LEDs.

Finally, the lead spacing on LEDs may not be standard. This oversight can prove not only annoying but costly. If you follow the usual 0.1-in. standard spacing on your PC boards, take heart: Litronix will shortly announce the RL-4480 line of LEDs in three brightness categories with leads spaced according to this standard.

**Neons for high voltages**

While both incandescents and LEDs operate best at low voltages, neons make good, low-cost, long-lived pilot lights for appliances that operate at power-mains voltages. High-brightness neons—about 23 mcd when operated at 2 mA—have standard initial max. breakdown (lighting) voltages of 95 V ac and 136 V dc, while standard-brightness lamps—about 2.5 mcd at 0.5 mA—need at least 65 V ac or 90 V dc. Note that the brightness of neons is comparable to LEDs, but neons cost much less than LEDs and are generally much more rugged.

Unfortunately, the life of neons is lower—generally about 25,000 h. And like LEDs, neons fade away with age. But standard and high-brightness units have different end-of-life criteria. Standard-brightness lamps are rated by Signalite for an end of life at the 50% light-output point. And a high-brightness neon's life ends when its firing voltage rises above the rated value.

Higher-than-rated currents reduce life proportional to the 3rd power in standard lamps and to the 4.5th power in high-brightness lamps. A mere 25% increase in current in a high-brightness unit rated at 25,000 h reduces its life to 8700 h.

Like incandescents, neon lamps live longer on ac than dc. Of course, the usual rated life for neon

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**One way to hold a lamp: a universal LED holder** with an insertable resistor. Data Display's PS200 unit accepts a LED and resistor for a supply voltage of your choice. The PS200 can accommodate the illustrated LEDy Bug, which has a 180° viewing angle and comes in red, amber and green. The wide viewing angle is produced by the Bug's flat-topped cylindrical lexan Fresnel lens.
indicator lamps is given for ac operation: The numbers look better. Dc cuts life to 50 to 60% of the ac value.

But unlike incandescents and LEDs, neon is limited to a single color, orange, which is produced by the combination of two wavelength bands—550 to 750 nm and 820 to 870 nm. You can get special lamps filled with argon gas, but the color is blue-violet (with some invisible ultraviolet), which doesn’t look too bright. Some manufacturers can supply a fluorescent-green “neon”—which doesn’t use neon, but an ultraviolet-producing gas mixture and a green-fluorescing coating inside the lamp bulb.

Like incandescents, you may flash a neon lamp at, say, a 10% duty cycle, but it doesn’t mean you can allow 10 times the rated current to flow for the short interval without paying for it. Although average current may be the rated value, 10-times rated current during the short conduction interval would use up a high-brightness-lamp’s life 31,000 times faster than normal. Under such conditions, a 10-ms on-time is the equivalent of about 5 min of normal steady burning at rated current. If the rated life is 15,000 h (60% of 25,000 h with dc), the lamp’s life is only about 5 h.

Whereas an incandescent can operate safely in an ambient of 100°C and even higher, neon, like LEDs, should not be exposed to high temperatures. Signalite recommends a maximum of about 75°C, even lower than the maximum 80°C for most LEDs. High temperatures cause chemical changes within a neon lamp that permanently modify its characteristics.

Of course, as with incandescents and LEDs, you must exercise care when bending leads near the glass envelope. Bends should be made at least 1/8 in. from the lamp’s lead-bulb seal (called a “press”), according to Signalite’s instructions.

One undesirable characteristic that is unique to neon lamps is flicker. But rarely is it mentioned. The phenomenon results when the neon corona discharge moves erratically from one portion of an electrode to another. If you are buying a lot of neon lamps, insist that the number of lamps that flicker in any lot doesn’t exceed, say 2.5%, which Signalite considers to be a reasonably low level. And get the guarantee in writing.

But having become aware of the pitfalls in choosing lamps, don’t get dazzled with the lamp and completely forget the lampholder. Despite the major importance of the lamp, electronic engineers often specify a complete assembly selected from a lamp-housing manufacturer’s catalog, and pay little attention to the lamp inside. Of course, the better lamp-housing makers carefully consider the characteristics of the lamps and are more than mere “machine shops” turning out gum-drop units.

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**Need more information?**

For further information on indicator lamps, readers may consult the manufacturers listed here by circling the appropriate numbers on the reader service card. More information on specific vendor lines may be found in ELECTRONIC DESIGN’S GOLD BOOK. Not all vendors listed here make their own lamps, but they can usually supply specified lamps with their lamp holders and displays.

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Lamp-holder kits are available like this collection from Industrial Devices, which contains working samples of a large variety of indicator-lamp holders for LED, neon and incandescent lamps.

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(continued on page 64)
The double-sided floppy from number 1

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Graphics CRT displays are easy to design with raster-scan imaging. Add a microprocessor and you’ll enjoy detailed, steady pictures in color or black-and-white.

Once you’ve decided on a µP-based graphics display, you’ll turn to the raster-scan method for its low-cost, standard TV monitor, easily refreshed, flicker-free images in color or grey scale, and its high resolution—up to 1024 points, or dots.

When designing your graphics display keep spatial resolution as low as you can. The basic graphics-display element is a dot known as a pixel. If, say, a graphics-display raster has $64 \times 64$ individual dots, and each dot can be one of eight colors, the display will have 4096 pixels, with a resolution of 3 bits/pixel.

The minimum resolution for serious work, however, is a $256 \times 256$-dot raster. Higher resolution, $512 \times 512$, will satisfy almost all applications, whereas $512 \times 1024$ resolution is the limit for graphics displays using a standard TV monitor.

The required color/grey-scale resolution in bits/pixel depends, of course, on the application. One bit/pixel usually suffices for curves, waveforms or alphanumerics; three bits/pixel give eight different colors or grey-scale levels. For broadcasting applications, eight bits/pixel is an accepted standard to generate an image roughly similar to the normal broadcast analog TV signal.

Memory isn’t far behind

Once you’ve selected the resolution, the refresh memory, usually dynamic, is your next concern. Automatic refresh of dynamic memories can be incorporated into normal TV-scan timing with very little additional circuitry.

But before you can generate a raster-scan graphics display, you’ll have to solve two conflicting refresh-memory problems: how big and how fast. On the one hand, the amount of stored information for one TV image can be as much as a couple of megabits; on the other hand, the memory must have fast access.

For just a standard $256 \times 256$ display, with 8 bits/pixel ($256$ different colors or grey-scale levels), the number of required bits = $256 \times 256 \times 8 = 2^{19} = 524,288$ bits, requiring 128 4-k dynamic-memory ICs. There’s no easy way to solve this problem. Fortunately, with the price per bit of the newest semiconductor memories going down, and density going up, at least the physical size and price of refresh memory are shrinking. (Note: just 32 16-k RAMs suffice in this example.)

Basically, there are three ways to store a graphics image in a refresh memory. The simplest, but not the cheapest, is dot-by-dot imaging, which breaks an image into a series of dots, then stores the parameters for each dot—intensity, saturation and hue for color, grey-scale level for black and white (Fig. 1). The only disadvantage is the large amount of refresh memory required.

When the amount of displayed information is too small for the total number of possible dots, you can save memory with a point plot, which stores in a table only those points actually displayed. If in a $256 \times 256$ display, with 8 bits/pixel, the total number of displayed dots is 1024, then the size of the refresh memory is $1024 \times (8 + 8 + 8) = 24,576$ bits. Of course, along with the pixel data, the X and Y positions of the displayed pixels must be stored.

The point-plot method can save memory only up to a “point.” In the outlined example of Fig. 1, if more

Branko Matic, Engineering Manager, and Lorne Trottier, Marketing Manager, Matrox Electronic Systems, P.O. Box 56, Ahuntsic Station, Montreal, Quebec, Canada, H3L 3N5.
Interlacing the video lines doubles the resolution. A display is interlaced by shifting the vertical sync pulse so that 1/16 of the dots are illuminated. This method requires less memory than the point plot method, which is difficult to implement on a raster scan. For positionable X-Y displays, refresh time and flicker become problems, and interlacing is more suitable.

A third method, known as character plot, is applied most often to TV games and other special displays. If a graphics image can be broken into a number of smaller fixed images, a simpler system can be designed. For example, an alphanumeric display is a fixed set of small images (character-set fonts) that are "randomly" positioned on the screen to form a message.

In video games, a set of fixed images (players, ball, paddle, etc.) are stored in ROM or RAM, then displayed on the screen at different positions. The advantage here is that the fixed-image set can be changed easily with software. A character-plot display can provide a limited repertoire of high-resolution patterns while using relatively little refresh memory.

Now you're ready for the TV sync generator. Graphics systems are similar in design to alphanumeric displays (see ED No. 19, Sept. 13, 1977, p. 68). For one thing, a TV sync generator is central to both displays. All necessary timing is generated by the decoding of appropriate counter states, and the generator provides all signals for refresh-memory, scanning and horizontal and vertical TV synchronization and blanking.

The resolution you have decided on plays a key role in the design of the sync generator. So do your image or plot techniques and your refresh memory.

The Mona Lisa in eight grey levels and in eight-level pseudocolor (colors arbitrarily assigned to grey levels) demonstrate the capabilities of raster-scan graphics. The color image is produced on a standard color monitor.
3. **The design of the sync generator** determines the timing, scanning and resolution characteristics of the display. Vertical and horizontal sync and memory-refresh signals stem from decoded counter outputs.

4. A **variation of address** interlacing saves memory by addressing each picture dot from X-Y position registers. A software command then controls each dot. In this way, a few locations can control thousands of bits.
For a character plot, the graphics interface is almost identical to that for alphanumeric CRT displays, with the character-generator ROM replaced with one containing graphics symbols. An addressable RAM is yet another choice, allowing the table of symbols to be changed to suit various applications.

An image-plot interface also parallels the one for alphanumeric displays: The screen is broken up into a number of cells, with each, say, composed of eight or 16 dots by 1 line. The dot clock frequency is given by 
\[
f_{\text{dot}} = f_{\text{line}} \times \text{number of lines} \times \text{number of cells per line} \times \text{number of horizontal dots per cell}.
\]

Designing the sync generator

Suppose you want a 256 X 256 standard American monitor display. Since a standard TV works with 262 lines and requires 10 to 20% of the total number of lines for vertical retrace, you can’t display 256 lines on the American standard. You must choose a non-standard number of video lines, or reduce the number of displayed lines. Say you choose 240 displayed lines and 262 video lines. You can reserve 22 lines for vertical retrace.

For the horizontal direction, choose a 16-dot cell. Sixteen cells yield the desired 256-point resolution. Allow six more cells for retrace, for a total of 22 cells per line. Note \( f_{\text{line}} = 60 \text{ Hz} \). Therefore:
\[
f_{\text{dot}} = 60 \times 262 \times 22 \times 16 = 5.53344 \text{ MHz}.
\]

The restriction on the total number of video lines per field appears to limit severely the vertical resolution of raster-scan graphics. Fortunately, interlacing —inserting a second set of lines between the first set’s—can double the number of lines. The even lines—0, 2, 4...524—are scanned first, then all odd lines. Each set of lines contains different data.

The sync generator will have to do some fancy electronic footwork to produce an interlaced display. The trick is to shift the position of the vertical sync pulse, relative to horizontal sync, on each alternate field (Fig. 2). In one field, vertical sync starts at the beginning of a line (odd lines scanned); in the other, vertical sync starts in the middle of a line (even lines scanned). In both cases, horizontal sync is not interrupted, and vertical sync has a total length of three or four lines.

Consequently, the lines of one field are shifted by half a line space with respect to the lines of the other field. Whether the odd or even lines are to be displayed is indicated to the refresh memory by a field signal coming from the sync generator.

Interlacing has some disadvantages, however. First, the circuitry is complex. Second, the over-all refresh rate drops to half that of noninterlaced units, so the display can flicker when you use a CRT with standard P4 phosphor. So when you need high-resolution line drawings, use CRTs with P33 or P39 high-persistence phosphors. When images are to be displayed, both fields contain almost identical information, which the eye is able to integrate, and flicker is not noticeable. Thus P4 is OK here.

All timing signals for refresh memory, latch, video-shift clock, \( \mu \text{P} \), synchronization, as well as horizontal and vertical sync and blank, can be decoded by combining different outputs of the sync-generator counters. In particular, dynamic RAM refresh signals —RAS, CAS, \( \mu \text{P} \) synchronization—are derived from the dot clock. Horizontal and vertical timing are decoded from the cell counter and line counter, respectively (Fig. 3).

Normally, only 10 to 20 MSI and SSI TTL IC circuits
Driving a color-TV monitor: Digital signals turn color guns on and off to form one of eight colors by mixing the correct proportions of the three primary colors. To get more colors, add image planes and d/a converters.

awkward to map points on the screen to a particular address/data bus position.

Using X-Y registers

An approach more elegant than interlaced memory relies on hardware to map the refresh memory into a pair of X-Y registers (Fig. 4). A given dot can then be turned on or off by a separate command after it is addressed by the registers. The advantage to this technique is that just two locations can address up to 262,000 bits (for a 512 x 512 display). Other advantages accrue: X-Y addressing is easy with software and the method allows easy color/grey-scale imaging. Note that only one dot at a time is written to or read from via the image-data-bit line.

To implement a color/grey-scale imaging system, a single sync generator/CRT controller drives multiple, identical image-memory planes (Fig. 5). The same X-Y registers and address multiplexer drive all image memories. Each image plane consists of a RAM array, video-shift register and image data-bit line. It is convenient to tie the image data-bit lines to different data-bus bits, so you can address all image planes simultaneously using the data bus as a Z coordinate.

You can combine the outputs of the video shift registers to form a color/grey-scale imaging system. Connect the video outputs to a high-speed d/a converter as shown in Fig. 6. Increasing the number of image planes improves the number of grey levels in the image; three planes give eight grey levels, four memories produce 16 levels.

A variation of the shift-register technique, found in several commercial single-board graphics controllers, operates the sync generator as a master or slave. The outputs of all cards are fully synchronous and can be combined.

Going to color is one way to get more image contrast, as well as more resolution per dot. In image process-
ing, color is a must. And, of course, a color display can increase a product’s marketing appeal.

Generating color

The simplest color graphics display is an eight-color system, in which each of three primary-color CRT electron guns is turned on or off by a digital signal (Fig. 7). If you need more colors, use more image planes, with an appropriate d/a converter for each primary color. The best results for graphics imaging applications are obtained with RBG monitors, in which the three primary color signals—red, green and blue—are direct inputs to the monitor.

TV monitors using a single, composite, color-signal input are not recommended because the color bandwidth is inherently low. A color encoder circuit would also be required to generate the correct composite color video signal. In choosing the correct color monitor for a given application, you must ensure that the color-dot density on the tube face is fine enough for the required resolution.

Pictures generated by feeding a TV camera output through a slow-scan a/d converter illustrate the imaging capabilities of graphics systems (Mona Lisa photos). The 3-bit digitized output is stored, via a µP, in a three-card Matrox MTX-256**2 graphics system. In turn, feeding the outputs of the three cards to a 3-bit d/a converter produces pictures with eight discrete grey levels. And delivering the card outputs to the red, blue and green inputs of an RBG color monitor produces pseudocolored, eight-color pictures. (In pseudocoloring, a different color is assigned arbitrarily to each grey level in the original picture.)

If alphanumerics are also to be displayed, characters can be interpreted as graphics figures and plotted dot-by-dot from a list in the computer memory. In this way, you get great flexibility in character size, set and position on the screen. The main disadvantage is that writing dot-by-dot is relatively slow.

Handling alphanumerics

Alternatively, you can superimpose alphanumerics and graphics video signals. The µP treats the alpha and graphics interfaces as separate peripheral units, and superposition can occur either digitally, by ORing alpha and graphics video, or by adding the two video signals to a resistor network. Obviously the alpha and graphics displays must be synchronized.

For low or medium-volume OEM applications, it is usually more economical to buy off-the-shelf graphics displays. These come basically as plug-in boards for popular computer buses, including Intel SBC, Digital Equipment LSI-11, HP 21MX, and the S100 bus; as self-contained systems with card cage, power supply and display processor; or as terminals with built-in CRT and keyboard.

Board-level graphics are the least expensive and most flexible for many applications. Companies such as Matrox, Intermedia, Hewlett-Packard, Cromemco and Miniterm Associates offer plug-in boards for many popular mini or microcomputer buses. Prices range from $400 to a couple of thousand dollars, depending on resolution and quantity.

Boards are usually easy to interface with the 8080 or 6800 µPs (Fig. 8). Add RAM, ROM and other I/O devices, and you can configure a very powerful system. Typical I/O devices include keyboards, data tablets, joysticks, and light pen. The capabilities of such a system are determined completely by the software in the program ROM.

Other features can be very helpful in certain graphics applications. A read-refresh memory function allows the µP to read back the displayed image, update it and write it back, which saves considerable time and storage. Additional hardware features include clear screen, scroll image, inverse video, and drawing a straight line between two dots. Those functions are often incorporated in an optional block, called a display processor.
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**Technology**

**Directly viewed incandescent displays**
are hard to beat for high brightness and choice of colors.
TTL-compatible, they also are easy to control and multiplex.

Directly viewed incandescent alphanumeric indicators make extremely bright, highly visible data displays for high-ambient-light surroundings such as in airplane cockpits and taxi meters, gasoline pumps and many outdoor applications. Although incandescent units use more power per digit than most other types of displays, they also are the brightest—about 9000 to 13,000 foot-lamberts (see comparison table). And the light output can be varied easily to maintain a good contrast to the ambient-light level. Furthermore, almost any color can be displayed merely by slipping the proper filter over the front of the display.

Incandescent displays are made also as indirectly viewed units. Indirectly viewed displays contain replaceable light bulbs that illuminate segmented bars (or project characters or messages on a screen) via fiber-optic arrays, or molded-plastic or glass-focusing rods. Directly viewed displays have tungsten filaments that directly form the segments (though some manufacturers, such as Chicago Miniature, Info-Lite and IEE arrange individual bulbs in a matrix, usually a 5 × 7 configuration).

Indirectly viewed units that usually use 5-V, 20-to-30-mA lamps, such as Master Specialties units, are rated at about 1000 ft-L light output. However, directly viewed segmented units, such as the 4-to-5-V, 10-to-18-mA Pinlite displays made by REFAC, are almost ten times brighter—about 9000 ft-L. Moreover, directly viewed units are more compact, consume less power, are mechanically simpler and more rugged. For example, RCA’s Numitrons enclose the filaments in a vacuum tube and REFAC’s Pinlite displays use a tough, flat, metal-backed package sealed with a glass front. And some segmented devices even pass MIL-STD-202 vibration tests.

One apparent advantage of indirectly viewed displays is that low-cost individual lamps can be replaced when they burn out, whereas a single segment burning out in a directly viewed unit forces you to discard the complete device. However, the segmented displays last an average 100,000 hours or more (Fig. 1a) before even a single segment burns out.

![This flight-navigation-management panel](image)

This flight-navigation-management panel uses both seven-segment numerical and 16-segment alphanumeric displays, such as these Pinlite incandescent units made by REFAC. They provide high visibility in the high ambient light found in aircraft cockpits. Color filters help increase it even more. The display on this Garrett AirNAV 100 & 200 keyboard is telling the pilot that he is 103.8 nautical miles or 19 minutes from way point 1.

But the key to good visibility in high ambient-light conditions is contrast ratio—the difference between ambient light reflected from the display’s background and the segment’s output-light intensity, all divided by the segment’s intensity.

**High contrast with directly viewed units**

With over 9000 ft-L of light brightness, directly viewed devices easily provide high contrast ratios in high ambient light. In addition, light filters can enhance contrast ratio by cutting down on reflected glare—a popular filter uses circular polarization. The contrast ratio of a typical filtered Pinlite display in an aircraft cockpit in bright sunlight is about 8:1, much higher than the 2 to 3:1 generally considered adequate for reasonably good visibility.

A circular-polarizing filter made by the Polaroid Corp. prevents ambient light from reflecting off the interior of the display. But most of these filters also attenuate about 90% of the light. Thus, a display rated

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**Walter Gilles**, Applications Engineer, REFAC Electronics Corp., P.O. Box 809, Winsted, CT 06098.

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ELECTRONIC DESIGN 4, February 15, 1978
at 9000 ft-L would deliver only 900 ft-L through a polarizing filter.

However, because such a filter cuts down more glare and reflections than desired light, contrast, and thus visibility, is enhanced. Some filters work merely because of nonglare or matte finishes on the filter’s surface. In addition, filters can be made of conductive glass to provide electromagnetic shielding, and may be sealed to protect against moisture and dust.

But how do you objectively compare display visibility among competing techniques, when different photometric units are used for each, and common

English words have special technical meanings that are far from obvious?

**Brightness and intensity mean different things**

The word “intensity” usually describes point light sources like LEDs, and “brightness” is reserved for extended sources, like incandescent-segment filaments. The distinction between intensity and brightness is vague enough, but photometric specialists have “refined” even these terms and replaced them with “luminous intensity” and “luminance,” respectively.

The “intensity” of LEDs is rated in candelas (cd). But as you already know, the “brightness” of incandescent segmented displays is rated in foot-lamberts, where 1 ft-L = 0.3183 cd/ft². However, candelas and candelas per square foot aren’t comparable. So much for comparisons.

As a matter of fact, the brightness of an incandescent display is hard to measure. Since segment filaments are small in diameter, usually less than 0.001 in., specialized equipment is needed to measure their brightness. True foot-lambert readings depend on being able to focus only on the filament. If the light meter “sees” background as well as filament, the readings taken are diluted—integrated with the background. Also, brightness isn’t uniform along the whole length of a filament, so several measurements must be made and averaged. A typical method measures light outputs in foot-lamberts at one-fourth, one-half and three-fourths portions along a filament. These three readings are repeated on three other randomly selected segments in a display, and the nine readings averaged.

Another method “looks” at the entire front face of the display with all segments on. Of course, such measurements don’t provide absolute values that you can compare against, say, a National Bureau of Standards brightness standard. However, with periodic calibration against a “standard” unit of your own, the relative measurements can serve as an internal quality-control criterion. Some users find it very effective merely to “eyeball” the displays and depend on judgment for quality control.

**Driving incandescent displays**

Though measuring the brightness of displays may be confusing, driving incandescent displays is relatively clear-cut. Incandescents are TTL-compatible. To take care of cold turn-on surges, the load-current capability of the driver should be double the maximum steady current of the display segment. For example,
a good choice for a direct decoder/driver for a Pinlite MD-650, whose maximum steady current can go to 20 mA, is a 5447, which can handle 40 mA (Fig. 1b).

You can generally save money, parts and space when four or more characters are multiplexed instead of directly driven. Multiplexing shares a single decoder among many display units by taking advantage of the thermal time constant of the hot filament and the persistence of the human eye. Each unit is electrically on for only a short time but appears to be visually on continuously.

However, you must observe some circuit precautions. To prevent the tungsten filaments from self-resonating, make sure that the multiplexing recurrence rate is higher than 1 kHz, which is well above the usual filament thermal constant. At resonance, the filaments can be damaged or distorted, but too far below resonance, they may flicker. Although combinations of high-voltage drive values and repetition rates well below the thermal time constant can be found so that the display appears to produce its nominal rated light output, the true filament temperature will be higher than normal and shorten the display’s life.

Furthermore, no more than 12 digits should be multiplexed, or else the duty cycle won’t be kept higher than 8%. Below an 8% duty cycle, the display may flicker and produce uneven brightness. Moreover, evidence shows that duty cycles lower than 8% cause filaments to degrade because of thermal stressing.

To select the correct multiplexing voltage, $V_{mx}$, use the following formula:

$$V_{mx} \approx \sqrt{\text{Number of displays}} \times \text{Nominal display voltage.}$$

Typical $V_{mx}$ curves are shown for 3, 4 and 5-V units in Fig. 2. But note that voltage drops across any diodes and ICs in series with the filaments must be added to the plotted values.

**Multiplexing circuits in detail**

A basic multiplexing circuit in block form, Fig. 3a, shows a single seven-segment decoder/driver time-shared between two digit displays by a scan counter. Isolation diodes in series with each filament segment must be employed to prevent “sneak” electrical paths. LEDs don’t need such diodes, because the LEDs are diodes, but they do need current-limiting resistors. Another circuit, Fig. 3b, replaces the diodes with individual driver/decoders. If memory is required, driver/decoder/latches also can be used.

Fig. 4 details the most commonly used multiplexing circuit. Corresponding display segments from all decades are connected in parallel to the seven outputs of a single 4-bit BCD-input-to-seven-segment-output decoder. A three-bit BCD output from a counter drives a BCD-to-decimal decoder, whose outputs then drive transistor Darlington circuits, which successively apply voltage to display-filament decades.

A fail-safe circuit detects clock failure and disables...
## Comparison of character displays

<table>
<thead>
<tr>
<th></th>
<th>Fluorescent</th>
<th>Gas discharge</th>
<th>Incandescent</th>
<th>LCD (dynamic scattering)</th>
<th>LCD (field effect)</th>
<th>LED</th>
<th>Nixie</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Brightness</strong></td>
<td>Good</td>
<td>Good to excellent</td>
<td>Excellent</td>
<td>Not applicable</td>
<td>Not applicable</td>
<td>Good</td>
<td>Good to excellent</td>
</tr>
<tr>
<td><strong>Contrast ratio</strong></td>
<td>10:1</td>
<td>20:1</td>
<td>20:1</td>
<td></td>
<td></td>
<td>10:1</td>
<td>8:1</td>
</tr>
<tr>
<td><strong>Voltage</strong></td>
<td>15 to 25 V</td>
<td>180 V</td>
<td>5 V</td>
<td>18 V</td>
<td>3 to 7 V</td>
<td>5 V</td>
<td>175 V</td>
</tr>
<tr>
<td><strong>Temperature range (°C)</strong></td>
<td>−55 to +100</td>
<td>0 to +55</td>
<td>−55 to +100</td>
<td>0 to +80</td>
<td>0 to +70</td>
<td>−55 to +100</td>
<td>0 to +70</td>
</tr>
<tr>
<td><strong>Switching speed</strong></td>
<td>1 ms</td>
<td>1 ms</td>
<td>150 µs</td>
<td>300 ms</td>
<td>100 to 300 ms</td>
<td>1 ms</td>
<td>150 ms</td>
</tr>
<tr>
<td><strong>Colors</strong></td>
<td>Blue-Green, others with filters</td>
<td>Orange, others with filters</td>
<td>All, with filters</td>
<td>Depends on illumination</td>
<td>Depends on illumination</td>
<td>Red, orange, yellow, green</td>
<td>Neon orange</td>
</tr>
<tr>
<td><strong>Appearance</strong></td>
<td>Excellent</td>
<td>Excellent</td>
<td>Excellent</td>
<td>Good</td>
<td>Good</td>
<td>Good</td>
<td>Fair</td>
</tr>
<tr>
<td><strong>Font</strong></td>
<td>7 segments</td>
<td>7 and 16 seg. dot matrix</td>
<td>7 and 16 segments</td>
<td>7 and 16 segments</td>
<td>7 and 16 seg. dot matrix</td>
<td>Individual characters</td>
<td></td>
</tr>
<tr>
<td><strong>Vertical size</strong></td>
<td>0.5 to 0.75 in.</td>
<td>0.2 to 1.0 in.</td>
<td>Up to 1 in.</td>
<td>0.2 to 8 in.</td>
<td>0.2 to 2 in.</td>
<td>0.1 to 0.6 in.</td>
<td>0.3 to 2 in.</td>
</tr>
<tr>
<td><strong>Power/digit</strong></td>
<td>100 mW</td>
<td>30 to 100 mW</td>
<td>250 mW to 1 W</td>
<td>100 mW</td>
<td>1 to 10 mW</td>
<td>10 to 140 mW</td>
<td>350 mW</td>
</tr>
<tr>
<td><strong>Life in hours</strong></td>
<td>100,000</td>
<td>30,000</td>
<td>100,000</td>
<td>10,000</td>
<td>10,000</td>
<td>100,000+</td>
<td>200,000</td>
</tr>
<tr>
<td><strong>Viewing angle</strong></td>
<td>150°</td>
<td>120°</td>
<td>150°</td>
<td>90 to 150°</td>
<td>90 to 120°</td>
<td>150°</td>
<td>100°</td>
</tr>
<tr>
<td><strong>Ease of mounting</strong></td>
<td>Good</td>
<td>Fair</td>
<td>Excellent</td>
<td>Poor</td>
<td>Poor</td>
<td>Excellent</td>
<td>Good</td>
</tr>
<tr>
<td><strong>Ruggedness</strong></td>
<td>Poor</td>
<td>Fair</td>
<td>Excellent</td>
<td>Good</td>
<td>Good</td>
<td>Excellent</td>
<td>Poor</td>
</tr>
<tr>
<td><strong>MOS compatibility</strong></td>
<td>Yes</td>
<td>Yes</td>
<td>No</td>
<td>Yes</td>
<td>Yes</td>
<td>Small Yes</td>
<td>Large No</td>
</tr>
<tr>
<td><strong>Safety factors</strong></td>
<td>Contain phosphors</td>
<td>Contain mercury</td>
<td>No special precautions</td>
<td>No special precautions</td>
<td>No special precautions</td>
<td>No special precautions</td>
<td></td>
</tr>
<tr>
<td><strong>Driving circuit</strong></td>
<td>Low voltage standard semiconductors</td>
<td>High voltage standard semiconductors</td>
<td>Low voltage standard semiconductors</td>
<td>Ac operation only, low voltage standard semiconductors</td>
<td>Ac operation only, low voltage standard semiconductors</td>
<td>Low voltage standard semiconductors</td>
<td></td>
</tr>
</tbody>
</table>

the address to avoid voltage from being applied for an excessively long time to any one display. Incoming clock pulses charge capacitor $C_1$ via diode $D_1$ to the peak voltage of the incoming pulses. The voltage across $C_1$ keeps transistor $Q_1$ on as long as the clock pulses are present. Thus transistor $Q_1$'s collector keeps the most-significant digit of the scan decoder normally low. Should the clock fail, the transistor collector goes high and the scan decoder addresses the two unused outputs, $O_s$ and $O_9$. With the components shown, the circuit operates satisfactorily from 1 kHz up, with duty-cycle pulse widths down to 8%.

Another circuit uses the storage capability of a seven-segment decoder/driver/latch combination, such as the DDL-20 made by REFAC (Fig. 5). Here, BCD-input display information is addressed in parallel to several decoder/driver/latches, while individual strobing lines from a scan decoder turn on one at a time. Isolation diodes aren't required since there aren't any sneak circuits. And the circuit consumes less power than that of Fig. 4, because there are no diode losses and no need for a failure-detection system.

Also the multiplex voltage requirement is lower.

Many seven-segment decoder/driver and decoder/driver/latch IC packages are available, and some come packaged inside the display's connector.

The multiplexing systems previously described in Figs. 2a and 2b can be used with 16-segment alphanumeric displays also, but IC-packaged ASCII-to-16-segment decoder/drivers aren't available yet. However, REFAC has a hybrid-packaged 16-segment decoder/driver in a 1.2 × 1 × 3/4-in. case with 24-pin DIP terminations. Development is under way for a more compact, versatile decoder/driver that will have latch capability and perform over the military-temperature range.

"Keep-aliases" don't help

One question remains to be answered: Do "keep-alive" resistors increase the life and reliability of tungsten-filament displays? This may be true for an indirectly viewed filament display, which uses replaceable lamps, but it's not necessarily true for a
4. A detailed implementation of the multiplexing circuit represented by the block diagram of Fig. 3a shows the separate isolating diodes and Darlington driver circuits needed for each display unit.

5. Separate decoder/driver/latch ICs are needed for each display unit in the multiplexer circuit of Fig. 3b, but the isolating diodes of Fig. 4 are no longer needed.

6. Though prewarming resistors are not needed to lengthen the life of a directly viewed incandescent display, many designers incorporate them anyway to reduce surge currents that can overload or even damage display driver circuits, which otherwise are adequate for the load.

directly viewed display.

Conventional tungsten lamps for area illumination operate at high temperatures, about 2500 K. However, since the filament in directly viewed displays need only provide enough light to define characters rather than illuminate a given area, it can operate much cooler. Consequently, in-rush current from a cold turn-on is much less of a shock for directly viewed displays than it is for indirectly viewed ones.

To reduce the shock of cold-tungsten surge, you can use a low-current bypass around the filament drivers (Fig. 6), which keeps the filaments hot enough to prevent a high cold-current surge, but not to provide luminance. However, in directly viewed incandescent displays, the effects of reduction in cold-segment current surge is often offset by the degradation in life that results from the constant current through the filament. Nevertheless, many designers still use heater resistors to minimize any detrimental surge effects on driver and logic circuits.
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CIRCLE 36 FOR PRODUCT DEMONSTRATION

Electronic Design 4, February 15, 1978
Control EMI from power supplies with computer-aided circuit analysis. You should not only save money, size and weight, but time to boot.

You are better off using computer-aided analysis to restrict emissions from power supplies for sensitive applications like military equipment. The traditional approach to controlling electromagnetic interference (EMI)—putting in some “catalog” filters, shielding thoroughly, testing and fixing—may burden the final product with excessive weight and size, jack up manufacturing costs and engineering expenses, or cause big delays.

Moreover, since EMI is only one of many design specifications you’ll be facing, you may have to trade EMI margin (especially at low frequencies) for more important factors, so you need accurate predictions of circuit performance. Computer-aided analysis also prevents unpleasant surprises due to interaction between low and high frequency filters and other components, and helps you with the MIL-STD-220 attenuation specs which are often hard to apply. Finally, analytical design takes the major parasitic circuit elements into account that limit power supply performance, and also line-impedance stabilization circuits that affect filter performance.

Computer-aided design (CAD) has helped control emissions from a very compact Army helicopter navigational computer with built-in display (ED No. 25, Dec. 6, 1977, p. 76). Measured emissions matched CAD predictions within 1 to 6 dB over a 160 kHz bandwidth and 90 dB dynamic range (Fig. 1). In this design, input current is independent of the input voltage, but not the load. So low-frequency noise current contains narrow pulses stemming from the inverter’s zero transitions, and contributions from an active load. The analytical approach, however, also applies to most other popular dc and ac power-supply architectures.

Before you jump into designing a low-EMI power supply, carefully study what limits are in force. As it applies to power supplies, MIL-STD-461A restricts the following emissions:

1. Conducted audio and rf-current emissions emanating from primary power leads.
2. Conducted audio and rf susceptibility in the primary power leads.
3. Conducted rf emissions emanating from other signal leads, caused by incidental coupling between wires in harnesses, housings and PC boards.
4. Radiated emissions from housing apertures, joints and controls, and from external leads.

The first two limits are treated directly by CAD. To satisfy the third and fourth, design your supply system with the fewest possible connecting wires and boxes. If you control emission currents in the external leads, radiated emissions from the external wires will usually fall within the specified limits. Shielding a cable run between boxes is usually pointless because the primary-power system renders complete shielding impractical. Furthermore, primary power emissions are usually orders of magnitude stronger than the incidental rf pickup in interbox wires. Indeed, much of the suspected interbox interference in aircraft (as well as in buildings) is really caused by radiation from primary power leads into faraway components acting as receiving antennas.

To add spice to the specs, Army, Navy and Air Force requirements differ radically for similar equipment. The low frequency range, where the differences are most pronounced, fortunately poses few interference problems.

Since each power supply architecture has advantages and disadvantages, make an EMI tradeoff chart, and choose the optimum approach for your

---

Louis A. Messer and Neville J. Sawyer, Group Engineers, Teledyne Ryan Aeronautical, 2701 Harbor Drive, San Diego, CA 92112.
Table 1. Tradeoffs for a helicopter power supply

<table>
<thead>
<tr>
<th>Choice</th>
<th>Significant advantages</th>
<th>Major disadvantages</th>
</tr>
</thead>
<tbody>
<tr>
<td>No post-regulators</td>
<td>Greatly reduced parts count, size and cost.</td>
<td>Some local decoupling is required.</td>
</tr>
<tr>
<td>Preregulator combined with power converter</td>
<td>Saves additional parts. Emission currents are low, as are filter cost, size, and weight.</td>
<td>Very poor efficiency makes this choice practical only for small loads, and requires good thermal design.</td>
</tr>
<tr>
<td>Separate supplies in each of two subsystem boxes</td>
<td>Lower wiring and shielding cost and weight, high reliability.</td>
<td>Cost of the second supply.</td>
</tr>
</tbody>
</table>

3. Conducted-emission limits of MIL-STD-461A differ for the Army, Navy, and Air Force. So do the terminations prescribed by the three service branches for testing.

but in the helicopter example the noise-sensitive loads were small and local active filters—which are generally simple and work well—proved adequate.

Make sure to choose a converter where frequency and intermodulation products will neither excite filter resonances, nor interfere with receiver i-f frequencies. The outstanding advantages of the architecture in Fig. 1 are low cost and low emission current (the ac components of the primary input current). All other preregulator types generate much higher emission currents.

For a flow chart of the design procedure to be used, check Fig. 2. Following it step by step; first determine the architecture (step A), then the preregulator's frequency response (step B). Measure the response, and—if needed—model it. Since the inverter switches are also series-pass elements, each transistor exerts linear control within its half-cycle of inverter operation. Conventional closed-loop effects limit the frequency response.

Select a low-frequency filter (step C). Part of its purpose is to add conducted-susceptibility protection where the regulator runs out of gain. If the filter is placed ahead of any switch, it acts directly upon any externally applied ripple frequency. Other architectures may have reversed order—e.g., a switching pre-

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2. A flow chart for the design procedure calls out the steps to be followed (A through G, also in the text.)
regulator or full-wave rectifier makes a rather decent balanced modulator. The filter then has to contend with sum and difference frequencies of the applied ripple and the switch (or ac) line, while the input fundamental frequencies are rejected.

This filter must also reduce low-frequency emissions from the power converter and from any active loads. Active voltage regulators don’t isolate ac load currents from the input, as do passive filters, which store energy. But don’t choose unnecessarily low frequencies—unless you want to add size, weight and cost. Remember that LC filters always resonate somewhere; a resonance can amplify emission, and overstress capacitors with ac currents.

Compromise, but with care

Ideally, you would place the low-frequency filter resonance far enough down the slope of the susceptibility spec so that the input ripple, multiplied by the filter’s Q doesn’t saturate the preregulator. This brings the resonance to a few kHz in Figs. 3 and 4. The resulting very small inductor would nicely augment the preregulator, while the resonance would still be well below an inverter frequency of about 10 kHz. The inductor’s parallel self-resonance would also remain high enough not to leave an isolation gap for susceptibility or emission, between the low frequency filter and the EMI filter.

However, other considerations may prevent you from choosing the optimum filter resonance frequency. The helicopter, for example, contained a variable duty cycle dimmer whose frequency was set to 1700 Hz so that its large interference currents fell into the region of high spec limits (see the Army curve in Fig. 3). To help reduce these emissions, the filter had to resonate well below the dimmer frequency.

You can control resonant Q by either choosing as high an inductor resistance as your power budget allows, or using a low L/C ratio for the filter’s LC product, to reduce the filter’s impedance. Keep in mind that the preregulator presents a high ac impedance to the filter so that a small input voltage change does not affect the current, and the filter’s Q isn’t limited by the reflected load.

If the best compromise for Q still does not meet

4. Conducted-susceptibility limits are referenced to the output voltage—in this example, 28 V dc.

the spec, you may have to request a waiver for susceptibility at resonance. In the case of the helicopter, it was most important to protect the customer’s lowest-frequency receiver “windows” (Fig. 5), and to hold down size, weight and cost. So challenge a specification when a waiver can produce good tradeoffs.

CAD to the rescue

Before you can apply a CAD program to your circuit, you must determine the noise generator’s approximate source impedance (step D). Without filters, most dc power converters look like high-impedance ac sources (current sources). Emission current is simply the ac component of the input current, but it is useful to consider the power converter as a generator and the dc source as the load. In the power supply of Fig. 1, the two inverter transistors alternately switch between a high impedance state and a linear-pass state, and the preregulator bleeds away excess base drive.

With the preregulator of Fig. 1 you achieve good line regulation while maintaining a nearly ideal 50% duty factor. The ac component stems from the fact that both transistors switch off briefly during each transition to ensure that they are never on simultaneously.

Because the current drawn during the on state is

5. Without knowing the rf environment of a power supply, intelligent tradeoffs are impossible. The Army helicopter

used as a design example is packed with receivers. Some antennas are only inches away from power leads.
nearly independent of line voltage, the emission waveform is a train of narrow constant-current pulses whose peak value is equal to the dc level. The emission's fundamental frequency is 22 kHz (twice the inverter frequency) and has a very flat comb spectrum. Lower-level components exist at the inverter frequency and its odd multiples (Fig. 6) because of the asymmetrical width of each cycle's two current pulses (caused by transistor-speed imbalances). For the helicopter, emissions from the display dimmer were similarly represented by an additional current source.

The frequency spectrum of an unfiltered source is more easily—and more accurately—measured than calculated (Step E). Use an engineering breadboard power supply at full load. However, you don't have to simulate digital logic loads at this point, because their frequencies are so high that there is little chance for interaction with the filter. Besides, the logic loads' effect on power-line emissions is very sensitive to packaging and shouldn't be measured in the breadboard form. The emission current must be bypassed with a broadband short circuit such as a 10-µF coaxial feedthrough capacitor.

For measuring the unfiltered source use a very low-impedance lab supply, rated for two or three times the needed capacity, and either calibrated tunable voltmeter (wavemeter) or a spectrum analyzer. (Fig. 6 shows the composite unfiltered spectrum for the design example, taken from a series of measurements.)

How close did you get?

In Fig. 3, the customer's specified emission limit, MIL-STD-461A, is overlayed on the spectrum. The shaded area indicates where emissions exceed the spec, and gives you a feel for the required filtering.

You want to predict the effect of the filter as accurately as possible, which requires a CAD program like ECAP, (step F). But to use CAD, you need a complete equivalent circuit, including parasitics. In a properly packaged power supply, these consist of the inductor's distributed capacity, equivalent series resistance (ESR), and capacitor residual inductance. Lead inductance is important and can help or hinder the desired attenuation—depending on the layout—because power-supply filters work at very low impedance levels. A 50-µF wet-slug tantalum capacitor has around 50-nH parasitic inductance (depending on lead length), and this combination resonates at a low 100 kHz. A 0.05-µF tubular bypass capacitor with 1-in. leads raises the series resonant point to around 3.3 MHz.

Fig. 7 shows the effects of proper and improper

6. Emissions measured with a spectrum analyzer (insets) must be combined and compared with the spec limits (colored line). The problem area (white) must be eliminated with low-pass filters.
Parasitics everywhere

In the complete low-frequency filter (Fig. 8) the capacitor actually consists of two 22-µF wet-slug tantalum capacitors in parallel. The 24-nH series inductance consists of 22 nH of self-inductance, in series with 22 nH of parasitic inductance from the two 1/2-in., AWG-22 leads of each capacitor. The 0.61-Ω resistance is the sum of an overload sensor (0.6 Ω) and the measured value of ESR (calculated from the measured dissipation factor D) at 10 kHz. Since ESR appears to exhibit a frequency dependence, you would be wrong to use the ESR at the commonly specified frequency.

Packaging on the circuit model. To optimize packaging, the electrical and mechanical product designers must work together closely. Put as much lead inductance as possible into the load current bus, and as little as possible into the shunt path.

The next significant parasitic reactance—inductor capacity—is easy to determine. Connecting the inductor through a 100-kΩ resistor across a signal generator. Then find the resonance frequency with a lightly coupled oscilloscope, and calculate the capacitance. Finally, measure the choke resistance with a dc bridge. Don't increase this value to compensate for skin effect, because the effect of resistance on Q is biggest when the circuit resonates at low frequency.

Parasitic elements can help or hurt. Those of a coaxial feedthrough capacitor with bulkhead mounting (a) help, while a discrete capacitor's parasitics (b) hurt.

7. Parasitic elements can help or hurt. Those of a coaxial feedthrough capacitor with bulkhead mounting (a) help, while a discrete capacitor's parasitics (b) hurt.
Table 2. Comparison between predicted and measured results

<table>
<thead>
<tr>
<th>Frequency (kHz)</th>
<th>Driver magnitude (dBµA)</th>
<th>Predicted filter attenuation (dB)</th>
<th>Predicted emission level (dBµA)</th>
<th>Measured emission level (dBµA)</th>
<th>Error (dB)</th>
<th>Army emission limit (dBµA)</th>
<th>Actual margin (dB)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.8</td>
<td>100</td>
<td>14</td>
<td>86</td>
<td>82</td>
<td>-6</td>
<td>108</td>
<td>16</td>
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<td>5.4</td>
<td>81</td>
<td>28</td>
<td>53</td>
<td>58</td>
<td>-5</td>
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<td>26</td>
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<td>22</td>
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<td>58</td>
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<tr>
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<td>100</td>
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<td>8</td>
<td>37</td>
<td>37</td>
<td>29</td>
</tr>
</tbody>
</table>

frequency of 120 Hz. Although ESR increases by a factor of 10 at 50°C, you can disregard that because emissions are only measured in the laboratory, and besides, the power supply warms up very quickly.

It is quite important, however, that you measure and model your low-frequency inductor at the dc current and ac voltage that it will see in actual use. Inductor weight and cost rise quickly with size, and you can't afford enough extra iron to make the inductor work in a linear region. Consequently, inductance usually changes rapidly with dc operating bias.

The topological circuit model (step G) is relatively independent of whatever CAD program you end up using. For the helicopter supply, IBM ECAP was used, and branch and node nomenclature have been included in Fig. 8.

Since the Army tests specified two different line terminations, one below, the other above 50 kHz, two separate models were made. The corresponding changes for low frequency are shown in Fig. 8. Although you haven't added an EMI filter at this point, the complete ECAP listing includes these components, because they will soon be needed.

Only a coaxial capacitor has a high enough self-resonance to be effective above the audio-ultrasonic transition band where the low-frequency filter resonates. Before you choose specific components, a few simple calculations on a pocket calculator can provide you with a good starting point (step H).

When you adjust the low-frequency filter's elements, remember that you chose this filter to control susceptibility, and that your basic design constraints may be strongly affected by the filter's relatively large circuit components.

So, determine the low frequency filter's frequency response to an ac current source. It is generally easier to plot the unfiltered emissions from a constant amplitude source and manually add the filter attenuation to the previously measured levels. However, some newer modeling programs such as NCSS Inc.'s ISPICE permit empirical data, even equations, to be inserted rather easily into topological circuit models.

Because the basic IBM ECAP program does not convert the results to dB, you may find a pocket calculator, pencil and semilog paper quite adequate for the first iteration. Some desk calculators, such as the HP9825, have very convenient interactive forms of ac ECAP, convert the answers to dB and plot it. Unfortunately, the HP9825 program has a fixed 1-V voltage source and does not handle branch currents.

So far, you haven't really needed a CAD program. But once you add the topology of a pair of EMI filters, (step I), the network becomes far too complex for manual evaluation, much less intuitive understanding by inspection. Keep in mind that discrete coaxial filters with low frequency corners and steep slopes often exhibit sharp ultrasonic resonances that can actually amplify emissions.

Pick a pair of values

Before you decide on filter values, define limitations imposed by size, weight, cost, voltage, current, standard-parts-list requirements and reliability. Some of these constraints may limit envelope ranges. Next determine the filter's optimum topology. If your termination network approaches 50 Ω at higher frequencies, a π or L network (with capacitor facing the input) is quite effective. However, even a 1-µF capacitor—the largest generally available—becomes meaningless when paralleled with the 10-µF capacitor used in the Navy and Air Force emissions test. So, a T or reversed-L filter would be most useful in these cases. Indeed, attenuation data measured to MIL-STD-220 may actually misguide you: In a 50-Ω system with a given filter volume, a π is more efficient than a T or L, and usually has a lower specified corner frequency.

To resolve the paradox, specify a filter model by topology, element values and tolerances. Examine MIL-STD-220 data above 10 MHz to control potential parasitic behavior in the vhf and uhf bands.

How do you determine for your modeling what's inside the filter? Measuring is poor practice because the topology usually isn't fixed. Another vendor or even a later production run could meet the specified attenuation with different components. If the filter is asymmetrical to boot (like the one in Fig. 8) it would be almost impossible to define by external measurement. The best thing to do is talk to the vendor's
9. The filter attenuation predicted by ECAP shows a resonance around 2 kHz. A second one (white) disappears after you connect the 10-µF coaxial line terminations specified under MIL-STD-462, method CE-01.

design engineers. Once they understand why you need the data, they are likely to cooperate because they too can benefit from modeling. Your filter vendor can likely give you a “shopping list” of available filters to try in your CAD model. Once you have an accurate model, you can quickly and economically iterate it as many times as necessary to optimize your design (steps J and K).

For the results of both the high and low-frequency ECAP runs check Fig. 9. The previously described loss of attenuation of a PI network against a 10-µF termination shows up very dramatically at the 50-kHz transition frequency of the two models. This presented no problem in the Army helicopter application, but if you wanted to sell the power supply to the Navy—knowing that it had been tested successfully to MIL-STD-461—you’d be in for an unpleasant surprise.

Note that the 2.6-kHz resonance (Fig. 9) is caused by elements at opposite ends of the model. Inspecting the complex model without CAD certainly doesn’t hint at this resonance, which fortunately does not coincide with an emission line and therefore poses no problem. But you would not find it by merely testing the filter circuit, with a lab supply for a power source.

For very low emission frequencies, where the reaction of the 10-µF capacitor is a few ohms, it’s advisable to insert some impedance between the dc source and the capacitor. MIL-STD-461 does not prescribe what to do on the other side of the network.

The proof of the pudding

Table 2 summarizes the test results for the design example. At one point, emissions just grazed the spec, but the customer had been forewarned, and had given his consent. The measured values in Table 2 stem from the official test report, and agree closely with the values predicted by ECAP. Differences at lower frequencies are most likely caused by the typical excess capacity of wet-slug tantalum capacitors over the minimum specified values. Above 160 kHz the actual attenuation exceeds the prediction substantially, but as most stray coupling mechanisms increase directly with frequency, some degradation must be expected.

Achieving desired attenuation with economical production techniques is a real problem when space is limited. Discrete tubular filters are the best choice for two or three wires. But the realizable isolation is limited by the isolation between the wires at the two ends of the filter, and you will usually have to mount the filter in the package wall.

A filter pin connector may be more economical for a larger number of leads, because it automatically isolates the quiet from the noisy side. To improve isolation even further, the power supply for the helicopter was designed so that the end of that for large loads. In the example of Fig. 1, the load current was smaller and essentially constant.
subassembly was also the end of the box, eliminating extra connectors and harnesses. Still, why should filters be optimized for a particular customer's specified test load rather than the low-impedance power distribution system that the supply sees in the real world? Quite simply, it's a compromise. A 10-µF capacitor in the Navy's spec appears overly pessimistic while the Army's 50-Ω stabilization network is probably optimistic.

Above 100 kHz, the impedance of airborne power-distribution systems varies too much to be characterized. While impedance generally rises with frequency, line resonances and discrete load reactances create wild impedance swings. The service branches compensate by over specifying emission limits at lower frequencies in different ways, supposedly, because of the different environments in which their equipments operate. The Navy and Air Force provide two limit extremes, with the Army in the middle (Fig. 3).

Navy emission limits are difficult to achieve with many power-supply architectures, particularly those with 400-Hz primaries. More realistically, the Air Force only requires emission limits below 20 kHz if the application warrants it. Above 2 MHz, requirements of all service branches converge on 10 µA for discrete narrow-band terms, because there the risk of radiating into a receiving antenna gets much higher.

Effective receiver sensitivity generally rises with increasing frequency because atmospheric noise falls off and local, man-made noise threatens to become more serious. Because most coupling modes between wires act as high-pass filters in the near zone, you can usually control conducted emissions in this region simply by packaging the EMI filter properly. Almost any coaxial filter other than a straight capacitor will work if it is properly installed to realize high isolation (Table 2).

If you test a low-powered box to MIL-STD-461, requirements CE-01 and 03, and the aircraft is large enough to have separate power leads to the source, the region up to a few hundred kHz poses little intersystem interference risk. So take advantage of any freedom the standard gives you, to run the test.

Not all power supplies are alike

The design example discussed so far has about as low a conducted-emission level as is possible, but at the expense of efficiency. Supplies for higher load power often cannot afford the high internal dissipation and primary power consumption of the supply in Fig. 1. Rather, a variable duty cycle preregulator (Fig. 10) is commonly used for high-efficiency applications. Control of conducted emissions then becomes more serious. Not only is the exciting load current higher, but the ac (fundamental)-to-dc current ratio jumps from the −30 dB-level to about 0 dB.

A variable duty-cycle switch guzzles input current in intermittent gulps. But the filter composed of L2 and C2 (Fig. 10) is required for the preregulator's basic operation, and does nothing to control conducted emissions. Consequently, the emission current could exceed spec limits by many dB, depending on the load, and which service branch is involved. The EMI filters of Fig. 10 may adequately control emissions above about 100 kHz in such a supply, but would probably amplify emissions at the first few spectral lines.

So, you need the best design for the low-frequency emission control filter L1-C1 you can get, and that requires CAD. This powerful design tool also helps you minimize ringing problems in the filter, and reduce EMI even more. In a switching supply, inductor L3 provides a current source in the emissions model. However, its magnitude is much higher than that in the helicopter study. Worst-case emissions don't occur at 50% duty factor because in this situation the even-order terms cancel, while the fundamental increases only slightly. A 33% duty factor is a good compromise for worst-case analysis.

An ac power supply with choke-input load filters also presents a current source to the EMI circuit model. A static resistive load creates a square wave term at the line frequency and the output filter superimposes a term at twice line frequency, chopped by the square wave (Fig. 11). Capacitive load filters, on the other hand, create voltage emission sources that are harder to characterize, but seldom would you use one where conducted-emissions currents are important. These filters generate very flat frequency combs, which extend out to limits dictated by the rectifier's, and the transformer's, parasitic impedances. Low-frequency emissions are a problem with both filter types since there is no low-frequency filter between the emission source and the EMI filter. Consequently, large EMI filters with low corner frequencies are required, ringing problems abound. Even with CAD, you will often require EMI-spec waivers. But computer modeling will strengthen your case.

Acknowledgment
The authors appreciate the cooperation of Bendix, Electrical Products Div., which performed extensive work to provide an exact CAD model of the discussed low-frequency filter.
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Certainly, the various types have similarities. Most line regulators produce outputs that vary only up to ±5% from nominal in the face of variations ranging from 10 or 15% under to 10 or 20% over the nominal input. And all line regulators can be filtered to suppress noise and transients. But for computer applications, five features will make or break it for line regulators:

- Response time.
- Cost.
- Maintenance requirements.
- Power-handling capacity.
- Output impedance.

For computer applications, a line regulator's output impedance is particularly important because momentarily high demand (current to the load) causes voltage sags proportional to the regulator's output impedance. And computers plus peripherals often pull heavy current surges—especially with the dc switching regulators and high-starting-current motors increasingly common in computer equipment.

How they line up

Often, to compensate for output impedance, a line regulator's power capacity must be double or even triple the system's average demand. The result? Heavier and bulkier regulators that cost more (see the regulator comparison table).

Motor-driven regulators are inexpensive as a rule and can handle high kVA loads (Fig. 1). But the problem is that they respond slowly to input voltage or load changes. And frequent maintenance and high output impedance pile on to take motor-driven units out of the computer game.

Saturable reactors—step-up autotransformers with feedback-controlled series impedances—have notable pulses: wide load range, low maintenance require-

1. Three motor-driven regulators are commonly used. Driven brushes (a) move across many taps to buck or boost the series transformer for a ±15% correction range. Motorized rotary-switch fingers (b) provide a ±10% correction range, and a reversing switch changes the connection from buck to boost, as needed. The rotor is turned for a ±15% range in an induction regulator (c).

ments and relatively low cost (Fig. 2). Most computers, however, can't tolerate either of the saturable reactor's main drawbacks—sluggish response (dragged out over five to 10 input cycles) and towering output impedance (often a full 30% of the load impedance). So saturable reactors, though tempting for large computer installations because of low cost, are at best stop-gaps.

Like the saturable reactor, another regulator, the ferroresonant transformer (Fig. 3), requires little maintenance and is inexpensive. Better yet, it isn't as sluggish (its response takes two cycles). Unfortunately, poor capacity for handling momentary overloads (output collapses at 150% of full load) and

Ruxton Tucker, Engineering Applications Manager, Topaz Electronics, 3855 Ruffin Road, San Diego, CA 92123
2. **Saturable reactors** (a) correct ±15% via sensing and control circuits that govern the dc into a control winding. Another magnetic regulator (b) has all the windings of the saturable reactor—but on one core. Primary, boosting and bucking windings act as a step-up autotransformer. Core saturation holds the output steady.

High output impedance (30% of load) veto ferroresonants for most computer systems.

Indeed, with computers using SCR or transistor-switching dc power supplies or, for that matter, any high-inrush or high periodic-demand load, ferroresonant regulators must be oversized. Motors, such as those used in computer equipment, often draw four to five times normal operating current at turn-on.

In addition, disc or tape drives in a computer facility often must be isolated from the ferroresonant transformer that feeds the central processor. And there goes your economy.

Switchers of course are the nemesis of ferroresonant regulators—they draw notoriously high-current pulses for short durations. Peak currents in them are so towering that current readings from typical clip-on peak-reading ammeters often fall short of reality by as much as three to seven times.

**Size is no defense in the noise game**

One drawback that motor-driven regulators, saturable reactors and ferroresonant transformers have in common is that they have trouble keeping line noise from computer loads. And this holds across the computer-size spectrum—from the largest data-processing facility down to microcomputer-based systems. Most motor-driven regulators and saturable reactors

---

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Regulator box score

<table>
<thead>
<tr>
<th>Regulator type</th>
<th>Response time</th>
<th>Cost ($/VA)</th>
<th>Maintenance required</th>
<th>Power capability (kVA)</th>
<th>Output Impedance (% of load)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Motor-driven mechanical tap changer</td>
<td>1 min</td>
<td>6</td>
<td>Periodic</td>
<td>50 up</td>
<td>10</td>
</tr>
<tr>
<td>Induction</td>
<td>5 s</td>
<td>8</td>
<td>Periodic</td>
<td>10 to 100</td>
<td>20</td>
</tr>
<tr>
<td>Electromechanical</td>
<td>5 s</td>
<td>16</td>
<td>High</td>
<td>2.5 to 500</td>
<td>30</td>
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<tr>
<td>No moving parts saturable reactor</td>
<td>5 to 10 C</td>
<td>15</td>
<td>Low</td>
<td>1 to 100</td>
<td>30</td>
</tr>
<tr>
<td>Ferroresonant transformer</td>
<td>2 C</td>
<td>20</td>
<td>Low</td>
<td>0.015 to 3</td>
<td>30</td>
</tr>
<tr>
<td>Electronic tap changer</td>
<td>1/2 C</td>
<td>20</td>
<td>Low</td>
<td>0.5 to 20</td>
<td>2</td>
</tr>
</tbody>
</table>

have little or no inherent noise filtering. Ferroresonant transformers have limited noise filtering.

Filtering, of course, can be added to each of these regulators. Just tack an L-C network onto the regulator's output. Line noise is attenuated—but then the output impedance goes up.

With all the drawbacks described, one regulator type that is very tempting is an electronic tap changer (Fig. 4). Reaction time isn't a problem for an ETC, not with half-cycle response. Its cost is within bounds, and with no moving parts, there isn't much to maintain. Its power-handling capability is good enough for today's computers. What's more, the output impedance is close to the distribution line's impedance.

Power-line disturbances scorecard

A few years ago, two IBM engineers1 monitored many computer installations for power-line disturbances. They divided line disturbances that affect computers into four categories:

• Voltage spikes—from lightning, power network switching and user-equipment operation;
• Decaying oscillatory transients—from switching power-factor-correction capacitors and other network or load switching;
• Undervoltages, and occasionally, overvoltages lasting more than a half cycle of the power voltage—from faults and the action of fault-clearing devices;
• Outages—total voltage loss for longer than one half cycle.

While individual voltage spikes are typically brief (10 to 100 μs), bursts of such pulses sometimes last for several milliseconds. The nominal ac-voltage peak changes by 150%. Also nanoseconds spikes occurred.

Oscillatory transients span the frequency range from 400 Hz to over 5 kHz. Their initial amplitudes are often 100% of nominal peak voltage. Usually they decay to zero within one ac-input cycle.

Undervoltages and overvoltages are sometimes caused by large changes of load and the resulting utility responses to these load variations.

Data from 109 months of monitoring are summarized in the “scorecard” table. Variations within the thresholds used in the “scorecard” were not recorded. (These minor disturbances are assumed not to affect the performance of the computers.)

Volatges outside the limits are considered capable of causing problems in computers. These problems take the form of errors, memory loss, program loss, and even complete shut-down.

Too often these problems are lumped with “computer” or “data-entry” faults. Except for voltage outages, the other power-line aberrations are hard to detect without unusual equipment.

So, usually, the programmer, keypuncher and service representative share the blame. And even after costly “fixes,” these random flaws continue.

Reference

in other words, very low.

The ETC uses an autotransformer with a number of input or output taps, which are switched appropriately, at zero-voltage crossover to eliminate noise. So the output voltage is automatically boosted-up or bucked-down as needed. Though the input varies, the output voltage can be kept within a narrow error band. The number of taps determines the width of the long-term output-voltage band.

An ETC's half-cycle response to input-line or load changes mates well with most computers. Usually, computers continue to operate during a complete cycle of power-line loss. So a regulator that responds within a half-cycle normally meets a computer's requirements. Obviously, regulators with above-one-cycle responses are inadequate.

But, in spite of the several good points that suit them to computers, ETCs are still only transformers. So filters must be added to suppress power-line noise. Then the ETC's low output impedance becomes a less advantageous high output impedance.

An ultra-isolation transformer, however, can protect computers from random power-line voltage deviations. It provides common-mode-noise attenuation of 125 dB and transverse-mode attenuation of 70 dB, virtually eliminating all power-line transients. And because of its low output impedance, an ultra-isolation transformer provides noise attenuation without the penalty of high insertion losses.

But this super-transformer does have one drawback—it can't regulate against long-term conditions.

The answer is teamwork

So to regulate today's utility lines for today's computers, team up an ultra-isolation transformer with an ETC. The result? High-quality regulation and filtering, low impedance, high efficiency, fast response and low maintenance.

This tandem regulator responds in less than one cycle and costs approximately 20¢ per VA. Maintenance requirements are very low and the power capability easily covers the required range for most computer installations. The output impedance is under 5% of the load impedance and the minimum efficiency is 94%. Compare this with the 15 to 20% efficiencies of line regulators that aren't motor-driven.

As good as the tandem device is, it isn't the ultimate. Complete voltage outages (0.5% of all disturbances) still slip the net. None of the regulating devices described continues to operate after input power fails.

While tandem-type line conditioners guard against virtually all the disturbances that affect computer operation, only an uninterruptible power source provides complete protection. But a UPS costs from five to ten times as much as an ordinary line conditioner.

Certainly this often may be too severe a price to pay for moving from 99.5 to 100% protection. However, there are installations where any lost computer time is so serious that the UPS makes sense.
Positive reference-voltage IC is flipped negative by adding a single component

A so-called "mirror-image" effect lets you use three-terminal reference ICs, which are all positive-output devices, to build negative voltage references. In the basic negative-reference circuit of Fig. 1, the addition of op amp \( A_2 \) (PMI's OP-02E) allows \( A_1 \)'s (PMI's REF-02E) positive reference voltage to flip over to its exact negative at the output terminals.

The reference IC, \( A_1 \), has its positive output connected in the negative-feedback path of \( A_2 \). Since \( A_2 \)'s noninverting (+) input is grounded, pin 6 of \( A_1 \) is also grounded. This forces \( A_1 \)'s negative-output terminal (pin 4) to drop below ground to the negative magnitude of its voltage (5 V for the REF-02E or 10 V for a REF-01 and Analog Devices AD580). About 5 mA of output current is delivered from \( A_2 \). Output voltage is optionally adjusted by \( R_1 \), which trims the voltage by \( \pm 6\% \), typically. Input-line-regulation error, 80 dB or better, results from combining the rejection coefficients of both \( A_1 \) and \( A_2 \).

Temperature stability can be a problem since each active device contributes to the drift. \( A_1 \)'s tempco is 8.5 ppm/°C max, while \( A_2 \)'s maximum drift of 8 \( \mu \)V/°C is equivalent to 1.6 ppm/°C. Total drift of the circuit, therefore, is about 10 ppm/°C.

You can adapt the basic circuit to scale output voltages (Fig. 2), which are a multiple of the reference voltage. In this circuit, the \( R_1 \), \( R_2 \) voltage divider multiplies \( A_1 \)'s reference voltage by \( R_1 + R_2 / R_2 \), and \( A_1 \) and \( A_2 \) operate essentially as in Fig. 1. Note that this circuit operates from a single negative supply, which gives it inherently better supply rejection than the basic circuit.

To make sure the circuit starts when power is applied, the \( R_5 \), \( R_6 \), \( D_1 \) network must be included. At start-up, but before \( A_1 \) comes up to its correct voltage level, the voltage across \( R_1 \) drives \( A_2 \)'s output negative (−5 V). Once \( A_1 \)'s output reaches its correct level, \( D_1 \) is zero-biased and contributes no error. Temperature stability may be slightly degraded by \( R_1 \) and \( R_2 \), so resistors with low tracking tempcos should be used for best results.

Walter G. Jung, Consultant, Forest Hill, MD 21050.

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... look to AVX.
Ideas for design

Multitask µP executive routine uses only six instructions

Here's a simple six-instruction subroutine that lets your 6800 microprocessor control several external processes simultaneously. To use it, organize your software as follows:
1. Set up a process-control block as shown for each process to be controlled.
2. Write a program for each process as if no other programs are running in the same microprocessor.
3. Insert JSR SPND instructions into each program at convenient points to allow other programs to run.

Whenever a process suspends itself by executing a subroutine jump to SPND, the SPND routine swaps the process-control block pointer (PCB) and the stack pointer to set up the next process. Then a simple return instruction causes the program for that process to start running again where it left off. Each process-control block contains at least two parameters: a pointer to the next control block and a stack pointer.

A six-instruction executive routine

| SPND LDX PCB | SET INDEX REGISTER TO CURRENT CONTROL BLOCK |
| STS 2,X     | SAVE CURRENT STACK POINTER                 |
| LDX X       | SET INDEX REGISTER TO NEXT CONTROL BLOCK   |
| STX PCB     | SAVE CONTROL BLOCK POINTER                 |
| LDS 2,X     | GET NEW STACK POINTER                      |
| RTS         | RETURN TO PROCESS                          |

Process-control block

Each block points to the next in a circular list.

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<table>
<thead>
<tr>
<th>SERIES</th>
<th>TYPICAL APPLICATION</th>
<th>$V_{CE}$ (V)</th>
<th>$P_m$ (Watts)</th>
<th>$P_{out}$ (Watts)</th>
<th>Frequency (MHz)</th>
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<td>2.0</td>
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<td></td>
<td>or mobile radio</td>
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CIRCLE NUMBER 44
for the current control block. The control blocks are arranged in a circular list so that SPND will automatically return to the first block after executing the last.

A sample program illustrates how process control (continued on page 105)

**Start-up routine**

* SET INDEX REGISTER TO FIRST CONTROL BLOCK, GET CORRESPONDING STACK POINTER, AND BEGIN EXECUTION:

```
START LDX PCB
LDS 2.X
RTS
```

**A sample program**

* APPLICATION DEPENDENT
* PROCESS CONTROL BLOCK
* PARAMETERS:

```
MODE EQU 4
STAT EQU 5
BUFIN EQU 6
BUFOUT EQU 8
```

* FETCH A BUFFER. IF NO BUFFERS ARE AVAILABLE, SUSPEND AND TRY AGAIN:

```
IDLE JSR BUFGET
BNE READY
JSR SPND
BRA IDLE
```

* PREPARE TO RECEIVE:

```
READY LDX PCB
CLR STAT,X
LDAA #1
STAA MODE,X
JSR RCV
```

* SUSPEND. THEN, IF AN INPUT MESSAGE HAS BEGUN, GO TO INPUT. IF AN OUTPUT MESSAGE IS WAITING, GO TO OUTPUT. OTHERWISE, REPEAT:

```
LOOP JSR SPND
LDAA STAT,X
BNE INPUT
LDX BUFOUT,X
BNE OUTPUT
BRA LOOP
```

ELECTRONIC DESIGN 4, February 15, 1978
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blocks and SPND instructions work together. Only the idle loop is shown. The complete program supervises an interrupt routine, which handles message flow to and from a teletypewriter. The program sets the teletypewriter port to receive, then waits for the interrupt routine to receive the first character. If an output message arrives first, the port is switched to output, and the message is printed on the teletypewriter. Notice that the program always suspends itself while waiting for something to happen.

All communication between background and interrupt levels occurs via the process-control block. MODE, for example, tells the interrupt routine whether to send or receive. STAT tells the background program that the interrupt routine has started or completed the message. BUFIN and BUFOUT are pointers to tell the interrupt routine where to store an input message or find an output message in microprocessor memory.

In a typical communications application, there might be several I/O ports, each having its own process-control block. Each control block may have a separate background program, or a single program may be shared by all control blocks. The sample program can be shared by multiple control blocks, because

- All data references are either to or through the control block.
- Each control block has its own return address stack.

Be careful with this multitask operation, however. Remember:

1. Processes aren't suspended by a "time-slicing" interrupt, but must suspend themselves often enough to let other programs run.
2. Processes should suspend themselves only at points where it is safe to lose the register contents, since SPND doesn't restore any registers except the stack pointer. (If this is a problem, register-save-and-restore instructions can easily be added to the SPND routine.)
3. Interrupts may remain enabled continuously, but every control block's return-address stack should be large enough to accommodate every interrupt routine's worst-case requirements.
4. All control blocks and stacks as well as the PCB pointer must contain proper initial values before starting the system. A brief start-up routine initiates normal operation.

David W. Johnson, formerly Senior Engineer with Control Data Corp., Santa Ana, CA 92704 now with NCR Corp., 3325 Platt Springs Rd., West Columbia, SC 29169.
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Solid-state relay improved with zero-volt switching circuit

With a simple modification, an opto-isolated solid-state relay previously published as an Idea for Design (ED No. 2, Jan. 18, 1977, p. 96) produces precise zero-voltage switching of a triac, while maintaining opto-isolation.

In the modified circuit, a 24-V pk-pk clipped sine wave develops across the zener diodes, D₁ and D₂, when the zeners are driven from an ac line through current-limiting resistor R₁. The clipped sine wave, further "squared-up" by inverting amplifier Q₁, feeds a 741 op-amp differentiator. Capacitor C₄ limits the high-frequency response of the differentiator, and C₃ and R₄ act as a differentiator network.

The output of the 741—a series of positive and negative-voltage spikes—is routed via diodes D₆ through D₉ and opto-isolator phototransistor Q₂ to the triac gate. The voltage spikes, which occur at the square-wave transitions, are in step with the zero-voltage crossings of the ac line. Also, the spikes have the right polarity to trigger the triac efficiently in the I₊ and III⁻ modes.

Trigger pulses appear at the triac gate only when the LED of the opto-isolator, D₅, is on, which causes phototransistor Q₂ to conduct. Supply voltages of about ±12 V for the 741 and inverting amplifier Q₁ are provided by diodes D₃ and D₄ and capacitors C₁ and C₂.

Devlin M. Gualtieri, Dept. of Chemistry, University of Pittsburgh, Pittsburgh, PA 15260.

CIRCLE NO. 313

This opto-isolated solid-state relay switches on (or off) only when the line voltage crosses zero. As a result, high transient in-rush currents and attendant EMI noise are eliminated.
Regulate motor-shaft speed better with an active bridge

Improve control of motor-shaft speed by putting your motor winding into a leg of an active bridge. What's more, with a µA759 power op amp as the active device, the circuit in the figure will make shaft speed proportional to the input voltage, $E_{in}$.

If you select the ratio of a motor's winding resistance to the bridge resistors for bridge balance, winding resistance will be actively cancelled. Then the motor speed becomes directly proportional to the op-amp's low-level input control voltage. In addition, the circuit improves performance under varying motor-load conditions. With winding resistance cancelled, motor shaft speed becomes independent of load.

Winding resistance $R_m$ (5 Ω) is five times larger than $R_1$ (1 Ω) on the inverting side of the op amp. In the upper leg, $R_1$ and $R_2$ also have a 5:1 ratio—but at much higher resistance values to minimize loading of $E_{in}$. Since the ratio of the resistances in both legs of the bridge is the same, and the op amp differential input voltage is low, $E_{in}$ equals the voltage at point A.

An increase in motor load causes a larger current to flow through the winding—this increases the drop across $R_m$. The output voltage at point B then rises proportionately to balance the voltage at A. If motor load decreases, the opposite voltage relationships occur. A network for frequency stabilization of the output, may be required, such as $R_4$, $C$, but it depends on the characteristics of your motor.

James M. Pihl, Design Engineer, Physio-Control Corp., 11811 Willows Rd., Redmond, WA 98052.

CIRCLE NO. 314

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CIRCLE NUMBER 94
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CIRCLE NUMBER 85
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Circle Card Number 92
Ferroelectric memory protects its information

A low-cost, nonvolatile analog memory with nondestructive readout is now possible thanks to newer lead titanate-lead zirconate ferroelectric materials. The memory, developed by researchers at Philips' Forschungslaboratorium in Aachen, West Germany, uses these ferroelectric materials to store levels of brightness and contrast in TV sets or hold telephone numbers frequently called in an automatic telephone device, among other things.

Information is stored in these materials by applying a voltage that polarizes the material to an extent determined by the voltage. The maximum value of an input quantity can be stored with 30 V in the memory. The input stored is not, however, directly proportional to the applied voltage because, like magnetic cores, the ferroelectrics have a nonlinear hysteresis curve.

The memory has a ferroelectric ceramic disc with an identical electrode pattern on both faces (see Fig.). The central electrode pair (D) is driven by the oscillator to excite a radial vibration in the material, which is also piezoelectric.

The material between both the D and R electrodes is permanently polarized. The R electrode pair generates a feedback signal that drives the disc at its resonant frequency.

The memory cells are formed by the M electrode pairs located around the disc's circumference. To write analog information in, the memory cells are polarized by a voltage in one direction with the oscillator turned off. To retrieve the information, the oscillator is turned on and the information is read out as voltages across the M electrodes developed by the piezoelectric vibrations of the disc.

Two poles instead of one make motor reversible

Conventional shaded-pole motors run in one direction. But by splitting the single pole of a motor into a wound main pole and an auxiliary pole with its own winding and shading ring, a researcher in Scotland has produced a reversible motor.

Besides being able to reverse the rotation at start-up, the design permits a motor to be reversed at full speed without harm. This feature will prove attractive for fan drives where shaded-pole motors already are widely used.

In a standard shaded-pole motor, the main pole of the stator carries a shading ring on one leg of its poleface. The shading ring, a single-turn, short-circuited winding, produces a flux lag that generates torque in a preferred direction. In theory, a shaded-pole motor having two shading rings, one on each leg of the main poleface, could be reversed by open-circuiting one ring and short-circuiting the other. But such motors are rare.

In the reversing design developed by Dr. S Williamson of the Dept. of Engineering at the University of Aberdeen, all the main-pole coils are connected in series as a "main winding," which produces reference magnetic fields around the stator periphery. The auxiliary-pole coils are similarly connected to provide a reversible field.

When the main and auxiliary windings are connected initially to a single-phase line, pole-flux phase shifts produce a torque in one direction. Reversing the connections to the auxiliary windings reverses the flux in these windings, with respect to that in the main windings. This produces rotation in the opposite direction.

Tomograph makes fast scans

A "third-generation" computer tomograph, the first developed in Germany, is said to be capable of performing a complete scan of the body in as little as 2.5 seconds.

Developed by Siemens in West Germany, the X-ray diagnostic instrument —called Somaton—is capable of taking transverse tomograms of the entire body from the skull to the extremities. Up to 14 scans can be taken in less than 5 minutes. Each scan covers an 8-mm or a 4-mm thick slice, and a tomogram is available on the monitor at once.

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CIRCLE NUMBER 55

Electronic Design 4, February 15, 1978
All-in-one development station makes programming easier, faster and less expensive

New products

All-in-one development station makes programming easier, faster and less expensive

Electronics Design 4, February 15, 1978
MICRO/MINI COMPUTING

(continued from page 117)

synchronously. The other part consists of the interface for the integral CRT, keyboard and standard peripherals such as printers, tape-reader/punch and PROM programmer.

The high-level software available in the Model 230 permits you to link together routines that have been developed in the language that is most efficient for you—thus, assembly-language programs can be combined with programs developed in Fortran or in PL/M.

All three systems are designed for 0 to 35-C operating environments and require 115/230 V ac, 50 to 60 Hz.

The Series II systems are restricted to the Intel family of processors, which, even though are widely alternate-sourced cannot satisfy every application. However, some “universal” development systems, such as the 8002 offered by Tektronix (Beaverton, OR) can do many of the things the high-end Model 230 can do. But you’ll have to pay well over $15,000 for what the 230 can do for less than $13,000. Nevertheless, if you’re doing development work with processors other than the MCS-48, MCS-80 or MCS-85 families, a universal system may be required.

The Intel systems are much more compact than many of the available development systems. As a matter of fact, they can just about be loaded onto a cart and wheeled about from workbench to workbench. However, for portability, you should also consider the MUPRO-80 made by Mupro (Sunnyvale, CA). Housed in a 4.6 x 6.6 x 15-in. case, the program-development/in-circuit emulator system offers programming capability in BSAL, a specially developed block-structured assembly language. The Mupro system, though, is limited to only the 8080 and 8085 processors.

Options for the Intel 210, 220 and 230 include ICE modules for 8080s, 8048s and 8085s, and they cost $1750, $1950 and $2700, respectively. In addition, kits for upgrading the 210 and 230 enable them to match the 230, any of the SBC-80 family memories and I/O cards, and such large peripherals as printers, disc drives, PROM programmers and paper-tape readers.

Delivery of the Intel systems is from stock.

Intel  
Mupro  
Tektronix

CIRCLE NO. 301  
CIRCLE NO. 302  
CIRCLE NO. 303

Minifloppy-disc storage installs in SS-50 bus

PerCom Data, 318 Barnes, Garland, TX 75042. (214) 276-1968. 2 to 3 wks.

The LFD-400 is a minifloppy-disc memory system for the SS-50 bus developed by Southwest Technical Products for their 6800 based microcomputer. A one-drive system includes a controller PC board, PROM disc operating system, disc drive and drive power supply, two minidiskettes, and an enclosure to house the drive and power supply. The controller board, which is installed in an SS-50 bus slot of the host computer, includes low-voltage regulators, a “bit-shifting” compensation circuit and provision for 3 kbytes of PROM.

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For catalogs and information on how to get lamp samples, call your local GE Miniature Lamp Products Representative or write: General Electric, Miniature Lamp Products Department #3382, Nela Park, Cleveland, Ohio 44112.

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MICRO/MINI COMPUTING

Self-contained micro is RFI protected

Labtest Equipment, 11828 La Grange Ave., Los Angeles, CA 90025. Wesley Kemp (213) 478-2518. $6500; 2 wks.

The Model 300 microcomputer uses no front-panel switches, is completely self-contained in a metal RFI enclosure and includes extensive RFI and noise filtering. The micro is based on the Intel 8080 microprocessor. Included are a 10-k memory, one I/O board, power panel, cables and connectors, two fans, power supply, I/O ports, and a 22-slot motherboard. The system operates with TTY or CRT and keyboard options.

CIRCLE NO. 316

Desktop computer is in 25-lb package


A 25-lb compact desktop computer, called the Attache, is built around the Intel 8080 CPU. The computer has a full ASCII keyboard and it uses the S-100 bus configuration with a 10-slot board capability. Standard features include LED indicators for on/off and system status; a reset switch that returns to PROM monitor; a monitor PROM that controls operation of the computer from the keyboard; and a video output jack. The video output provides full upper and lower case character generation with 16 lines of 64 characters. A 1-k RAM with extra sockets added for PROMs on the turnkey board is provided. Options include an audio cassette recorder board, 110 to 9600-baud RS-232 port, floppy disc systems and software.

CIRCLE NO. 317
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MICRO/MINI COMPUTING

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ELECTRONIC DESIGN 4, February 15, 1978

CIRCLE NUMBER 63

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CIRCLE NO. 319

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PerSci, 12210 Nebraska Ave., West Los Angeles, CA 90025. (213) 820-3764. $1595.

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127
I/O board contains most parts for micro

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CIRCLE NO. 323

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CIRCLE NUMBER 68
Intersil, 1967-

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William Shakespeare, 1564-1616

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CIRCLE NUMBER 69
**Micro/Mini Computing**

**Mini CPU is on one chip**

Fairchild Semiconductor Products, 464 Ellis St., Mountain View, CA 94042. Bill Callahan (415) 962-3816. $750.

The 9440 Microflame is a complete minicomputer CPU on one chip. Packaged in a 40-pin DIP it executes the NOVA 2200 instruction set. Data and instructions are stored in external memory and a 16-bit-wide, three-state information bus carries both data and addresses between the CPU and other computer circuits. The 9440 can address 32,768 16-bit words, and its I/O ports can serve up to 63 peripheral devices using programmed I/O, interrupt-driven I/O or direct memory access.

**CIRCLE NO. 324**

**I/O board uses 8251 USART**


The Bit Streamer I/O board combines two parallel input and output ports and a serial I/O port using an 8251 programmable universal synchronous/asynchronous receiver-transmitter (USART). Communications with board circuitry is accomplished by the CPU. One parallel port also can be used as a keyboard input port. The board interfaces to an S-100 bus and can be configured for a wide variety of communication formats.

**CIRCLE NO. 325**

**Self-contained computer develops software**

Noval, 8401 Aero Dr., San Diego, CA 92123. Jerry Hansen (714) 277-8700. $3385; 4 to 6 wks.

The Model 760 computer system is a self-contained unit for software development. Interaction between the editor and assembler allows the user to edit, assemble and debug applications programs without the need to externally save or reload source or object code. The system has a Z-80 microprocessor, 32-kbyte RAM plus an additional 1-k scratchpad and 1-k video refresh memory. Also included are a programmable character generator, 3-k system-utility routines on PROM, 12-in. TV monitor, digital cassette recorder, 32-column matrix printer and a full keyboard. There are three 8-bit parallel I/O ports for general-purpose use and a programmable audio-tone generator and speaker within the enclosure.

**CIRCLE NO. 326**

**Static RAM runs with any S-100 system**

Digital Micro Systems, Box 1212, Orem, UT 84057. (801) 224-2102. $595.

A 16-k static RAM for the S-100 bus uses the 2114 memory chip. The board runs with any S-100 system including Z-80 systems at the full 4-MHz clock rate. The memory has individually addressable 4-k blocks, software write protection in 4-k blocks and a paging or block-select feature that allows memory expansion beyond 64 k.

**CIRCLE NO. 327**

**µC uses Z-80 and AMD9511 on one board**


The PCS 1880 is a microcomputer module that uses both the 4-MHz Z-80 µP and the 4-MHz AMD9511 math unit on one board. The basic system includes 1 k of RAM, sockets for 3 or 6 kbytes of EPROM, optically isolated three-function serial port, switch-selectable data rates from 50 to 9600 baud, five internal vectored priority interrupts and switch-selectable real-time clock providing time bases from 1 ms to 1 h. In the hardware math version, the AMD9511 provides a math package that includes add, subtract, multiply, divide, floating point, square root and trig functions.

**CIRCLE NO. 328**

**Disc system upgrades Heathkit H8 to Z-80**

Info 2000, 20630 S. Leapwood Ave., Carson, CA 90746. (213) 532-1702. $2750; 4 wks.

Heathkit users may add the Info 2000 disc system and upgrade their 8080 computer to a Z-80 system by replacing the Heathkit 8080 CPU board with the Z-80/disc adapter board. The complete system includes PerSci dual-diskette drives, power supply, case, intelligent controller, adapter, cables and disc monitor in EPROM. The adapter board contains the Z-80 microprocessor and all support chips, 7-k EPROM, 1-k scratchpad RAM for the disc monitor and all necessary logic for interfacing the disc system to the H8. With these modifications, the H8 can operate in either of two switch-selectable modes. One mode enables continued use of the H8 EPROM monitor with the existing Heathkit software. No modification is required and the H8 performs at Z-80 speed.

**CIRCLE NO. 329**
Opening new frontiers with electro optics

Just what the doctors ordered: RCA-developed PMTs that allow whole-body CT scanning in only 2 seconds.

Computerized tomographic (CT) X-ray scanners are creating a lot of excitement in medical circles. Unlike conventional X-rays, where a dense object can block out something important such as a tumor, a CT scan from hundreds of directions produces a highly revealing, complete cross-sectional view of the patient.

Vital links in this process are the hundreds of photomultiplier tubes which measure light scintillations caused by X-ray beams passing through the body and striking individual crystal detectors. RCA, of course, has a long background in the design and manufacture of PMTs. So we've been able to provide extremely reliable tubes with the performance required for critical measurements at ever-faster scanning speeds—users report as fast as 2 seconds.

These PMTs feature a wide dynamic operating range due to a highly conductive cathode surface and low anode dark current characteristics. Cathode currents of several nano-amperes and anode dark current in the picoampere range are possible when using the PMTs at operating voltages around 600 volts, characteristic of most CT scanning systems.

Two sizes of RCA 10-stage head-on tubes are being used in scanners. The 4886 has a 3/4" diameter and the S83001E a 1/2" diameter bialkali photocathode.

They represent a clear case where RCA saw a need and applied years of PMT experience to meeting it. Now, what can we do for you?

For spectroscopists: PMT with improved responsivity out to 850 nanometers.

The popular RCA 4840 1-1/8" dia., 9-stage PMT has been improved again. Its high responsivity now extends over a broader spectral range—to 850 nm typical. And there are some other benefits from buying this RCA tube. The assurance that comes from domestic manufacture. Prompt delivery. Price—about $55. And in-depth application support from people who really know how to help you get the most from a PMT.

So if you're involved in broadband spectroscopic analysis or low-level light detection systems—analyze the extra benefits you get from buying your PMTs from RCA.

If electro optics can solve your problem, remember: EO and RCA are practically synonymous. No one offers a broader product spectrum. Or more success in meeting special needs.

Call us for design help or product information. RCA Electro Optics, Lancaster, PA 17604. Phone 717-397-7661. Sunbury-on-Thames, Middlesex TW16 7HW, England; Ste.-Anne-de-Bellevue, Quebec, Canada; Sao Paulo, Brazil; Hong Kong.
MICRO/MINI COMPUTING

Disc drive packs 10 to 80 Mbytes

R2E of America, 3406 University Ave. S.E., Minneapolis, MN 55414. Ron Larsen (212) 562-9908.

A 10 to 80-Mbyte disc system with removable media plugs into the Micral C microcomputer system. The drive packs 10 Mbytes onto a removable or fixed-platter, 10.5 in. diameter disc. The removable disc cartridge is 11 in. square × 1 in. high. The system can use up to four drives, each with a 10-Mbyte fixed and/or 10-Mbyte removable disc. The data transfer is 920 kbytes/s. A Micral C System with 10 Mbyte disc costs $15,950 and delivery is 90 days.

CIRCLE NO. 330

I/O cards are compatible with LSI-11/2 µC

ADAC, 15 Cummings Park, Woburn, MA 01801. (617) 935-6668. $595; 4 to 6 wks.

A family of analog and digital I/O cards is compatible with the DEC LSI-11/2 microcomputer, the ADAC1000 System and the DEC LSI-11 and PDP-11/03 microcomputers. The cards include an analog-to-digital input card, analog multiplexer expansion card, direct-memory-access card and digital optically isolated input and output card. The cards are half-quad in size. One of these cards, the analog to digital Model 1012, is jumper selectable to accommodate either 16 single-ended, 16 pseudo differential or 8 fully differential analog inputs. Four input ranges of ±10, ±5, 0 to 5 and 0 to 10-V FS are also jumper selectable. Resolution is 12 bits with a throughput rate of 35 kHz.

CIRCLE NO. 331

Single-chip µC has twice memory of MCS-48s


The 8049/8039 single-chip microcomputers contain all elements including memory on a single chip. The chips have 128 bytes of read/write memory, twice that of other MCS-48 devices. In addition, the 8049 contains 2048 bytes of program memory. Both chips contain an 8-bit general-purpose central processor, three programmable 8-bit I/O ports and eight other control and timing lines, programmable interval timer/event counter, priority interrupt controls, system clock generator and a full set of system controls and utilities. The 8039 is the equivalent of the 8049 without program memory.

CIRCLE NO. 332

Computer offers choice of central processors

Digital Equipment, Maynard, MA 01754. John Bond (617) 493-3300. $19,200 to $24,500. 8 wks.

A series of PDP-8 computers, PDP-8T, has standard peripheral and cabinet configurations and a choice of PDP-8A central processors. The PDP-8/T3 has a processor with 16 kwords of core memory and a four-slot Omnibus expansion capability. The PDP-8/T5 processor has 32 kwords of core and an 11-slot expansion capability. The PDP-8/T7 has 64 kwords of MOS memory and an 11-slot expansion capability. All three computers include a 1.6 × 12-bit-word removable-disc cartridge drive and a 3.2 × 12-bit-word nonremovable disc drive; a DECwriter II terminal printer, a VT52 CRT with the RTS/8 real-time operating system and the OS/8 operating system as standard software.

CIRCLE NO. 333
Data-acquisition system slips inside its PDP-11 host computer

Datel Systems, 1020 Turnpike St., Canton, MA 02021. L. Copeland (617) 828-8000. See text; 4 to 8 wks.

With Datel's new single-board data-acquisition system, you'll have an easy time connecting your minicomputer to analog voltages that represent physical variables like temperature and pressure. You just slide the ST-PDP 1X1C5 right inside the PDP-11 minicomputer from Digital Equipment Corp.

The price is quite attractive. A basic 64-a/d-channel system in Datel's Sine Trac PDP series of data-acquisition cards starts at $1235 in single quantities.

Operating on 5 V dc from the computer backplane, the ST-PDP 1X1C5 even generates its own ±15-V analog power. So all you have to do is wire the analog inputs to the basic system, and you'll have 64 single-ended or 32 differential-input channels that are selectively digitized into 12-bit words.

These data are placed on the PDP-11's Unibus via an assembly-language program on the diagnostic tape that comes with the card. The ST-PDP 1X1C5 then operates either under program control or in an interrupt addressing mode.

Minutes after the system is installed and the paper tape is loaded, the diagnostic program can put the system “on the air,” printing out on your teletypewriter. A complete printout of the diagnostic program is contained in a comprehensive systems manual, which comes with the system.

Options bring a system all the way up to 256 a/d or d/a channels, which can be located either locally (at the computer) or remotely. With an option for direct memory access (DMA), the 20-µs conversion speed of the system’s a/d’s can put through 45,000 samples per second.

If your PDP-11 is so loaded that there's no spare card slot for the data-acquisition system, Datel can supply the BB-11 connector blocks required by the computer. These connectors come completely wired, with leads for the inputs—but they cost $540. And cost might well be the deciding factor when you choose an analog interface for your PDP-11. ADAC (Woburn, MA) sells a basic 64-channel PDP-11 system, the 635-11, for $1595. ADAC’s slide-in zips data along at 35,000 samples per second without DMA.

Digital Equipment Corp. (Maynard, MA) offers, for $3750, a two-card system consisting of the AM 11K multiplexer and the AD 11K a/d converter. Though this system gives you just 60 channels, you do get automatic zeroing.

Datel CIRCLE NO. 307
ADAC CIRCLE NO. 308
DEC CIRCLE NO. 309
CIRCLE NUMBER 73
MODULES & SUBASSEMBLIES

Crystal oscillators cover 5 to 500 MHz range

Vectron Lab, 166 Glover Ave., Norwalk, CT 06850. (203) 853-4433. $140 up; 4 to 10 wks.

The CO-233FW crystal oscillator provides a stable output at any specified frequency in the 5 to 500-MHz range. Stability is ±0.0025% from 0 to 70°C and output level is 0.5 V rms into 50Ω (+7 dBm), with +13 dBm optional. While the oscillator is factory set to within ±0.001% of the specified frequency, an adjustment for setting to ±0.0001% is optionally available. The package size is 2 x 2 x 3/4 in.

CIRCLE NO. 334

Light pen has 2-way activator

Information Control, 9610 Bellanca Ave., Los Angeles, CA 90045. (213) 641-8520. $195 (100 qty).

The LP-212 Light Pen has both “Push Tip” (the operator presses the pen against the CRT) and “Touch Sense” (the operator touches the tip of the pen with the index finger for each desired hit) activation. On tight targets, luminous sensitivity is 2 ft-lamberts. Response time is 300 ns. Spectral response is from 4200 to 1000 A. Minimum vector speed is 20 cm/ms at a minimum input separation of 20 µs.

CIRCLE NO. 335

Tape transport handles 3M cartridge

Qantex, 200 Terminal Dr., Plainview, NY 11803. Leon Malmed (516) 681-8350. $920; stock to 4 wks.

The Model 650 tape transport uses the 3M DC300A data cartridge. The transport makes an OEM memory module capable of storing up to 23 Mbits of unformatted digital data on the four tracks of a DC300A cartridge’s 300 ft of 1/4-in. tape. The transport provides precise 30 in./s tape speed that yields a 48-kbits/in. data transfer rate. This data throughput fills up a typical CRT terminal in about 1/2 s. The transport drives the tape at 90 in./s for rewind or fast search.

CIRCLE NO. 336
Handle most PC needs with rugged, compact CTS rotary switches.

Specify CTS and satisfy nearly every printed circuit switch mounting requirement. Cut production time, too. And at lower cost than with conventional wiring.

Choose from thousands of variations of shorting, nonshorting or mixed circuitry; plus a wide selection of index assemblies and wafer constructions for either perpendicular or parallel PCB mounting. Available in combination with AC power switches and variable resistors. You get a one-source supply for the complete switch package.

Two popular choices include the new CTS Series 223 parallel mount style (view A) measuring only 1 1/2" wide by 1 3/8" above the PC board permitting 12 PC terminals on .100" centers; up to 1-pole, 11-position circuitry. An optional 13th PC or solder lug terminal gives a full 12-position switching capacity. Shown at (B) above is the CTS Series 227 rotary selector switch, which provides years of virtual problem-free performance in all kinds of applications. Parallel mount...single or multiple wafer constructions...compact 1 3/4" wide by 1 5/8" above PC board. One and two wafer designs are also available with shaft axis perpendicular to board. Ask about our NEW 14-terminal 1-pole, 12-position or 2-pole, 6-position PC switches, too.

Made to your exact specifications CTS switches assure proven reliability, design flexibility and MIL quality. Call your CTS representative or write CTS Corporation, 905 N. West Blvd., Elkhart, IN 46514. Phone: (219) 293-7511.
DATA PRECISION GIVES YOU PORTABILITY AND MUCH MORE.
Data Precision’s leadership in digital instrumentation is based on high-value product planning, years-ahead engineering, and painstaking quality control. As a result, nearly 100,000 Data Precision instruments are now providing dependable service, at sustained accuracy, all over the world. Each model is designed to respond to different user price/performance needs, and has been optimized for a particular class of applications. The instrument for your specific requirements will deliver the accuracy and reliability your work demands.

In addition to the following portables, Data Precision manufactures a complete line of bench and system multimeters and additional counters.

**Model 175 Portable 3½-Digit DMM — $189**

Our Model 175 gives you 32 ranges of measurement capability, six functions, 0.1% DCV accuracy, and 100 microvolts resolution. You can measure DCV from ±100 microvolts to ±1000V, ACV from 100 microvolts to 500V with a frequency response of 30Hz to 50kHz, DC Current from ±100 nanoAmps to ±2A, AC Current from 100 nanoAmps to 2A with a frequency response of 30Hz to 50kHz. Resistance from 100 milliohms to 20 Megohms in two excitation voltages.

The 175 also features auto-polarity, automatic zero, 100% overrange, and a big, bright 0.43" LED display.

**Model 245 Portable 4½-Digit DMM — $295**

The more than 50,000 units currently in use in the field attest to the outstanding performance and wide acceptance of this portable multimeter. The Model 245 is a lab-quality, 5-function instrument with a basic DC accuracy of ±0.05% of input ±1 l.s.d., 0.005% resolution and 100% overranging.

It will measure ACV (100µV to 500V RMS), DCV ±100µV to 1000V, Resistance 100 milliohms to 20 Megohms, AC and DC Current 1 microamp to 2 Amps, AC voltage/current response 30Hz to 50kHz. And it has a large, easy-to-read display.

**Model 248 Portable 4½-Digit DMM, 10µV resolution, True RMS — $345**

This high-resolution instrument measures Resistance 100mΩ to 20MΩ, DC Volts ±10µV to ±1V, True RMS AC Volts 10µV to 500V, both DC Current and True RMS AC Current 10 nanoAmps to 2A. The Model 248 features sensitivity of 10µV. Basic DC accuracy is ±0.05% of input ±1 l.s.d., guaranteed for a full year, 100% overrange, overload protection, and large LED display.

**Complete Package**

Data Precision Portable DMM’s include rechargeable NiCd battery module, a pair of test leads, line cord with charger, carrying case, instruction manual, and individual test documentation – a complete report on your instrument, including temperature test results.

**Optional Accessories**

You can make your DMM even more versatile with optional accessories, including a 40KV high voltage probe, AC clamp-on current probes (150A or 1000A), RF probe bench stand, rack mount, mini-to-standard banana adaptors, deluxe leather case, and high impact fiberglass carrying case.

**Model 585 Portable, 250 MHz, 8-Digit Frequency Counter — $345**

This 8-digit counter will measure frequency from 10Hz to 250MHz—always reading directly in MHz, with correctly positioned decimal point. Resolution is 0.1Hz.

This counter has excellent sensitivity —10mV RMS to 50MHz, 50mV RMS to 250MHz—as well as dual Input Impedance (50Ω/1MΩ); a wide-range 3 position attenuator; 3 gate times (10 sec., 1 sec., 0.1 sec.); resolution: 0.1Hz, 1Hz, 10Hz and a bright 0.3” LED display.

Model 585 comes complete with rechargeable NiCd batteries, line cord charger, (operates on line and battery) carrying case, full instruction manual, Certificate of Conformance, and final test data.

**For complete information or a demonstration, call your local Data Precision representative or Data Precision Corporation, Audubon Road, Wakefield, MA 01880, U.S.A., (617) 246-1600. TELEX (0650) 949341.**
Pulse Engineering again broadens its Delay Line Series

Digital Delay Modules

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Dynamic RAM Timing Modules

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For complete information write for data sheets 772-3, 772-4, 772-7

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INPUT TEST CONDITIONS

VCC +4.5 to 5.5V DC
Logic 1 input current 50µA MAX.
Logic 0 input current -2MA MAX.
Pulse voltage 3.2V
Risetime 3 nsec
Input Current 60 MA TYP
Pulse width Min. 40% of total delay

Drive Capabilities

Logic 0 output
10 TTL loads tap max.
20 TTL loads unit max.
Logic 1 output
20 TTL loads unit max.

Output
Logic 1
Vout = 2.4V Min.
Logic 0
Vout = 0.4V Max.
Measured with no load on taps
*20% for Dynamic RAM Timing Modules


The DigiTec Models 6310, 6320 and 6330 printers have internal µPs that simplify interfacing and reduce the component count as much as 10 to 1. The printers use the electro sensitive-printing technique, giving quiet operation and a high-contrast printout. The µP delivers double-font printing and variable data formats. A crystal-controlled 24-h clock plus a day and month calendar operate even with the printer turned off. The 6310 takes data rates from 110 to 600 baud. The 6320 operates up to 1200 baud and the 6330 with an 8-bit parallel-bus input accepts data rates up to 1000 char/s.

CIRCLE NO. 337

Build data-acquisition system with DIPs

Micro Networks, 324 Clark St., Worcester, MA 01606. (617) 852-5400.
See text.

You can build a data-acquisition system on the same board as your microprocessor using standard DIP components. The MN7130 multiplexed sample/hold amplifier contains all the system's front-end components including two 8-channel analog multiplexers, instrumentation amplifier and sample/hold amplifier. The multiplexers are digitally addressable and can be connected for either 16 single-ended or eight differential-input channels. The second package in the system is the ADC 80 12-bit a/d converter. The only other components required are two 10-turn trimpots, power supply bypass capacitors and a few logic packages to interface the microprocessor. The MN7130 price is $80 and the ADC 80 is $47.50 in 100 quantity.

CIRCLE NO. 338
Finally, there’s a realistic solution to your need for fast, fast delivery on 2316E 16K ROM orders. It’s Intel’s “OTP Program.” And it puts parts in your hands just two weeks after you place your order with Intel. Not six weeks. Not four. Two weeks.

OTP is the key. It means One Time Programmable. When you place an order for a thousand or more 2316E ROMs, you’ll get 25 programmed OTP 2616 EPROMs just 14 days later. That’s the kind of offer only Intel can make. Then we’ll ship 100 masked ROMs just six weeks A.R.O., up to 1000 more two weeks after that. No extra charge for the special service. No rushing the ROM process. And no production delays for you. The same goes for 2308 8K ROMs, using OTP 2608 EPROMs.

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Telephone encoder yields all 16 tone pairs

Data Signal, 40-44 Hunt St., Watertown, MA 02172. Clarence Walker (617) 926-5080. $24.95; stock to 3 wks.

The Model DTE-100 dual tone encoder is an epoxy-encapsulated dual-frequency signal-generator module that provides all of the 16 tone pairs required for multifrequency telephone-tone dialing. The unit has an internal voltage regulator and ceramic oscillator and requires no external components to operate. Specs include 900-mV rms adjustable composite output into 600Ω, ±1.7-dB high-frequency pre-emphasis, ±0.3% output stability versus line voltage, 5% total harmonic distortion, ±0.25% frequency stability from -55 to +80°C, 100-mW power drain and dimensions of $2 \times 2 \times 0.5$ in.

CIRCLE NO. 339

Switch-select 10 cut-offs in active LP filter

Linear Networks, P.O. Box 775, Westminster, CA 92883. Jim Hogen (213) 430-9342. 8128; stock to 3 wks.

The Model L1402V active low-pass filter networks with four-pole Butterworth or Bessel frequency responses have mechanically switched cut-off frequencies that can be selected by setting four 10-position switches. Standard nominal cut-off frequencies are 10, 100 and 1000 Hz. For each nominal frequency, the cut-off can be switched from 40 to 300% in 10 steps. Other cut-off frequencies and switching schedules are available on special order.

CIRCLE NO. 340

Dual drivers each sink heavy loads

Fairchild Camera & Instrument, 464 Ellis St., Mountain View, CA 94042. Bill Callahan (415) 962-3816. $5.00 (100 qty); stock.

The SH3011 has two independent drivers, each capable of sinking 5 A. The hybrid device can withstand 80 V between collector and emitter of the output transistors, which makes it suited for use in high-voltage impact printers, stepper-motor controls, solenoid drivers and large printers. Inputs are fully TTL compatible and the device is packaged in a TO-3 metal can.

CIRCLE NO. 341

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CIRCLE NUMBER 80

CIRCLE NUMBER 81

ELECTRONIC DESIGN 4, February 15, 1978
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Hybrid Systems, Crosby Dr., Bedford, MA 01730. Larry Lauanger (617) 275-1570. $194/$214; stock to 4 wks.

The ADC581 series of 12-bit hybrid a/d converters consumes 570 mW. Conversion time is 30 µs to a ±1/2 LSB of 12 bits. Each model can be short-cycled where less resolution is required. The unit features an internal clock-rate control and the option to use an external clock for synchronization. Low-gain tempco is ±15 ppm/°C max. Five input ranges can be selected and three output codes are available. Each model is packaged in a 32-pin hermetically sealed, dual-in-line metal case.

Sorbus, 150 Allendale Rd., King of Prussia, PA 19406. (215) 265-6700. $89; 4 to 8 wks.

A compact signal-display device, Traffic Light, placed in-line between data sets and data-communication terminals isolates failures. The unit monitors without disrupting communications. The pocket-size monitor uses LEDs to constantly display the status of seven key signals on the EIA RS-232 25-pin interface. The signals monitored are transmitted data, received data, request-to-send, clear-to-send, data-set ready, carrier detect and data-terminal ready. A spare LED circuit shows the status of any other signal. Test points are provided for scopes, meters and logic probes.


A series of solid-state synchro control-transformer modules, SCT 40, can directly replace conventional electromechanical control transformers and provide digital control to existing analog-servo systems. The modules have standard accuracies of ±6, ±15 or ±30 minutes of arc. They simultaneously accept one input from a synchro (or resolver) of 11.8 or 90 V at 400 Hz or 90 V at 60 Hz and another input coded in 14, 12 or 10-bit binary. The output is the sine of the difference between two input angles. Bidirectional input data are accepted and no adjustments are required.
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CIRCLE NUMBER 300

Burr-Brown, International Airport Industrial Park, Tucson, AZ 85734. Steve Howard (602) 294-1131. $47.00/$49.50 (100 qty).

The Models ACD80AGZ-12 and ADC80AGZ-10 a/d converters operate from ±11.4 to ±16-V-dc supplies making them compatible with ±12-V applications. The 32-pin ceramic package includes a logic supply of +4.75 to +16 V dc. Conversion speeds are 25 µs for 12-bit and 21 µs for 10-bit resolution. Linearity error is ±1/2 LSB max; gain drift is ±30 ppm/°C. Internal scaling resistors are provided for programmable selection of analog input ranges of ±2.5, ±5, ±10 and 0 to +10 V.

CIRCLE NO. 346

Amplifier isolates high input/output voltage

Intronics, 57 Chapel St., Newton, MA 02158. Dick Sakakeeny (617) 332-7350. $58; stock in 4 wks.

The Model IA286 isolation amplifier has gain adjustable from 1 to 100, isolated power (±10 V at 10 mA), and isolated output section for input common-mode voltages up to ±5-kV dc, 6.5-kV pk. The input circuit presents a differential impedance of 10^{12} Ω in parallel with 3 pF and a common-mode impedance of 10^9 Ω in parallel with 10 pF. Input noise is held to 8 µV pk-pk, measured in a band from 0.5 to 100 Hz, and 5 µV rms in a band from 10 Hz to 1 kHz. Noise current is 10 pA pk-pk, from 10 Hz to 1 kHz. The input section includes a ±10-V dc, 10-mA supply for powering an external transducer or circuit.

CIRCLE NO. 347
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CIRCLE NUMBER 86

Electronic Design 4, February 15, 1978
Bus analyzer ‘thinks big’ but goes for small price

E-H International, 515 11th St., Oakland, CA 94504. Jay Long (415) 833-9030. $1000 (basic), 30 days.

The MBA-1 is the first inexpensive ($1000) logic analyzer to offer a large memory. The unit can trap 128 32-bit words at clock rates up to 5 MHz. Made for real-time monitoring of µP-based systems, the MBA-1 Micro Bus Analyzer offers interchangeable probes for the 8080, the 6800, or the Z-80. You select the appropriate probe, plug one end into the MBA-1, and clip the other end onto the µP.

The MBA-1 captures a 128-word sequential block of data in memory. It traps the last 32 words before the selected trigger and the first 96 after. Of the 32 bits in each word stored, 28 come from the µP itself—16 address bits, 8 data bits, and 4 µP status bits. The others come from four external input lines brought to the MBA-1 front-panel jacks.

The ONE/ZERO values of all 32 bits are clocked into MBA-1 memory at the µP clock rate. Thus the trapped data will span a full 128 µP commands only if they are single-clock-cycle instructions. But ordinarily, two and three-clock-period commands are interspersed, so that the effective capacity of the “snapshot” is significantly less than 128 lines of machine-language code. (For more on this feature, see “Focus on Logic and µP Analyzers,” ED No. 3, Feb. 1, 1977, p. 40.)

Data are displayed on six hex LED digits for address and data and eight single LEDs. Together, these show one full 32-bit word at a time. Also, the display can be stepped back and forth through the memory.

Since no scope is required for this data-domain analyzer, no scope display signals are available from the digital memory. But the MBA-1 does output a scope trigger signal each time its trigger-conditions are met so waveform investigations are possible.

Trapping occurs when 36 specified bit conditions are met, as set by front-panel switches. Two hex thumbwheels, called pass-count switches, permit trapping to be delayed by up to 256 (FF) trigger events. You select the number by setting four hex thumbwheel switches for the 16 bits of address, two hex thumbwheels for the eight bits of data, and "don't-care" miniswitches for address and data and for the four bits of external input data.

Input-impedance of the E-H analyzer is 10 MΩ in parallel with 10 pF. Logic thresholds are 0.8 V max for a ZERO and 2.0 V min for a ONE. The probes are buffered.

Self-contained in an 18 × 13 × 4-in. attaché case, the 12-lb unit operates over 0 to 50 C.

The $1000 base price does not include the personality probes, which cost about $200 each.

CIRCLE NO. 304
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At Centralab we are concerned with the same things you are – performance, reliability, on-time delivery. But any ceramic disc capacitor supplier can say that. We go that critical step further; we give you a choice of leads to help you cut your manufacturing costs.

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7158 Merchant Ave., El Paso, TX 79915 (915) 779-3961
CIRCLE NUMBER 88
INSTRUMENTATION

Timer programs output cycles with 100-division IBM card


You can time sequences on parallel channels without the usual fuss using the Xanadu Controls universal programmable timer, the UP Timer. To set precise, repeatable sequences, all you do is mark the IBM card that comes with the timer, slide it into the timer's reader and hit a button. The instrument does the rest. It memorizes the on-off intervals marked on the card and opens and closes the output-reed or solid-state relays at the appropriate moments.

Up to 10 timing tracks are available, and cycle durations can be set on thumbwheel switches to last anywhere between 10 milliseconds and 100 hours. Each cycle is divided into 100 divisions. Those segments darkened on the IBM card represent the ON condition; those unmarked represent OFF.

To change a program, just slide in a different card, and off you go. You can store a bunch of cards for various applications, and change programs quickly when necessary.

The Xanadu instrument can repeat a given program, stop after a single cycle, or "pause"—that is, freeze in the state just before switching to the pause mode. A LED display shows the status of the various cycle and time relationships.

The UP Timer goes for $1066 to $1588, depending on the number of channels. Delivery is from stock.

CIRCLE NO. 305

Band-reject filters cover 225 to 400 MHz

K & L Microwave, 408 Coles Circle, Salisbury, MD 21801. Charles Schaub (301) 749-2424.

The TND tunable band-reject filter covers the frequency range of 225 to 400 MHz and has a direct readout with frequency resolution of 100 kHz throughout the range. The filter is in a one-piece aluminum housing with plated cavities to achieve a high "Q." A constant 0.05-dB Chebyshev-ripple response is maintained by varying the loaded capacitance of each section inversely as the frequency is increased. Resettablity of the direct readout, which is calibrated to the notch frequency, is within ±500 kHz.

CIRCLE NO. 350

S-D puts the squeeze on time code!

Systron-Donner's new compact-sized family of precision time code equipment offers you everything you could possibly need in generators, readers, and remote displays. All 8700 series models measure just 1¾" high x 9½" wide. These advanced instruments generate, read and display standard modulated serial time codes. Both the time code reader and generator may be powered by either 115 VAC or 12 VDC. For digital system operation, a parallel BCD output with computer read command is available as an option on the reader and generator.

For complete details on the very latest in time code technology, contact your local Scientific Devices office or Systron-Donner, Data Products Division, 935 Detroit Avenue, Concord, CA 94518. Phone (415) 798-9900.

Model 8720 Compact Time Code Generator  
Model 8730 Compact Time Code Reader  
Model 8781 Compact Remote Display

SYSTRON DONNER  
CIRCLE NUMBER 87
The Complete Solution to your F3870 and F8 Design-In Problems

The Formulator Development System

The Formulator family is designed to allow easy, efficient software development and real-time hardware simulation of F8 or F3870 based systems. It is supported by a complete line of functional modules including memory, I/O and simulation cards that plug directly into the Formulator cardframe.

The Formulator can, itself, be used as the system breadboard. It provides microprocessor hardware, plus card slots for breadboarding your system. Thus the entire system may reside within the Formulator or in a combination of external and internal configurations.

In-Circuit Emulation

To develop, test, and debug F8 and F3870 based products, Fairchild offers simulation options that extend the functional features of the microprocessor from the Formulator to the 40-pin socket on your breadboard. This allows complete ROM firmware development, real-time symbolic debugging of your breadboard and freezing of ROM codes during the breadboard stage.

PROM Prototypes

The 3870 Emulator is a PROM-based substitute for the F3870 microprocessor. The Emulator measures 5" x 7" and contains two 2708 or 2716 EROMs in place of the F3870 so ROM codes can be verified and easily changed if necessary.

The Formulator-Floppy Disk Marriage

An inexpensive plug-in module interfaces the Formulator with up to four plug-compatible ICOM Floppy Disc Drives, providing over one megabyte of storage. If you prefer other Floppies, an application note provides the information necessary to modify Drivers for your system.

And That Isn't All

There is a lot more to Fairchild's line of design aids: PCB modules, memory options, PROM programmer, application and peripheral options, design kits, one card microcomputers, software, user’s guides and training courses.

No one offers the extensive F8 and F3870 support that you can get from Fairchild. Just ask us about it.

Fairchild Instrumentation and Controls, a division of Fairchild Camera and Instrument Corp., 1725 Technology Drive, San Jose, California 95110 (408) 998-0123, Ext. 220.
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CHOMERICS

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Tel. (617) 935-4850 / TWX 710-393-0173

CIRCLE NUMBER 93

Darlingtons include commutating diodes

Kertron, 7516 Central Industrial Dr., Riviera Beach, FL 33404, (305) 838-9606. $4.50/$4.95 (100 qty); stock.

Hybrid Darlington devices (KDM922, 924, 902, 904) include emitter-base resistors and a high-speed commutating diode. Two transistors, KDM922 and 924, are available in the TO-66 package. The KDM922 has a BVCEO rating of 80 V. The KDM924 has a BVCEO rating of 120 V. The TO-5 packaged devices are the KDM902 and KDM904 with the KDM902 rated at 80 V and the KDM904 rated at 120 V. All the devices have a minimum current gain of 1000 at a collector current of 5 A and a collector voltage of 5 V.

CIRCLE NO. 352

Thrifty d/a converters are on monolithic chips

Micro Power Systems, 3100 Alfred St., Santa Clara, CA 95050. Tarlton Fleming (408) 247-5350. See text; stock.

Four d/a converters, MP7520G, 7520H, 7521G and 7521H, consist of a thin-film R-2R ladder plus a number of CMOS current switches on a monolithic chip. For most uses, these converters only require the addition of an output op amp and a voltage or current reference. The MP7520G and 7521G have 6-bit linearity, while the 7520H and 7521H are rated at 7 bits. The 7520 units have 10-bit resolution and are housed in a 16-pin DIP. The 7521s have 12-bit resolution and are housed in 18-pin DIPs. Prices in 1000 quantity are $2.60 for the MP7520G; $3.05 for the 7520H; $2.85 for the 7521G and $3.35 for the 7521H.

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ICs & SEMICONDUCTORS

EPROMs are organized as 2048 eight-bit words

Motorola, 3501 Ed Bluestein Blvd.,
Austin, TX 78721. (512) 928-2600.
$29.25 (100 qty); samples available.

Two EPROMs are organized as eight-bit words. The MCM2716L and MCM2717L are n-channel silicon-gate devices for operation with power supplies of +12, +5, and -5 V. Max access time and min cycle time is 450 ns. The MCM2716L is a pin-for-pin replacement for the TMS2716 and is pin-compatible to the MCM2708L, MCM2708P, MCM2708C and MCM68708L EPROMs. For mask-ROM compatibility, use the MCM2717L that is pin-compatible to the MCM68316E (2 k x 8 single 5-V supply, mask-programmable ROM). Both memories are in 24-pin ceramic “window” packages.

CIRCLE NO. 354

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Until now, the field has been pretty limited in low power, high reliability 400Hz to DC single phase power supplies. Tecnetics changed all that with the introduction of its new 400 Series. There are over 130 different power supplies in all: 3, 6, 10, 15 and 20 watt units with single, dual and triple outputs. Output range is from 5 to 28 volts. Military type components are standard and High-Rel components are available. All fully documented and ready to go.

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So, when you’re designing complex military hardware, airborne instrumentation, or anyplace you have 400Hz to DC power conversion requirements, look to Tecnetics wide selection of power supplies. Send for our catalog now.

400 Series Prices (1-9)

<table>
<thead>
<tr>
<th>Power (W)</th>
<th>Single</th>
<th>Dual</th>
<th>Triple</th>
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</thead>
<tbody>
<tr>
<td>3W</td>
<td>$500</td>
<td>$245</td>
<td>$295</td>
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<td>6W</td>
<td>210</td>
<td>255</td>
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<td>10W</td>
<td>230</td>
<td>275</td>
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<td>15W</td>
<td>240</td>
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<td>335</td>
</tr>
<tr>
<td>20W</td>
<td>250</td>
<td>295</td>
<td>395</td>
</tr>
</tbody>
</table>

Contact factory for prices on hi-rel units.

CIRCLE NO. 355

Medium power transistor has 10-dB gain at 2 GHz


The silicon bipolar microwave transistor, AT3850, typically produces 150 mW (at 1-dB gain compression) with 10 dB associated gain at 2 GHz. At 3 GHz, the output power is 100 mW with an associated gain of 8 dB. The transistor has platinum silicide contact structures to minimize contact resistance and a gold system that produces uniform conductors more than one micron thick. Package is a hermetic ceramic/metal 0.1-in. square stripline.

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- Control
- Mapping
- Printing & publishing
- Halftone
- Business graphics

Models:
- Commercial (3000 series)
- Militarized (7000 series)

BIG OUTPUT

<table>
<thead>
<tr>
<th>Models</th>
<th>printers, plotters and printer/plotters commercial and militarized</th>
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</thead>
<tbody>
<tr>
<td>Resolution</td>
<td>100 or 200 (dots-per-inch)</td>
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<tr>
<td>Print speed</td>
<td>500 or 1000 (132-column lines per minute)</td>
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<tr>
<td>Plot speed</td>
<td>1.0 or 2.0 (inches per second)</td>
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<tr>
<td>Interfaces</td>
<td>4.4 or 8.5 (square feet per minute)</td>
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<tr>
<td>Characters</td>
<td>all popular computers and CRTs</td>
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<td>96 ASCII (standard)</td>
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<td>Characters</td>
<td>124 scientific/engineering (optional)</td>
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<td>Characters</td>
<td>128 typesetting (optional)</td>
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SMALL PACKAGES

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<th>Width (inches)</th>
<th>Depth (inches)</th>
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<tbody>
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<td>100</td>
<td>17½</td>
<td>19</td>
<td>22½</td>
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<tr>
<td>Desk top with cabinet</td>
<td>160</td>
<td>46</td>
<td>19</td>
<td>22½</td>
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<tr>
<td>Rack-mount</td>
<td>120</td>
<td>21</td>
<td>19</td>
<td>22½</td>
</tr>
</tbody>
</table>
Ion-implanted diodes sub for Schottky types

Solid State Devices, 14830 Valley View Ave., La Mirada, CA 90638. Dee Peden (213) 921-9660. $0.50 (5000 qty); stock to 4 wks.

A line of ion-implanted diodes have high forward conductance, low-reverse leakage and nanosecond switching-time characteristics similar to germanium and Schottky diodes. The 1E.5 through 10E.5 series have peak reverse voltages of 10, 20, 30, 40, 50, 70 and 100 V at 0.5-A rectified current. Peak repetitive forward current is 5 A and peak surge current is 25 A. The max forward-voltage drop is 375 mV at 1 mA and 850 mV at 500 mA. Max reverse leakage is 10 µA, while recovery time is 9 ns max. The diodes are in molded plastic TO-92 cases.

CIRCLE NO. 360

Single chip provides all FM-i-f functions

RCA Solid State, Route 202, Somerville, NJ 08876. (201) 688-6423. $1.88 (100 qty); stock.

The CA3189E IC provides all the functions of a comprehensive FM-i-f system for use in high fidelity, automotive and communications receivers. The device contains externally programmable age threshold, audio output level, and meter drive voltage that is depressed at very low signal levels. The circuit also includes power-supply regulators that maintain nearly constant current drain over the supply range of +8.5 to +16 V. Other features include a three-stage limiting amplifier, doubly balanced quadrature FM detector, audio amplifier, afc drive circuit, zero-point tuning meter output, age circuit, and signal-to-noise mute drive voltage. The unit is housed in a 16-lead plastic DIP.

CIRCLE NO. 361

SCRs and diode bridges run 10 C cooler

Gentron, 6667 N. Sidney Pl., Milwaukee, WI 53209. Lance Kaufman (414) 351-1600. See text; stock.

In the B series of SCR and diode bridges, junction temperatures have been measured to be 10 C less than units of previous metal leak plate design. The number of thermal resistance paths has been reduced and the ceramic interfaces directly with the heat-sink mount surface. The devices are rated to 25 A. In addition to fast-acting SCRs and three diodes sells for $8 in 1000 quantity.

CIRCLE NO. 358

Static RAM stores 1024 × 4 bits

Motorola, 3501 Ed Bluestein Blvd., Austin, TX 78721. (512) 928-2600. $12.25 (100 qty); stock.

The MCM2114, a 1024 × 4-bit static RAM, requires no clocks, no timing strobes, nor refreshing because of fully static operation. Data-out and data-in are of the same polarity, and no address set-up time is required. Four speed ranges are available: 200, 250, 300 and 450 ns. Two power versions are the MCM2114 at 550 mW and the MCM21L14 at 385 mW (max), both using a single 5-V supply with ±10% tolerance. The memories are housed in plastic or ceramic 18-pin DIPs.

CIRCLE NO. 359
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Now take a look at the Intecolor 8051. Perfect if your needs call for a large-screen format. It comes with the same standard features as the 8031, but it has a big 19-inch diagonal screen and external mini disk drive.

We also have a variety of options available for both units, including a convenient bi-directional desk top printer and a new 2708/2716 PROM programmer.

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**ICs & SEMICONDUCTORS**

**Dynamic RAM has 4 k × 1-bit storage**

Motorola, 3501 Ed Bluestein Blvd., Austin, TX 78721. (512) 928-2600. §5.75 (100 qty); stock.

The MCM4096 is a 4 k × 1-bit dynamic RAM using n-channel silicon gate technology. All inputs are TTL compatible and the output is three-state TTL compatible. Each of the 64-row addresses requires a memory cycle every 2 ms to refresh the contents of the RAM. Max power dissipation is 445 mW in the active mode and 19 mW for standby. Three speeds are available: 250, 300 and 350 ns (max access time). Package types are 16-pin ceramic or frit-seal ceramic.

**Power Darlingtons are qualified to MIL spec**

Silicon Transistor, Katrina Rd., Chelmsford, MA 01824. Bill Schronum (617) 256-3221. §6.45 to $10.80 (100 qty); bonded stock.

The JAN/JTX 2N6283 and JAN/JTX 2N6284 silicon power Darlington transistors have been qualified to MIL-S-19500/504 (USAF). The transistors are hermetically packaged in TO-3, metal cases. VCEO is 80 and 100 V, respectively, and continuous collector current is 20 A. The dc gain is specified at 750 to 18,000 at an IC of 10 A. The collector-emitter saturation voltage is 2 V at 10 A.

**Prescaler requires only a single 5-V supply**

Plessey Semiconductors, 1641 Kaiser Ave., Irvine, CA 92714. Bob Huish (714) 540-9979. §89 (100 qty); stock.

The SP8610 is a +4 prescaler that requires only a single 5-V-dc supply. The device has a maximum operating frequency of 1 GHz from 0 to 70 C. Capacitive or dc input coupling may be used and ECL-compatible complementary emitter-follower outputs are provided. The unit has a 100-Ω line drive capability. Power dissipation is 350 mW. Output swing is 600 mV.
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2-mA reference diodes have high stability

Codi, Pollitt Dr. S., Fair Lawn, NJ 07410; John Holgren (201) 797-3900; $33 (100 qty); stock to 6 wks.

The PRD2005 voltage reference diodes have a time stability of 5 ppm per year, 2 ppm per 1000 h of operation. Nominal operating current is 2 mA at 6.4 V. The PRD2030 provides stability of 30 ppm per year and 5 ppm per 1000 h. Other specs include a 1 ppm output noise figure and a 1 ppm tempco at zero tempco current from 25 to 45 C.

Monolithic 12-bit d/a has high speed

Harris Semiconductor, P.O. Box 883, Melbourne, FL 32901. (305) 724-7430. $29 (100 qty); stock.

The HI-562 is a 12-bit monolithic d/a converter that has fast current-output settling to ±1/2 LSB in 200 ns (typical); 400 ns (max). The output-current capability is 5 mA. In addition to an external reference, the device requires a +4.75 to +12-V logic supply and a −15-V supply for operation. Digital inputs are TTL/DTL/CMOS compatible. Packaging is a hermetic 24-pin DIP and the operating temperature range is 0 to 75 C.

Low-drop HV rectifiers give 99.8% efficiency

Solid State Devices, 14830 Valley View Ave., La Mirada, CA 90638. (213) 921-9660. $0.90 to $2.00; stock to 4 wks.

A line of 2 to 6-kV rectifiers, HVM, rated at 250 mA, has a reverse current of 100 µA at 125 C and 8-V max forward-voltage drop at rated current. The devices dissipate a maximum of 2.8 W, producing a rectification efficiency of more than 99.8%. The rectifiers withstand peak recurrent transient voltages 1.2 times rated blocking voltage. They also take 30-A current surges for up to 8.3 ms. Cases are 0.63 X 0.43 X 0.22 in.
CMOS device is a 8-bit multiplier

A multiplying CMOS d/a converter, AD7523, provides 8-bit resolution and 10-bit accuracy. The device does four-quadrant multiplying and has a settling time under 100 ns. Three versions are the AD7523JN that provides linearity of ±1/2 LSB, the AD7523KN providing ±1 LSB and the AD7523LN providing ±1/2 LSB. All have a feedthrough of ±1/2 LSB at 200 kHz. The units are in 16-pin plastic DIPs.

Fast-switching SCR is for high-power switching

The fast-switching SCR, T72H, allows the design of high-power frequency inverters with low duty cycle operation and reduced snubber circuitry for greater efficiency. The SCR is rated at 100 to 1200 V. Turn-off time is 25 to 50 µs for the 250 or 350-A devices and 30 to 50 µs for the 450-A device. The devices are available in air and water-cooled assemblies. Typical price is $352.50 for the 350-A, 1200-V, 25 µs unit.

Ultra-linear transistor has 1.6-dB NF at 500 MHz

The TP491 ultra-linear transistor has a noise figure of 1.6 dB at 500 MHz and is suitable for CATV, MATV and other uhf and vhf use. Maximum collector-emitter breakdown voltage is 14 V and the collector current capability is 50 mA. At 500 MHz, the cut-off frequency is 3.2 GHz and power gain is 14.8 dB.
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CIRCLE NUMBER 113

Electronic Design 4, February 15, 1978
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CIRCLE 114 FOR INFORMATION ONLY
CIRCLE 115 FOR DEMONSTRATION ONLY
Fiber-optic system aims at digital data links

RCA Electro Optics and Devices, Lancaster, PA 17604, Ted Grabowski (717) 397-7661. P&A: See text.

A fiber-optic digital data communications link, the C86003E from RCA Electro Optics, consists of a transmitter and receiver, each in a rectangular box measuring less than two inches on a side and an inch deep.

The transmitter contains a GaAlAs (gallium aluminum arsenide) light-emitting diode and drive circuitry. A fiber-optic cable is internally coupled from the emitting region of the LED chip to an optical bulkhead connector, which interfaces with a DuPont PFXS120 single silica optical fiber. Other fibers can be accommodated to meet customer needs.

The transmitter provides a peak optical power of at least 100 µW. Input is TTL-compatible with a fan-in of three loads, and can accept signals up to 20 Mbits/s for NRZ (nonreturn-to-zero) coding or 10 Mbits/s for RZ coding. The transmitter requires 5 V ±5% at 250 mA.

The receiver uses a silicon p-n photodiode with amplifier and threshold drive circuits to convert input light pulses to TTL output signals.

The receiver's output can drive four TTL loads over the same frequency range as the transmitter. Optical sensitivity is 2 µW, with a calculated bit error rate of 10^-9 for this minimum optical signal.

The receiver requires three power supplies: 6 to 8 V at 30 mA, -6 to -8 V at 20 mA, and a diode bias of 6 to 75 V.

In small quantities, the initial price is between $700 and $1000 per system. Delivery takes 60 days.

Character buffer is pollable

The model 721Z buffers data or messages, consisting of ASCII characters, in centralized polling applications over switched telephone networks. The unit has a microprocessor-based control section and up to 16 memory modules in a small card file. An input port connects the data/message source. An output port connects to a telephone line for polling by a distant location via dial-up connection over the public switched network.

Multiplexer yields one data stream from two

The 350 MIC asynchronous bipolar multiplexer relieves overcrowded cable facilities by combining two T1 (1.544 Mbita/s) input streams into a single T1C (3.152 Mbit/s) output stream. The device is compatible with Western Electric's M1C multiplexer, and with D3, D2 or D1D encoding formats. The 350 M1C consists of eight modules with two multiplexers per shelf. The 12-in. deep shelves mount in a 19-in. rack. Power is supplied from a ~48-V office battery.

Printers operate at 160 char/s

Datapoint, 9725 Datapoint Dr., San Antonio, TX 78284, Hal Morrow (512) 689-7029. $8550/65450.

Two models of the Freedom Printer operate at 160 char/s. The 9236 parallel-data printer is a system printer for business DP systems with parallel data output. Its address and control code sequences are identical to those of the 80 char/s Model 9232. Another model, the 9235, accepts serial data and is a receive-only terminal printer for business time-sharing systems. It may also be connected to video displays for hard-copy output.

Video adapter prints out displays

Honeywell Test Instruments, P.O. Box 5227, Denver, CO 80217. R.T. Michel (303) 771-4700.

A video adapter for the Honeywell 1856A and LS-6A line-scan recorders enables the recorders to print out framed images from composite video signals. The adapter produces a 16 × 12-cm printout of a display, gray scale, characters or graphics within seconds. The recorder can be switched from normal line scan to video frame operation. The adapter can provide printouts from scan converters, computer terminals and video tape or disc recorders. It operates with either 525-line, 60-Hz, or 625-line, 50-Hz, field rates.
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Contact TRW/IRC Resistors, 4222 South Staples, Corpus Christi, Texas 78411. (512) 854-4872, Dept. M. For standards in all types of resistors, call your local TRW distributor.
DATA PROCESSING

Mini diskette system stores and edits

Western Telematic, 2435 S. Anne St., Santa Ana, CA 92704. (714) 979-0336. $1750.

The DataMate minifloppy disc system is a data storage and editing unit that connects between any RS-232 asynchronous ASCII-coded display terminal and its modem. The system stores 560 addressable records of up to 128 characters. Selective data rates go to 9600 baud. Editing features include backspace erase, insert, delete, skip, go-to link, printable line ID, and auto line feed. Search modes are: Mode 1, find variable and read to stop code; Mode 2, find each occurrence of variable.

Mobile unit stores and transmits data


Cassettterm II is a mobile storage and telecommunications device that transmits data at 110 or 300 baud. The device includes a minicassette memory system for serial storage of up to 40,000 alphanumeric characters per cassette, a universal acoustic coupler, a 32-character display panel and full ASCII compatibility. The unit is powered by internal rechargeable batteries, and measures 3 x 8 x 10 in.

Kit converts Selectric into hard-copy terminal

ESCON, 171 Mayhew Way, Pleasant Hill, CA 94523. (415) 935-4590. $455.

A kit that converts an IBM electric typewriter into a hard-copy output terminal includes interface card, power supply and driver, cables and all mechanical parts. The kit fits all models and installs without drilling holes or cutting metal parts. The profile of the typewriter remains unchanged and its normal operation is not affected. The kit is compatible with computers using the S-100 bus.
Only one thing beats our Super-Mini Impact Printer...

Why stop with the data/text versatility of our 120 cps, 20-column multiple-copy mini. It works even harder as a complete system. Teamed with its own microprocessor interface and power supply, there's virtually nothing our DMPT-3 can't handle — from telemetry to process control, from unattended system recording to providing hard-copy data terminal output, even in POS and inventory control. Mated with any ASCII system, it takes either parallel or serial input at speeds up to 16 KHz or 1200 bps.

Alone or as a system, of course, the industry's smallest alphanumeric impact printer lets you economize with ordinary adding machine roll paper.

The Whole System

Our 50-watt switch.
The low-cost alternative.

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HEAT-A-DIP desolder/solder IC rework head consists of two miniature solder pots in a dual inline configuration to fit all IC sizes of 0.3" to 0.6" and 6 to 40 pins in six models. HEAT-A-DIP can be used with any temperature controlled iron, eliminating any problem of PCB measing. ICs can be removed and a new one inserted in 5 to 10 seconds. Fast and inexpensive. Contact your local distributor.

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DATA PROCESSING

Coupler transfers data to and from computer I/O

Daltec Systems, P.O. Box 157, Onandaga Branch, Syracuse, NY 13215. Joe Strock (315) 699-3830, $800; 12 wks.

The data intercoupler, Model D1488, can transfer BCD and binary data to and from computer I/Os in excess of 30 kbytes/s. The device can act as a controller; it makes any digital instrument compatible with the IEEE 488-1975 bus. The data format is programmable. Options include remote data accumulation with an RS232C link, double-buffered outputs to assure simultaneous change of all bits, and optical isolation to eliminate system ground loop problems. A front-panel keyboard and display are also available.

CIRCLE NO. 379

Pinlite® displays.

Almost an endless variety.

Every day the variety of high-contrast Pinlite incandescent digital displays gets more endless as we add new, feature-packed models to meet the needs of military, avionics, marine, and business machine applications. And we’re adding complementary connectors and connector/diode assemblies to make them easier to package, too.

Even though we’ve expanded our line, every Pinlite display still incorporates all those outstanding features you need, including 9,000 foot lambert brightness, 120° viewing angle, per-segment life of over 100,000 hours, wide operating temperature range, and direct compatibility with standard TTL driving networks and multiplex circuits. Every one of our 3/16” to 5/8” characters is enhanced by our patented cross-over filament arrangement which eliminates open corners for improved readability.

With Pinlite displays now more available right from stock, it will pay you to check them out. Write or call today for the whole story.

CIRCLE NUMBER 123

Line printer runs at 300 lines/min


The LP3036 line printer provides 300-line/min printout in 36, 42 or 60-column format or character-at-a-time operation for message/compose terminal use. The printer has low-inertia voice-coil actuators that apply pressure to paper against a rotating helical scanner to print characters in a 9 × 7 matrix. Each actuator scans multiple columns and travels only 0.002 in. The basic unit is a 10-char/in., 36-column printer. The printers meet the environmental requirements of MIL-T-21200, MIL-E-16400, MIL-E-5400 and EMI per MIL-STD-461.

CIRCLE NO. 380

TTY monitor module switches and patches

International Data Sciences, 100 Nashua St., Providence, RI 02904. (401) 274-5100. $200; 4 wks.

The Model-8916 A/B selector, patch and monitor module for teletypewriters contains the switching, patching and serial monitoring functions for two independent data channels at the terminal-modem interface. The modules operate with the Model-8964 controller and power module. Bulk switching of up to eight 8916 modules is performed by means of a master A/B switch on each 8916. Magnetic latching relays ensure system immunity from power failures and line transients. Jacks allow patching and serial monitoring of signals at the TTY current interface.

CIRCLE NO. 381
We brought you the first 4K static RAM — and delivered it a year and a half ahead of anyone else. We were the first to put it and its many descendents into volume production.

Now we'd like you to meet the new King of the Static RAMs... the 1K x 8, 300 nsec SEMI 8108. Look at his credentials!

A 1K Byte memory system in a single package.

300 nsec access time. The speed you'll need for microprocessor systems.

Low operating power — just 33 µW per bit. (7 µW per bit standby.)

Packaged in industry standard 22-pin DIP, for a 30% saving in board space over 16-pin 4K devices.

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Memory at Work

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CIRCLE NUMBER 124

ELECTRONIC DESIGN 4, February 15, 1978

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COMPONENTS

Get a circuit protector for $1.00 plus blown fuse

Heinemann Electric, P.O. Box CNO1908, Trenton, NJ 08650. (609) 882-4800. See text.

Heinemann will send you a sample of its new RE-Cirk-It circuit protector, the successor to the fuse, for a dollar and a blown fuse. The device protects like a fuse, but is resettable. It is cost-competitive with fuses and fuseholders and installs in the same panel space as a conventional 0.625-in. diameter fuseholder. The protector trips instantaneously on short circuits and with delay on sustained overloads. It can only be electrically tripped, and it can’t be turned off or held against a fault. Current ratings are from 0.25 through 10 A. The units are UL-recognized and CSA-approved.

CIRCLE NO. 382

Heat dissipators serve TO-3 devices

International Electronic Research, 135 W. Magnolia Blvd., Burbank, CA 91502. Ed Byrne (213) 849-2481. $0.238 (5000 qty); stock.

LA 363 heat dissipators can serve all TO-3 semiconductor devices. The dissipators are up to 23% more efficient in high power ranges than conventional “push-on” style heat sinks. Because the dissipators attach to semiconductor bases where most of the heat originates, rather than to the can, the dissipators are more efficient. Staggered fingers maximize radiation and convection cooling. In forced-air modes, the design maximizes air turbulence, which increases the heat transfer efficiency.

CIRCLE NO. 383

DPMs sub for 4-1/2-in. analog meters


The universal digital panel meter, DPM-31, is a mechanical replacement for standard 4-1/2-in. analog meters. The meter is designed around a 3-digit DPM, is ac powered and has universal range adjustment for dc and ac input signals. Jumpers on the rear select full-scale input ranges of 50 mV dc (100 mV ac), 5 V dc (100 V ac), 50 V dc (100 V ac) and 500 V dc (460 V ac). The full-scale readout of 999 can be programmed to read any lesser required number by means of an internal multturn pot. Accuracy is 0.5% of reading ±1 digit. Input resistance is 20 kΩ/V dc and 9 kΩ/V ac. Max signal frequency is 2 kHz.

CIRCLE NO. 384

Thumbwheel switches have PC stators

AMP, Harrisburg, PA 17105. (717) 564-0100.

In a new thumbwheel switch concept, AMP provides the usual rotor and housing; however, the stator is in the form of artwork which is used to produce a photo-etch PC-board master. Eliminating the fabricated stator results in substantial material and manufacturing cost savings. Also, electrical connections to the stator contacts are eliminated. A single 0.85 × 0.83 × 0.3-in. housing contains the rotating contacts. Current ratings are 1.5 A, non-switching, and 0.125 A, switching. Stator artwork is available for outputs in decimal BCD, BCD-9 complements and other codings. Special orders for variations in output codes as well as artwork patterns, wheel marking and housing colors can be accommodated.

CIRCLE NO. 385

Tantalum capacitors dipped in epoxy

Siemens, 186 Wood Ave. S., Iselin, NJ 08830. (201) 494-1000. See text.

A line of economy miniature epoxy-dipped solid-tantalum capacitors, ST841/842, includes sizes from 0.1 to 680 µF in eight voltage ratings from 3 to 50 V. Tolerances of 5, 10 or 20% are available. The capacitors have radial leads and are available with straight or “lock-in” crimp leads for easy PC-board insertion. Typical high-quantity OEM pricing is in the 6 to 8-cent range.

CIRCLE NO. 386

Submini toggle switch features locking lever

C & K Components, 103 Morse St., Watertown, MA 02172. Jim Martinec (617) 926-0800. Free sample.

An accident-prevention toggle switch option, the K1 locking lever, is available on a line of subminiature SPDT, DPDT, 3PDT and 4PDT switches. Lock-slots milled into the top of the bushing secure the switch in any one of up to three positions. To move the switch from one position to another, you must pull upward on the lever and then move the actuator to the desired position. This built-in safety feature prevents accidental tripping of the switch in critical switching applications.

CIRCLE NO. 387

CIRCLE NUMBER 125
In the financial community, you have to move money to make money. Since data communications among widely dispersed locations are the lifeblood of banking, insurance, and other financial institutions, it’s not surprising that many of the big ones choose Universal Data Systems as their modem supplier.

Some of these institutions choose UDS because they like the technical superiority of CMOS design in 103s, 201s, 202s or ACUs. Others select the RM-16 for fast, reliable 16-channel communication. For economy, others like the two-wire full-duplex 1200 bps capability of the UDS 12-12 and the direct access provided by FCC-approved DAAs.

If you’re a datacomm user or an OEM and you have a similar need for confidence in communications, follow the smart money — discuss your modem requirements with Universal Data Systems, 4900 Bradford Drive, Huntsville, Alabama 35805. Telephone 205/837-8100; TWX 810/726-2100.

Universal Modems
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Components

Thumbwheel switches set digital time-delay relay

International Microtronics, 4016 E. Tennessee St., Tucson, AZ 85714. Dr. Otto Fest (602) 748-7900. $79; stock to 4 wks.

A low-power, solid-state, time-delay relay set by direct-reading thumbwheel switches operates from 12 V dc. Series 280 Digilay times, in on or off modes, from 1 ms to 9999 s. Accuracy and repeatability is ±0.5%. Maximum power turn-on time is 30 ms and minimum power-recycle time is 10 ms. External frequency modulation permits fine tuning of the oscillator’s base frequency or, with an external waveform, actual modulation of the time delay. Three switch options are spdt relay, spdt reed relay and spdt triac.

Electronic counter adds and subtracts 8 digits


The Type K 8-digit electronic counter has a 0.17-in. LED display that adds and subtracts while recording even overlapping count inputs. The device has a built-in battery that self-charges and supports data for six months. Packaging is in a 1 x 2-in. case.
Elmwood makes Sense

Why overspecify? Choose the fast-response snap-acting Elmwood thermostat model that just fits your needs. Choose the levels of tolerance and differential that are right for your product, without wasted details or dollars.

For worldwide sales many Elmwood models meet CSA and European requirements (and DIN norms), as well as U.L. Ratings are to 15 amps, for exposures -65°F to 550°F (-54°C to 288°C), and each unit is factory pre-set, tested and tamperproof. Doesn't Elmwood make Sense? Ask for prototypes.

Elmwood Sensors, Inc. 1655 Elmwood Avenue, Cranston, R.I. 02907

European Div., Elmwood Sensors, Ltd.,
North Shields, Tyne and Wear, NE29 8SA
England Ph (089) 45-82821. Telex: 53284

Elmwood Sensors
Precision Controls
CIRCLE NUMBER 129
Cables match D plugs to DIP sockets

Aries Electronics, P.O. Box 231, Frenchtown, NJ 08825. (201) 986-1096.

D-type subminiature connectors with 9, 15, 25, 37 and 50 pins are in ready-to-install flat-cable assemblies that fit DIP sockets. Connections are soldered; backshell potting provides cable strain relief. The cable can exit from back or sides of the connector. Normally the cable is EIA color-coded 26 AWG wire. Cable ends come stripped and tinned or terminated into a covered DIP header, ready to solder into a PC board or plugged into a DIP socket.

CIRCLE NO. 390

Panel-mounting frame holds standard boards

EECO, 1441 E. Chestnut Ave., Santa Ana, CA 92701. (714) 833-6000. $1775; stock.

Model 14G frames hold standard-sized pin-in-board wrapped-wire panels with widths of 2.7, 5.4, 10.8 or 15.8 in. The frames can be assembled or reassembled to hold either 6.9 or 7.5-in.-high panels. The panels mount on extruded-aluminum side rails. The end pieces provide firm snap-in positioning in a 19-in. drawer. Depressing the end pieces at the locking points permits you to swing the frame up for full access, or to remove it completely from the drawer.

CIRCLE NO. 391

High density connectors handle 24 to 96 pins


The 0.1-in. Series 8223 connector can be ordered with 24 to 96 contacts in a wide range of termination styles. Applicable PC cards range from 1/16 to 1/8 in. Current rating is 5 A using 22 AWG wire and contact resistance is 6 mΩ. Insulators are diallyl phthalate, glass-filled and flame resistant per MIL-M-14F. With Mil plating, the connectors qualify to MIL-C-55302.

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BNC connectors install quickly

Cambridge Products, 244 Woodland Ave., Bloomfield, CT 06002. Ed Selig (203) 243-1761. $0.90/$1.15 (100 qty).

Two Fastfit BNC connectors assemble easily and rapidly. The crimp version has a body assembly and crimp ferrule for the braid. The field-installable version is a one-piece connector with no loose parts. Both types use a self-energizing contact, pre-assembled into the body. The contact captures the cable's center conductor upon assembly. The field-installable BNC is simply twisted on to the cable and attaches in seconds without the use of tools or solder. The crimp connector requires crimping of the braid only. Both versions are available for RG-58 and RG-59 cables.

CIRCLE NO. 394

NEW 8 &10-Bit Hybrid D/A's settle in 25 NS:

The fastest hybrid microcircuit D/A converters on the market. That's what the Computer Labs HDS-0820 and HDS-1025 are since they exhibit settling times as low as 25 ns. And even though their power dissipation of less than 3/4 watt is almost one-half that of competitive D/A's, a full 10 mA output current is maintained. So they can be used to drive transmission lines or other low-impedance loads, directly.

Active laser trimming has been used in the construction of the HDS Series to produce high accuracy and adjustment-free performance. Each is housed in a 24-pin DIP case and has an internal precision reference. They are ideally suited to operate with high-speed A/D converters, CRT displays, television picture reconstruction equipment, automatic test equipment, and much more. Call or write now for more information on these outstanding hybrid converters.

CIRCLE NUMBER 140

Harnessing system shows wire-end points

RG Systems, 80 Fountain St., Pawtucket, RI 02860. Joe Rheaume (401) 738-8110, $3500.

The harness fabrication system, Model 201-A, is a programmed sequential wiring system that eliminates wire-run lists and operator search time. It uses LEDs to illuminate the origin and destination of each wire on the board. After automatically testing installed wires, the device indicates, in proper sequence, the next wire to be installed. A corresponding LED at the storage bin lights up to indicate which precut wire to use. The system is field expandable to handle harnesses with up to 300 wires.

CIRCLE NO. 395

One-part coating is electrically conductive

Electro-Kinetic Systems, 2500 E. Ridley Ave., Chester, PA 19013. (215) 876-6192. $75/gal; stock.

A one-part electrically conductive coating, X-Coat 200, can be used for rf shielding, electrostatic discharging and grounding. Based on specially processed base metals, the coating exhibits a resistivity of less than 5 Ω/ft² and may be applied by spraying, dipping or rolling. The coating can provide shielding of up to 50 dB at most frequencies.

CIRCLE NO. 396

Semiconductor cooler needs less space


The FCA-880 semiconductor cooling package gives high cooling performance with a small sized unit, because high-fin-density extrusions provide efficient thermal coupling to the atmosphere. The space between fins is as little as 1/10 the height, which doubles the cooling efficiency compared to other types. The FCA-880 is a confined airflow package that provides cooling for over 30 discrete devices. The assemblies can be any length in 6-in. increments and come in a variety of hole patterns.

CIRCLE NO. 397

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CIRCLE NUMBER 140

ELECTRONIC DESIGN 4, February 15, 1978
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CIRCLE NUMBER 142

Electronic Design 4, February 15, 1978
**POWER SOURCES**

**Dc/dc power converters deliver ±15 V at 60 mA**

*Intronics, 57 Chapel St., Newton, MA 02158. Dick Sakakeeny (617) 332-7350. $59; 2 to 4 wks.*

DCI dc converters deliver floating power at ±15 V dc at 60 mA. Regulation is 0.05% for line and load variations and output ripple is 11 mV. Input to output isolation is 10^11 Ω in parallel with 5 pF and breakdown voltage is 800 V dc. Three models offer a choice of operation from 5, 12 or 28 V dc. Size is 3 × 2 × 0.6 in.

**Power 10-W dc/dc units from a choice of inputs**

*Reliability, P.O. Box 37409, Houston, TX 77266. Bob Miller (713) 492-0550. $89; 6 wks.*

Single and dual-output dc-to-dc power sources operate from input voltages of 5, 12 or 24 V. Single outputs are 5 V at 2 A. Dual outputs are 12 V at 425 mA or 15 V at 330 mA. All units are encapsulated in a metal case and have line regulation of 0.02% from no-load to full-load and load regulation of 0.02% from low-line to high-line. Max output ripple is 1 mV rms and 20 mV pk-pk, while input-reflected ripple is 1% of max input voltage.

**Regulator adjusts from -2 to -24 V at 5 A**

*Fairchild Semiconductor, 464 Ellis St., Mountain View, CA 94042. Bill Callahan (415) 962-3816. $7.15 (100 qty); stock.*

The µA79HGKC negative-voltage regulator features adjustable output between -2 and -24 V at 5 A with a resistor divider. The device is packaged in a 4-pin TO-3 case with a rated power dissipation of 50 W at 25 C. The case is electrically neutral, eliminating need for insulating washers.

**Evaluation samples**

**Switches**

Rocker, toggle and lever-operated subminiature switches come in a wide variety of function, actuation, termination and mounting options, and contacts for low-level circuits, or for up to 5 A at 125 V ac, 2 A at 250 V ac. Dialight.

**Insulators**

The Insul-Cote system offers mica and film insulators pre-coated with thermal grease heat-sealed in 2000 piece “ammo pack” continuous strips. The strips are fed into automated dispensers, which present the coated insulators one at a time for production-line use. Three machine styles are available (from semi-automatic to variable-speed automatic). Prices and specifications are included in a 4-page brochure. A product sample is available. Thermalloy.

**Substrates**

RT/duroid 5870 microwave-circuit substrate clad with conductive material comes in 5 × 8-in. samples. The glass microfiber-reinforced PTFE has a dielectric constant of 2.34 ±0.03% at 10^6 through 10^10 Hz. Rogers Corp.

**25-A bridge rectifiers**

Silicon-bridge rectifiers are rated at 25 A, have a 300-A surge; and peak reverse voltages from 50 to 1000 V. The Series PB bridges are 1.125 in. sq. by 0.438 in. high; have 1500-V-rms dielectric strength, U.L. component recognition, and 0.25-in. quick-connect terminals. Free samples to OEMs who outline their application. Electronic Devices.

**Application notes**

**Glass-to-metal seals**

The technology and materials used to produce hermetically sealed packages such as TO headers, relay headers, dual in-line packs, flat packs, single-pin terminals, frame packages, and cold-weld packs is explained in a six-page article. Airpax Electronics, Cambridge, MD

**X-Y recorders**

Several traditional and not-so-traditional methods of reducing effects of common-mode noise are described in “X-Y Recorder Input Configuration and Input Noise.” Hewlett-Packard, Palo Alto, CA

**Digital tester**

Applications information, concerning the 851 digital tester, consists of a data sheet, 13 technique briefs, and 5 application notes. The data sheet provides specifications; the technique briefs explain how to use the tester in troubleshooting disc drives, tape drives, terminals, data-communications devices, and microprocessor systems. Tektronix, Beaverton, OR

**Count controls**

A 40-page comprehensive training manual illustrates operating principles, design and application of count controls. The booklet contains photos, circuitry and wiring diagrams, sequence and program charts. Eagle Signal Div., Gulf & Western, Davenport, IA

**A/d-d/a converter testing**

An eight-page application note discusses a/d and d/a converter specifications and testing. Examples of testing techniques are completely defined and illustrated. GenRad, Concord, MA
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In addition to complete descriptions and specifications for National pressure gauges and differential pressure devices, a 145-page book gives detailed discussions of the how and why of transducer applications, from barometers and medical electronics to refrigeration and building control. This informative handbook can be ordered for $4.00 postpaid. National Semiconductor, 2900 Semiconductor Dr., Santa Clara, CA 95051

**μP display interface**

Descriptions and tables summarizing the physical and electrical characteristics of microprocessor-display interfaces are included in a four-page brochure. Matrox, Montreal, Quebec

**DIP switch**

Photos, description, features, options, technical specifications and code truth tables for a 16-position binary-coded DIP switch are given in a four-page brochure. EECO, Santa Ana, CA

**Analog components**

Application information, electrical characteristics, dimensional drawings and photographs of synchros, resolvers, gimbal pickoffs, stepper motors, torque motors, ac and dc servo motors, and tachometers are given in a 48-page catalog. Clifton Precision, Clifton Heights, PA

**DC power supplies**

A 16-page booklet provides instructions, graphs, charts, tables and application notes for the power-supply designer who wishes to create a custom system from readily available sub-modules and accessories. Powertec, Chatsworth, CA

**Digital panel meters**

Specifications of solid-state digital panel meters are included in an eight-page brochure. Fairchild Camera and Instrument, Instrument Operation, San Jose, CA

---

**Thin-film resistors**

A six-page brochure describes thin-film resistors. Hybrid Systems, Bedford, MA

**Minicomputers**

Hardware and software features of the Eclipse S/130 systems are detailed in a 16-page brochure. Topics include computation abilities, languages, operating systems, programming aids, peripherals and typical-system configurations. Data General, Westboro, MA

**Conductive cells**

Eight data sheets provide applications, specifications and dimensions of plastic and glass conductivity cells. Other information includes a quick-reference chart listing standard features and available questions. Beckman Instruments, Cedar Grove Operations, Cedar Grove, NJ

**IC log amps**

Ultramiiniature and miniature IC logarithmic amplifiers, spanning 30 to 160 MHz, are featured in a data sheet. Outline drawings with English/metric dimensions, a pulse response photo, typical input-output characteristics, and a discussion of dc outputs are included. RHG Electronics Laboratory, Deer Park, NY
Analog panel instruments

API series analog panel instruments are highlighted in a four-page brochure. LFE Corp., Waltham, MA

CIRCLE NO. 423

Leads, connectors

Nearly 250 interconnecting leads and hermetic connectors for high-voltage applications are described in a 74-page catalog. The catalog includes electrical and mechanical specifications along with application and dimensional data. AMP, Elizabethtown, PA

CIRCLE NO. 424

Holography, laser systems

A 100-page publication describes nearly 1000 holography and laser systems. Extensive technical-product information is included along with application data. Information is fully illustrated with curves, graphs and other test data, all produced by sophisticated computer-based test techniques. Newport Research Corp., Fountain Valley, CA

CIRCLE NO. 425

Printers

Dot matrix printers capable of printing 64 characters in 40 columns at 50 cps are featured in a four-page brochure. Anadex, Chatsworth, CA

CIRCLE NO. 426

Diodes and transistors

A 238-page publication contains detailed device characterization and applications information on diodes, transistors, voltage suppressors and switching transistors. For ease of use, the catalog lists the devices numerically within specific categories. Both JEDEC and General Semiconductor type numbers are shown in each product section. General Semiconductor, Tempe, AZ

CIRCLE NO. 427

Rental test equipment

Nearly 200 pieces of telecommunications test equipment is covered in an eight-page catalog. Electro Rent, Burbank, CA

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Electronic Design 4, February 15, 1978

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CIRCLE NUMBER 148

We've got everything in the book

and it's yours free.

You'll find more than 600 electronic test accessories between the covers of ITT Pomona Electronics new 90-page catalog for 1978.

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DIGITAL DC CLAMP-ON AMMETER

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CONTROL KNOBS

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FUNCTION GENERATOR 190

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LED LAMP ASSEMBLY 191

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POWER SUPPLIES 202

OEM HeNe Redline™ Lasers offer design engineers output powers from 2mW to 6mW and are used for measuring quality control, reading and writing. They're used in facsimile transceivers, graphics, industrial bar code readers, alignment systems, surface inspection devices and a dozen other applications. Coherent offers singular laser units or integrated systems complete with acousto-optic modulator and power supply driven by TTL logic levels. Contact Coherent, 3210 Porter Drive, Palo Alto, CA 94304. (415) 493-2111.

LASERS 206

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ROCKER/TOGGLE SUBMINIATURES 207
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SPECIAL SEMINARS:
MONDAY, APRIL 17
MINI/MICROCOMPUTER APPLICATIONS

COURSE OBJECTIVE: Beginning with a brief review of microcomputer hardware and software, this applications course is intended to build on your knowledge of basic hardware configurations, memory systems, I/O Schema, and debugging methods. Understanding the differences in approach for applying minicomputers and microcomputers will be the theme of the course. The emphasis will be on microcomputer applications. Specifically, the software development process, development of the hardware system, hardware/software tradeoffs, interfacing, system specification, and some development cases will be covered. A general understanding of the process is one goal of the course. The course will close with an explanation of the important highlights of the hardware development process.

COURSE OUTLINE:
1. Reminder on current minicomputer characteristics and capabilities.
2. Review of microcomputer hardware and software.
3. The software development process.
4. Development of the hardware system.
5. Hardware, software tradeoffs.
6. Interfacing.
8. Some Development Case Studies.

Sponsor: The Institute of Electrical and Electronics Engineers (IEEE)

WEDNESDAY, APRIL 19
STEP-BY-STEP DESIGN OF MICROPROCESSOR SYSTEMS

The aim of the course is to expose the participants to step-by-step procedures for the design and implementation of microprocessor systems using the following modes of operation: (1) Wait/go; (2) Test-and-go (test and skip); (3) Interrupts; and (4) Direct Memory Access.

The design procedures which are accomplished in five well-defined steps, will be demonstrated and verified experimentally in class. Lecturer: Prof. D. Zissos, The University of Calgary, Canada.

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- Senior Electronics Engineer — BSEE plus 7 yrs. experience in automatic test equipment using minicomputers and microprocessors. Assignments will include development of automatic test equipment for machinery and electronics.

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- Electrical Design 4, February 15, 1978
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