SATA Connector with gold fingers facing up (reverse)

Locate All 0.1μF capacitance as close as possible to Sii3811 pins

Soldering an EFT to the PCIE GND will improve the signal integrity

- Select ATAPI DMA method
  Pin 20, 16 = 11b: Decode command packet MMC commands
  Pin 20, 18 = 11b: Use-SCMP(S) of Pin 18
  Pin 20, 19 = 11b: Use-SCMP(S) of Pin 19
  Pin 20, 16 = 00b: Use bit 2 in Features Register

For the termination requirement for non-direct attached application, please refer Sii-JAN-0147

Revision History
Rev. A - Initial Released

Information Subject to Change without Notice. Please consult Silicon Image.