Using COP800 Devices to Control DC Stepper Motors

INTRODUCTION

COP800 devices can be used to control DC stepper motors with limited effort. This application note describes the use of a COP820 to control the speed, direction and rotation angle of a stepper motor. In addition to the COP820, this application requires a quad high current peripheral driver (DS3658) to meet the high current needs of the stepper motor.

DC STEPPER MOTOR

A DC stepper motor translates current pulses into rotor movement. A typical motor contains four winding coils labeled red, yellow/white, red/white, and yellow. Applying current to these windings forces the motor to step. For normal operation, two windings are activated (pulsed) concurrently. The motor moves clockwise one step per change in windings activated with the following activation sequence: red and yellow, yellow and red/white, red/white and yellow/ white, yellow/white and red, repeat. Half-steps may be generated by altering the sequence to: red and yellow, yellow, yellow and red/white, red/white, red/white and yellow/ white, yellow/white, yellow/white and red, red, repeat. The motor runs in a counterclockwise direction if either sequence is applied in reverse order. The speed of rotation (number of steps/second) is controlled by the frequency of the pulses.

COP820 CONTROL OF STEPPER MOTOR

The COP820 controls the stepper motor by sending pulse sequences to the motor windings in response to control commands. Commands executed by the code in this application include: single step the motor in a clockwise or counterclockwise direction (i.e. rotate the rotor through a certain number of degrees), run the motor continuously at one of four speeds in a clockwise or counterclockwise direction, and stop the motor.

Note: Half-stepping is not implemented in this example.

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During continuous mode operation, the 16-bit timer of the COP820 is used to control the speed of the stepper motor. The timer is set up with a value that causes an underflow once every x seconds or at a frequency of 1/x. Each underflow of the timer interrupts the microcontroller. In response to the timer interrupt, the microcontroller generates a new pulse and causes a single step of the motor. Thus the motor steps at the frequency of the timer underflows. This application sets up the timer to generate interrupts at four different frequencies. These frequencies produce the following motor speeds: 25 steps/second, 100 steps/second, 200 steps/ second, and 400 steps/second.

The determination of which windings to activate and deactivate to step the motor is performed by a single subroutine in this example. A block of memory is allocated to store a step pointer and the four possible stepper drive values are shown in Table I (9,C,G,3). Consecutive memory locations are used to store the stepper drive values so that applying the value from location X and then location X+1 (or X-1) causes the motor to step once. The motor drive subroutine increments or decrements the pointer to the current drive value based on the selection of a clockwise or counterclockwise direction. Writing the value from the newly selected location to the motor causes a single step of the motor in the appropriate direction.

During single step operation, the microcontroller steps the motor the exact number of times requested in the control command. Each step corresponds to 1.8 degrees of rotor movement. Therefore, a request to perform 200 steps will rotate the rotor through one complete revolution (360 degrees) at a fixed speed.

A block diagram of the application is shown in *Figure 1*. A flowchart of the code used to control the motor is given in *Figure 2*. The complete code is given at the end.

TABLE I. Stepper Motor Drive Sequence

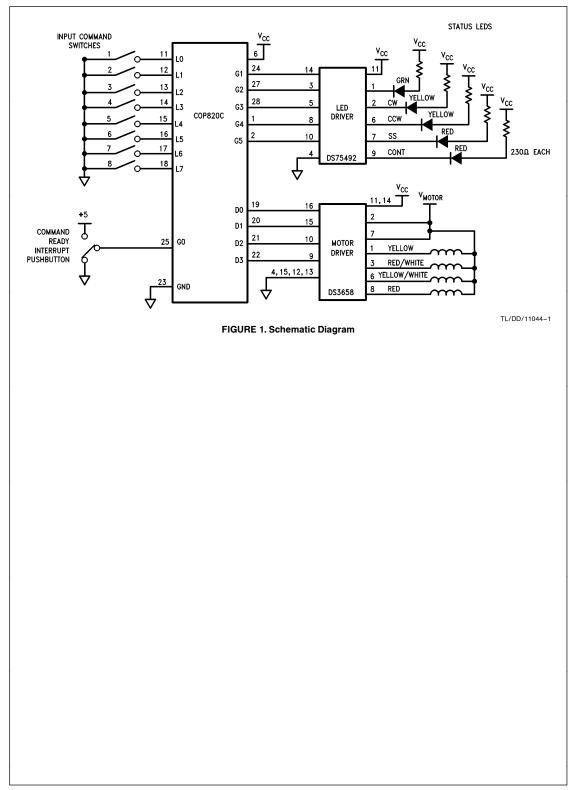
Step	Yellow	Red/White	Yellow/White	Red	Hex Value			
0	ON	OFF	OFF	ON	9			
1	ON	ON	OFF	OFF	С			
2	OFF	ON	ON	OFF	6			
3	OFF	OFF	ON	ON	3			
4	ON	OFF	OFF	ON	9			

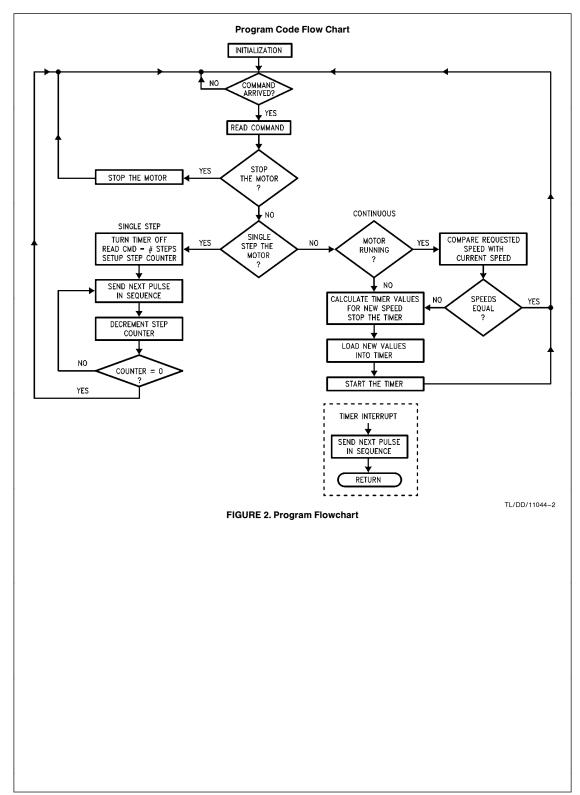
Ising COP800 Devices to Control DC Stepper Motors

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NATIONAL SEMICONDUCTOR CORPORATION COP800 CROSS ASSEMBLER, REV: D1, 12 OCT 88 ; STEPPER MOTOR CONTROL PROGRAM 2 3 ;MAY 1990 This program controls the speed, direction, and degree of rotation of a DC stepper motor. 4 5 6 7 Memory Map Memory MapCONTENTS(MSO) step motor drive value 09H(two windings active per pulse)(MS1) step motor drive value 06H(MS2) step motor drive value 03H(CMD) control commandbit 7 - bit4 = motor speed or upper nibble of # single stepsbit 3 = unusedbit 2 = (MODE) single step or continuous mode select (1 = ss)bit 1 = (DIR) cw or ccw direction select (1 = cw)bit 1 = (GO) motor go or motor stop select (1 = stop)(STEPS) lower byte of number of single steps(FLGREG) flag registerbit 0 = (INT) ready to read in cmd (ext int occured)bit 1 - bit7 = unused(TVAL0) value to load in lower byte of timer for speed X(PORTLP) port L input pins used for incomming commands(PORTD) port D data pins used to drive status LEDs(PORTD) port D data pins used to ouput pulses to the stepper motor(CREG0) step counter register zero(CREG0) step counter register one(STPPTR) pointer to current step motor drive value (RAM 00 - 03) 8 RAM CONTENTS 9 00 10 01 02 11 12 03 13 04 14 15 16 17 18 19 05 20 07 21 22 23 14 24 25 15 D2 D4 DC 26 27 28 FO 29 F1 F2 30 31 32 REGISTER AND CONSTANT DEFINITIONS 33 34 ; COMMAND BITS 35 0000 GO = 0 GO COMMAND BIT 0 = GO36 1 = STOP; DIRECTION COMMAND BIT ; 1 = CW 0 = CCW ; MODE COMMAND BIT 37 0001 DIR = 1 38 39 0002 MODE = 2 ; 1 = SINGLE STEP 0 = CONTINUOUS 40 41 ; PORTG BITS 42 ;FLAG BIT (SET IF EXTINT OCCURS) ;READY LED ;CLOCKWISE LED 43 0000 INT = 0 = 0 = 1 = 2 = 3 44 0001 READY 0002 45 CW CCW COUNTER CLOCKWISE LED 46 47 0004 SS = 4 ;CONTINUOUS (NON-STOP) LED 48 0005 NS 49 ; REGISTERS 50 51 0004 = 04 CMD ;INPUT COMMAND STORAGE REGISTER TL/DD/11044-3

4

52	0005	STEPS	= 05	;INPUT #STEPS/SPEED REGISTER
53	00F0	CREGO	= 0F0	COUNTER REGISTER
54	00F1	CREG1	= 0F1	COUNTER REGISTER
55	0007	FLGREG		;FLAG REGISTER (FLAG BITS)
56	00F2		= 0F2	CURRENT MOTOR STEP POINTER
57	0014	TVALO	= 014	;MOTOR SPEED LOAD VALUES
58	0015	TVAL1	= 015	
59 60	0000 0001	MSO MS1	= 00 = 01	STEPPER MOTOR DRIVE VALUES
61	0002	MS2	= 01	
62	0003	MS3	= 02	
63			00	
64		;ASSIGNMENTS FO	DR COP820	
65				
66	00D5	PORTGC	= 0D5	
67	00D4	PORTGD	= 0D4	
68	00D6	PORTGP		
69 70	00D1	PORTLC	= 0D1	
71	00D0 00D2	PORTLD		
72	OODC	PORTD	= 0D2 = 0DC	
73	00D7	PORTI		
74	00E9	SIOR	= 0E9	
75	00EA	TMRLO	= 0EA	
76	00EB	TMRHI	= 0EB	
77	OOEC	TAULO		
78 79	00ED 00EE	TAUHI	= 0ED = 0EE	
80	OOEF	PSW	= 0EE = 0EF	
81	0021	154	- 061	
82				
83	0000	GIE	= 0	
84	0001	ENI	= 1	
85	0002	BUSY	= 2	
86	0003	I PND	= 3	
87 88	0004 0005	ENT I TPND	= 4 = 5	
89	0000	IFND	- 5	
90	0002	I EDG	= 2	
91	0003	MSEL	= 3	
92	0004	TRUN	= 4	
93	0005	тсз	= 5	
94	0006	TC2	= 6	
95 96	0007	TC1	= 7	
97				
98		. CH I P	820	
99				
100		; INITIALIZATION		;*****************
	DO DD2F	LD	SP,#02F	
102 00	02 BCEE80	LD	CNTRL,#080	
				TL/DD/110

03 0005 BCEF03 04 0008 BCD401 05 000B BCD53E 06 000E BCDC09 07 0011 BCD100 08 0014 BCD0FF 09 0017 5F 10 0018 9A09 11 001A 9A0C 12 001C 9A08 13 001E 9E03 14 0020 D200 15 0022 BC0700		LD	PSW, #003	;GLOBAL INT ENABLE/EXTINT ENAB
04 0008 BCD401			PORTGD,#01 PORTGC #03F	;CONFIG PORTG FOR OUTPUTS ;START MOTOR DRIVE VALUE ;CONFIG PORTL FOR INPUTS ;CONFIG PORTL FOR WEAK PULL-UP; ;SETUP MOTOR DRIVE VALUES
06 000F BCDC09			PORTD. #09	START MOTOR DRIVE VALUE
07 0011 BCD100		LD	PORTLC, #00	CONFIG PORTL FOR INPUTS
08 0014 BCD0FF		LD	PORTLD, #0FF	CONFIG PORTL FOR WEAK PULL-UP
09 0017 5F			B,#MSU (B+1 #09	SETUP MOTOR DRIVE VALUES
11 001A 9A0C		LD	[B+],#0C	
12 001C 9A06		LD	[B+],#06	
13 001E 9E03		LD	[B],#03	
14 0020 D200 15 0022 BC0700		L9 LD	STPPTR,#00 FLGREG #00	;INIT STEP POINTER ;INIT FLAG REGISTER
		20	I Bakea, ****	, THIT I DAG REGISTER
	; READ,	DECODE,	AND EXECUTE COMMAND	
19 20 0025 BDD479	TOP:	SBIT	READY . PORTGD	TURN ON READY FOR NEXT CMD LE
21 0028 3081		JSR	WAIT	WAIT FOR CMD AND READ CMD
22 002A BDD469		RBIT	READY, PORTGD	;TURN OFF READY FOR NEXT CMD L
23 002D 9C04 24 002E 8D0470		X	A, CMD	STORE IN CMD REGISTER
25 0032 08		JP	STOP	THEN STOP MOTOR
26 0033 BD0472		IFBIT	MODE, CMD	ELSE CHEK MODE
27 0036 3041		JSR	SSTEP	; IF MODE SET THEN GO SINGLE ST
28 0038 305F		J SR J P	CONT	ELSE GO CONTINUOUS
29 003A EA 30	STOP:	JP	TOP	STOP THE MOTOR
31 003B 308E	2.01.1	JSR	TMRSET	STOP THE TIMER
32 003D BCD401		LD	PORTGD, #01	;TURN OFF ALL LEDS
20 0025 BDD479 21 0028 3081 22 002A BDD469 23 002D 9C04 24 002F BD0470 25 0032 08 26 0033 BD0472 27 0036 3041 28 0038 305F 29 003A EA 30 31 003B 308E 32 003B BCD401 33 0040 E4		15	тор	;TURN ON READY FOR NEXT CMD LE ;WAIT FOR CMD AND READ CMD ;TURN OFF READY FOR NEXT CMD L ;STORE IN CMD REGISTER ;IF STOP BIT SET ;THEN STOP MOTOR ;ELSE CHEK MODE ;IF MODE SET THEN GO SINGLE STI ;ELSE GO CONTINUOUS ;GO WAIT FOR NEXT COMMAND ;STOP THE TIMER ;TURN OFF ALL LEDS ;GO WAIT FOR NEXT CMD
36			HE MOTOR (SS)	
37 38	SSTEP:			;****************************
38 39 0041 308E		JSR	TMRSET	STOP TIMER
40 0043 BCD410			PORTGD, #010	TURN ON SS LED (RST ALL OTHER
42 0048 8A		INC	A	ADD 1 TO CORRECT FOR LOOP
43 0049 9CF0		X	A, CREGO	STORE #STEPS IN LOBYTE COUNT I
44 004B 9D04		LD	A, CMD	;LOAD HIBYTE # STEPS
46 004E 950F		AND	A A.#0F	GET RID OF UPPER BITS
47 0050 8A		INC	A	ADD 1 TO CORRECT FOR LOOP
48 0051 9CF1		X	A, CREG1	; MOVE TO HIBYTE OF COUNT REG
49 0053 C0 50 0054 05	122:	JP	DO	THEN GO DO A STEP
51 0055 C1	MID:	DRSZ	CREG1	ELSE DECR HIBYTE AND IF NOT Z
52 0056 01		JP	DO2	THEN GO DO A STEP AND RST LO
38 39 0041 308E 40 0043 BCD410 41 0046 3081 42 0048 8A 43 0049 9CF0 44 0048 9D04 45 0040 65 46 004E 950F 47 0050 8A 48 0051 9CF1 49 0053 C0 50 0054 05 51 0055 C1 52 0056 01 53 0057 8D		RETSK		STOP TIMER ;TURN ON SS LED (RST ALL OTHER ;WAIT FOR CMD BYTE 2 (* STEPS) ;ADD 1 TO CORRECT FOR LOOP ;STORE *STEPS IN LOBYTE COUNT * ;LOAD HIBYTE * STEPS ;MOYE TO LOWER NIBBLE ;GET RID OF UPPER BITS ;ADD 1 TO CORRECT FOR LOOP ;MOYE TO HIBYTE OF COUNT REG ;DECR LOBYTE AND IF NOT ZERO ;THEN GO DO A STEP ;ELSE DECR HIBYTE AND IF NOT ZI ;THEN GO DO A STEP AND RST LOO ;ELSE END OF LOOP RETURN
				TL/D

54 0058		DO2:	LD	CREG0, #0FF	RESET LOBYTE OF COUNTER
55 005A 56 005C		DO:	JSR JSR	NXTVAL DELAY	;STEP THE MOTOR ;Slow the stepping
57 005E 58			JP	TP2	GO TO TOP OF LOOP
59 60			- NOTOR	CONTINUOUSIY (NS -	NON-STOP = CONTINUOUSLY)
61			E MOIOR	CONTINUOUSLI (NS -	
62 63 005F	BDEE74	CONT:	IFBIT	TRUN, CNTRL	;*************************************
64 0062	01		ID.	CUVODD	THEN CHECK THE CURRENT CREED
65 0063 66 0064	03 3148	CHKSPD:	JP JSR	SETGO SPEED	;ELSE GO START THE MOTOR :COMPARE INPUT WITH ACTUAL SPD
67 0066	8E		RET	Mup and	; IF EQUAL RET ELSE RESTART MOTOR
68 0067 69 0069		SETGO:	JSR LD	PORTGD, #020	TURN ON CONTINUOUS LED
70 006C 71 006E			JSR LD	TIMVAL	CALCULATE TIMER (SPEED) VALUE
72 006F	9CEB		X	A, TMRHI	MOVE SPEED VAL INTO TIMER
73 0071 74 0072			LD X	A, [B-]	LOAD A WITH TVAL1 POINT TO TVALO
75 0074	AE		ĹD	A, [B]	;LOAD A WITH TVALO
76 0075			X LD	A,TMRLO	; THEN CHECK THE CURRENT SPEED ; ELSE GO START THE MOTOR ; COMPARE INPUT WITH ACTUAL SPD ; IF EQUAL RET ELSE RESTART MOTOR ; STOP THE TIMER ; TURN ON CONTINUOUS LED ; CALCULATE TIMER ; LOAD A WITH TVAL1 ; MOVE SPEED VAL INTO TIMER ; LOAD A WITH TVAL0 ; MOVE SPEED VAL INTO TIMER ; LOAD A WITH TVAL0
78 0078	9CEC		X SBIT	A, TAULO	
79 007A 80 007D	BDEF7C		SBIT	A, TAULO ENTI, PSW TRUN, CNTRL	;ENABLE TIMER INTERRUPT ;START THE TIMER
81 0080			RET	inon, on the	; RET TO MAIN AND WAIT FOR THRINT
82 83		; SUPPOR	ROUTIN	NES *************	*********
84 85		WAIT:			;*************************************
86 87				FOR AN EXTERNAL INTE	ERRUPT TO SIGNAL AN INCOMMING COMMAND
88 0081	BD0770		IFBIT	INT, FLGREG	; IF EXTERNAL INTERRUPT OCCURED
89 0084 90 0085			JP JP	OUT WAIT	;THEN JUMP OUT OF LOOP ;Else continue to wait
91 0086	BD0768	OUT:	RBIT	INT, FLGREG	RESET EXTERNAL INTERRUPT FLAG READ INCOMMING COMMAND
92 0089 93 008B			LD XOR	INT, FLGREG A, PORTLP A, #0FF	;READ INCOMMING COMMAND ;Complement incomming command
94 008D			RET	.,	;RETURN COMMAND IN ACC
95 96 97		THREE .			********
98		TMRSET:	; RESET	THE TIMER	,
99 008E 00 0091	BDEE6C BDEE6D		RBIT RBIT	TRUN, CNTRL TPND, PSW ENTI, PSW	;STOP THE TIMER ;RESET THE TIMER PENDING BIT
01 0094	BDEF6C		RBIT	ENTI, PSW	DISABLE TIMER INTERRUPT
02 0097 03	8 E		RET		
04					TL/DD/11044

05 06	NXTVAL		THE NEXT DRIVE VALUE TO	;*************************************
		; APPROI	PRIATE DIRECTION (CW OF	R CCW)
07 08 0098 9DF2 09 0094 DFD4		LD LD	A, STPPTR B #PORTGD	:LOAD STEP VALUE POINTER ;POINT TO PORT G ;IF CLOCKWISE :THEN GO INCREMENT POINTER ;ELSE RST CW LED ;TURN ON CCW LED ;AND DECREMENT POINTER ;IF OFF BOTTOM OF STEPS ;THEN LOOP TO TOP OF STEPS ;A -> STPPTR (SAVE NEW STPPTR) ;[STPPTR] -> PORTD (LOOKUP VAL)
09 009A DED4 10 009C BD0471 11 009F 11		IFBIT	DIR, CMD	; IF CLOCKWISE
		JP	IPTR	THEN GO INCREMENT POINTER
12 00A0 6A 13 00A1 7B	DPTR:	RBIT SBIT	CW,[B] CCW [B]	;ELSE RST CW LED •TURN ON CCW LED
14 00A2 8B		DEC	A	AND DECREMENT POINTER
14 00A2 8B 15 00A3 92FF 16 00A5 9803 17 00A7 9CF2		IFEQ	A,#0FF	; IF OFF BOTTOM OF STEPS
16 00A5 9803 17 00A7 9CF2	WRVAL		A,#03 A STEPTE	THEN LOOP TO TOP OF STEPS
18 00A9 9DF2		ĹD	A, STPPTR	;[STPPTR] -> PORTD (LOOKUP VAL
19 OOAB 9CFE		IFEQ LD X LD X LD X RET RBIT SBIT	A, B	
20 00AD AE 21 00AE 9CDC		LD X	A, [B] A, PORTD	WRITE STEP VALUE TO MOTOR
22 0080 8F		RET		
23 00B1 6B	IPTR:	RBIT	CCW,[B]	TURN OFF CCW LED
24 00B2 7A 25 00B3 8A		SBIT	CW,[B]	TURN ON CW LED
26 00B4 9204 27 00B6 64 28 00B7 EF		IFEQ	A,#04	; IF OFF TOP OF STEPS
27 00B6 64		CLR	A	THEN LOOP TO BOTTOM OF STEPS
28 00B7 EF 29		lb	WRVAL	;TURN OFF CCW LED ;TURN ON CW LED ;INCREMENT THE STEP POINTER ;IF OFF TOP OF STEPS ;THEN LOOP TO BOTTOM OF STEPS ;GO WRITE VALUE TO MOTOR
30 31	; INTERF		I FRS	
32 00FF		. = 0FF	7	;**************************************
33 34 00FF BDEF75		; BRANCH IFBIT	I TO THE APPROPRIATE IN TPND, PSW	
35 0102 08		JP	THDINT	
36 0103 BDEF73		IFBIT	IPND, PSW	;EXTERNAL INTERRUPT
37 0106 16 38 0107 BDFF78		JP Sbit	EXTINT GIE, PSW	;SOFTWARE TRAP
33 4 00FF BDEF75 35 0102 08 36 0103 BDEF73 37 0106 16 38 0107 BDEF78 39 010A 8D		RETSK		, of this that
40 41	TMRINT:			;*************************************
42 42 0108 0CE0				ENDING BIT AND STEP THE MOTOR
43 010B 9CF9 44 010D 9DFE		X LD	A, 0F9 A, B	;CONTEXT SAVE ROUTINE
45 010F 9CFA		x	A.0FA	
46 0111 BDEF6D		RBIT	TPND, PSW NXTVAL A, OFA	RESET PENDING BIT
47 0114 3098 48 0116 9DFA		J SK LD	A.OFA	;STEP THE MOTOR ;CONTEXT RESTORE ROUTINE
49 0118 9CFE		x	А, D	, contact mastering mooting
50 011A 9DF9 51 011C 8F		LD RETI	A,0F9	
02				
53 54 011D BD0778	EXTINT	SBIT	INT, FLGREG Delay	;*************************************
55 0120 3158		JSR	DELAY	;SET INTERRUPT OCCURED FLAG ;WAIT
				,
				30, 11011

NATIONAL SEMICONDUCTOR CORPORATION COP800 CROSS ASSEMBLER, REV: D1, 12 OCT 88 256 0122 BDEF6B 257 0125 8F 258 RBIT IPND, PSW RESET PENDING BIT RETI 259 260 261 262 ;SUPPORT ROUTINES CONTINUED ;***************************** TIMVAL: 262 263 264 265 266 267 268 269 270 271 272 0126 DE14 273 0128 BD0474 274 0128 17 275 012C BD0475 276 012F 0E 277 0130 BD0476 278 0133 05 279 0134 9A02 280 0136 9E08 281 0138 8E 282 0139 9A88 283 0138 9E13 284 013D 8E 285 013E 9A18 269 ;POINT TO STORAGE FOR TIMVAL ;IF LOWEST SPEED BIT SET ;THEN USE SLOWEST SPEED ;IF SECOND LOWEST SPD BIT SET ;THEN USE SLOW SPEED ;IF SECOND HIGHEST SPD BIT SET ;THEN USE FAST SPEED ;ELSE USE FASTEST SPEED ;400steps/sec = 2rev/sec B. #TVALO LD LD IFBIT JP IFBIT 4,CMD SLOWER 5,CMD SLOW 6,CMD FAST JP IFBIT JP [B+],#02 [B],#08 FASTER: LD LD RET FAST: [B+],#088 [B],#013 LD ;200steps/sec = 1rev/sec LD RET [B+],#018 [B],#027 SLOW: LD ;100steps/sec = .5rev/sec 286 0140 9E27 287 0142 8E 288 0143 9A54 289 0145 9E9C LD RET SLOWER: LD [B+],#054 [B],#09C ;25steps/sec = .125rev/sec LD 290 0147 8E 291 RET 292 292 293 294 295 0148 3126 296 014A 9D14 297 014C BDEC82 298 014F 01 299 0150 8D 300 0151 9D15 301 0153 BDED82 302 0156 8E SPEED: ***** COMPARE CURRENT MOTOR SPEED WITH DESIRED MOTOR SPEED JSR LD IFEQ TIMVAL A,TVALO A,TAULO TSTHI ;CALCULATED DESIRED SPEED VAL ; IF DESIRED LBYTE EQUALS CURRENT LBYTE THEN GO TEST HI-BYTE ;ELSE NOT EQUAL RETURN AND SKIP JP RETSK LD TSTHI: A, TVAL1 ;IF HI-BYTE EQUALS CURRENT HI-BYTE ;THEN DESIRED = CURRENT RETURN ;ELSE DESIRED != CURRENT RET & SKIP A. TAUHI RET 303 0157 8D 304 RETSK 305 DELAY: 306 ; INSERT A DELAY TL/DD/11044-8

AN-714

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307 0158 D301 308 015A D4FF 309 015C C4 310 015D FE 311 015E C3 312 015F FA 313 0160 8E 314 315	LD DLY1: LD DLY2: DRSZ JP DRSZ JP RET .END	0F3,#01 0F4,#0FF 0F4 DLY2 0F3 DLY1		INGLE STEP & EXT X .256mS X 6	
	NUCK 0000 -				TL/DD/11044-9
B 00FE CMD 0004 CREG1 00F1 DLY1 015A DPTR 00A0 FAST 0139 GO 0000 IPTR 00B1 MS1 0001 PORTGC 00D5 PORTLC 00D1 SLOWER 0143 SSTEP 0041 TAUHI 00ED TC3 0005 TMRLO 00EA TPND 0005	BUSY 0002 * CNTRL 00EE CW 0002 DLY2 015C ENI 0001 * FASTER 0134 * IEDG 0002 * MID 0055 * MS2 0002 * NXTVAL 0098 PORTGD 00D4 PORTLD 00D0 SETGO 0067 STEPS 0005 * TAULO 00EC TIMVAL 0126 TMRSET 0081	CCW 0003 CONT 0055F DELAY 0158 DO 005A ENTI 0004 FLGREG 0007 INT 0000 MODE 0002 MS3 0003 OUT 0086 PORTGP 00D6 PORTGP 00D6 PORTCP 00D2 SIOR 00E9 SPEED 0148 STOP 003B TC1 0007 TMRHI 00EB TOP 0025 TSTHI 0151 WRVAL 00A7	DIR DO2 EXTINT GIE MS0 * MSEL PORTD * PORTD * SLOW * SLOW * SLOW * TC2 TMRINT TP2 TVALO	00F0 0001 00058 011D 0000 0003 0000 0003 x 000C 00D7 x 000F 013E 0004 x 000F 0005 x	
MACRO TABLE					TL/DD/11044-10
NO WARNING LINES					
NO ERROR LINES					
282 ROM BYTES US	ED				
SOURCE CHECKSUM = 8 Object checksum = 0					
INPUT FILE C:MOTO LISTING FILE C:MOTO OBJECT FILE C:MOTO	R.PRN		TL/DD	/11044-11	
LIFE SUPPORT POLIC	Ŷ				
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